

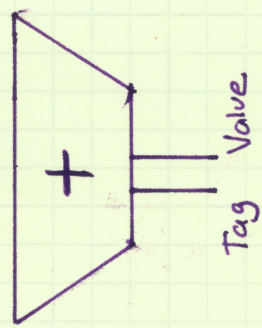
CYCLE —

MUL R1, R2 → R3
 ADD R3, R4 → R5
 ADD R2, R6 → R7
 ADD R8, R9 → R10
 MUL R7, R10 → R11
 ADD R5, R11 → R5

Register Alias Table

	V	Tag	Value
R1			
R2			
R3			
R4			
R5			
R6			
R7			
R8			
R9			
R10			
R11			

SOURCE 1			SOURCE 2		
V	Tag	Value	V	Tag	Value
a					
b					
c					
d					



SOURCE 1			SOURCE 2		
V	Tag	Value	V	Tag	Value
x					
y					
z					
t					

