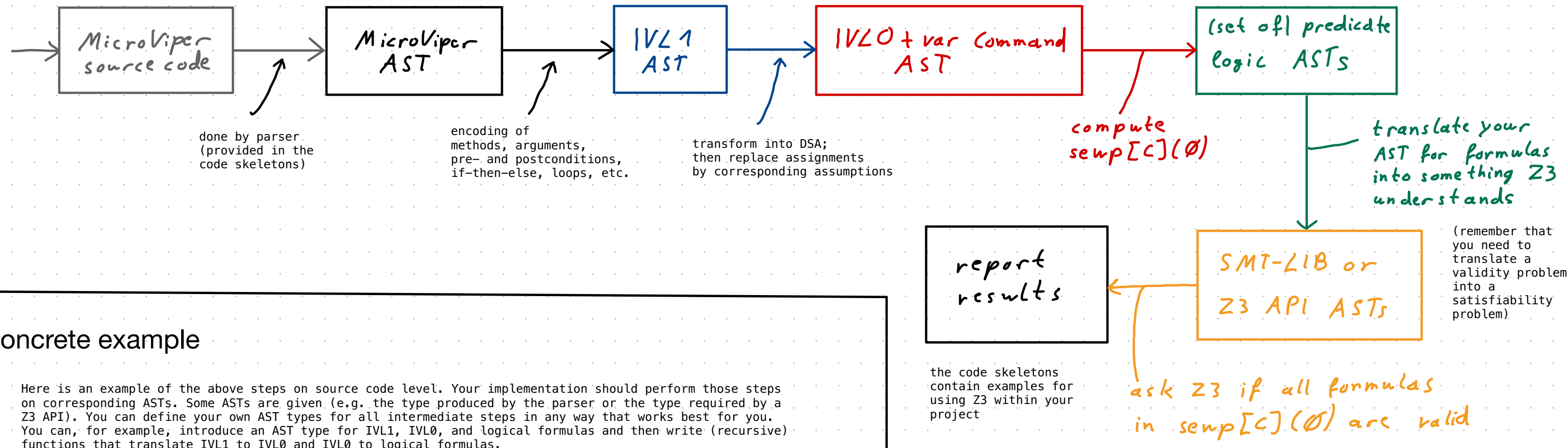


Sketch of one possible approach to get started with project A

Main idea: verification by compilation, that is, translate microViper ASTs (abstract syntax trees) obtained from a microViper Parser into ASTs of verification conditions that you can pass to Z3 via multiple translation steps as discussed in modules 1 - 4.

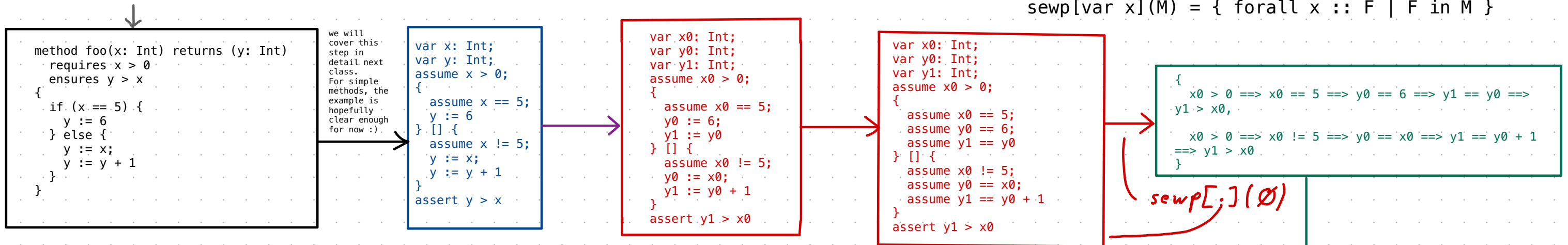


Concrete example

Here is an example of the above steps on source code level. Your implementation should perform those steps on corresponding ASTs. Some ASTs are given (e.g. the type produced by the parser or the type required by a Z3 API). You can define your own AST types for all intermediate steps in any way that works best for you. You can, for example, introduce an AST type for IVL1, IVL0, and logical formulas and then write (recursive) functions that translate IVL1 to IVL0 and IVL0 to logical formulas.

The example is for the first core goal. If you can implement a working verifier this way, then all other features can be constructed on top by encoding additional features into IVL1.

You can find more examples in the code examples that accompany the slides.



you can eliminate variable declarations at the top or introduce quantifiers using $\text{sewp}[\text{var } x](M) = \{ \text{forall } x :: F \mid F \text{ in } M \}$

This is not *the* approach to solve the project. You can make many different design choices as long as you can explain your choices (and they are sound) :)

For example, you can eliminate some variable declarations or always use universal quantifiers. You can also add an assumption after every assertion to deal with masked verification errors (see module 4). You also do not have to introduce explicit AST types for every translation step if you do not want to (but then you have to be careful).