

Component-Based Modeling in Mediator

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Abstract. In this paper we propose a new language Mediator to formalize component-based models. Mediator provides a two-step modeling approach. *Automata*, encapsulated with a interface of ports, is the basic behavior unit. *Systems* make it able to declare automata as components or connectors, and then glue them together as a more complex model. With help of Mediator, components and systems can be modeled separately, while the formal nature is still precisely guaranteed. Through some simple examples, we show that this new language can be used in various practical scenarios.

1 Introduction

Component-based software engineering, as one of the *software reuse* approaches, has been prospering for a long time. Through proper encapsulation and clearly declared interface, a *component* can be invoked by different applications without knowledge on its implementation details. Currently, there are various tool supports on component-based modeling:

1. *Industrial tools*, including commercial tools like NI LabVIEW[12], MATLAB Simulink[7], and academic tools like Ptolomy[8]. These tools provide powerful formalism and a large number of built-in component to support commonly-used platforms. However, due to the complexity of models, such tools mainly focus on synthesis and simulation, instead of formal verification.
2. *Formal tools*, e.g. Esterel SCADE[1] and rCOS[11]. SCADE, based on a synchronous data flow language LUSTRE, is equipped with a powerful tool-chain and widely used development of embedded systems. rCOS, on the other hand, is a refinement calculus on object-oriented design.

Existing work[13] has shown that, formal verification based on existing industrial tools is hard to realize due to its complexity and non-open architecture. However, according to the feedbacks from programmers, unfamiliarity of formal specifications is still the main obstacle stopping the from using formal tools. For example, even in the most famous formal modeling tools with graphical user interfaces (e.g. PRISM[9], Uppaal[2]), it requires at least knowledge on automata theory to properly encode the models.

Reo[3], the coordination language, provides a solution where advantages of both can be integrated in a natural way. Reo is a channel-based language where

its semantics are clearly specified in the very beginning. And thanks to its graphical notations, organization of components can be illustrated and exhibited in a natural way.

Inspired by Reo, we present a new modeling language, Mediator. Mediator is a hierarchical modeling language that provides formalism for both high-level *system* layouts and low-level *automata*-based behavioral units. With help of a rich-featured type system, we can describe complex data structure and powerful automata in a rather formal way. And automata (or other systems) can be then declared as either components or connectors in a system. Both automata and systems are encapsulated with a interface containing *a) a set of input or output ports* and *b) a set of template parameters* so that they can be easily reused in multiple projects.

The paper is structured as follows. In Section 2, we briefly present the syntax of Mediator and formalizations of the language entities. Then in Section 3, we introduce the formal semantics of Mediator. Section 4 presents a case study where a commonly used coordination algorithm *leader election* is modeled in Mediator. The conclusion and future work can be found in Section 5.

2 Syntax of Mediator

In this section, we introduce the syntax of Mediator. A Mediator program, as shown in the following block, essentially contains several parts,

$$\langle \text{program} \rangle ::= (\langle \text{typedef} \rangle \mid \langle \text{function} \rangle \mid \langle \text{automaton} \rangle \mid \langle \text{system} \rangle)^*$$

1. *Typedefs* that give aliases to specified types.
2. *Function* definitions that defines customized functions.
3. *Automaton* blocks that describe an automaton with given parameters.
4. *System* blocks to compose automata as components or connections.

2.1 Type System

Mediator provides a rich-featured type system that supports various commonly-used data types in both formal modeling languages and programming languages.

Primitive Types. Table. 1 shows the primitive types supported in Mediator.

Composite Types. Composite types offer an approach to construct complex data types with simpler ones. Several composite patterns are introduced as follows,

- *Tuple.* The *tuple* operator ‘,’ can be used to construct a finite tuple type with several base types.
- *Union.* The *union* operator ‘|’ is designed to combine *disjoint* types as a more complicated one. This is similar to the union type in C language but much easier to use.
- *Array and Slice.* An *array* $T[n]$ is a finite ordered collection containing exactly n elements of type T . Moreover, a *slice* is an array of which the capacity is not specified, i.e. slice is a dynamic array.

Table 1. Primitive Data Types

Name	Declaration	Term Example
Bounded Integer	<code>int lowerBound .. upperBound</code>	<code>-1,0,1</code>
Integer	<code>int</code>	<code>-1,0,1</code>
Real	<code>real</code>	<code>0.1, 1E-3</code>
Boolean	<code>bool</code>	<code>true, false</code>
Character	<code>char</code>	<code>'a', 'b'</code>
Enumeration	<code>enum item₁, ..., item_n</code>	<code>enumname.item</code>

Table 2. Composite Data Types (T denotes an arbitrary data type)

Name	Declaration
Tuple	<code>T₁, ..., T_n</code>
Union	<code>T₁ ... T_n</code>
Array	<code>T [length]</code>
Slice	<code>T []</code>
Map	<code>map [T_{key}] T_{value}</code>
Struct	<code>struct { field₁:T₁, ..., field_n:T_n }</code>
Initialized	<code>T_{base} init term</code>

- *Map*. A `map [Tkey] Tval` is a dictionary that maps a key of type T_{key} to a value of type T_{val} .
- *Struct*. A `struct { field1 : T1, ..., fieldn : Tn }` contain n fields, each has a particular type T_i and a unique identifier id_i .
- *Initialized*. A initialized type make it able to specify default values to types.

For simplicity in formalizing data types, we introduce the concept *domain* of a type.

Formalization 1 (Domain) We use $Dom(T)$ to denote the value domain of type T , i.e. the set of all possible value of T .

Example 1 (Types Used in A Queue). Now let us introduce some type declarations and local variables used in an automaton **Queue**. As shown in the following code fragment, we declares a singleton enumeration **NULL**, which contains only one element **null**. The buffer of a queue is in turn formalized as an array of **T** or **NULL**, indicating that a queue element can be either an assigned item or empty. The head and tail pointer are defined as two bounded integers.

```

1  typedef enum {null} init null as NULL;
2  automaton <T:type,size:int> Queue(A:in T, B:out T) {
3    variables {
4      buf : ((T | NULL) init null) [size];
5      phead : int 0 .. (size - 1) init 0;
6      ptail : int 0 .. (size - 1) init 0;
7    }

```

```

8      ...
9  }

```

Parameter Types. On many occasions, you may want to define a generalizable structure that includes a template function or template component. For example, a binary operator that support various operation (+, ×, etc.), or an encrypted communication system that can make use of different encryption components. Parameter types make it able to take functions and components (or systems, of course) as a template parameter. But such types will be resolved in instantiation, and hence can only be used in templates of instantiable structures (automata and systems).

1. An *Interface*, denoted by **interface** (**port**₁:T₁, ..., **port**_n:T_n), defines a parameter that could be any *automaton* or *system* that have exactly the same interface (i.e. both number and directions of the ports are matching). Interfaces are only used in templates of *systems*.
2. A *Function*, denoted by **func** (**arg**₁:T₁, ..., **arg**_n:T_n) : T defines a function that have the same argument types and return types. Functions are permitted to show up in templates of *systems* and *automata*.

In Example. 7 we have a system with a *interface* parameter.

2.2 Functions

Functions make it able to encapsulate complex computations and reuse them. In Mediator, the functions are a bit different from common programming languages – they include no control statements but assignments. This design makes functions' behavior more predictable (i.e. it can be simplified into a single mathematical representation). For the same reason, functions have access only to its local variables and arguments.

The abstract syntax tree of functions is shown as follows.

$ \begin{aligned} \langle funcDecl \rangle &::= \mathbf{function} \langle template \rangle^? \langle identifier \rangle (\langle arguments \rangle) \{ \\ &\quad \mathbf{variables} \{ \langle varDecl \rangle^* \}^? \\ &\quad \mathbf{statements} \{ \langle assignStmt \rangle^* \langle returnStmt \rangle \} \\ \langle assignStmt \rangle &::= \langle term \rangle := \langle term \rangle \\ \langle returnStmt \rangle &::= \mathbf{return} \langle term \rangle \\ \langle varDecl \rangle &::= \langle identifier \rangle : \langle type \rangle (\mathbf{init} \langle term \rangle)^? \end{aligned} $
--

Basically, definition of a function includes the following parts.

Template. A function may contains an optional template including a set of parameters. A parameter can be either a *type* parameter (decorated by **type**) or a *value* parameter (decorated by its type). Values of the parameters should be determined in compiling-time. Once a parameter is declared, it can be referenced in

all the following language elements, e.g. *a) the following parameter declarations, b) arguments and return types and c) function statements.*

Name. An identifier that indicates the name of this function.

Type. Type of a function (**func** type in Section. 2.1) is determined by its *a) number of arguments, b) type of arguments and c) type of return value.* Note that here the arguments are read-only. In other words, any assignment to an argument is strictly prohibited.

Body. Body of a function includes an optional set of local variables and a list of ordered statements that describes how the return value is calculated. It must be ended by a **return** statement.

Example 2 (Incline Operation on Bounded Integers). Incline operation of pointers are commonly used in a *round-robin* queue, where storage are reused circularly. The **next** function shows that how pointers in such queues (denoted by a bounded integer) incline.

```
1  function <size:int> next(pcurr:int 0..(size-1)) : int 0..(size-1) {
2      statements { return (pcurr + 1) % size; }
3  }
```

2.3 Automaton : The Basic Behavioral Unit

$$\begin{aligned}
 \langle \text{automaton} \rangle &::= \text{automaton } \langle \text{template} \rangle^? \langle \text{identifier} \rangle (\langle \text{port} \rangle^*) \{ \\
 &\quad (\text{variables } \{ \langle \text{varDecl} \rangle^* \})^? \\
 &\quad \text{transitions } \{ \langle \text{transition} \rangle^* \} \} \\
 \langle \text{port} \rangle &::= \langle \text{identifier} \rangle : (\text{in} \mid \text{out}) \langle \text{type} \rangle \\
 \langle \text{transition} \rangle &::= \langle \text{guardedStmt} \rangle \mid \text{group } \{ \langle \text{guardedStmt} \rangle^* \} \\
 \langle \text{guardedStmt} \rangle &::= \langle \text{term} \rangle \rightarrow (\langle \text{stmt} \rangle \mid \{ \langle \text{stmt} \rangle^* \}) \\
 \langle \text{stmt} \rangle &::= \langle \text{term} \rangle := \langle \text{term} \rangle \mid \text{sync } \langle \text{identifier} \rangle^+
 \end{aligned}$$

Template. Compared with templates in functions, template in an automaton supports parameters of *function type*.

Name. The identifier of automaton.

Type. Type of an automaton (an **interface** type in Section 2.1) is determined by the *number* and *types* of its ports. Type of a port in an automaton contains a prefix, either **in** or **out**, indicating the direction of its data-flow, and a normal data type as a suffix. To ensure the well-defineness of automata, ports are required to have an *initialized* type, e.g. **int 0..1 init 0** instead of ~~**int 0..1**~~.

Variables. Two types of variables are used in a automaton definition, they are:

1. *Local variables* that are declared in the *variables* section.

2. *Adjoint variables* used to describe the status of ports. Syntactically, they are denoted as built-in fields of ports. For example, considering a port **A**, we assume that it has two boolean fields **A.reqRead** and **A.reqWrite** indicating if there is a pending *read* or *write* request on this port, and a data field **A.value** indicating the current value of this port. In this paper, adjoint variables have the same meaning with *shared variables*.

We require that for an output port the **reqRead** field is read-only and the **reqWrite** field is writable. Similarly, for an input port the **reqRead** field is writable but its **reqWrite** field is read-only. The **value** field can be overwritten only in an output port.

Transitions. In Mediator, behavior of a automaton is described by a set of guarded transitions (groups), with no explicit concept of locations. As shown in Example 3, a *transition* (denoted by *guard* \rightarrow *statements*) comprises two parts, a boolean term *guard* that declares the activating condition of this transition, and a (set of) statement(s) that describe how the variables are updated when the transition is fired.

Currently, we have two types of statements supported in automata, they are:

- *Assignment Statement* (**var**₁, ..., **var**_n := **term**₁, ..., **term**_n). An assignment statement supports multiple assignments at the same time, where local variables and writable adjoint variables are permitted to be assigned.
- *Synchronizing Statement* (**sync port**₁, ..., **port**_n). Synchronizing statements are synchronizing *flags* used when joining multiple automata. More details about synchronizing statements are introduced in Section 3.3.

With the introduction of shared variables, synchronizing transitions in automata joining is not as easy as in traditional automata where all variables are local. Informally speaking, the synchronizing statements are used to create a proper schedule of assignment statements so that assignments of shared variables are performed before referring them.

Synchronizing statements are also important flags to distinguish external transitions and internal transitions. A transition is called *external* iff. it synchronizes with its environment through some ports, or *internal* otherwise. Literally, all transitions, where synchronizing statements are involved, are *external* transitions. In such transitions, the following rules are strictly required to avoid read/write conflicts.

1. Any assignment statements including reference to an input port (**A**, for example) should be placed after its corresponding synchronizing statement **sync A**.
2. Any assignment statements to an output port (**B**, for example) should be placed before its corresponding synchronizing statement **sync B**.

Formalization 2 (Transitions) *Formally, we use $g \rightarrow S$ to denote a transition, where g is the guard formula and $S = \{s_1, \dots, s_n\}$ is a set of statements.*

Different from a typical automaton, transitions in Mediator automata are organized with *priority*. A transition has higher priority iff. it is placed in front of the other one. And when multiple transitions are activated by the environment, the one with highest priority will be fired first. For example, suppose $g_1 \rightarrow S_1, \dots, g_n \rightarrow S_n$ is a list of transitions, we could use an equivalent form to rewrite them as the followings, where priority is not required any more.

$$g_1 \rightarrow S_1, \neg g_1 \wedge g_2 \rightarrow S_2, \dots, \neg g_1 \wedge \neg g_2 \wedge \dots \wedge \neg g_{n-1} \wedge g_n \rightarrow S_n$$

Example 3 (Transitions in Queue). In a queue, we use internal transitions to capture the changes of environment and perform corresponding updates consistently. For example, the input port A (already defined in Example. 1) becomes ready to read (i.e. `reqRead` set to `true`) when the buffer is not full, and the output port B becomes ready to write when the buffer is not empty, and vice versa. External transitions, on the other hand, mainly show the details of the enqueue and dequeue operations.

```

1 // internal transitions
2 B.reqWrite && (buf[ptail] == null) -> B.reqWrite := false;
3 !B.reqWrite && (buf[ptail] != null) -> B.reqWrite := true;
4 A.reqRead && (buf[phead] != null) -> B.reqRead := false;
5 !A.reqRead && (buf[phead] == null) -> B.reqRead := true;
6
7 // enqueue operation (as an external transition)
8 (A.reqRead && A.reqWrite) -> {
9     sync A; buf[phead] := A.value; phead := next(phead);
10 }
11 // dequeue operation (as an external transition)
12 (B.reqRead && B.reqWrite) -> {
13     B.value := buf[ptail]; ptail := next(ptail); sync B;
14 }
```

Priority of transitions make the automaton fully deterministic. However, in some cases non-determinism is still more than necessary. *Transition groups* are, consequently, imported to handle such cases. When encapsulated by a group, transitions are unordered and don't ruled by priority. Instead, the group itself is literally ordered w.r.t. other groups and single transitions (basically we can take all single transitions as a trivial transition group).

Formalization 3 (Transition Groups) A transition group t_G is formalized as a finite list of guarded transitions

$$t_G = \{t_1, \dots, t_n\}, t_i = g_i \rightarrow S_i$$

where t_i is a single transition with guard g_i and a set of statements S_i .

Since a single transition $g \rightarrow S$ can be equivalently written as a singleton group $\{g \rightarrow S\}$, it's acceptable if we assume that each automaton comprises a set of transition groups but no standalone transitions.

Example 4 (Yet Another Queue Implementation). Let's consider the external transitions in Example. 3 which captures the core behavior of a queue. When both enqueue and dequeue operations are activated, in that example, reading will always be fired first. Such a queue may get stuff up immediately when requests start accumulating. But here we presents another non-deterministic implementation based on *transition groups* to solve this problem.

```

1  group {
2      // enqueue operation (as an external transition)
3      (A.reqRead && A.reqWrite) -> {
4          sync A; buf[phead] := A.value; phead := next(phead);
5      }
6      // dequeue operation (as an external transition)
7      (B.reqRead && B.reqWrite) -> {
8          B.value := buf[ptail]; ptail := next(ptail); sync B;
9      }
10 }
```

In this code fragment above, the two transitions are encapsulated in a group. Consequently, firing of the dequeue operation doesn't rely on deactivation of the enqueue operation.

With all the language elements of an automaton properly formalized, now we introduced the formalization of a complete automaton.

Formalization 4 (Automata) We use a tuple $A = \langle Ports, Vars, Trans_G \rangle$ to represent an automaton, where *Ports* is a set of ports, *Vars* is a set of local variables and $Trans_G = \{t_{G_1}, \dots, t_{G_n}\}$ is a set of transition groups that are defines in Formalization. 3.

2.4 System : The Composition Approach

Theoretically, an automaton in Mediator is powerful enough to represent any classical software system (where time and probability are not involved, of course). However, modeling complex systems through a mess of transitions and tons of local variables may become a real disaster.

As we have mentioned previously, Mediator is designed to help the programmers, even nonprofessionals, to enjoy the convenience of formal tools. To achieve this goal, we introduce a new language element called *system*. Basically, a *system* is a textural format of a hierarchical diagram (see in Figure. 1) where automata and smaller systems are naturally organized as different roles (*components* or *connections*). Both *components* and *connectors* (or *channels*) are well-known concepts in component-based software engineering. Though having different names, their semantics all turn out to the same nature, *automata*.

Hierarchical diagrams have already been used in various modeling tools (for example, SCADE[1, 4], Simulink[7] and LabVIEW[12]) and formal languages (Reo[3], AADL). However, in most tools, connections are simply synchronous link that seal two ports together. Inspired by Reo, we make it able to declare an automaton as a connection, which lead to more powerful and intuitive diagrams.

Example 5 (Hierarchical Diagram of a Middleware). Figure. 1 gives a simple diagram of a message-oriented middleware, where a queue work as a connector to coordinate between the components (message producers and consumers).

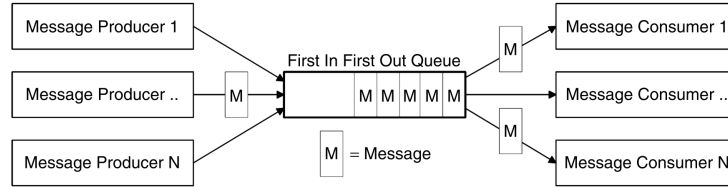


Fig. 1. A Senerio where Queue is used in Message-Oriented Middleware[5]

The abstract syntax tree of *systems* is shown as follows.

$ \begin{aligned} \langle system \rangle &::= \mathbf{system} \langle template \rangle^? \langle identifier \rangle (\langle port \rangle^*) \{ \\ &\quad (\mathbf{internals} \langle identifier \rangle^+)^? \\ &\quad (\mathbf{components} \{ \langle componentDecl \rangle^* \})^? \\ &\quad \mathbf{connections} \{ \langle connectionDecl \rangle^* \} \} \\ \langle componentDecl \rangle &::= \langle identifier \rangle^+ : \langle systemType \rangle \\ \langle connectionDecl \rangle &::= \langle systemType \rangle \langle params \rangle (\langle portName \rangle^+) \end{aligned} $

The *type* of a system (i.e. its template, name, and ports) shares exactly the same form and meaning with *type* of an automaton. This also suggests that system is NOT a special semantics unit, but simply an compositional approach to pile up automata. We declare a system with its template, name and type, then it is implemented by an optional set of *internal nodes*, an optional set of *components* and a set of *connections*.

Template. In templates of systems, all parameters types are supported including *a)* parameters of abstract type **type**, *b)* parameters of primitive types and composite types, and *c)* interfaces and functions.

Name and Type. Exactly the same with *name* and *type* of an automaton.

Components. In the **components** segment, we can declare components of an *interface type*, e.g. name of an automaton (in Example. 6), name of a system, or a parameter of interface type (in Example. 7). Concrete values should be provided in declaration if required. After being declared, ports of a component can be referred simply by **component.portname**.

Connections. Connections, e.g. the arrows in Figure. 1, are used to link between *a) the ports of itself*, *b) the ports of components*, and *c) the internal nodes*. We

declare the connections is a **connections** segment as shown in the following example. Both components and connections are supposed to execute concurrently as automata.

Internals. In certain cases, we need to combine multiple connections to perform more complicated coordination. Internal nodes, as declared in **internals** segment, are untyped identifiers which are capable to be linked to two other internal nodes or ports. Essentially, data flow in an internal node should always follow the same direction, i.e. an internal node doesn't collect or generate any data, it only receives from one end and forward it simultaneously. The direction, together with the type of an internal node, should be determined when being compiled.

Example 6 (Mediator Model of the System in Figure. 1). In the previous figure, a simple scenario is presented where a queue is used as a message-oriented middleware. To model this scenario, we need two automata *Producer* and *Consumer* (definitions are omitted due to space limit) that produce or consume message of type *T*.

```

1  automaton <T:type> Producer (OUT: out T) { ... }
2  automaton <T:type> Consumer (IN: in T) { ... }
3
4  system <T:type> middleware_in_use () {
5      components {
6          producer_1, producer_2, producer_3 : Producer<T>;
7          consumer_1, consumer_2, consumer_3 : Consumer<T>;
8      }
9      internals M1, M2 ;
10     connections {
11         Merger<T>(producer_1.OUT, producer_2.OUT, producer_3.OUT, M1);
12         Queue<T>(M1, M2);
13         Replicator<T>(M2, consumer_1.IN, consumer_2.IN, consumer_3.IN);
14     }
15 }
```

Now we introduce the formalization of systems. Since both components and connections are automata, we will not distinguish them in the formal structure.

Formalization 5 (System) *A system is denoted by a 4-tuple*

$$S = \langle Ports, Automata, Internals, Links \rangle$$

where *Ports* is a set of ports, *Automata* is a set of automata defined in Formalization. 4 (both components and connections), *Internals* is a set of internal nodes and *Links* is a set of pairs, where each element is a port or an internal node. A link $\langle p_1, p_2 \rangle$ suggests that p_1 and p_2 are linked together.

A well-defined system satisfies the following assumptions:

1. $\forall \langle p_1, p_2 \rangle \in \text{Links}$, data transfer from p_1 to p_2 . For example, if $p_1 \in \text{Ports}$ is an input port, p_2 could be a) an output port of the system ($p_2 \in \text{Ports}$), b) an input port of some automaton $A_i \in \text{Automata}$ ($p_2 \in A_i.\text{Ports}$) or c) an internal node ($p_2 \in \text{Internals}$).
2. $\forall n \in \text{Internals}, \exists! p_1, p_2$ i.e. $\langle p_1, n \rangle, \langle n, p_2 \rangle \in \text{Links}$.
3. The *type* function can be extended to *Internals* and satisfies $\forall \langle p_1, p_2 \rangle \in \text{Links}, \text{type}(p_1) = \text{type}(p_2)$.

3 Semantics

In the section, we introduce the formal semantics of Mediator through the following steps. First we introduce the concept *configuration* to describe the state of an automaton. Then we consider the formalizations proposed in Section. 2 and canonicalize them. Finally, based on the canonical form of automata, we show how define their semantics as *transition systems*.

As we have mentioned before, systems are only composition of automata. Consequently, we will not introduce its formal semantics directly. But instead, we show how automata in a system are composed and flattened into a single automaton.

3.1 Configurations of Automata

In a Mediator automaton, due to absence of locations, the state only depends on the values of its *local variables* and *adjoint variables*. First we introduce the definition of *valuation* on a set of variables. Basically, a valuation is a function that maps variables to one of its valid values. To make things more clear, in the rest of the paper we use \mathbb{D} to denote the set of all values of all types.

Definition 1 (Valuation). A valuation of a set of variables V is defined as a function $v : V \rightarrow \mathbb{D}$ that satisfies $\forall x \in V, v(x) \in \text{Dom}(\text{type}(x))$. We denote the set of all possible valuations of *Vars* by $\text{Val}(\text{Vars})$.

Definition 2 (Configuration). A configuration of an automaton $A = \langle \text{Ports}, \text{Vars}, \text{Trans}_G \rangle$ is defined as a tuple (v_{loc}, v_{adj}) where $v_{loc} \in \text{Val}(\text{Vars})$ is a valuation on local variables, and $v_{adj} \in \text{Val}(\text{Adj}(P))$ is a valuation on adjoint variables. We use $\text{Conf}(A)$ to denote all the configurations of A .

With *configurations* formally given, it's easy to give mathematical descriptions on all the language elements in an automaton. We don't care about systems since they are not semantics units. Table. 3 shows their mathematical descriptions where we assume that all the elements belong to an automaton A .

Table 3. Mathematical Description of the Formalizations

Name	Formalization	Mathematical Description
Assignment Statement	s	$s : Conf(A) \rightarrow Conf(A)$
Transition	$g = g \rightarrow S$	$g : Conf(A) \rightarrow Bool, S \in Statement^*$
Transition Group	$t_G = \{t_1, \dots, t_n\}$	$t_G \in P(TR)$
Enumeration	$enum \text{ item}_1, \dots, \text{ item}_n$	

3.2 Canonical Form of Transitions and Automata

It is clear that we can use various combination of transitions to, actually, describe the same thing. For example, $a := b; c := d$ and $a, c := b, d$. These irregular transitions make lead to a non-intuitive algorithm we joining multiple automata. In this subsection we show how to canonicalize them.

Definition 3 (Canonical Transitions). *A transition $t = g \rightarrow \{s_1, \dots, s_n\}$ is canonical iff. its statements $\{s_i\}$ is an interleaving sequence of assignments and synchronizing statements which start from and end by assignments, e.g. $a := exp_1; sync A; b := exp_2; \dots c := exp_3$.*

Suppose $g \rightarrow \{s_1, \dots, s_n\}$ is a transition of automaton A , the following steps show how it is canonicalized,

- S1** If we can find a continuous subsequence s_i, \dots, s_j (where s_k is an assignment statement for all $k = i, i + 1, \dots, j$, and $j > i$), we will merge them as a single one. Since an assignment statement is formalized as a function $f : Conf(A) \rightarrow Conf(A)$. Thus a list of multiple assignments s_i, \dots, s_j can be replaced using $s' = s_i \circ \dots \circ s_j$.
- S2** Keep going with **S1** until there is no further subsequence to merge.
- S3** Put identical assignments $id_{Conf(A)}$ into any adjacent synchronizing statements. Similarly, if the statements' list start from or end with a synchronizing statement, we should also use $id_{Conf(A)}$ to decorate its head and tail.

It's clear that once we found such a continuous subsequence, the merging operation will reduce the number of statements. Obviously, number of statements is initialized finite, so the looping algorithm always terminates within certain time.

Definition 4 (Canonical Automata). *An automaton $A = \langle Ports, Vars, Trans_G \rangle$ is canonical iff. a) $Trans_G$ includes only one transition group and b) all transitions in this group are also canonical.*

Now we show how $Trans_G$ is reformed to make the automaton canonical. Suppose $Trans_G$ is composed of a set of transition groups:

$$\{t_{G_1} = \{g_{11} \rightarrow S_{11}, \dots, g_{1l_1} \rightarrow S_{1l_1}\}, \dots, t_{G_n} = \{g_{n1} \rightarrow S_{n1}, \dots, g_{nl_n} \rightarrow S_{nl_n}\}\}$$

Informally speaking, once a transition in t_{G_1} is activated, all the other transitions in $t_{G_i} (i > 1)$ should be strictly prohibited from being fired. We use $activated(t_G)$ to denote the condition where at least one transition in t_G is enabled, formalized as

$$activated(t_G = \{g_1 \rightarrow S_1, \dots, g_n \rightarrow S_n\}) = g_1 \vee \dots \vee g_n$$

Then we can generate the new set of transitions with no dependency on priority as followings.

$$\begin{aligned} &g_{11} \rightarrow S_{11}, \dots, g_{1l_1} \rightarrow S_{1l_1}, \\ &g_{21} \wedge \neg activated(t_{G_1}) \rightarrow S_{21}, \dots, g_{2l_2} \wedge \neg activated(t_{G_1}) \rightarrow S_{2l_2}, \dots \\ &g_{n1} \wedge \neg activated(t_{G_1}, \dots, t_{G_{n-1}}) \rightarrow S_{n1}, \dots, g_{nl_n} \wedge \neg activated(t_{G_1}, \dots, t_{G_{n-1}}) \rightarrow S_{nl_n} \end{aligned}$$

To simplify the equations, we use $activated(t_{G_1}, \dots, t_{G_{n-1}})$ to indicate that at least one group in $t_{G_1}, \dots, t_{G_{n-1}}$ is activated. It's equivalent form is $activated(t_{G_1}) \vee \dots \vee activated(t_{G_{n-1}})$.

3.3 From System to Automaton

System in Mediator provides an approach to construct hierarchical models with automata (declared as *components* and *connectors*). In this section, we present the algorithms to flat the hierarchical model as a typical automaton.

Algorithm 1. shows how a system is flattened as an automaton, where we assume that all the sub-automata are canonical. First we refactor all the variables in its sub-automata to avoid name conflicts, so that transitions will not suffer from ambiguity. Links are established by replacing occurrence of one port (or internal node) with the other.

After preparation of sub-automata, we then put all the transitions together, both *internal* ones and *external* ones.

Internal transitions are easy to handle. Since an internal transition do not synchronize with other transitions, we directly put all the internal transitions in all sub-automata into the flattened automaton.

External transitions, on the other hand, have to synchronize with its corresponding external transitions in other sub-automata. For example, when an automaton want to read some thing from a input port P_1 , there must be another one that is writing something to its output port P_2 where P_1 and P_2 are overlapped in the system.

Algorithm 1 Flatting a System to an Automaton

Require: A system $S = \langle Ports, Automata, Internals, Links \rangle$

Ensure: An automaton A

```
1:  $A \leftarrow$  an empty automaton
2:  $A.Ports \leftarrow S.Ports$ 
3: rename local variables in  $Automata = \{A_1, \dots, A_n\}$  to avoid duplicated names
4: for  $l = \langle p_1, p_2 \rangle \in S.Links$  do
5:   if  $p_1 \in S.Ports$  then
6:     replace all occurrence of  $p_2$  with  $p_1$ 
7:   else
8:     replace all occurrence of  $p_1$  with  $p_2$ 
9:   end if
10: end for
11:  $ext\_trans \leftarrow \{\}$ 
12: for  $i \leftarrow 1, 2, \dots, n$  do
13:    $A.Vars \leftarrow A.Vars + A_i.Vars$ 
14:    $A.Trans_G \leftarrow A.Trans_G + Internal(A_i.Trans_G)$ 
15:    $ext\_trans \leftarrow ext\_trans + External(A_i.Trans_G)$ 
16: end for
17: for  $set\_trans \in P(ext\_trans)$  do
18:    $new\_edge \leftarrow Schedule(S, set\_trans)$ 
19:   if  $new\_edge \neq null$  then
20:      $A.Trans_G = A.Trans_G + \{new\_edge\}$ 
21:   end if
22: end for
```

In Mediator *systems*, only adjoint variables (**reqRead**, **reqWrite** and **value**) are shared between automata. During synchronization, the most important principle is to make sure assignments to shared variables are executed before they are dereferenced. Basically, this is a topological sorting problem. A detailed algorithm is described in Algorithm 2.

Algorithm. 2 does not always produce a synchronized transitions. Line 25 shows several situations where the synchronization process fails:

1. The dependency graph includes a *ring*, which is a sign of *circular dependencies*. For example, transition $g_1 \rightarrow \{\text{sync } A; \text{sync } B; \}$ and transition $g_2 \rightarrow \{\text{sync } B; \text{sync } A; \}$, where both ports require to be triggered first.
2. The dependency graph includes a non-trivial vertex (neither \perp nor \top) whose degree is not equal to 4. In other words, a port is not properly synchronized or synchronized with more than two transitions (as mentioned in Section. 2.4, any communication happens only between two automata).

Topological sorting, as we all knows, may generate different schedules for the same dependency graph. The following theorem shows that all these schedules are equivalent as transition statements.

Theorem 1 (Equivalence between Schedules). *If two set of assignment statements S_1, S_2 are generated from the same set of external transitions, they*

Algorithm 2 Scheduling in a Synchronous Set of External Transitions

Require: A System S , a set of external canonical transitions t_1, t_2, \dots, t_n

Ensure: A synchronized transition t

```
1: if  $\{t_i\}$  don't belong to different automata or  $\exists t_i$  is internal then
2:    $t \leftarrow null$ 
3:   return
4: end if
5:  $t.g, t.S \leftarrow \bigwedge_i t_i.g, \{\}$ 
6:
7:  $G \leftarrow$  a Graph  $\langle V, E \rangle$  {create a dependency graph}
8: for  $i \leftarrow 1, \dots, n$  do
9:   add  $\perp_i, \top_i$  to  $G.V$ 
10:   $lasts \leftarrow \{\perp_i\}$ 
11:  for  $j \leftarrow 1, 3, \dots, len(t_i.S) - 1$  do
12:     $ports \leftarrow$  all the synchronized ports in  $t_i.S_{j+1}$ 
13:    for  $l \in lasts, p \in ports$  do
14:      if  $p \notin G.V$  then
15:        add  $p$  to  $G.V$ 
16:      end if
17:      add edge  $l \xrightarrow{t_i.S_j} p$  to  $G.E$ 
18:    end for
19:  end for
20:  for  $l \in lasts$  do
21:    add edge  $l \xrightarrow{t_i.S_{len(t_i.S)}} \top_i$  to  $G.E$ 
22:  end for
23: end for
24:
25: if ( $G$  comprises a ring) or  $(\exists v \in G.v \setminus S.Ports$  is a port whose degree  $\neq 4$ ) then
26:    $t \leftarrow null$ 
27: else
28:    $t.S \leftarrow \{ \text{select all the statements in } G.E \text{ using topological sort} \}$ 
29:    $\forall P \in G.v \setminus S.Ports$  replace sync  $P$  in  $t.S$  with
      $P.reqRead, P.reqWrite := false, false$ 
30: end if
```

have exactly the same behavior (i.e. S_1 and S_2 will lead to the same result when executed under the same configuration).

3.4 Automaton as Labelled Transition System

With all the language elements properly formalized, now we introduce the formal semantics of *automata* based on *labelled transition system*.

Definition 5 (Labelled Transition System, LTS). A transition system is a tuple $(S, \Sigma, \rightarrow, s_0)$ where S is a set of states with initial state $s_0 \in S$, Σ is a set of actions, and $\rightarrow \subseteq S \times \Sigma \times S$ is a set of transitions. For simplicity reasons, we use $s \xrightarrow{a} s'$ to denote $(s, a, s') \in \rightarrow$.

Suppose $A = \langle Ports, Vars, Trans_G \rangle$ is an automaton, its semantics can be captured by a labelled transition system $\langle S_A, \Sigma_A, \rightarrow_A, s_0 \rangle$ where

- S_A is the set of all configurations of A .
- s_0 is the initial configuration where all variables (except **reqRead** and **reqWrite**) are initialized with their default value, and **reqRead** and **reqWrite** are initialized as **false**.
- $\Sigma_A = \{i\} \cup P(Ports)$ is the set of all actions.
- $\rightarrow_A \subseteq S_A \times \Sigma_A \times S_A$ is a set of transitions constructed by the following rules.

$$\frac{p \in P_{in}}{(v_{loc}, v_{adj}) \rightarrow_A (v_{loc}, v_{adj}[p.reqWrite \mapsto \neg p.reqWrite])} \text{ R-INPUTSTATUS}$$

$$\frac{p \in P_{in}, val \in Dom(Type(p.value))}{(v_{loc}, v_{adj}) \rightarrow_A (v_{loc}, v_{adj}[p.value \mapsto val])} \text{ R-INPUTVALUE}$$

$$\frac{p \in P_{out}}{(v_{loc}, v_{adj}) \rightarrow_A (v_{loc}, v_{adj}[p.reqRead \mapsto \neg p.reqRead])} \text{ R-OUTPUTSTATUS}$$

$$\frac{\{g \rightarrow \{s\}\} \in Trans_G \text{ is internal}}{(v_{loc}, v_{adj}) \xrightarrow{i}_A s(v_{loc}, v_{adj})} \text{ R-INTERNAL}$$

$$\frac{\begin{array}{l} \{g \rightarrow S\} \in Trans_G \text{ is external, } \{s_1, \dots, s_n\} \text{ are the assignments in } S \\ \{p_1, \dots, p_m\} \text{ are the synchronized ports} \end{array}}{(v_{loc}, v_{adj}) \xrightarrow{\{p_1, \dots, p_m\}}_A s_n \circ \dots \circ s_1(v_{loc}, v_{adj})} \text{ R-EXTERNAL}$$

The first three rules describe the potential change of context, i.e. the adjoint variables. R-InputStatus and R-OutputStatus shows that the reading status of an output port and status of an input port may changed by the context randomly. And R-InputValue shows that the value of an input port may also be updated.

The rule R-Internal models the internal transitions in $Trans_G$. As illustrated previously, an internal transition doesn't contains any synchronizing statement. So its canonical form comprises only one assignment s . Firing such a transition will simply apply s to the current configuration.

Meanwhile, R-External models the external transitions, where the automaton need to interact with its context. Fortunately, since all the context change are captured by the first three rules, we can simply regard the context as another set of local variables. Consequently, the only difference between an internal transition and an external transitions is that the later may contains multiple assignments.

4 Case Study

In modern distributed computing frameworks (e.g. MPI and ZooKeeper), *leader election* plays an important role to organize multiple servers efficiently and consistently. This section shows how a classical leader election algorithm is modeled and easily used to coordinate other components in Mediator.

[6] proposed an classical algorithm for a typical leader election scenario, as shown in Figure. 2. Distributed processes are organized as a *asynchronous unidirectional* ring where communication take place only between adjacent processes and following certain direction (from left to right in this case).

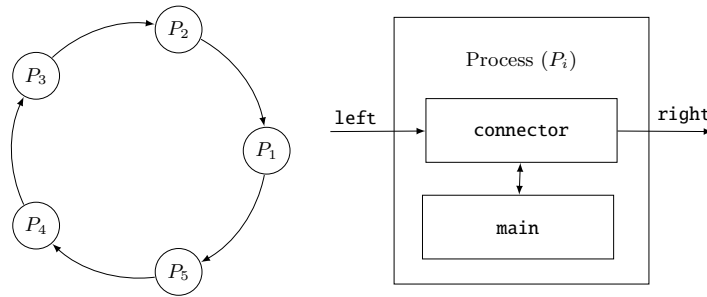


Fig. 2. (a) Topology of an Asynchronous Ring and (b) Structure of a Process

The algorithm mainly includes the following steps:

1. To begin with, each process sends a voting message including its own *id* to its successor.
2. A process, when receives a voting message, will
 - forward the message to its successor if it contains a larger *id* than itself,
 - ignore the message if it contains a smaller *id* than itself, and
 - take itself as a leader if it contains the same *id* with itself.

Here we formalize this algorithm through a more general approach. Leader election is encapsulated as **connector** since it is also responsible to handle the communication between processes. A computing module **main** is attached to the connector, and used to model computing tasks.

Two types of messages, **msgVote** and **msgLocal**, are supported when formalizing this architecture. Voting messages **msgVote** are transferred between the connectors. A voting message carries two fields, *vtype* that declares the stage of leader election (either it is still voting or some process has already been acknowledged) and a *id* an identifier of the current leader (if have). On the other hand, **msgLocal** is used when a worker want to communicate with its corresponding connector.

```
1  typedef struct { vtype: enum {vote, ack}, id: int } as msgVote;
```

```

2  typedef struct {
3    status : enum { pending, acknowledged },
4    idLocal : int,
5    idLeader : int | NULL
6  } as msgLocal;

1  automaton <id:int> election_module (
2    left : in msgVote, right : out msgVote,
3    query : out msgLocal
4  ) { ... }

```

The following code fragment encodes a parallel program containing 3 workers and their corresponding election modules. It is a simplified version of the one in Figure. 2. In this example, *worker*, the main calculating, is passed as a parameter since we expect that this system should be capable handling different working process.

As we are modeling the leader election algorithm on a synchronous ring, only synchronous communications channel *Syncs* are involved in this example. *Sync* is a Reo channel in the first, but also modeled as an *automaton* in our framework. It's implementation details can be found in [10].

Example 7 (A Complete Cluster System with 3 Instances).

```

1  system <worker: interface (query:in msgLocal)> parallel_instance() {
2    components {
3      E1 : election_module<1>; E2 : election_module<2>;
4      E3 : election_module<3>;
5      C1, C2, C2 : worker;
6    }
7    connections {
8      Sync<msgVote>(E1.left, E2.right);
9      Sync<msgVote>(E2.right, E3.left );
10     Sync<msgVote>(E3.right, E1.left );
11
12     Sync<msgLocal>(C1,query, E1.query );
13     Sync<msgLocal>(C2,query, E2.query );
14     Sync<msgLocal>(C3,query, E3.query );
15   }
16 }

```

5 Conclusion and Future Work

A new modeling language Mediator is proposed in this paper to help with component-based software engineering through a formal way. With the basic semantics unit *automata* that capture the formal nature of a component, and *systems* for hierarchical composition, the language is easy-to-use for both formal method researchers and system designers.

This paper is a preface of a set of under-development tools. We plan to build a model checking algorithm for a finite subset of Mediator, and then extend it through symbolic approach. A automatic code-generator is also being built to generate platform-specific codes like *Arduino*.

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References

1. Abdulla, P., Deneux, J., Stålmarch, G., Ågren, H., Åkerlund, O.: Designing safe, reliable systems using SCADE. In: Tiziana, M., Bernhard, S. (eds.) Proceedings of ISoLA 2004. LNCS, vol. 4313, pp. 115–129. Springer (2006)
2. Amnell, T., Behrmann, G., Bengtsson, J., D’argenio, P.R., David, A., Fehnker, A., Hune, T., Jeannet, B., Larsen, K.G., Möller, M.O., Others: UPPAAL - Now, Next, and Future. In: Cassez, F., Jard, C., Brigitte, R., Ryan, M.D. (eds.) Proceedings of MOVEP 2000. LNCS, vol. 2067, pp. 99–124. Springer (2001)
3. Arbab, F.: Reo: a channel-based coordination model for component composition. Mathematical Structures in Computer Science 14(3), 329–366 (2004)
4. Berry, G., Gonthier, G.: The Esterel synchronous programming language: design, semantics, implementation. Science of Computer Programming 19(2), 87–152 (1992)
5. Curry, E.: Message-Oriented Middleware. Middleware For Communications pp. 1–28 (2004)
6. Hagit, A., Jennifer, W.: Distributed computing: fundamentals, simulations, and advanced topics. John Wiley & Sons (2004)
7. Hahn, B., Valentine, D.T.: Essential MATLAB for engineers and scientists. pp. 337–351. Academic Press (2016)
8. Kim, H., Lee, E.A., Broman, D.: A Toolkit for Construction of Authorization Service Infrastructure for the Internet of Things. In: Proceedings of IoTDI 2017. pp. 147–158. ACM (2017)
9. Kwiatkowska, M., Norman, G., Parker, D.: PRISM 4.0: Verification of Probabilistic Real-Time Systems. In: Gopalakrishnan, G., Qadeer, S. (eds.) Proceedings of CAV 2011. LNCS, vol. 6806, pp. 1–6. Springer (2011)
10. Li, Y.: A list of Mediator models that are referenced in this paper, <https://fmgroupliyi.today/git/MediatE/PaperProposal/tree/master/models>
11. Liu, Z., Morisset, C., Stolz, V.: rCOS: Theory and tool for component-based model driven development. In: Arbab, F., Sirjani, M. (eds.) Proceedings of FSEN 2009. LNCS, vol. 5961, pp. 62–80. Springer (2010)
12. National Instruments, title = LabVIEW, u.h.:
13. Zou, L., Zhany, N., Wang, S., Fränzle, M., Qin, S.: Verifying Simulink diagrams via a Hybrid Hoare Logic Prover. In: Proceedings of EMSOFT 2013 (2013)

Appendix

Theorem 1 (Equivalence between Schedules). *If two set of assignment statements S_1, S_2 are generated from the same set of external transitions, they have exactly the same behavior (i.e. execution of S_1 and S_2 under the same configuration will lead to the same result).*

Proof. Apparently, when executing statements, all the changes on configurations come from *assignments*. Once we successfully prove that for each assignment, its pre-configuration and post-configuration in S_1 and S_2 are exactly the same, we are able to finish this proof.

In the following proof, we denote S_1 and S_2 by $S_1 = \{s_1, \dots, s_n\}, S_2 = \{s'_1, \dots, s'_n\}$, and the automaton that a transition belongs to by $Automaton(s)$. We try to use an inductive approach to prove the hypothesis that *for each assignment $s \in S_1$ and its corresponding assignment $s' \in S_2$, the shared variables it changes have the same evaluation in their post-configurations*.

1. Let's come to the *FIRST* assignment state s in S_1 where shared variables is assigned. We assume that its corresponding statement in S_2 is s' . Comparing s and s' , we have:
 - (a) s' is also the first assignment in S_2 which modifies *this set* of assigned variables. (A shared variable can be assigned in one of its owner, thus all assignments that modifies this variable belong to the same transition, and their order is strictly maintained.)
 - (b) s and s' include no reference to other shared variables. (A shared variable can be referenced only when it has been assigned before, however s is the first assignment which modifies a shared variable.)
 - (c) In the pre-configuration of s and s' , all the local variables of $Automaton(s)$ have the same evaluation. (Derived from the same reason in (a)).
 Consequently, in the post-configuration of s and s' , all the shared variables have the *SAME* evaluation.
2. Assume that all assignments (to shared variables) in s_1, \dots, s_i have been proved to satisfy the hypothesis, now we are going to prove that s , the first transition where shared variables are referenced in s_{i+1}, \dots, s_n and its corresponding s' also satisfy the hypothesis.
 - (a) In the pre-configuration of s and s' , all the shared variables that are referenced in s and s' have the *SAME* evaluation. (Thanks to the assumption, all assignments to shared variables in s_1, \dots, s_i share the same evaluation (on referenced variables only) with their corresponding assignments in s' . And on the other hand, for any assignments to the referenced shared variables in S_2 , its index in S_1 must be less than s , and in turn satisfy the hypothesis due to the assumption.)
 - (b) In the pre-configuration of s and s' , all the local variables of $Automaton(s)$ have the *SAME* evaluation. (Derived by the same reason as in 1.(c))
 It's apparent that in the post-configuration of s and s' , all the *assigned* shared variables have the *SAME* evaluation.