

Image Filter

1 Introduction

Filtering is a common task in image processing, typically used to apply an effect like blurring or extract useful image characteristics like edges. A *filtering mask* is a 2D matrix of "weights" that can be combined with an input image using some operator. A commonly used operator in image processing is *convolution*. The result of convolution is a new image, where each pixel is a weighted sum of the corresponding pixel in the original image and its neighboring pixels, the weights being specified by the filtering mask. To perform convolution, the center of the filtering mask is placed over each pixel in the input image, and a sum of the products of corresponding pixels is computed to obtain the value of the new pixel in the output image. Note that only filtering masks with odd dimensions have a well-defined "center"; thus, only these are considered valid inputs to a convolution operation.

Define an input image $I(m \times n)$ and a filtering mask $C(r \times r)$ and an output image $I'(m \times n)$. Imagine that for mask C:

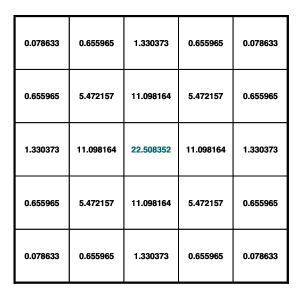
- the center of the mask **C** is at **C**(0,0),
- the upper left corner is at C(-⌊r/2⌋, -⌊r/2⌋), and
- the bottom right corner is at $C(+\lfloor r/2 \rfloor, +\lfloor r/2 \rfloor)$.

Then, the output pixel $\mathbf{l}'(i,j)$ can be computed using the convolution operator using the following expression:

$$\mathbf{I'}(i,j) = \sum_{p = -\lfloor r/2 \rfloor}^{-\lfloor r/2 \rfloor} \mathbf{C}(p,q) * \mathbf{I}(i+p,j+q)$$

Figure 1 shows an example of a 5x5 Gaussian filter and a sample image blurred using this filter.

Image Filter 1 of 3



1/100





Figure 1 A 5x5 Gaussian Blur Filter; Left is Input Image, Right is Output Image With Gaussian Blur Filter Applied

2 Image Filtering on Brook+

In image filtering, each pixel in the output image is a function of the corresponding pixel in the input image and its surrounding pixels. The computation is localized to a neighborhood. Moreover, the same set of arithmetic operations is applied to each pixel in the input image. The data-independent and repetitive nature of the task makes image processing favorable for implementation on SIMD stream processors. We can achieve a massive computational acceleration by unlocking the high rate of inherent data-parallelism.

We illustrate a simple image processing task in this Brook+ sample. A 3x3 filtering mask is applied to an input image. A simple Brook+ kernel performs the convolution. The inputs to the Brook kernel are: the input image bound as an array, and the 3x3 filtering mask. For each pixel in the output image, the kernel gathers the surrounding pixels in the input image, applies the mask, and accumulates the result. Static offsets are used (instead of a loop) to traverse the 3x3 region around each pixel.

Contact

Advanced Micro Devices, Inc. One AMD Place P.O. Box 3453 Sunnyvale, CA, 94088-3453

Phone: +1.408.749.4000

For Stream Computing:

URL: http://ati.amd.com/technology/streamcomputing

Questions: streamcomputing@amd.com Developing: streamdeveloper@amd.com

Forum: http://forums.amd.com/devforum/categories.cfm?catid=38





Printed in USA

The contents of this document are provided in connection with Advanced Micro Devices, Inc. ("AMD") products. AMD makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make changes to specifications and product descriptions at any time without notice. The information contained herein may be of a preliminary or advance nature and is subject to change without notice. No license, whether express, implied, arising by estoppel or otherwise, to any intellectual property rights is granted by this publication. Except as set forth in AMD's Standard Terms and Conditions of Sale, AMD assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

AMD's products are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or in any other application in which the failure of AMD's product could create a situation where personal injury, death, or severe property or environmental damage may occur. AMD reserves the right to discontinue or make changes to its products at any time without notice

Copyright and Trademarks

© 2008 Advanced Micro Devices, Inc. All rights reserved. AMD, the AMD Arrow logo, ATI, the ATI logo, Radeon, FireStream, and combinations thereof are trademarks of Advanced Micro Devices, Inc. Other names are for informational purposes only and may be trademarks of their respective owners.