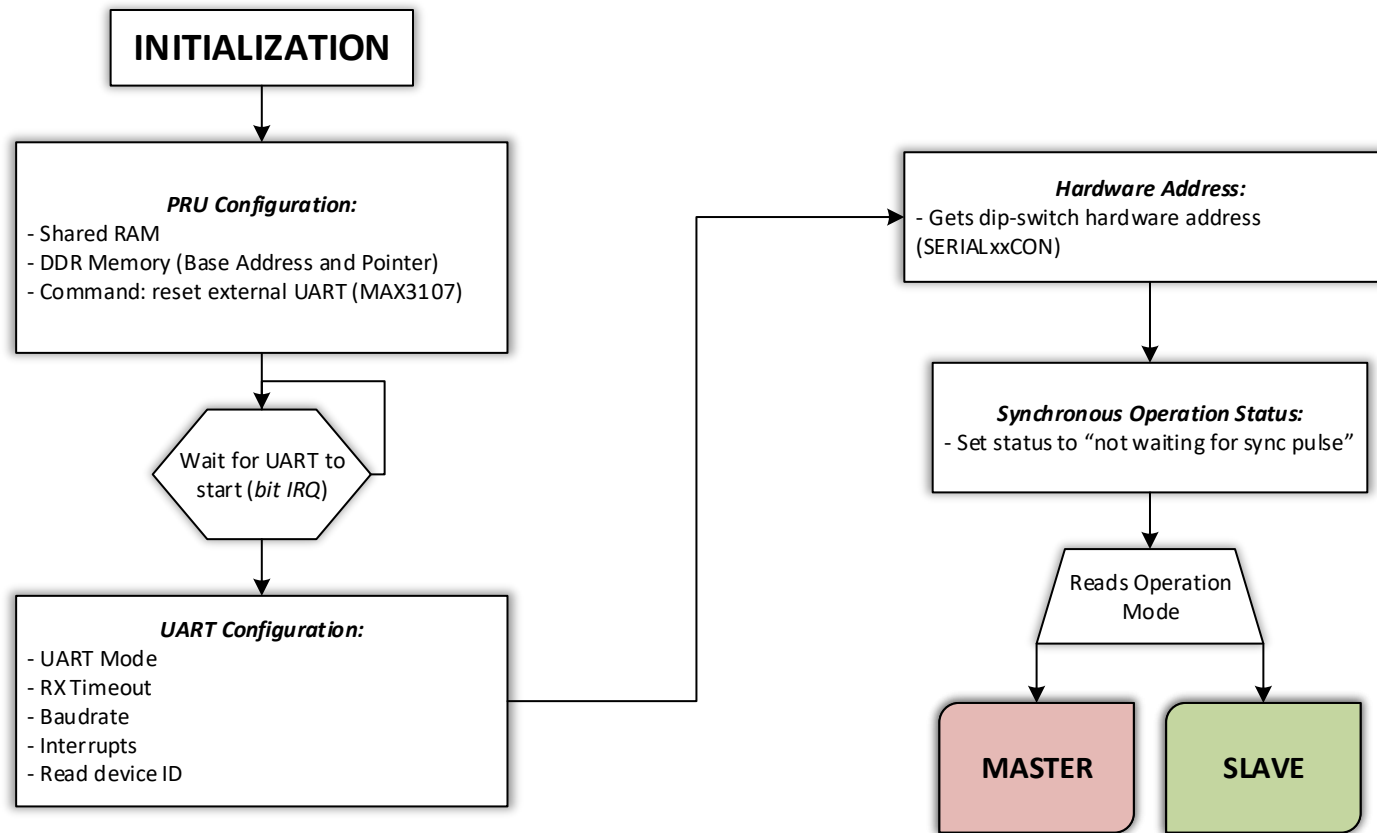


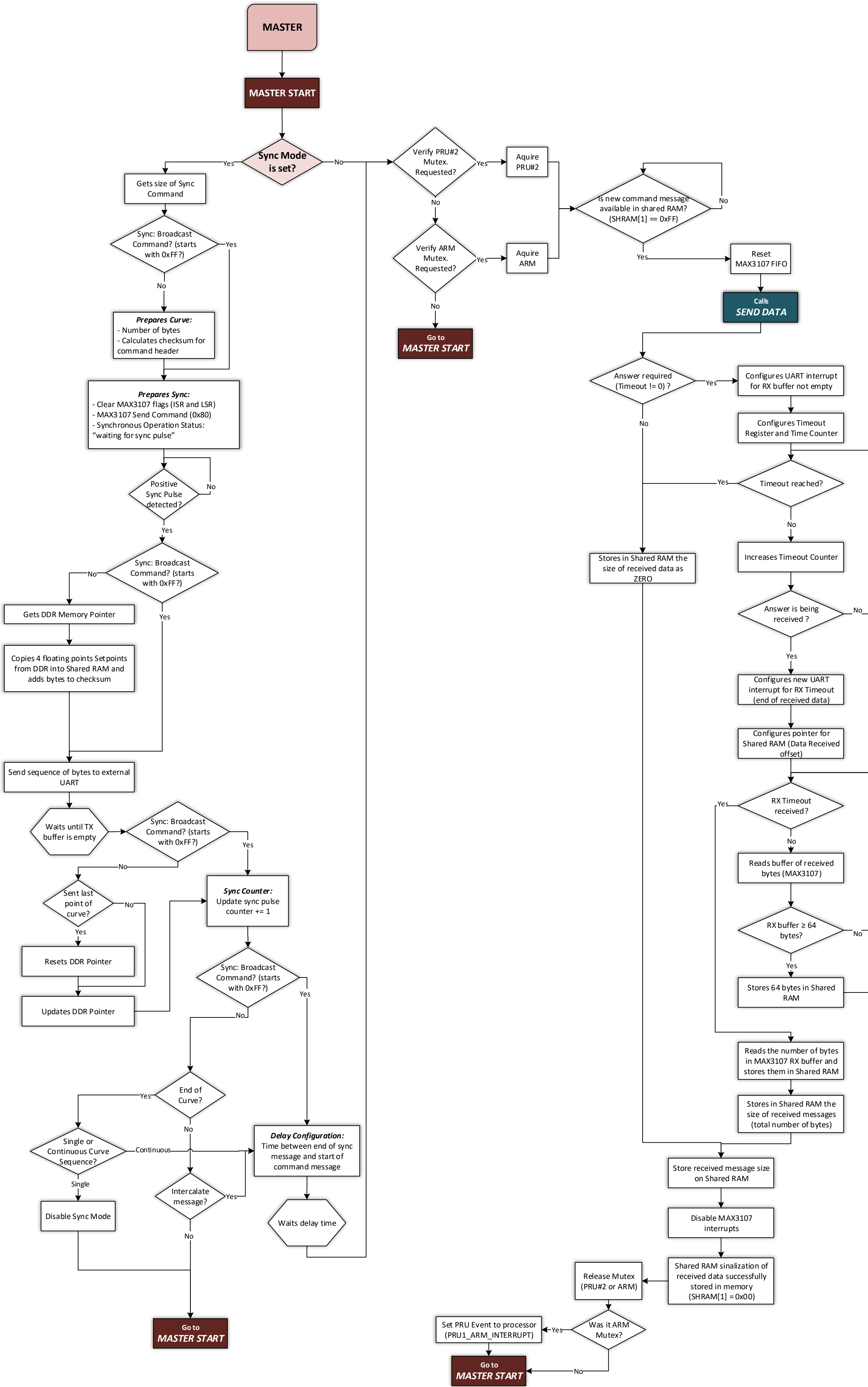
# PRUserial485

v1.7.0



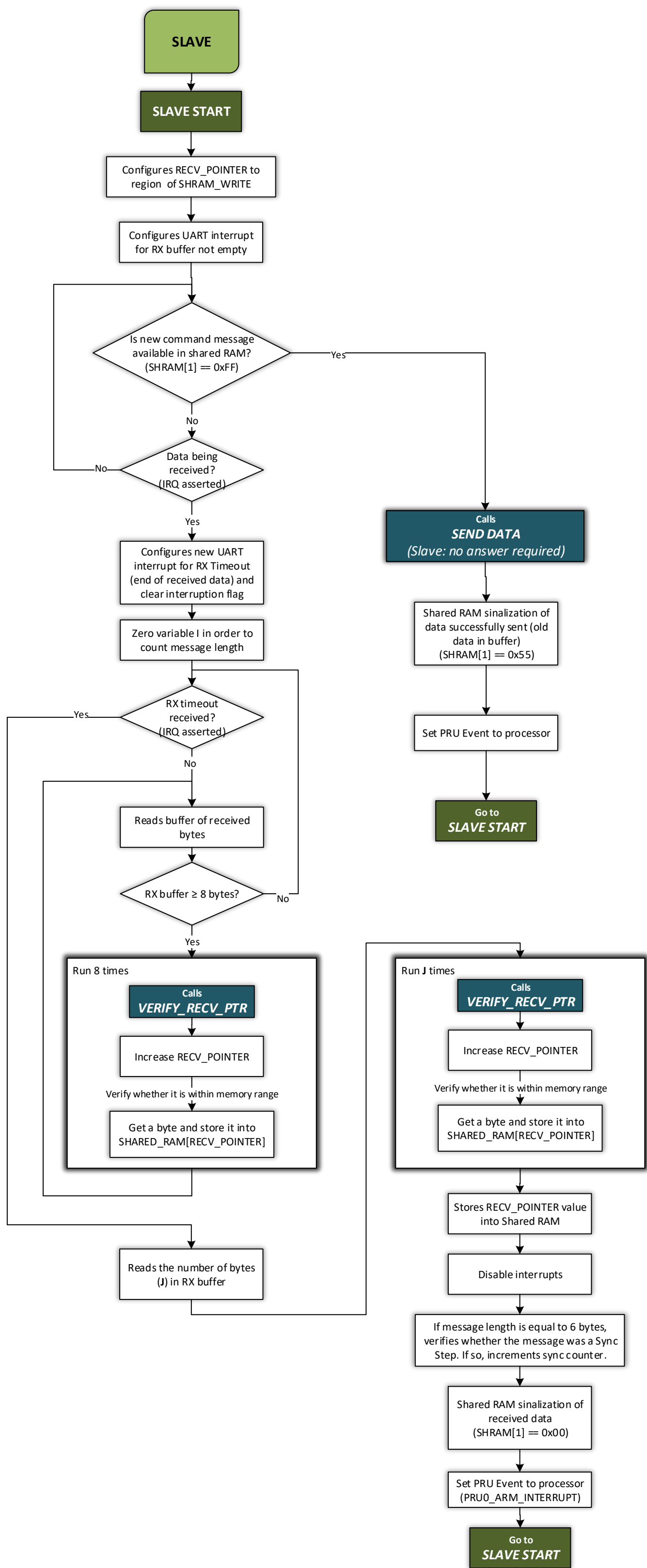
PRUserial485

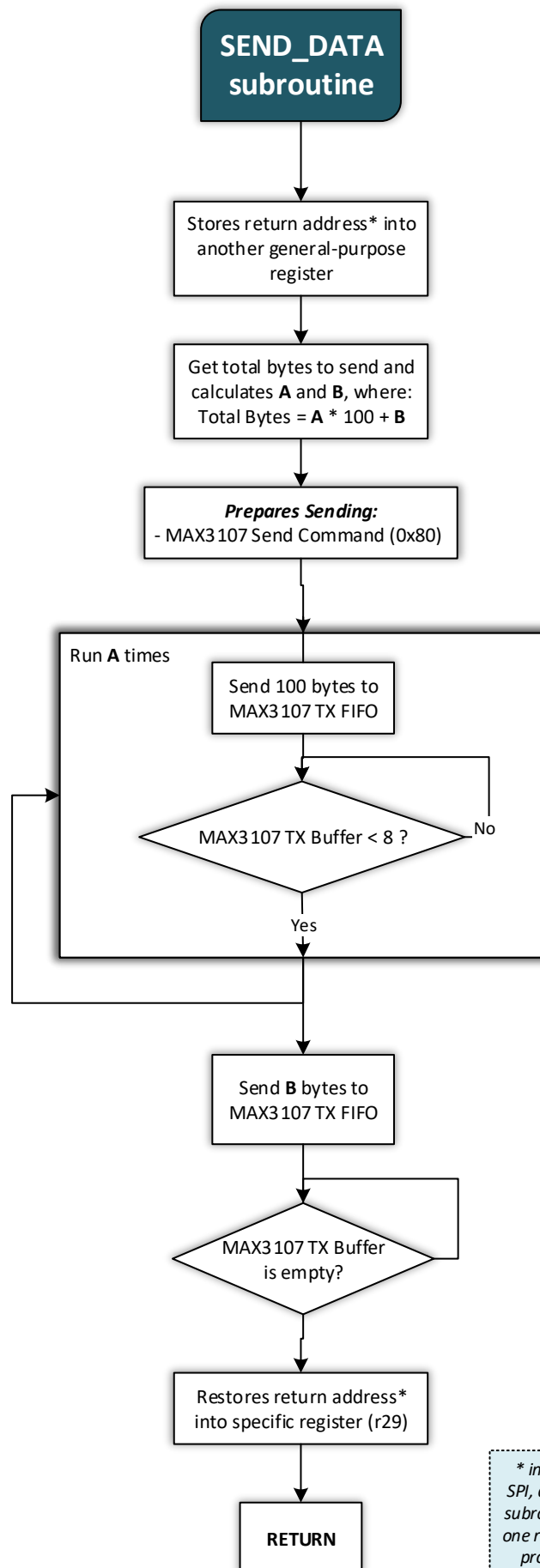
Master Mode



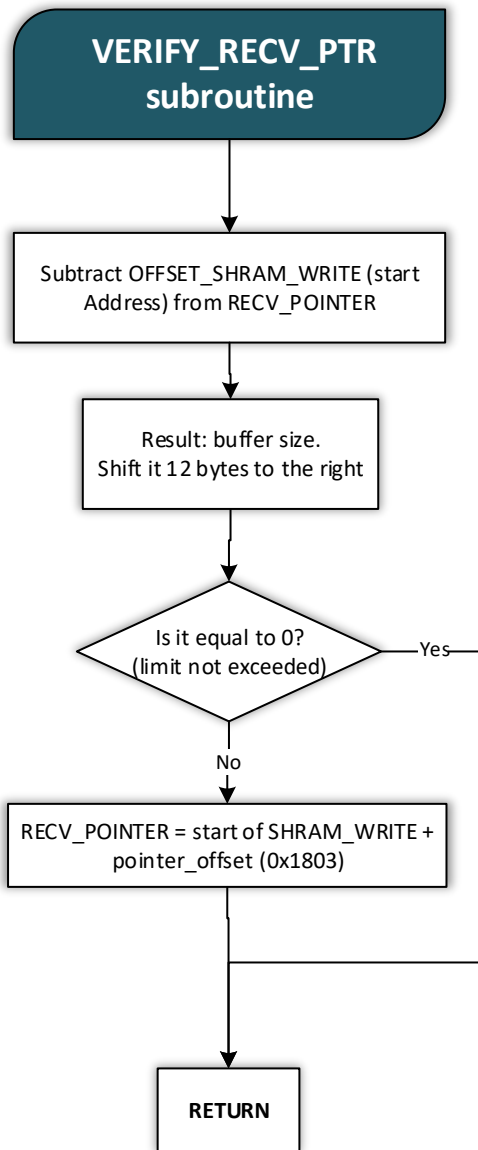
# PRUserial485

## Slave Mode





\* in order to send data through SPI, a subroutine is called inside a subroutine. However, there is only one register to automatically store program address to return to.



# PRUserial485

## Receive FIFO – ARM level

