Implementing a Capability Machine model into Iris

Aïna Linn Georges

Alix Trieu

Lars Birkedal

Aarhus University

ageorges@cs.au.dk

January 1, 2020

- Capability machines allow for fine grained control over pointer permissions
- Good target for secure compilation
- ▶ In particular: we are interested in enforcing certain higher level abstractions such as *local state encapsulation* as *well-bracketed control flow* at the lowest level of the machin
- We need tools to reason about these subtle properties in a language that does not enforce them
- ► These tools are elaborate and complex: we want to mechanize them, and facilitate the process of using them

- Capability machines allow for fine grained control over pointer permissions
- Good target for secure compilation
- In particular: we are interested in enforcing certain higher level abstractions such as local state encapsulation as well-bracketed control flow at the lowest level of the machin
- We need tools to reason about these subtle properties in a language that does not enforce them
- ► These tools are elaborate and complex: we want to mechanize them, and facilitate the process of using them

- Capability machines allow for fine grained control over pointer permissions
- ► Good target for secure compilation
- ▶ In particular: we are interested in enforcing certain higher level abstractions such as local state encapsulation as well-bracketed control flow at the lowest level of the machine
- We need tools to reason about these subtle properties in a language that does not enforce them
- ► These tools are elaborate and complex: we want to mechanize them, and facilitate the process of using them

- Capability machines allow for fine grained control over pointer permissions
- Good target for secure compilation
- In particular: we are interested in enforcing certain higher level abstractions such as local state encapsulation as well-bracketed control flow at the lowest level of the machine
- We need tools to reason about these subtle properties in a language that does not enforce them
- ► These tools are elaborate and complex: we want to mechanize them, and facilitate the process of using them

- Capability machines allow for fine grained control over pointer permissions
- ► Good target for secure compilation
- In particular: we are interested in enforcing certain higher level abstractions such as local state encapsulation as well-bracketed control flow at the lowest level of the machine
- We need tools to reason about these subtle properties in a language that does not enforce them
- ► These tools are elaborate and complex: we want to mechanize them, and facilitate the process of using them

Overview

Capability Machines

Reasoning about Capability Safety

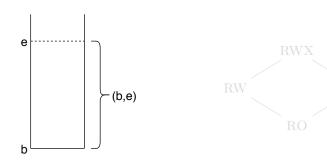
Program Logic

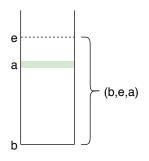
A Unary Logical Relation for Reasoning about Semantic Properties of an Untyped Language

The Fundamental Theorem of Logical Relations

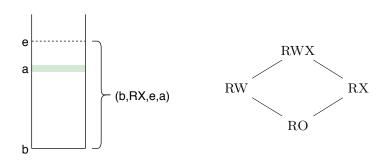
Reasoning about Unknown Code

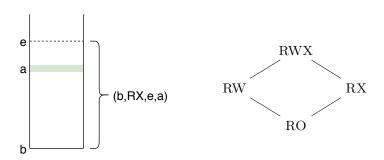






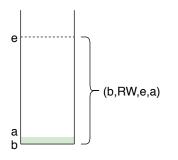






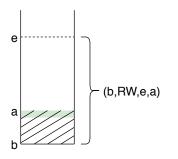
Enforcing Local Stack Encapsulation using Capabilities

Local State Encapsulation



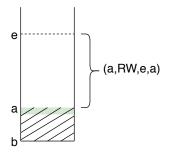
```
push r_stk 1
scall r
pop r_stk r_1
assert r_1 1
halt
```

Local State Encapsulation



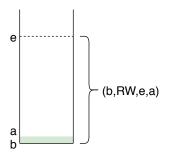
```
push r_stk 1
scall r
pop r_stk r_1
assert r_1 1
halt
```

Local State Encapsulation

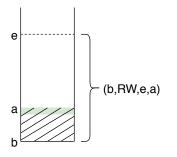


```
push r_stk 1
scall r
pop r_stk r_1
assert r_1 1
halt
```

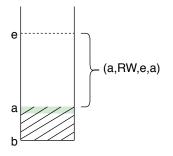
Enforcing Well Bracketed Control Flow using Capabilities



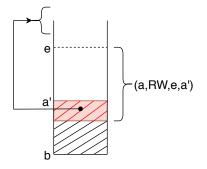
```
push r_stk 1
scall r
pop r_stk r_1
assert r_1 1
push r_stk 2
scall r
halt
```



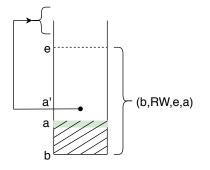
```
push r_stk 1
scall r
pop r_stk r_1
assert r_1 1
push r_stk 2
scall r
halt
```



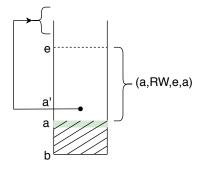
```
push r_stk 1
scall r
pop r_stk r_1
assert r_1 1
push r_stk 2
scall r
halt
```



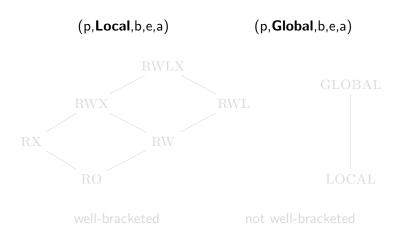
```
push r_stk 1
scall r
pop r_stk r_1
assert r_1 1
push r_stk 2
scall r
halt
```

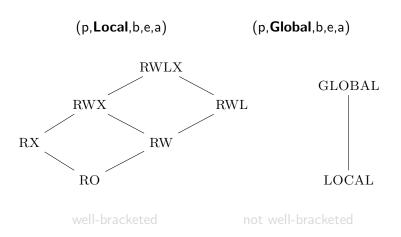


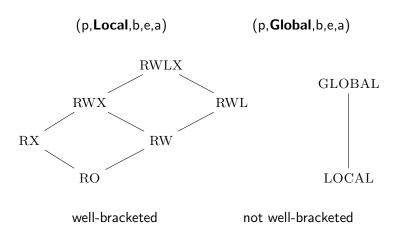
```
push r_stk 1
scall r
pop r_stk r_1
assert r_1 1
push r_stk 2
scall r
halt
```



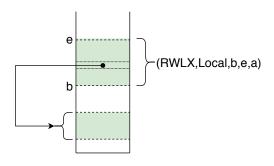
```
push r_stk 1
scall r
pop r_stk r_1
assert r_1 1
push r_stk 2
scall r
halt
```





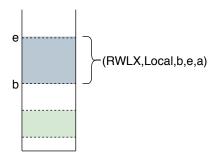


Calling Convention



 $r_{stk} \mid (RWLX, Local, b, e, a)$

Calling Convention



 $r_{stk} \mid (RWLX, Local, b, e, a)$

Reasoning about Capability Safety

- using a Program Logic
- using a logical relation to capture invariants on the type system
- using a logical relation on an untyped (or uni-typed)
 language to capture semantic properties of the language

- using a Program Logic
- using a logical relation to capture invariants on the type system
- using a logical relation on an untyped (or uni-typed)language to capture semantic properties of the language

- ▶ using a Program Logic
- using a logical relation to capture invariants on the type system
- using a logical relation on an untyped (or uni-typed)
 language to capture semantic properties of the language

- using a Program Logic
- using a logical relation to capture invariants on the type system
- using a logical relation on an untyped (or uni-typed)
 language to capture semantic properties of the language

Step-indexed Kripke Logical Relation

$$\mathcal{V}(W) \triangleq \{n, (RW, g, b, e, a) | \cdots\} \cup \cdots$$

- World-circularity problem
 - Step indexing
- ► The world may evolve: we need future world relation
 - Local capabilities are revoked whereas Global capabilities are not. the relation needs to model this distinction:



and



$$V(W) \triangleq \{n, (RW, g, b, e, a) | \cdots \} \cup \cdots$$

- World-circularity problem
 - Step indexing
- ► The world may evolve: we need future world relation
 - Local capabilities are revoked whereas Global capabilities are not, the relation needs to model this distinction:



$$\mathcal{V}(W) \triangleq \{ (RW, g, b, e, a) | \exists r, W(r) = \iota_{[b,e]} \} \cup \cdots$$

- World-circularity problem
 - Step indexing
- ► The world may evolve: we need future world relation
 - Local capabilities are revoked whereas Global capabilities are not. the relation needs to model this distinction:



and

□ priv

$$\mathcal{V}(W) \triangleq \{n, (RW, g, b, e, a) | \exists r, W(r) = \iota_{[b,e]}\} \cup \cdots$$

- World-circularity problem
 - Step indexing
- ► The world may evolve: we need future world relation
 - Local capabilities are revoked whereas Global capabilities are not, the relation needs to model this distinction:



$$\mathcal{V}(W) \triangleq \{ n, (RW, g, b, e, a) | \exists r, W(r) \stackrel{\mathsf{n}}{=} \iota_{[b,e]} \} \cup \cdots$$

- World-circularity problem
 - Step indexing
- ► The world may evolve: we need future world relation
 - Local capabilities are revoked whereas Global capabilities are not, the relation needs to model this distinction:



$$\mathcal{V}(W) \triangleq \{ n, (RW, g, b, e, a) | \exists r, W(r) \stackrel{\mathsf{n}}{=} \iota_{[b,e]} \} \cup \cdots$$

- World-circularity problem
 - Step indexing
- The world may evolve: we need future world relation
 - Local capabilities are revoked whereas Global capabilities are not, the relation needs to model this distinction:

_pub

and

= priv

$$\mathcal{V}(W) \triangleq \{ n, (RW, g, b, e, a) | \exists r, W(r) \stackrel{\mathsf{n}}{=} \iota_{[b,e]} \} \cup \cdots$$

- World-circularity problem
 - Step indexing
- ► The world may evolve: we need future world relation
 - Local capabilities are revoked whereas Global capabilities are not, the relation needs to model this distinction:

$$\supseteq_{pub}$$
 and \supseteq_{priv}

Iris: Higher-order Concurrent Separation Logic Framework

- Foundational
- Implemented in Coq equipped with an interactive proof mode
- ► Framework embed any language and its operational semantics into Iris
- Comes equipped with:
 - Invariants
 - ► Ghost state
 - Always and Later Modalities

Iris: Higher-order Concurrent Separation Logic Framework

- Foundational
- Implemented in Coq equipped with an interactive proof mode
- ► Framework embed any language and its operational semantics into Iris
- Comes equipped with:
 - Invariants
 - ▶ Ghost state
 - Always and Later Modalities

Iris: Higher-order Concurrent Separation Logic Framework

- Foundational
- Implemented in Coq equipped with an interactive proof mode
- ► Framework embed any language and its operational semantics into Iris
- Comes equipped with:
 - Invariants
 - Ghost state
 - Always and Later Modalities

Iris: Higher-order Concurrent Separation Logic Framework

- Foundational
- Implemented in Coq equipped with an interactive proof mode
- ► Framework embed any language and its operational semantics into Iris
- Comes equipped with:
 - Invariants
 - Ghost state
 - Always and Later Modalities

Iris: Higher-order Concurrent Separation Logic Framework

- Foundational
- Implemented in Coq equipped with an interactive proof mode
- ► Framework embed any language and its operational semantics into Iris
- Comes equipped with:
 - Invariants
 - Ghost state
 - Always and Later Modalities

Iris: Higher-order Concurrent Separation Logic Framework

- Foundational
- Implemented in Coq equipped with an interactive proof mode
- ► Framework embed any language and its operational semantics into Iris
- Comes equipped with:
 - Invariants
 - Ghost state
 - Always and Later Modalities

Iris: Higher-order Concurrent Separation Logic Framework

- Foundational
- Implemented in Coq equipped with an interactive proof mode
- Framework embed any language and its operational semantics into Iris
- Comes equipped with:
 - Invariants
 - Ghost state
 - Always and Later Modalities

Iris: Higher-order Concurrent Separation Logic Framework

- Foundational
- Implemented in Coq equipped with an interactive proof mode
- Framework embed any language and its operational semantics into Iris
- Comes equipped with:
 - Invariants
 - Ghost state
 - Always and Later Modalities

- Region invariants: Iris invariants
- ► Future world relation: frame preserving updates and world satisfaction
- Step indexing: later modality

- Iris was designed with more high level languages in mind, how do we embed a low level machine language into Iris
- Iris abstracts away certain details we want to reason about directly
- ► There is only one frame preserving update, we need to distinguish between two future world relations

- Region invariants: Iris invariants
- ► Future world relation: frame preserving updates and world satisfaction
- Step indexing: later modality

- ► Iris was designed with more high level languages in mind, how do we embed a low level machine language into Iris
- Iris abstracts away certain details we want to reason about directly
- ► There is only one frame preserving update, we need to distinguish between two future world relations

- Region invariants: Iris invariants
- Future world relation: frame preserving updates and world satisfaction
- Step indexing: later modality

- ► Iris was designed with more high level languages in mind, how do we embed a low level machine language into Iris
- Iris abstracts away certain details we want to reason about directly
- ► There is only one frame preserving update, we need to distinguish between two future world relations

- Region invariants: Iris invariants
- Future world relation: frame preserving updates and world satisfaction
- Step indexing: later modality

- ► Iris was designed with more high level languages in mind, how do we embed a low level machine language into Iris
- Iris abstracts away certain details we want to reason about directly
- ► There is only one frame preserving update, we need to distinguish between two future world relations

- embed the language into Iris
- define a program logic by proving Hoare Triples
- define the logical relation using Iris tools to solve the world circularity problem
- prove the fundamental theorem of logical relations
- use the logical relation to prove examples that rely on local state encapsulation and well-bracketed control flow with calls to unknown adversary

- embed the language into Iris
- define a program logic by proving Hoare Triples
- define the logical relation using Iris tools to solve the world circularity problem
- prove the fundamental theorem of logical relations
- use the logical relation to prove examples that rely on local state encapsulation and well-bracketed control flow with calls to unknown adversary

- embed the language into Iris
- define a program logic by proving Hoare Triples
- define the logical relation using Iris tools to solve the world circularity problem
- prove the fundamental theorem of logical relations
- use the logical relation to prove examples that rely on local state encapsulation and well-bracketed control flow with calls to unknown adversary

- embed the language into Iris
- define a program logic by proving Hoare Triples
- define the logical relation using Iris tools to solve the world circularity problem
- prove the fundamental theorem of logical relations
- use the logical relation to prove examples that rely on local state encapsulation and well-bracketed control flow with calls to unknown adversary

- embed the language into Iris
- define a program logic by proving Hoare Triples
- define the logical relation using Iris tools to solve the world circularity problem
- prove the fundamental theorem of logical relations
- use the logical relation to prove examples that rely on local state encapsulation and well-bracketed control flow with calls to unknown adversary

- embed the language into Iris
- define a program logic by proving Hoare Triples
- define the logical relation using Iris tools to solve the world circularity problem
- prove the fundamental theorem of logical relations
- use the logical relation to prove examples that rely on local state encapsulation and well-bracketed control flow with calls to unknown adversary

Program Logic

$$(\textit{reg}, \textit{mem}) \rightarrow (\textit{reg}', \textit{mem}')$$

- Instr Executable
- ► Instr Halted → HaltedV
- ▶ Instr Failed → FailedV

$$(\textit{reg}, \textit{mem}) \rightarrow (\textit{reg}', \textit{mem}')$$

- ► Instr Executable
- ▶ Instr Halted → HaltedV
- ▶ Instr Failed → FailedV

$$(\textit{reg}, \textit{mem}) \rightarrow (\textit{reg}', \textit{mem}')$$

- ► Instr Executable
- ▶ Instr Halted → HaltedV
- ► Instr Failed → FailedV

$$(\textit{reg}, \textit{mem}) \rightarrow (\textit{reg}', \textit{mem}')$$

- ► Instr Executable
- ▶ Instr Halted → HaltedV
- ► Instr Failed → FailedV

$$(\textit{reg}, \textit{mem}) \rightarrow (\textit{reg}', \textit{mem}')$$

- ► Instr Executable
- ▶ Instr Halted → HaltedV
- ▶ Instr Failed \rightarrow FailedV

A Capability Points-to Predicate

$$a\mapsto_a [RWL]w$$

$$a \mapsto_a [RWL]w \Longrightarrow a \mapsto_a [RWL]((p, Local), b, e, l)$$

$$a \mapsto_a [RWL]w \Longrightarrow a \mapsto_a [RWL]((p, Local), b, e, l)$$

$$\Longrightarrow a \mapsto_a [RW]((p, Local), b, e, l)$$

$$a \mapsto_{a} [RWL]w \Longrightarrow a \mapsto_{a} [RWL]((p, Local), b, e, l)$$

$$\Longrightarrow a \mapsto_{a} [RW]((p, Local), b, e, l)$$

$$\Longrightarrow a \mapsto_{a} [RW]((p', Local), b', e', l')$$

A Unary Logical Relation for Reasoning about Semantic Properties of an Untyped Language

A unary logical relation of an un-typed language

$$\mathcal{V}: \textit{Word} \rightarrow \textit{iProp} \ \Sigma$$

Challenge: distinguish between Local and Global capabilities:

- At the level of the value relation
- ► Model revocation

$$\mathcal{V}((\mathsf{RW},g),b,e,a) \triangleq \underset{a \in [b,e]}{\bigstar} \boxed{\exists w,a \mapsto_a [RW]w \twoheadrightarrow \mathcal{V}(w)}$$

A unary logical relation of an un-typed language

$$\mathcal{V}: \textit{Word} \rightarrow \textit{iProp} \ \Sigma$$

Challenge: distinguish between Local and Global capabilities:

- At the level of the value relation
- Model revocation

$$\mathcal{V}((\mathsf{RW},g),b,e,a) \triangleq \underset{a \in [b,e]}{\bigstar} \boxed{\exists w, a \mapsto_a [RW]w \twoheadrightarrow \mathcal{V}(w)}$$

A unary logical relation of an un-typed language

$$\mathcal{V}: \textit{Word} \rightarrow \textit{iProp} \ \Sigma$$

Challenge: distinguish between Local and Global capabilities:

- ► At the level of the value relation
- ► Model revocation

$$\mathcal{V}((\mathsf{RW},g),b,e,a) \triangleq \underset{a \in [b,e]}{\bigstar} \boxed{\exists w, a \mapsto_a [RW]w \twoheadrightarrow \mathcal{V}(w)}$$

A unary logical relation of an un-typed language

$$\mathcal{V}: \textit{Word} \rightarrow \textit{iProp} \ \Sigma$$

Challenge: distinguish between Local and Global capabilities:

- ▶ At the level of the value relation
- ► Model revocation

$$\mathcal{V}((\mathsf{RW},g),b,e,a) \triangleq \underset{a \in [b,e]}{\bigstar} \boxed{\exists w, a \mapsto_a [RW]w \twoheadrightarrow \mathcal{V}(w)}$$

A unary logical relation of an un-typed language

$$\mathcal{V}: \textit{Word} \rightarrow \textit{iProp} \ \Sigma$$

Challenge: distinguish between Local and Global capabilities:

- ► At the level of the value relation
- Model revocation

$$\mathcal{V}((\mathsf{RW},g),b,e,a) \triangleq \underset{a \in [b,e]}{\bigstar} \boxed{\exists w, a \mapsto_a [RW]w \twoheadrightarrow \mathcal{V}(W)(w)}$$

A unary logical relation of an un-typed language

$$\mathcal{V}: STS \rightarrow Word \rightarrow iProp \Sigma$$

Challenge: distinguish between Local and Global capabilities:

- At the level of the value relation
- Model revocation

$$\mathcal{V}(W)((RW,g),b,e,a) \triangleq \underset{a \in [b,e]}{\bigstar} \boxed{\exists w, a \mapsto_a [RW]w \twoheadrightarrow \mathcal{V}(W)(w)}$$

The Execute Condition

The Execute Condition

$$\mathsf{exec_cond}(\mathsf{W})(\mathsf{p},\mathsf{g},\mathsf{b},\mathsf{e}) \triangleq \begin{cases} \forall \mathsf{a} \in [b\ e], W' \sqsubseteq_{\mathsf{pub}} W. \\ \rhd \ \mathcal{E}(W')(((\mathsf{p},\mathsf{g}),\mathsf{b},\mathsf{e},\mathsf{a})) \quad \mathsf{g} = \mathsf{Local} \end{cases}$$

$$\forall \mathsf{a} \in [b\ e], W' \sqsubseteq_{\mathsf{priv}} W. \\ \rhd \ \mathcal{E}(W')(((\mathsf{p},\mathsf{g}),\mathsf{b},\mathsf{e},\mathsf{a})) \quad \mathsf{g} = \mathsf{Global} \end{cases}$$

```
\mathcal{E}(W)(pc) \triangleq \forall r, \mathcal{R}(W)(r) * \operatorname{context}(W)(r[\operatorname{PC} := pc])
-* \operatorname{WP} \operatorname{Seq} (\operatorname{Instr} \operatorname{Executable})
\{v, v = \operatorname{\textit{HaltedV}} \implies \exists W'r', W' \sqsubseteq_{\textit{priv}} W
* \operatorname{context}(W')(r')\}
```

```
\mathcal{E}(W)(pc) \triangleq \forall r, \mathcal{R}(W)(r) * \operatorname{context}(W)(r[PC := pc])
-* WP Seq (Instr Executable)
\{v, v = HaltedV \implies \exists W'r', W' \sqsubseteq_{priv} W
* \operatorname{context}(W')(r')\}
```

$$context(W)(r) = ?$$

$$\mathcal{E}(W)(pc) \triangleq \forall r, \mathcal{R}(W)(r) * \operatorname{context}(W)(r[PC := pc])$$

$$-* WP Seq (Instr Executable)$$

$$\{v, v = HaltedV \implies \exists W'r', W' \sqsubseteq_{priv} W$$

$$* \operatorname{context}(W')(r')\}$$

$$context(W)(r) = (\underset{r_i \mapsto w \in r}{\bigstar} r_i \mapsto_r w) \land full_map r$$

$$\mathcal{E}(W)(pc) \triangleq \forall r, \mathcal{R}(W)(r) * \operatorname{context}(W)(r[PC := pc])$$

$$-* WP Seq (Instr Executable)$$

$$\{v, v = HaltedV \implies \exists W'r', W' \sqsubseteq_{priv} W$$

$$* \operatorname{context}(W')(r')\}$$

$$\frac{\mathsf{context}(W)(r)}{r_i \mapsto w \in r} = (\underset{r_i \mapsto w \in r}{\bigstar} r_i \mapsto_r w) \land \mathsf{full_map} \ r$$

$$* \mathsf{na_inv} \ \gamma_{na} \top$$

```
\mathcal{E}(W)(pc) \triangleq \forall r, \mathcal{R}(W)(r) * \operatorname{context}(W)(r[PC := pc])
-* WP \operatorname{Seq} (\operatorname{Instr} \operatorname{Executable})
\{v, v = \operatorname{Halted}V \implies \exists W'r', W' \sqsubseteq_{\operatorname{priv}} W
* \operatorname{context}(W')(r')\}
```

$$\mathcal{E}(W)(pc) \triangleq \forall r, \mathcal{R}(W)(r) * \operatorname{context}(W)(r[PC := pc])$$

$$-* WP Seq (Instr Executable)$$

$$\{v, v = HaltedV \implies \exists W'r', W' \sqsubseteq_{priv} W$$

$$* \operatorname{context}(W')(r')\}$$

$$\mathsf{context}(W)(r) = (\underset{r_i \mapsto w \in r}{\bigstar} r_i \mapsto_r w) \land \mathsf{full_map} \ r$$

$$* \mathsf{na_inv} \ \gamma_{na} \top$$

$$* \mathsf{sts_full} \ W$$

$$* \mathsf{region} \ W$$

The Fundamental Theorem of Logical Relations

The Fundamental Theorem of logical relations

If we can read a region, and every word in that region is safe, then we can safely execute it

- ▶ "If we can read a region" : $p = RX \lor p = RWX \lor p = RWLX$
- "and every word in that region is safe":
 read_write_cond (p, b, e)
- ▶ "then we can safely execute it": $\mathcal{E}(W)(((p,g),b,e,a))$

$$(p = RX \lor p = RWX \lor p = RWLX) \Longrightarrow$$

read_write_cond $(p, b, e) \Longrightarrow \mathcal{E}(W)(((p, g), b, e, a))$

- ▶ "If we can read a region" : $p = RX \lor p = RWX \lor p = RWLX$
- "and every word in that region is safe":
 read_write_cond (p, b, e)
- ▶ "then we can safely execute it": $\mathcal{E}(W)(((p,g),b,e,a))$

$$(p = RX \lor p = RWX \lor p = RWLX) \Longrightarrow$$

read_write_cond $(p, b, e) \Longrightarrow \mathcal{E}(W)(((p, g), b, e, a))$

- ▶ "If we can read a region" : $p = RX \lor p = RWX \lor p = RWLX$
- "and every word in that region is safe": read_write_cond (p, b, e)
- ▶ "then we can safely execute it": $\mathcal{E}(W)(((p,g),b,e,a))$

$$(p = RX \lor p = RWX \lor p = RWLX) \Longrightarrow$$

read_write_cond $(p, b, e) \Longrightarrow \mathcal{E}(W)(((p, g), b, e, a))$

- ▶ "If we can read a region" : $p = RX \lor p = RWX \lor p = RWLX$
- "and every word in that region is safe": read_write_cond (p, b, e)
- ▶ "then we can safely execute it": $\mathcal{E}(W)(((p,g),b,e,a))$

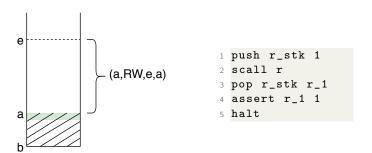
$$(p = RX \lor p = RWX \lor p = RWLX) \Longrightarrow$$

read_write_cond $(p, b, e) \Longrightarrow \mathcal{E}(W)(((p, g), b, e, a))$

Reasoning about Unknown Code

Reasoning about Unknown Code

We use the fundamental theorem to reason about calls to an unknown adversary



$$\mathcal{E}(W)(pc) \triangleq \forall r, \mathcal{R}(W)(r) * \operatorname{context}(W)(r[\operatorname{PC} := pc])$$

$$-* \operatorname{WP} \operatorname{Seq} (\operatorname{Instr} \operatorname{Executable})$$

$$\{v, v = \operatorname{Halted}V \implies \exists W'r', W' \sqsubseteq_{\operatorname{priv}} W$$

$$* \operatorname{context}(W')(r')\}$$

34 / 36

Conclusion

- Embed a capability machine into Iris
- ► Define its program logic
- Mechanize a unary logical relation for an untyped capability machine language
- Prove the fundamental theorem of logical relations
- Reason about examples that rely on Local Stack Encapsulation and Well-Bracketed Control Flow with calls to an unknown adversary

References



Lau Skorstengaard, Dominique Devriese, and Lars Birkedal (2018) Reasoning About a Machine with Local Capabilities ESOP *Programming Languages and Systems* 475–501.



Derek Dreyer, Georg Neis, Lars Birkedal (2012)

The impact of higher-order state and control effects on local relational reasoning

Journal of Functional Programming 22(4-5) 477-528.



Derek Dreyer, Amal Ahmed, Lars Birkedal (2011) Logical Step-Indexed Logical Relations *LMCS* 7(2:16).