Implementing a Capability Machine model into Iris

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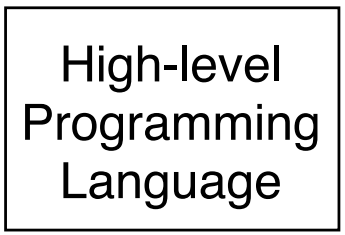
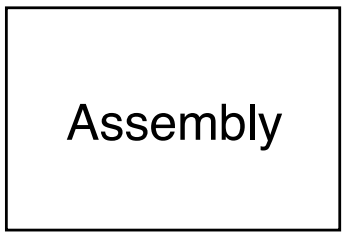
Introduction





- Local state encapsulation
- Well bracketed control flow

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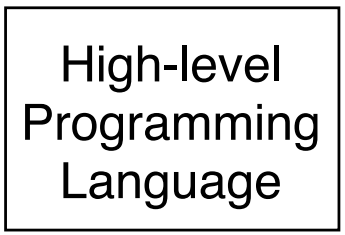

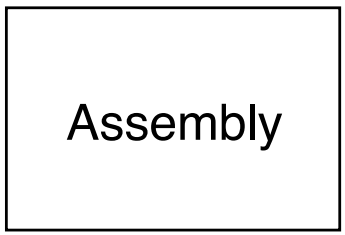

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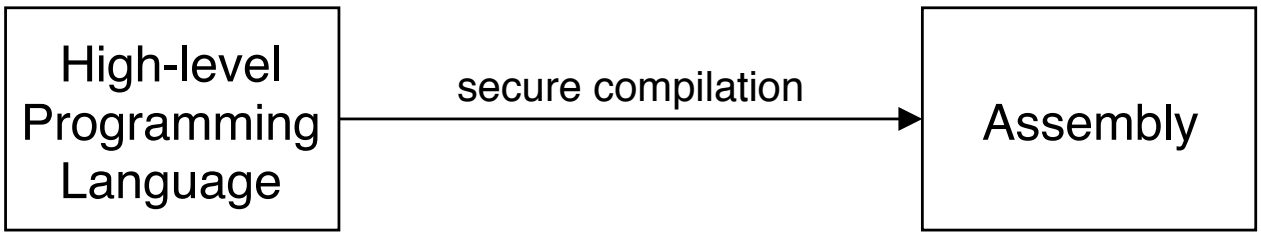

- Local state encapsulation
- Well bracketed control flow

Layers of Abstraction

Programming Languages

- Local State Encapsulation
- Well Bracketed Control Flow

Assembly Language

- Programs lie in Memory, Program Counter, ...
- Arbitrary Pointer Manipulation
- Arbitrary Jumps

Machine Code

- Instruction Decoding, Cache, etc.

...

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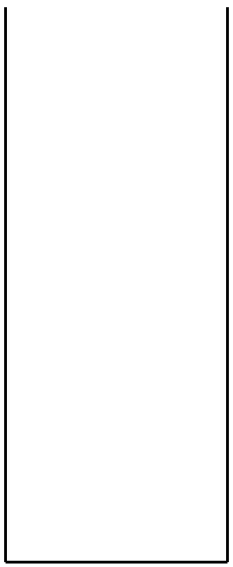
Overview

Capability Machines

Capability Machine

Capability: An unforgeable token of authority



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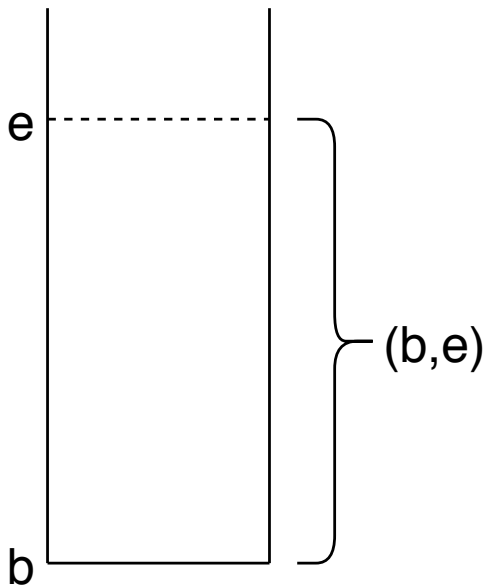

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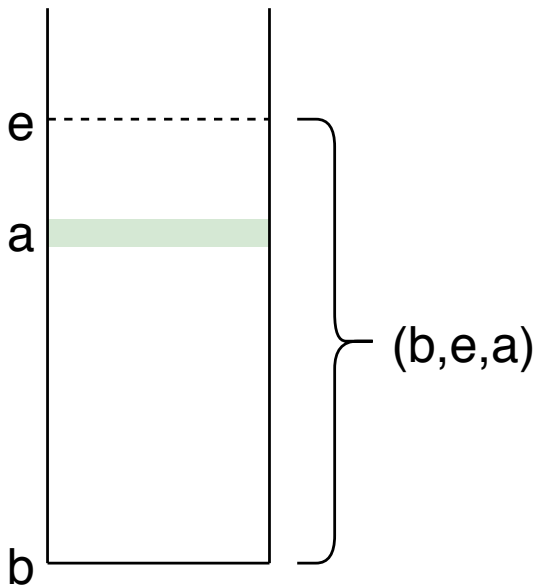

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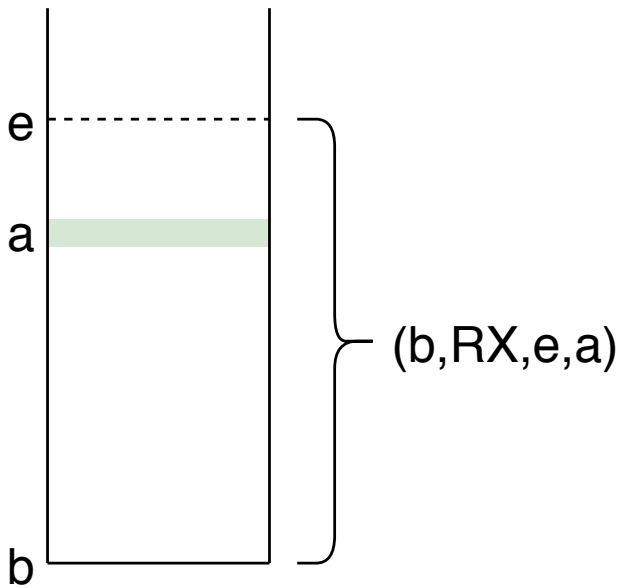

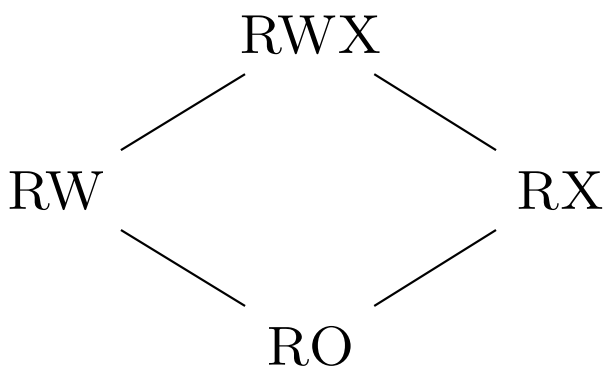

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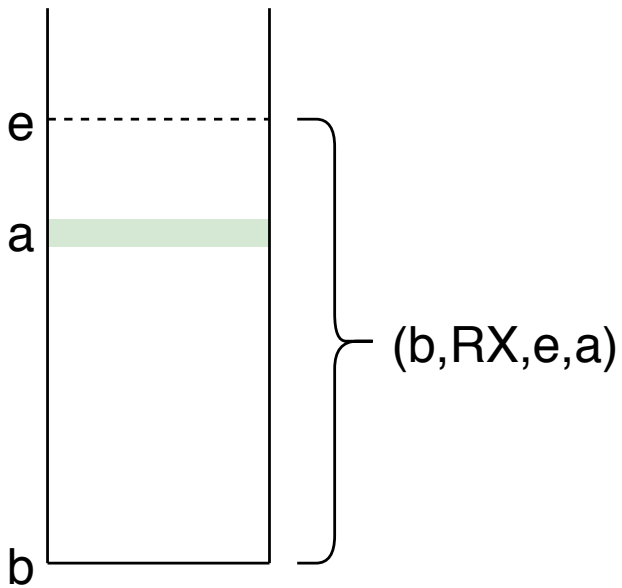
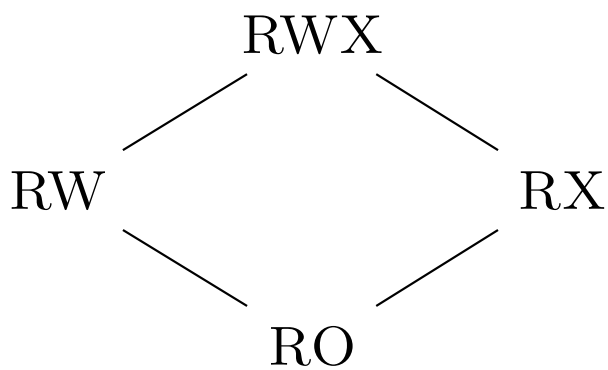
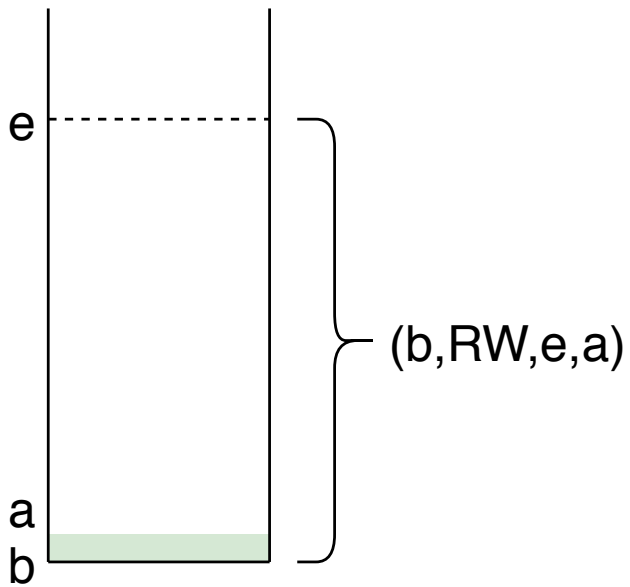




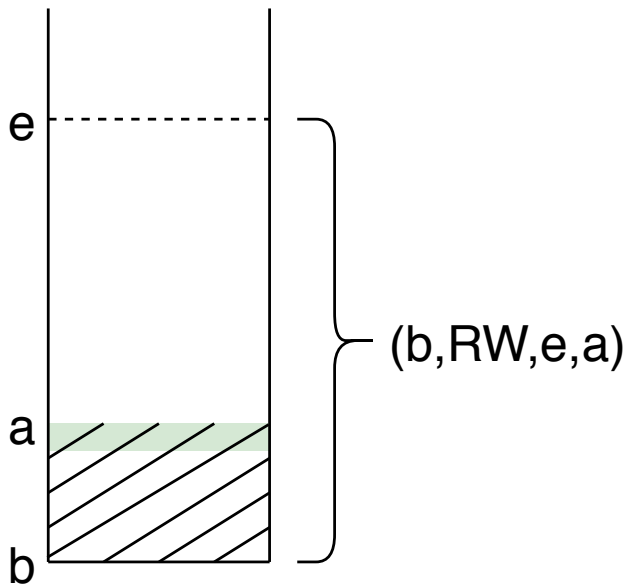
Enforcing Local Stack Encapsulation using Capabilities

Local State Encapsulation




```
1 push r_stk 1
2 scall r
3 pop r_stk r_1
4 assert r_1 1
5 halt
```

Local State Encapsulation

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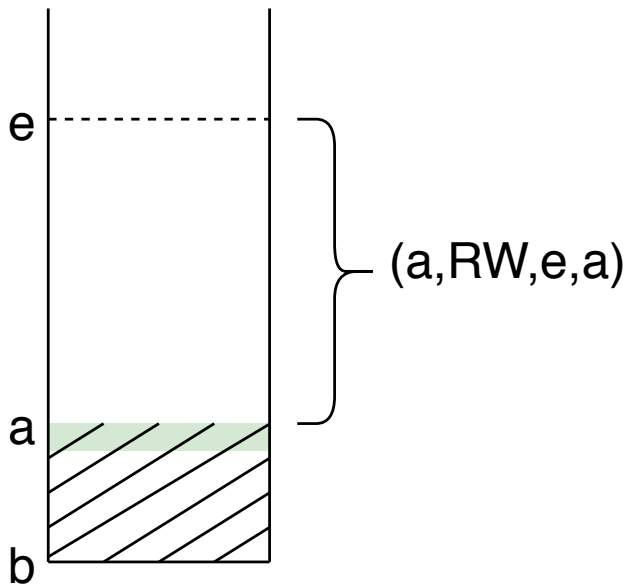


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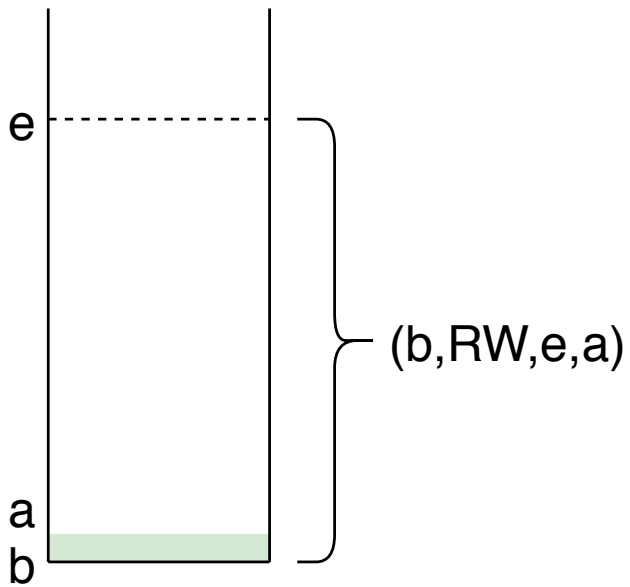
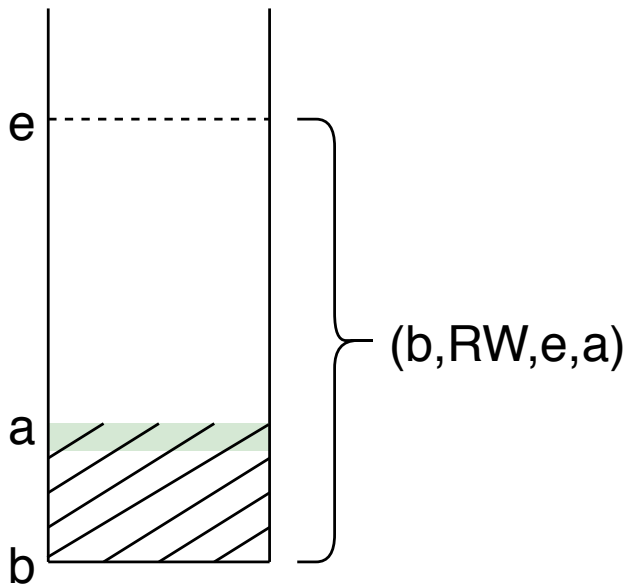
Enforcing Well Bracketed Control Flow using Capabilities

Well Bracketed Control Flow




```
1 push r_stk 1
2 scall r
3 pop r_stk r_1
4 assert r_1 1
5 push r_stk 2
6 scall r
7 halt
```

Well Bracketed Control Flow

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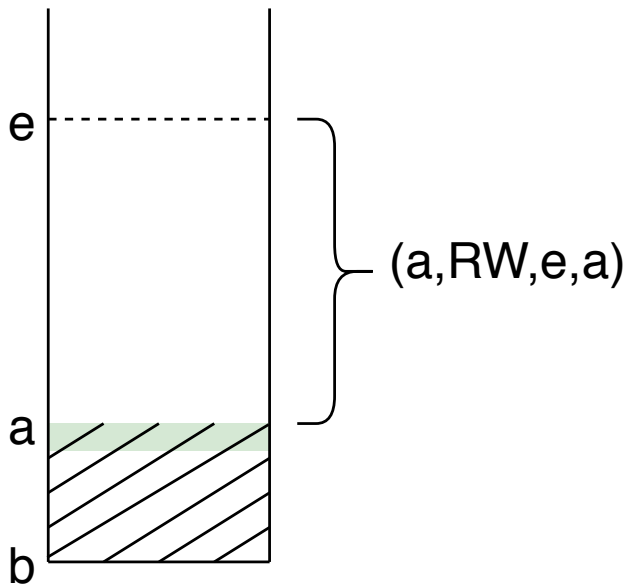


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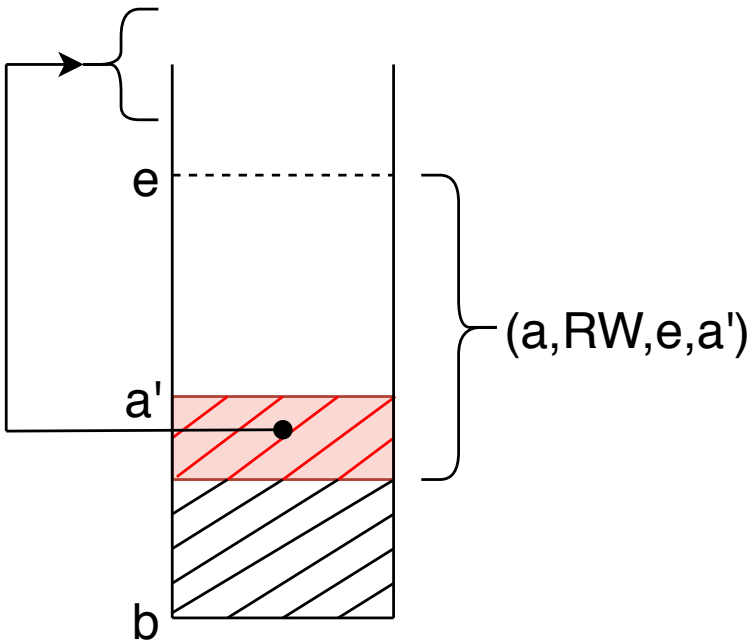


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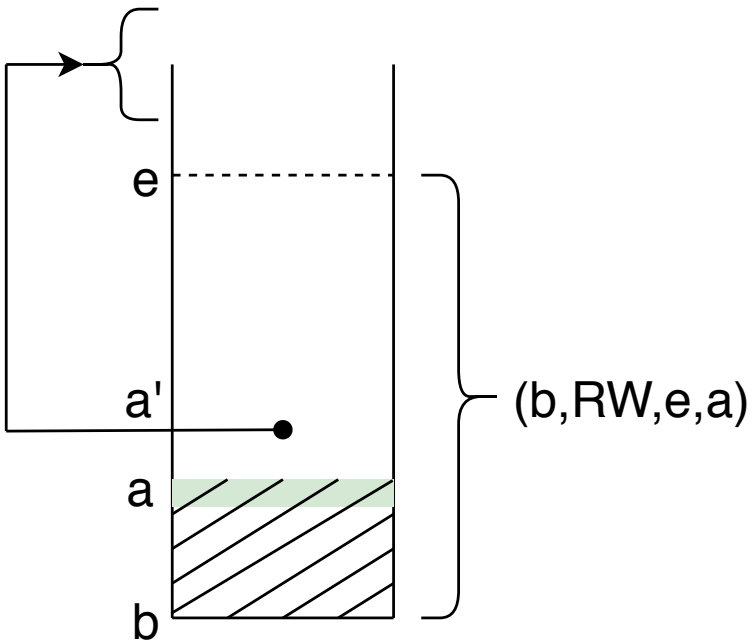


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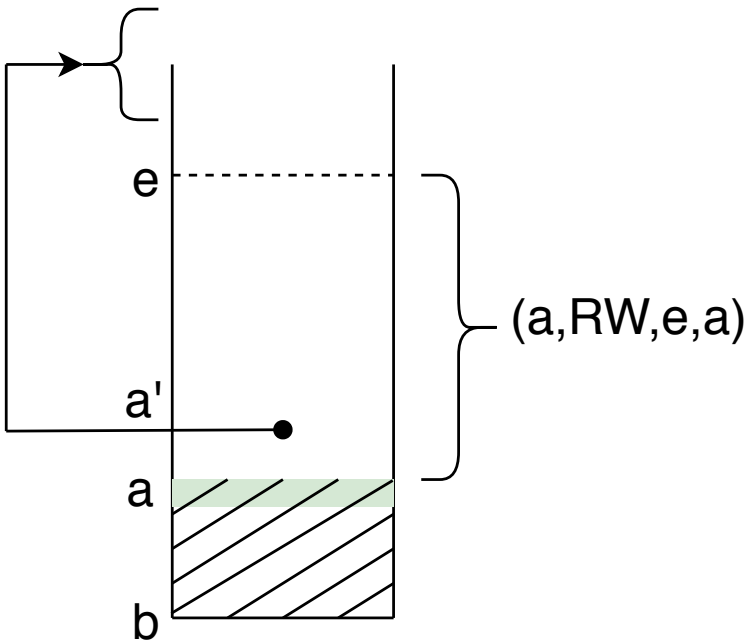


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Local Capabilities

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(p,**Local**,b,e,a)

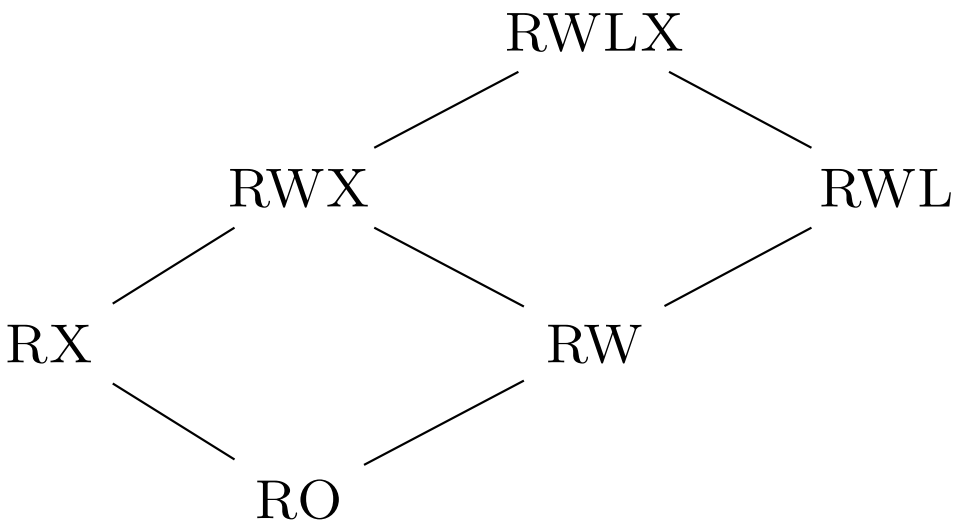
well-bracketed

(p,**Global**,b,e,a)

not well-bracketed

Local Capabilities

(p,**Local**,b,e,a)



well-bracketed

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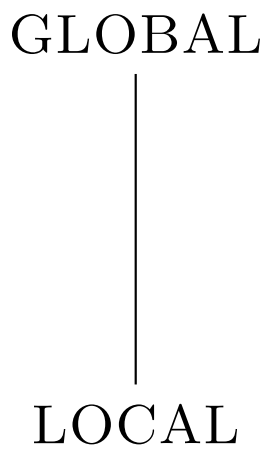

not well-bracketed

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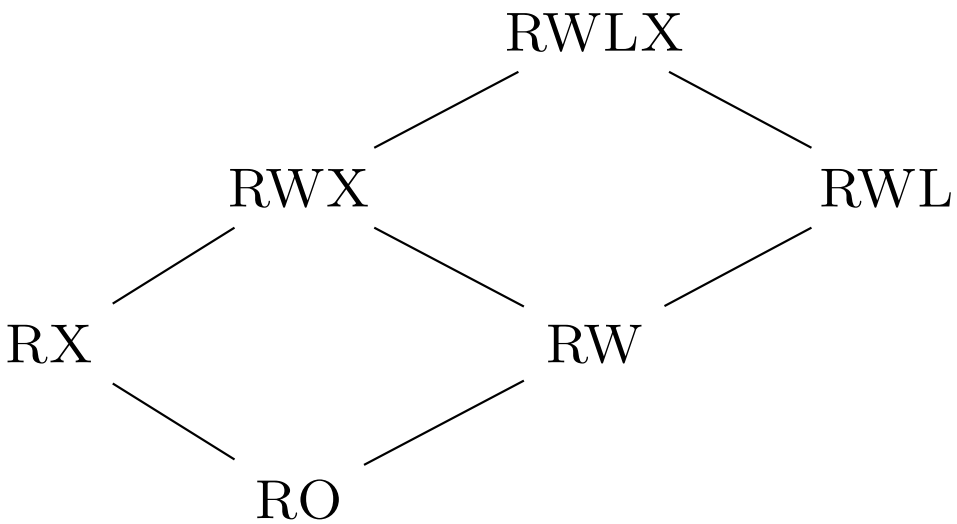

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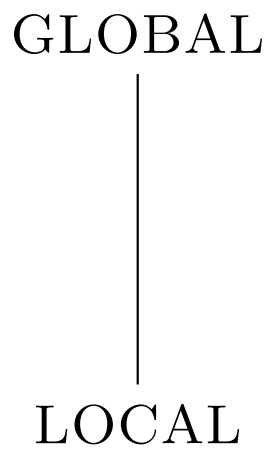

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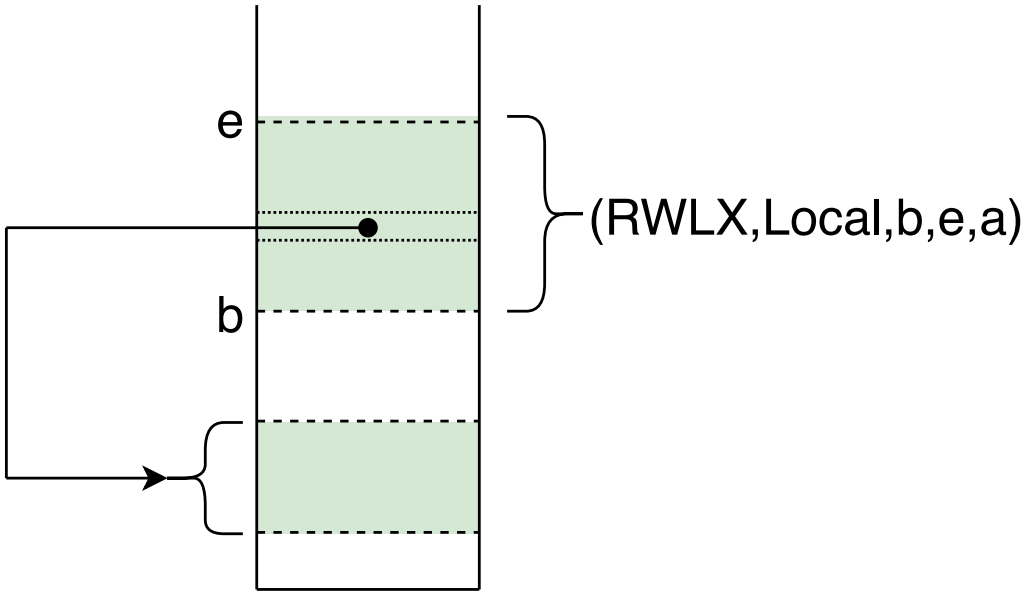
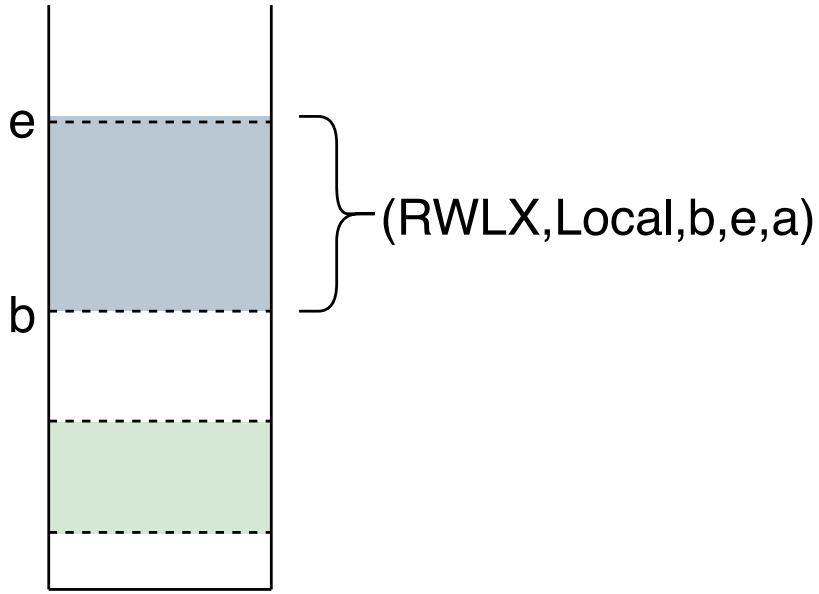
Calling Convention



r_stk	$(RWLX, Local, b, e, a)$

Calling Convention



r_stk	$(RWLX, Local, b, e, a)$

Reasoning about Capability Safety

Expressing Capability Safety

- using a Program Logic
- using a logical relation to capture invariants on the type system
- **using a logical relation on an untyped (or uni-typed) language to capture semantic properties of the language**

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  1. embed the language into Iris
  2. define a program logic by proving Hoare Triples
  3. define the logical relation
  4. prove the fundamental theorem of logical relations
  5. use the logical relation to prove examples that rely on local state encapsulation and well-bracketed control flow with calls to unknown adversary

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Program Logic

Abstract Instructions

$$(reg, mem) \rightarrow (reg', mem')$$

- Instr Executable
- Instr Halted $\rightarrow$ HaltedV
- Instr Failed $\rightarrow$ FailedV

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A Capability Points-to Predicate

Points-to Predicate with Permissions

$$a \mapsto_a [RWL]w$$

Points-to Predicate with Permissions

$$a \mapsto_a [RWL]w \Longrightarrow\!\!* \; a \mapsto_a [RWL]((p, Local), b, e, l)$$

Points-to Predicate with Permissions

$$
\begin{aligned}
a \mapsto_a [RWL]w &\Longrightarrow\!\!* \; a \mapsto_a [RWL]((p, Local), b, e, l) \\
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\end{aligned}
$$

Points-to Predicate with Permissions

$$
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&\Longrightarrow\!\!\ast\; a \mapsto_a [RW]((p, Local), b, e, l) \\
&\not\Longrightarrow\!\!\ast\; a \mapsto_a [RW]((p', Local), b', e', l')
\end{aligned}
$$

Proving Hoare Triples

Successful Execution

Hoare Triples of the Program Logic: Success

$$
\begin{aligned}
&\mathit{decode}(w) = \text{Load dst src} \\
&\wedge\ \text{isCorrectPC}\ ((p_{pc}, g_{pc}), b_{pc}, e_{pc}, a_{pc}) \\
&\wedge\ \text{readAllowed}\ p_{src}\ \wedge\ \text{withinBounds}\ (b_{src}, e_{src}, a_{src})
\end{aligned}
$$

$$
\begin{aligned}
\{\{\{\ &\text{PC} \mapsto_r ((p_{pc}, g_{pc}), b_{pc}, e_{pc}, a_{pc}) * a_{pc} \mapsto_a [p_{pc}]w \\
&* \mathit{dst} \mapsto_r w_{dst} * \mathit{src} \mapsto_r ((p_{src}, g_{src}), b_{src}, e_{src}, a_{src}) \\
&* a_{src} \mapsto_a [p_{src}]w_{src}\}\}\} \\
&\text{Instr Executable} \\
\{\{\{\ &\text{PC} \mapsto_r ((p_{pc}, g_{pc}), b_{pc}, e_{pc}, a_{pc} + 1) * a_{pc} \mapsto_a [p_{pc}]w \\
&* \mathit{dst} \mapsto_r w_{src} * \mathit{src} \mapsto_r ((p_{src}, g_{src}), b_{src}, e_{src}, a_{src}) \\
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&* \mathit{dst} \mapsto_r w_{src} * \mathit{src} \mapsto_r ((p_{src}, g_{src}), b_{src}, e_{src}, a_{src}) \\
&* a_{src} \mapsto_a [p_{src}]w_{src}\}\}\}
\end{aligned}
$$

Hoare Triples of the Program Logic: Success

$$
\begin{aligned}
&\mathit{decode}(w) = \text{Load dst src} \\
&\wedge\ \text{isCorrectPC}\ ((p_{pc}, g_{pc}), b_{pc}, e_{pc}, a_{pc}) \\
&\wedge\ \text{readAllowed}\ p_{src}\ \wedge\ \text{withinBounds}\ (b_{src}, e_{src}, a_{src})
\end{aligned}
$$

$$
\begin{aligned}
\{\{\{\ &\text{PC} \mapsto_r ((p_{pc}, g_{pc}), b_{pc}, e_{pc}, a_{pc}) * a_{pc} \mapsto_a [p_{pc}]w \\
&* \mathit{dst} \mapsto_r w_{dst} * \mathit{src} \mapsto_r ((p_{src}, g_{src}), b_{src}, e_{src}, a_{src}) \\
&* a_{src} \mapsto_a [p_{src}]w_{src}\}\}\} \\
&\text{Instr Executable} \\
\{\{\{\ &\text{PC} \mapsto_r ((p_{pc}, g_{pc}), b_{pc}, e_{pc}, a_{pc} + 1) * a_{pc} \mapsto_a [p_{pc}]w \\
&* \mathit{dst} \mapsto_r w_{src} * \mathit{src} \mapsto_r ((p_{src}, g_{src}), b_{src}, e_{src}, a_{src}) \\
&* a_{src} \mapsto_a [p_{src}]w_{src}\}\}\}
\end{aligned}
$$

Failed Execution

Hoare Triples of the Program Logic: Failure

$$
\begin{array}{l}
\quad \mathit{decode}(w) = \text{Load dst src} \\
\wedge\ \text{isCorrectPC}\ ((p_{pc}, g_{pc}), b_{pc}, e_{pc}, a_{pc}) \\
\wedge\ \neg\text{readAllowed}\ p_{src}\ \vee\ \neg\text{withinBounds}\ (b_{src}, e_{src}, a_{src}) \\
\{\{\{\ \text{PC} \mapsto_r ((p_{pc}, g_{pc}), b_{pc}, e_{pc}, a_{pc}) * a_{pc} \mapsto_a [p_{pc}]w \\
\quad * \mathit{src} \mapsto_r ((p_{src}, g_{src}), b_{src}, e_{src}, a_{src})\}\}\} \\
\quad \text{Instr Executable} \\
\{\{\{\ \text{FailedV}, \top\}\}\}
\end{array}
$$

Hoare Triples of the Program Logic: Failure

$$
\begin{array}{l}
\quad \mathit{decode}(w) = \text{Load dst src} \\
\wedge\ \text{isCorrectPC}\ ((p_{pc}, g_{pc}), b_{pc}, e_{pc}, a_{pc}) \\
\wedge\ \neg\text{readAllowed}\ p_{src}\ \vee\ \neg\text{withinBounds}\ (b_{src}, e_{src}, a_{src}) \\
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\quad \text{Instr Executable} \\
\{\{\{\ \text{FailedV}, \top\}\}\}
\end{array}
$$

A Unary Logical Relation for Reasoning about Semantic Properties of an Untyped Language

The Value Relation

The Value Relation

A unary logical relation of an un-typed language

$$\mathcal{V} : \mathit{Word} \to \mathit{iProp}\ \Sigma$$

- World: A collection of state transition systems to reason about *local state*

$$\mathcal{V}(W)(z) \triangleq \exists z' \in \mathbb{Z}.z = z'$$

$$\mathcal{V}(W)(((\text{RO}, g), b, e, a)) \triangleq \text{read\_write\_cond(RO,b,e)}$$

$$\mathcal{V}(W)(((\text{RX}, g), b, e, a)) \triangleq \text{read\_write\_cond(RX,b,e)} * \square\ \text{exec\_cond(W)(RX,g,b,e)}$$

The Value Relation

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The Execute Condition

The Execute Condition

$$\text{exec\_cond(W)(p,g,b,e)} \triangleq \begin{cases} \forall a \in [b\ e], W' \sqsubseteq_{pub} W. & \\ \quad \triangleright\ \mathcal{E}(W')(((p,g),b,e,a)) & g = Local \\ \forall a \in [b\ e], W' \sqsubseteq_{priv} W. & \\ \quad \triangleright\ \mathcal{E}(W')(((p,g),b,e,a)) & g = Global \end{cases}$$

The Expression Relation

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$$\mathcal{E}(W)(pc) \triangleq \forall r, \mathcal{R}(W)(r) \ * \ \mathsf{context}(W)(r[\mathsf{PC} := pc]) \\ \twoheadrightarrow \ \mathrm{WP}\ \mathsf{Seq}\ (\mathsf{Instr\ Executable}) \\ \{v, v = \mathit{HaltedV} \implies \exists W' r', W' \sqsubseteq_{\mathit{priv}} W \\ * \mathsf{context}(W')(r')\}$$

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$$\mathcal{E}(W)(pc) \triangleq \forall r, \mathcal{R}(W)(r) \ * \ \text{context}(W)(r[\text{PC} := pc])$$
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$$\text{context}(W)(r) = \ ?$$

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$$\mathsf{context}(W)(r) = \left(\bigast_{r_i \mapsto w \in r} r_i \mapsto_r w \right) \wedge \mathsf{full\_map}\ r$$

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$$\text{context}(W)(r) = \ (\ \mathop{\Large *}_{r_i \mapsto w \in r} \ r_i \mapsto_r w) \wedge \text{full\_map } r \\ * \ \text{na\_inv } \gamma_{na} \top$$

The Expression Relation

$$\mathcal{E}(W)(pc) \triangleq \forall r, \mathcal{R}(W)(r) \ * \ \text{context}(W)(r[\text{PC} := pc])$$
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$$* \ \text{na\_inv } \gamma_{na} \top$$
$$* \ \text{sts\_full } W$$
$$* \ \text{region } W$$

The Fundamental Theorem of Logical Relations

The Fundamental Theorem of logical relations

If we can read a region, and every word in that region is safe, then we can safely execute it

- "If we can read a region" : $p = \mathrm{RX} \vee p = \mathrm{RWX} \vee p = \mathrm{RWLX}$
- "and every word in that region is safe": read_write_cond (p, b, e)
- "then we can safely execute it": $\mathcal{E}(W)(((p, g), b, e, a))$

$$(p = \mathrm{RX} \vee p = \mathrm{RWX} \vee p = \mathrm{RWLX}) \implies$$
$$\text{read\_write\_cond } (p, b, e) \implies \mathcal{E}(W)(((p, g), b, e, a))$$

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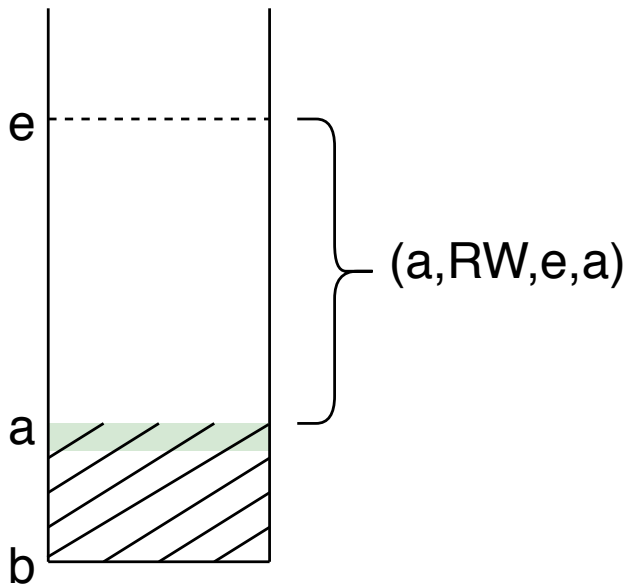
Reasoning about Unknown Code

Reasoning about Unknown Code

We use the fundamental theorem to reason about calls to an unknown adversary




```
1 push r_stk 1
2 scall r
3 pop r_stk r_1
4 assert r_1 1
5 halt
```

Conclusion

Conclusion

- Embed a capability machine into Iris
- Define its program logic
- Mechanize a unary logical relation for an untyped capability machine language
- Prove the fundamental theorem of logical relations
- Reason about examples that rely on Local Stack Encapsulation and Well-Bracketed Control Flow with calls to an unknown adversary

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