

# Implementing a Capability Machine model into Iris

**Aïna Linn Georges**

Alix Trieu

Lars Birkedal

Aarhus University

*ageorges@cs.au.dk*

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# Introduction

- ▶ Capability machines allow for fine grained control over pointer permissions
- ▶ Good target for secure compilation
- ▶ In particular: we are interested in enforcing certain higher level abstractions such as *local state encapsulation* as *well-bracketed control flow* at the lowest level of the machine
- ▶ We need tools to reason about these subtle properties in a language that does not enforce them
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# Capability Machines

# Capability Machine

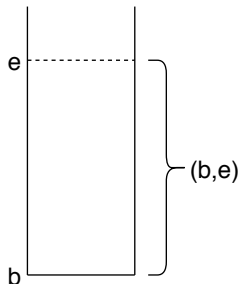
**Capability:** An unforgeable token of authority





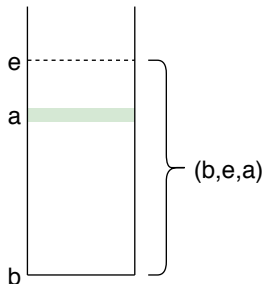
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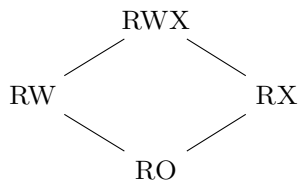
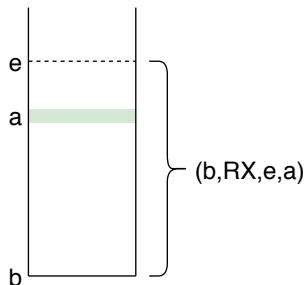
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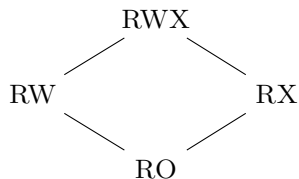
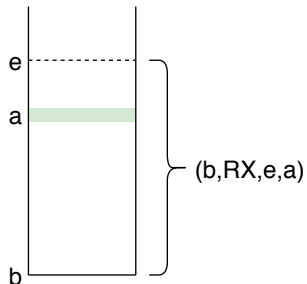
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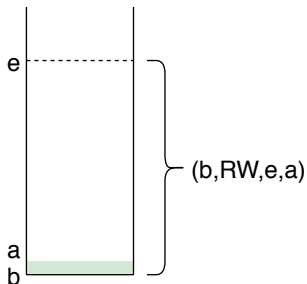
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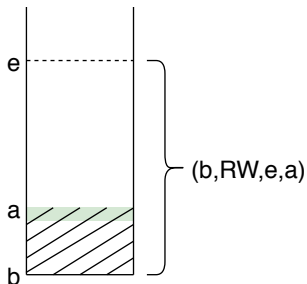
## Enforcing Well Bracketed Control Flow using Capabilities

## Well Bracketed Control Flow



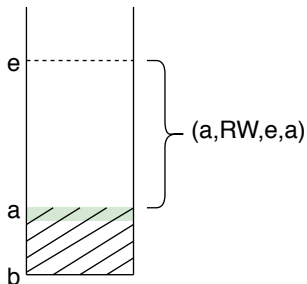
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4 assert r_1 1
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7 halt
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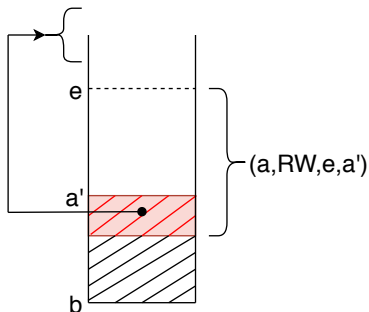
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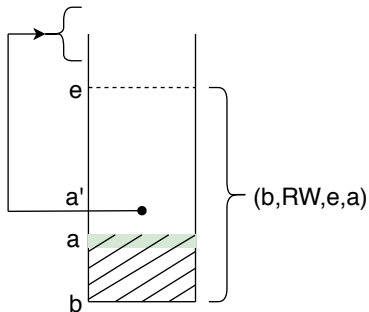


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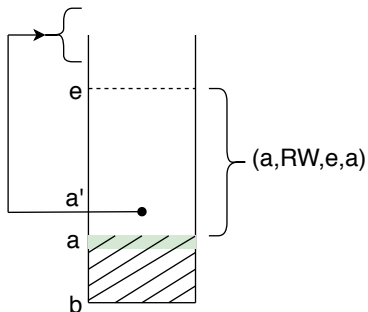
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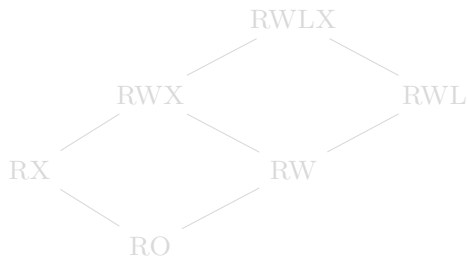


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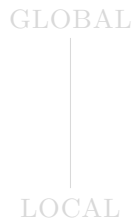
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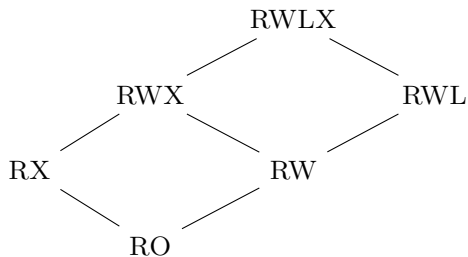


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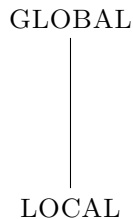


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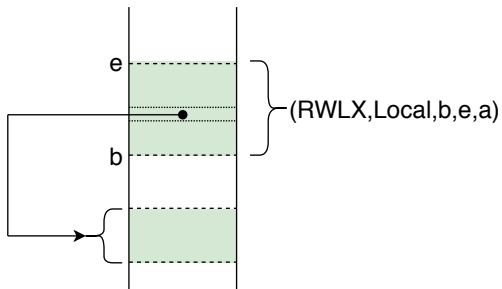
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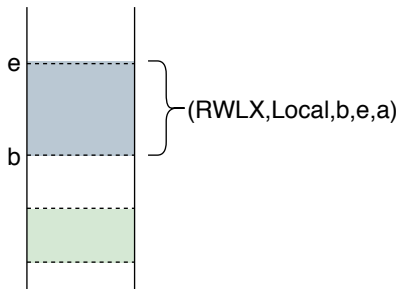
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# Calling Convention



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## Reasoning about Capability Safety

# Expressing Capability Safety

- ▶ using a Program Logic
- ▶ using a logical relation to capture invariants on the type system
- ▶ **using a logical relation on an untyped (or uni-typed) language to capture semantic properties of the language**

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# Step-indexed Kripke Logical Relation

$$\mathcal{V}(W) \triangleq \{n, (RW, g, b, e, a) | \dots\} \cup \dots$$

- ▶ World-circularity problem
  - ▶ Step indexing
- ▶ The world may evolve: we need future world relation
  - ▶ Local capabilities are revoked whereas Global capabilities are not, *the relation needs to model this distinction:*

$\sqsubseteq_{pub}$       *and*       $\sqsubseteq_{priv}$

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# Expressing Capability Safety in Iris - an Iris primer

## **Iris:** Higher-order Concurrent Separation Logic Framework

- ▶ Foundational
- ▶ Implemented in Coq – equipped with an interactive proof mode
- ▶ Framework – embed any language and its operational semantics into Iris
- ▶ Comes equipped with:
  - ▶ Invariants
  - ▶ Ghost state
  - ▶ Always and Later Modalities

We can take advantage of Iris' step-indexed model and invariants to mechanize step-indexed Kripke logical relations with recursive worlds in a succinct and elegant way.

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# Expressing Capability Safety in Iris - Challenges

- ▶ Region invariants: Iris invariants
- ▶ Future world relation: frame preserving updates and world satisfaction
- ▶ Step indexing: later modality

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- ▶ Iris was designed with more high level languages in mind, how do we embed a low level machine language into Iris
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- ▶ embed the language into Iris
- ▶ define a program logic by proving Hoare Triples
- ▶ define the logical relation – using Iris tools to solve the world circularity problem
- ▶ prove the fundamental theorem of logical relations
- ▶ use the logical relation to prove examples that rely on local state encapsulation and well-bracketed control flow with calls to unknown adversary

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## Program Logic

# Abstract Instructions

$$(reg, mem) \rightarrow (reg', mem')$$

- ▶ Instr Executable
- ▶ Instr Halted  $\rightarrow$  HaltedV
- ▶ Instr Failed  $\rightarrow$  FailedV



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## A Capability Points-to Predicate

# Points-to Predicate with Permissions

$$a \mapsto_a [RWL]w$$

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$$a \mapsto_a [RWL]w \Rightarrow a \mapsto_a [RWL]((p, Local), b, e, l)$$

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$$\begin{aligned} a \mapsto_a [RWL]w &\Rightarrow a \mapsto_a [RWL]((p, Local), b, e, l) \\ &\Rightarrow a \mapsto_a [RW]((p, Local), b, e, l) \end{aligned}$$



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# A Unary Logical Relation for Reasoning about Semantic Properties of an Untyped Language

## The Value Relation

## A unary logical relation of an un-typed language

$$\mathcal{V} : \text{Word} \rightarrow iProp \Sigma$$

**Challenge:** distinguish between Local and Global capabilities:

- ▶ At the level of the value relation
- ▶ Model revocation

**STS:** A collection of state transition systems

$$\mathcal{V}((_{RW}, g), b, e, a) \triangleq \bigstar_{a \in [b, e]} \boxed{\exists w, a \mapsto_a [RW]_w * \mathcal{V}(w)}$$

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# From World to state transition system collection

On paper:

$$\begin{aligned}\text{Region} = & \{ \text{Revoked} \} \uplus \\ & \{ \text{Temporary} \} \times \text{State} \times \text{Rels} \\ & \times (\text{State} \rightarrow (\text{Wor} \xrightarrow[\exists_{pub}]{mon, ne} \text{UPred}(\text{MemSeg}))) \uplus \\ & \{ \text{Permanent} \} \times \text{State} \times \text{Rels} \\ & \times (\text{State} \rightarrow (\text{Wor} \xrightarrow[\exists_{priv}]{mon, ne} \text{UPred}(\text{MemSeg}))) \\ \text{World} = & \mathbb{N} \rightarrow \text{Region}\end{aligned}$$

In the Iris mechanization, we use a collection of state transition systems:

$$\Sigma : \mathbb{N} \rightarrow \text{States} \times \mathbb{N} \rightarrow \text{Rels}$$

The world circularity problem is now handled using Iris invariants and saved predicates.

# From World to state transition system collection

On paper:

$$\begin{aligned} \text{Region} = & \{ \text{Revoked} \} \uplus \\ & \{ \text{Temporary} \} \times \text{State} \times \text{Rels} \\ & \times (\text{State} \rightarrow (\text{Wor} \xrightarrow[\exists_{pub}]{mon, ne} \text{UPred}(\text{MemSeg}))) \uplus \\ & \{ \text{Permanent} \} \times \text{State} \times \text{Rels} \\ & \times (\text{State} \rightarrow (\text{Wor} \xrightarrow[\exists_{priv}]{mon, ne} \text{UPred}(\text{MemSeg}))) \end{aligned}$$

$$\text{World} = \mathbb{N} \rightarrow \text{Region}$$

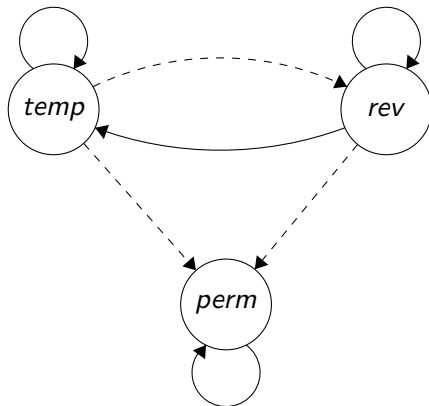
In the Iris mechanization, we use a collection of state transition systems:

$$\Sigma : \mathbb{N} \rightarrow \text{States} \times \mathbb{N} \rightarrow \text{Rels}$$

The world circularity problem is now handled using Iris invariants and saved predicates.

# Standard STS

$$\Sigma : \mathbb{N} \rightarrow States \times \mathbb{N} \rightarrow Rels$$



## What's new: capability machine viewpoint

- ▶ Mechanized formalization: currently  $\sim 25000$  lines of Iris code
- ▶ At a higher level of abstraction
  - ▶ Step index  $\rightarrow$  later modality
  - ▶ World  $\rightarrow$  collection of state transition systems

## What's new: Iris formalization viewpoint

- ▶ Formalization of a machine level language, with no distinction between program and memory
- ▶ Distinction between well-bracketed and non well-bracketed calls: using public/private transitions

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# Conclusion

- ▶ Embed a capability machine into Iris
- ▶ Define its program logic
- ▶ Mechanize a unary logical relation for an untyped capability machine language
- ▶ Prove the fundamental theorem of logical relations
- ▶ Reason about examples that rely on Local Stack Encapsulation and Well-Bracketed Control Flow with calls to an unknown adversary



# References



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Logical Step-Indexed Logical Relations  
*LMCS* 7(2:16).

## The Execute Condition

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$$\text{exec\_cond}(\Sigma)(p, g, b, e) \triangleq \begin{cases} \forall a \in [b \ e], \Sigma' \sqsupseteq_{pub} \Sigma. \\ \quad \triangleright \mathcal{E}(\Sigma')(((p, g), b, e, a)) \quad g = Local \\ \\ \forall a \in [b \ e], \Sigma' \sqsupseteq_{priv} \Sigma. \\ \quad \triangleright \mathcal{E}(\Sigma')(((p, g), b, e, a)) \quad g = Global \end{cases}$$

## The Expression Relation

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$$\begin{aligned}\mathcal{E}(\Sigma)(pc) &\triangleq \forall r, \mathcal{R}(\Sigma)(r) * \text{context}(\Sigma)(r[\text{PC} := pc]) \\ &\quad * \text{WP Seq (Instr Executable)} \\ &\quad \{v, v = \text{HaltedV} \implies \exists \Sigma' r', \Sigma' \sqsubseteq_{\text{priv}} \Sigma \\ &\quad * \text{context}(\Sigma')(r')\}\end{aligned}$$

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# The Fundamental Theorem of Logical Relations

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If we can read a region, and every word in that region is safe, then  
we can safely execute it

- ▶ "If we can read a region" :  $p = \text{RX} \vee p = \text{RWX} \vee p = \text{RWLX}$
- ▶ "and every word in that region is safe":  
 $\text{read\_write\_cond}(p, b, e)$
- ▶ "then we can safely execute it":  $\mathcal{E}(\Sigma)((p, g), b, e, a)$

$$(p = \text{RX} \vee p = \text{RWX} \vee p = \text{RWLX}) \implies \\ \text{read\_write\_cond}(p, b, e) \implies \mathcal{E}(\Sigma)((p, g), b, e, a)$$

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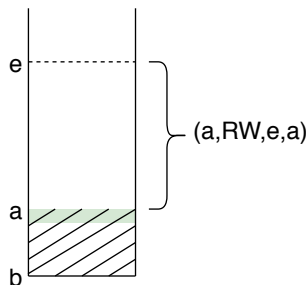
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## Reasoning about Unknown Code

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**We use the fundamental theorem to reason about calls to an unknown adversary**



```
1 push r_stk 1
2 scall r
3 pop r_stk r_1
4 assert r_1 1
5 halt
```

$$\mathcal{E}(\Sigma)(pc) \triangleq \forall r, \mathcal{R}(\Sigma)(r) * \text{context}(\Sigma)(r[PC := pc])$$

\* WP Seq (Instr Executable)

$$\{v, v = \text{Halted}V \implies \exists \Sigma' r', \Sigma' \sqsubseteq_{\text{priv}} \Sigma$$
$$* \text{context}(\Sigma')(r')\}$$

# Proving the Fundamental Theorem