Damn Vulnerable IOS Application Solutions http://damnvulnerableiosapp.com/

Jailbreak Detection – Jailbreak Test 2

By now you must have figured out that the solution for Jailbreak Test 1 doesn't work on Jailbreak Test 2. So things are a bit complicated here. Let's look at the class information for this application. You can find the class information for this application in the folder *Other Files* or you can just use class-dump to dump out the information. After scrolling down a bit, we can see the method that gets called on tapping the button *Jailbreak Test 2*

```
@interface JailbreakDetectionVC : /Users/Prateek/Desktop/DVIA/DamnVulnerableIOSApp/
DamnVulnerableIOSApp/View Controllers/
{
}
- (B00L)isJailbroken;
- (void)jailbreakTest2Tapped:(id)fp8;
- (void)jailbreakTest1Tapped:(id)fp8;
- (void)readArticleTapped:(id)fp8;
- (void)didReceiveMemoryWarning;
- (void)viewDidLoad;
- (id)initWithNibName:(id)fp8 bundle:(id)fp12;
@end
```

Let's start GDB on our device. After starting GDB, make sure the application is running on foreground and attach to to it.

Prateeks-iPhone: root# gdb GNU gdb 6.3.50-20050815 (Apple version gdb-1708) (Mon Oct 17 16:55:57 UTC 2011)

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```
GDB is free software, covered by the GNU General Public License, and you are welcome to change it and/or distribute copies of it under certain conditions. Type "show copying" to see the conditions.
There is absolutely no warranty for GDB. Type "show warranty" for details.
This GDB was configured as "arm-apple-darwin".
(gdb) attach DammyVulnerableIO.374
AttachDammyVulnerableIO.374
Attaching to process 374.
Reading symbols for shared libraries . done
unable to read unknown load command 0x80000028
bfd_mach_o_scan: unknown architecture 0x100000c/0x0
bfd_mach_o_scan: unknown architecture 0x100000c/0x0
Reading symbols for shared libraries warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerableIOSApp-ecjpfluolzzysiauzhxscrableIOSApp-build/Debug-iphoneos/DamnVulnerableIOSApp-build/Objects-normal/armv7s/SolutionsViewController.o" - no debug information available for "SolutionsViewControl
```

warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerable10SApp-ecjpfluolzzysiauzhxscixocyay/Build/Intermediates/DamnVuln Vulnerable10SApp.build/Objects-normal/armv7s/RuntimeManipulationSuccessVC.o" - no debug information available for "RuntimeManipulationSuccessVC.m".

warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerableIOSApp-ecjpfluolzzysiauzhxscixocyay/Build/Intermediates/DamnVulnerableIOSApp.build/Objects-normal/armv7s/TransportLayerProtectionVC.o" - no debug information available for "TransportLayerProtectionVC.m".

warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerableIOSApp-ecjpfluolzzysiauzhxscixocyay/Build/Intermediates/DamnVulnerableIOSApp.build/Objects-normal/armv7s/RNOpenSSLCryptor.o" - no debug information available for "RNOpenSSLCryptor.m".

warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerableIOSApp-ecjpfluolzzysiauzhxscixocyay/Build/Intermediates/DamnVulnerableIOSApp.build/Objects-normal/armv7s/RNOpenSSLEncryptor.o" - no debug information available for "RNOpenSSLEncryptor.m".

warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerableIOSApp-ecjpfluolzzysiauzhxscixocyay/Build/Intermediates/DamnVulnerableIOSApp.build/Objects-normal/armv7s/User.o" - no debug information available for "User.m".

warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerableIOSApp-ecjpfluolzzysiauzhxscixocyay/Build/Intermediates/DamnVulnerableIOSApp.build/Objects-normal/armv7s/AppDelegate.o" - no debug information available for "AppDelegate.m".

warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerableIOSApp-ecjpfluolzzysiauzhxscixocyay/Build/Intermediates/DamnVulnerableIOSApp.build/Objects-normal/armv7s/LearnViewController.o" - no debug information available for "LearnViewController.m".

warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerableIOSApp-ecjpfluolzzysiauzhxscixocyay/Build/Intermediates/DamnVulnerableIOSApp.build/Objects-normal/armv7s/ApplicationPatchingVC.o" - no debug information available for "ApplicationPatchingVC.m".

warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerableIOSApp-ecjpfluolzzysiauzhxscixocyay/Build/Intermediates/DamnVulnerableIOSApp.build/Objects-normal/armv7s/RNEncryptor.o" - no debug information available for "RNEncryptor.m".

```
warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerableIOSAp er-dummy.o)" - no debug information available for "Pods-ECSlidingViewController-dummy.m".
```

warning: Could not find object file "/Users/Prateek/Library/Developer/Xcode/DerivedData/DamnVulnerableIOSAp ViewController.o)" - no debug information available for "UIViewController+ECSlidingViewController.m".

```
unable to read unknown load command 0x80000028
bfd_mach_o_scan: unknown architecture 0x100000c/0x0
bfd_mach_o_scan: unknown architecture 0x100000c/0x0
Reading symbols for shared libraries + done
0x3b27aa84 in mach_msg_trap ()
```

(gdb)

Then let's set a breakpoint for the method that gets called on tapping the button *Jailbreak Test 2*. You can set it by using the command *b jailbreakTest2Tapped*:

```
(gdb) b jailbreakTest2Tapped:
Breakpoint 1 at 0x828ea
(gdb)
```

Now type the **c** command to continue the application. You will see that the application on the device will now begin responsive.

```
(gdb) c
Continuing.
```

Now let's go ahead and tap on Jailbreak Test 2. We will see our breakpoint being hit.

```
(gdb) c
Continuing.

Breakpoint 1, 0x000828ea in -[JailbreakDetectionVC jailbreakTest2Tapped:] ()
(gdb) ■
```

Let's disassemble the code. Use the *disassemble* command to see the disassembly.

```
(qdb) disassemble
Dump of assembler code for function -[JailbreakDetectionVC jailbreakTest2Tapped:]:
0x000828e4 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+0>:
                                                                   push
                                                                           {r4, r5, r7, lr}
0x000828e6 <-[JailbreakDetectionVC</pre>
                                    jailbreakTest2Tapped:]+2>:
                                                                           r7, sp, #8
                                                                           sp, #204
0x000828e8 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+4>:
                                                                   sub
                                                                           r3, sp, #192
r9, #0 ; 0x
0x000828ea <-[JailbreakDetectionVC
                                    iailbreakTest2Tapped:1+6>:
                                                                   add
                                                                                   ; 0×0
0x000828ec <-[JailbreakDetectionVC</pre>
                                    jailbreakTest2Tapped:]+8>:
                                                                   movw
0x000828f0 <-[JailbreakDetectionVC</pre>
                                    jailbreakTest2Tapped:]+12>:
                                                                           r9, #0 ; 0x0
0x000828f4 <- [JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+16>:
                                                                           r0, [sp, #200]
                                                                           r1, [sp, #196]
0x000828f6 <-[JailbreakDetectionVC
                                    iailbreakTest2Tapped:l+18>:
                                                                   str
0x000828f8 <-[JailbreakDetectionVC</pre>
                                    jailbreakTest2Tapped:]+20>:
                                                                   str.w
                                                                           r9, [sp, #192]
                                    jailbreakTest2Tapped:]+24>:
0x000828fc <-[JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+26>:
0x000828fe <-[JailbreakDetectionVC
                                                                           r1, r2
                                                                   mov
0x00082900 <- [JailbreakDetectionVC
                                    iailbreakTest2Tapped:]+28>:
                                                                   blx
                                                                           0x9af60 <dvld stub objc storeStrong>
0x00082904 <- [JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+32>:
                                                                   movw
                                                                           r0, #34808
                                                                                            ; 0x87f8
0x00082908 <- [JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+36>:
                                                                           r0, #1 ; 0x1
                                                                           r0, pc
r0, [r0, #0]
0x0008290c <-[JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+40>:
                                                                   add
0x0008290e <-[JailbreakDetectionVC
                                    iailbreakTest2Tapped:1+42>:
                                                                   ldr
0x00082910 <-[JailbreakDetectionVC</pre>
                                    jailbreakTest2Tapped:]+44>:
                                                                           r1, #56908
                                                                                            ; 0xde4c
                                                                   movw
0x00082914 <-[JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+48>:
                                                                           r1, #1 ; 0x1
0x00082918 <-[JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+52>:
                                                                           r1, pc
                                                                   add
                                                                           r2. #57738
0x0008291a <-[JailbreakDetectionVC</pre>
                                    iailbreakTest2Tapped:1+54>:
                                                                   movw
                                                                                            : 0xe18a
0x0008291e <-[JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+58>:
                                                                           r2, #1 ; 0x1
0x00082922 <-[JailbreakDetectionVC
                                                                           r2, pc
                                    jailbreakTest2Tapped:]+62>;
0x00082924 <-[JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+64>:
                                                                           r3,
                                                                   movs
0x00082926 <-[JailbreakDetectionVC</pre>
                                    iailbreakTest2Tapped:1+66>:
                                                                   movt
                                                                           r3, #0 ; 0x0
0x0008292a <-[JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+70>:
                                                                           r3, [sp, #188]
                                                                   strb.w
                                    jailbreakTest2Tapped:]+74>:
                                                                           r2, [r2, #0]
0x0008292e <-[JailbreakDetectionVC
                                                                   ldr
0x00082930 <-[JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+76>:
                                                                           r1, [r1, #0]
0x00082932 <-[JailbreakDetectionVC
                                    iailbreakTest2Tapped:1+78>:
                                                                   str
                                                                           r0, [sp, #172]
0x00082934 <-[JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+80>:
                                                                           r0, r2
                                                                   mov
0x00082936 <-[JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+82>:
                                                                           r2, [sp, #172]
                                    jailbreakTest2Tapped:]+84>:
0x00082938 <-[JailbreakDetectionVC
                                                                   blx
                                                                           r2
0x0008293a <-[JailbreakDetectionVC
                                    jailbreakTest2Tapped:]+86>:
                                                                   mov
0x0008293c <-[JailbreakDetectionVC jailbreakTest2Tapped:]+88>:
                                                                           0x9af54 <dyld_stub_objc_retainAutoreleasedReturnValue>
```

We know that whenever an external method is called or a property is accessed, the *objc_msgSend* function is called. But there are thousands of *objc_msgSend* calls called in any application. We should only be concerned with the *objc_msgSend* calls related to this function. So we find out the addresses of all the instructions that call *objc_msgSend* and set a breakpoint for it. A very simple way to do it is to look for the *blx* instruction, note its address and set a breakpoint for it. A nice trick here would be to note the *blx* instructions from the bottom, note their address, set a breakpoint for them and move upward. This is because we can safely assume that the validation will end somewhere at the end of the function and hence starting from the bottom would make more sense. This is however not guaranteed but let's just do it as it may prove to be a more efficient way.

In this case, let me select the last 5 blx instructions

```
0x00082d72 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1166>:
0x00082d76 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1178>:
0x00082d78 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1178>:
0x00082d78 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1176>:
0x00082d76 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1176>:
0x00082d76 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1176>:
0x00082d80 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1180>:
0x00082d84 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1184>:
0x00082d84 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1184>:
0x00082d86 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1186>:
0x00082d86 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1196>:
0x00082d80 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1196>:
0x00082d90 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1196>:
0x00082d90 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1126>:
0x00082d90 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1206>:
0x00082d90 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1206>:
0x00082d90 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1210>:
0x00082d90 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1210>:
0x00082d90 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1210>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1210>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1212>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1212>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1224>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1224>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1224>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1223>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1223>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1223>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1234>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1244>:
0x00082d00 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1244>:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0.9af44 <dyld_stub_objc_release>
r0, [sp, #40]
0x9af44 <dyld_stub_objc_release>
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   blx
ldr
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         0.000177, 43762

r0, [sp, #20]

r1, r0

r1, #0

0x82d8e <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1194>
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   sxtb
cmp
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           r0, #1
r0, #0 ; 0x0
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   movt
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         r0, #0 ; 0x0
r0, [sp, #188]
r1, #0
r1, #0 ; 0x0
r0, sp, #180
r2, #33638
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      strb.w
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   r2, #33638

r2, #1; 0x1

r2, pc

r2, [r2, #0]

r3, #55734

r3, pc

r9, #56440

r9, #1; 0x1

r9, pc

r9, [r9]

r12, [sp, #188]

r3, [r3, #0]

r0, [sp, #16]

r0, r9, r1, [sp, #12]

r1, [sp, #12]
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         : 0x8366
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   add
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         ; 0xdc78
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      ldrb.w
     0x00082dba <-[JailbreakDetectionVC jailbreakTest2Tapped:]+12385:
0x00082dbc <-[JailbreakDetectionVC jailbreakTest2Tapped:]+1242<:
0x00082dc0 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+12446</pre>
0x00082dc2 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+12465:
0x00082dc4 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+12485:
0x00082dc8 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+12550:
0x00082dcc <-[JailbreakDetectionVC jailbreakTest2Tapped:]+12565:
0x00082dcd <-[JailbreakDetectionVC jailbreakTest2Tapped:]+12565:
0x00082dd2 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+12665:
0x00082dd2 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+12665:
0x00082dd3 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+126665:
0x00082dd3 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+126665:
0x00082dd4 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+126665:
0x00082dd2 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+126665:
0x00082dd2 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+127657:
0x00082de3 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+127657:
0x00082de4 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+127657:
0x00082de4 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+127657:
0x00082de4 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+127657:
0x00082de4 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+127857:
0x00082de7 <-[JailbreakDetectionVC jailbreakTest2Tapped:]+128657:

                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      r3, r3
r3, r12
r2, [sp, #8]
r2, r3
r3, [sp, #8]
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   sxtb.w
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           r0, [sp, #16]
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         r1, [sp, #12]
0x9af60 <dyld_stub_objc_storeStrong>
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      0x9af60 <dyld_stub_objc_storeStrong>
r1, #0; 0x0
r0, sp, #184
0x9af60 <dyld_stub_objc_storeStrong>
r0, sp, #192
r1, #0; 0x0
0x9af60 <dyld_stub_objc_storeStrong>
sp, #204
{r4, r5, r7, pc}
           End of assembler dump. (gdb) ■
```

And set breakpoints for it.

```
End of assembler dump.
(gdb) b *0x00082d78
Breakpoint 2 at 0x82d78
(gdb) b *0x00082dd2
Breakpoint 3 at 0x82dd2
(gdb) b *0x00082dd8
Breakpoint 4 at 0x82dd8
(gdb) b *0x00082de4
Breakpoint 5 at 0x82de4
(gdb) b *0x00082df0
Breakpoint 6 at 0x82df0
(gdb)
```

Now let me continue by using the \mathbf{c} command. As we move through every $objc_msgSend$ instruction one by one, we will print out the registers and see if there is anything of interest. We are printing out the value of rl register with every $objc_msgSend$ call here. Then if there is nothing of interest, we just type \mathbf{c} to continue until the next breakpoint is hit.

Well, there is something of interest almost immediately. We can see a method with the name <code>showAlertForJailbreakTestIsJailbroken:</code> Let's not do anything with this and try to look up this method in the class information file. On looking up this method in the class information file, we can see the full prototype for this method.

```
@interface DamnVulnerableAppUtilities : /Users/Prateek/Desktop/DVIA/DamnVulnerableIOSApp/
DamnVulnerableIOSApp/View Controllers/
{
}
+ (void)addCommonBackgroundImageToViewController:(id)fp8;
+ (void)showAlertWithMessage:(id)fp8;
+ (void)showAlertForJailbreakTestIsJailbroken:(BOOL)fp8;
+ (void)pushWebVCWithURL:(id)fp8 viewController:(id)fp12;
@end
```

This method accepts a Boolean parameter. Well, this is what we have been looking for. All we need to do is pass the Bool parameter *NO* to this method. And if you are familiar with the basics of ARM and GDB, you will know that the first parameter goes inside the r2 register. If you don't understand this concept, I would recommend you read the article on ARM and GDB basics at http://highaltitudehacks.com/2013/11/08/ios-application-security-part-21-arm-and-gdb-basics/

So let's just set the value of r2 to 0 (1 = YES and 0 = NO) and type **c** to continue. Also, let's skip all the breakpoints after this by continuing.

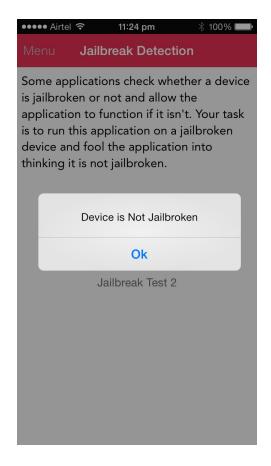
```
warning. Data breakpoint Number at Of Hear att
(gdb) set $r2 = 0
(gdb) c
Continuing.

Breakpoint 4, 0x00082dd8 in -[JailbreakDetectionVC jailbreakTest2Tapped:] ()
(gdb) c
Continuing.

Breakpoint 5, 0x00082de4 in -[JailbreakDetectionVC jailbreakTest2Tapped:] ()
(gdb) c
Continuing.

Breakpoint 6, 0x00082df0 in -[JailbreakDetectionVC jailbreakTest2Tapped:] ()
(gdb) c
Continuing.
```

As you can see on the device, it says "Device is Not Jailbroken" even though our device is jailbroken. We just bypassed the check for a jailbroken device.



Many developers check for a jailbroken device in their application and disable the application if the device is jailbroken. This is because critical applications are subject to risk if the device on which they run is jailbroken. However, no technique for detecting a jailbroken device is foolproof and can be bypassed by using such techniques.