

Array of folded patches

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Tchebyshev array factor design

The design parameters for the array are:

Parameter	Value
# elements	$2N + 1 = 5$
Mean lobe/side lobe ratio	$R = 120 \cong 41.58 \text{ dB}$
Frequency	$f = 2.1 \text{ GHz}$

It's been specifically required to find the optimal inter-element spacing so that the minimum of the beamwidth will be reached:

$$d_{opt} \rightsquigarrow \min\{BW_{fn}\}$$

$$d_{opt} = \lambda \left[1 - \frac{\arccos\left(\frac{1}{\gamma}\right)}{\pi} \right] \quad (1)$$

$$\gamma = \cosh \left[\frac{1}{2N} \ln \left(R + \sqrt{R^2 - 1} \right) \right]$$

where $\lambda = \frac{c}{f}$ is the frequency in the free-space. In this case, $d_{opt} \in \left(\frac{\lambda}{2}, \lambda \right]$, which means that The coefficients a and b related to the Tchebyshev polynomial approximation for the array will be chosen by following the $d_{opt} \in \left(\frac{\lambda}{2}, \lambda \right]$ condition:

$$T_2[x = a + b \cos u] = C_0 + 2C_1 \cos u + C_2 \cos 2u = (2a^2 + b^2 - 1) + 4ab \cos u + b^2 \cos 2u \quad (2)$$

Once the amplitude current feed coefficients are computed ($C_n, n = \overline{0, 2}$), the tapering efficiency can be calculated:

$$\eta_T = \frac{1}{2N + 1} \frac{|C_0 + 2C_1 + C_2|^2}{C_0^2 + 2C_1^2 + C_2^2} \quad (3)$$

Let's consider two cases of uniform spacing array and:

$$\text{Uniform Amplitude (UA)} \quad || \quad \text{Non-uniform Amplitude (NUA, Tchebyshev)} \quad (4)$$

The comparison will show how

$$BW_{fn}^{[UA]} < BW_{fn}^{[NUA]}$$

$$BW_{fn}^{[NUA]} = 2 \frac{180}{\pi} \left[\frac{\pi}{2} - \arccos \left(\frac{\arccos \left(\frac{\cos \left(\frac{\pi}{2N} - a \right)}{b} \right)}{k_0 d} \right) \right] \quad (5)$$

$$BW_{fn}^{[UA]} = \frac{2\lambda}{Nd} \frac{180}{\pi}$$

Parameter	Value		
Feed coefficients [A]	C_0 41.2	$C_1 = C_{-1}$ 29.8	$C_2 = C_{-2}$ 9.6
Normalized feed coefficients to C_{\max}	C_0^* 1.000	$C_1^* = C_{-1}^*$ 0.7215	$C_2^* = C_{-2}^*$ 0.2336
Tapering efficiency	$\eta_T = 79\%$		
Beamwidth	Tchebyshev 50.6°	Uniform 34.8°	

Now, discussing the results is mandatory:

Max/min feed ratio Even if this is the design of a Non-Uniform Amplitude Array, the less the ratio $r_{\max/\min} = \frac{C_{\max}}{C_{\min}}$ is, the more efficient distribution of current is reached. In this particular design:

$$r_{\max/\min} \cong 4.39 \quad (6)$$

meaning that if a damage of the element with the C_{\max} level of feed occurs, most part of the efficiency will be lost. In any case, the tapering efficiency shows how it will not be possible to take advantage of 21 % of the array in an ideal situation, remembering that this design model can be discerned by the real circumstance in terms of the Tchebyshev error [Balanis].

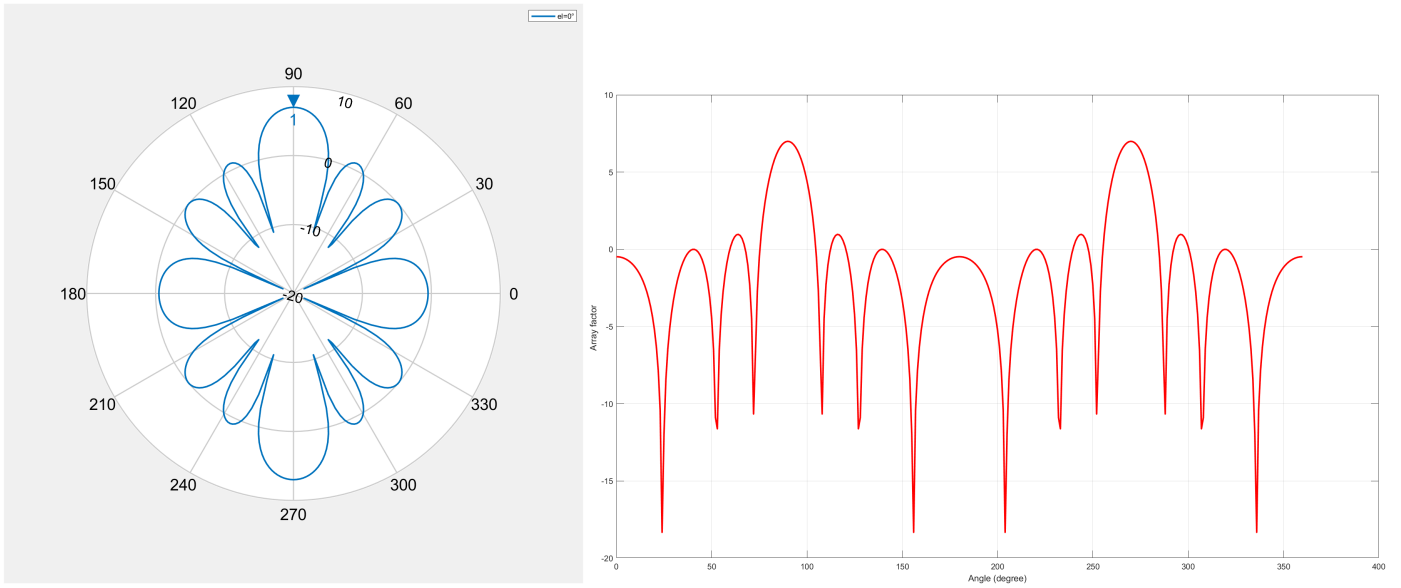
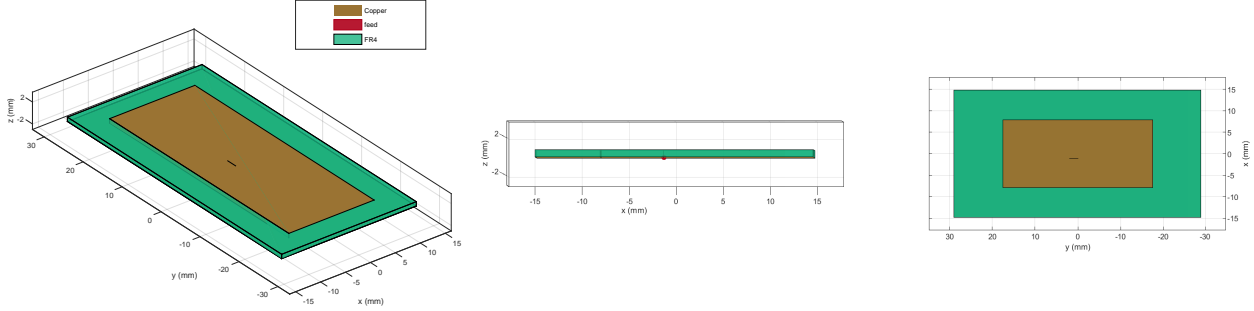


Figure 1: Array factor polar (left) and rectangular (right) diagrams

Rectangular folded patch design

The main components of a rectangular folded patch are: the patch, the substrate (generally accessory, but used in this project), the ground, the rectangular shorting pin between the patch and the ground, and the feed. More details about them will be presented in a short while. Before that, some other remarks are necessary: this antenna will be the element of the array, which will be designed starting actually from a PIFA (*Planar Inverted F Antenna*), given the limitations of the **Antenna Toolbox**, which will be discussed and overcome later on. A general PIFA realized with a dielectric substrate looks like:



By imposing the particular condition by which the width of the rectangular shorting (w_{sc}) equals the patch width size (W_{patch}), the PIFA and the folded patch antenna will be two equivalent structures:

$$W_{patch} = w_{sc} \quad (7)$$

This remark on the PIFA is necessary because generally its structure is not equivalent to that of the folded patch antenna because of the possible variability of the shorting width (w_{sc}), which doesn't always satisfy the above imposed condition. That said, the design requirements are listed below: A preliminary evaluation of the patch parameters have been realized by leaning on a theoretical

Folded patch design parameters	
Parameter/Component	Value/Type/Material
Frequency	2.1 GHz
Matched input resistance	$R_{in} = 50 \Omega$
Substrate	FR4
Relative permittivity	$\epsilon_{FR4} = 4.8$
Relative permeability	$\mu_{FR4} \cong 1$
Loss tangent	$\{\tan(\delta)\}_{FR4} = 0.0260$
Thickness	$h_{FR4} = 0.8 \text{ mm}$
Patch (pre-optimized features)	Copper
Conductivity	$\kappa_{copper} = 5.96 \cdot 10^7 \text{ S/m}$
Thickness	$h_{patch} = 3.556 \cdot 10^{-5} \text{ m}$
Length	$L_{patch} \cong \frac{\lambda_{FR4}}{4} = 0.0171 \text{ m}$
Width	$W_{patch} \cong 0.419 \text{ m}$
Ground (pre-optimized features)	Copper (same conductivity listed above)
Thickness	$h_{GND} = h_{patch}$
Length	$L_{GND} = 0.04 \text{ m}$
Width	$W_{GND} = 0.06 \text{ m}$
Feed	Coaxial cable

set of formulas [Balanis]. That's the reason why the characteristics of the patch shown into the

table are called "*pre-optimized features*" (same thing applies to the ground component). Thus, an optimization process of all those parameters will be performed in some following steps. Just before that, the formulas of the theoretical model will be pointed out:

$$L_{patch} + W_{patch} - w_{sc} = \frac{\lambda_{FR4}}{4} + h_{FR4} \quad (8)$$

$$W_{patch} = \frac{\lambda}{2} \sqrt{\frac{2}{\epsilon_{FR4} + 1}}$$

$$\epsilon_{eff} = \frac{\epsilon_{FR4} + 1}{2} + \frac{\epsilon_{FR4} - 1}{2} \left(1 + 12 \frac{h_{FR4}}{W_{patch}} \right)^{-\frac{1}{2}} \quad (9)$$

$$L_{eff} = \frac{\lambda_{FR4}}{4}$$

$$\Delta L = 0.412 h \left[\frac{(\epsilon_{eff} + 0.3) \left(\frac{W_{patch}}{h_{FR4}} + 0.268 \right)}{(\epsilon_{eff} - 0.258) \left(\frac{W_{patch}}{h_{FR4}} + 0.8 \right)} \right]$$

$$L = L_{eff} - 2\Delta L$$

$$R_r = \frac{120 \lambda}{W_{patch}} \left[1 - \frac{1}{24} \left(2\pi \frac{h_{FR4}}{\lambda} \right)^2 \right]^{-1} \quad (10)$$

$$\Theta_E = 2 \arccos \sqrt{\frac{7.03 \lambda^2}{4 (3 L_e^2 + h_{FR4}^2) \pi^2}} \quad (11)$$

$$\Theta_H = 2 \arccos \sqrt{\frac{1}{2 + 2\pi \frac{W_{patch}}{\lambda}}}$$

$$\ell_{feed} = \frac{L_{patch}}{\pi} \arccos \sqrt{\frac{R_{in}}{R_r}} \quad (12)$$

Where Θ_i ($i = \{E, H\}$) are the half-power beamwidth values given by the E-cut and the H-cut. $\lambda = c/f$ is the free-space wavelength ($c = 299792458 \text{ m/s}$ is the light-speed in the free space). R_{in} is the input impedance (a resistance), while R_r is the radiation resistance

Mesh density refinement

A FR4 substrate thickness of $h_{sub} = 0.8 \text{ mm}$ has been selected so it could be considered as a thin one:

$$\lambda_{sub} = 0.0652 \text{ m} \rightsquigarrow \frac{h_{sub}}{\lambda_{sub}} \cong \frac{1}{81}$$

In case of thin substrates ($h/\lambda \leq 1/50$), the **Antenna Toolbox** suggests to mesh the antenna using dielectric in auto mode. The other two available substrate thicknesses (1.0 mm and 1.6 mm) have not been adopted because the **Antenna Toolbox** reference doesn't give any information about accuracy of the results in case of $h_{sub} \in \left(\frac{\lambda}{50}, \frac{\lambda}{10} \right)$.

Patch parameters

$$L + W - w_{sc} = \frac{\lambda}{4} + h_{sub} \quad (13)$$

$$W = \frac{\lambda_0}{2} \sqrt{\frac{2}{\epsilon_r + 1}}$$

$$BW_E = 2 \arccos \sqrt{\frac{7.03 \lambda_0^2}{4(3L_e^2 + h^2)\pi^2}} \quad (14)$$

$$BW_H = 2 \arccos \sqrt{\frac{1}{2 + k_0 W}} \quad (15)$$

$$\ell_{feed} = \frac{L}{\pi} \arccos \sqrt{\frac{R_{in}}{R_r}} \quad (15)$$

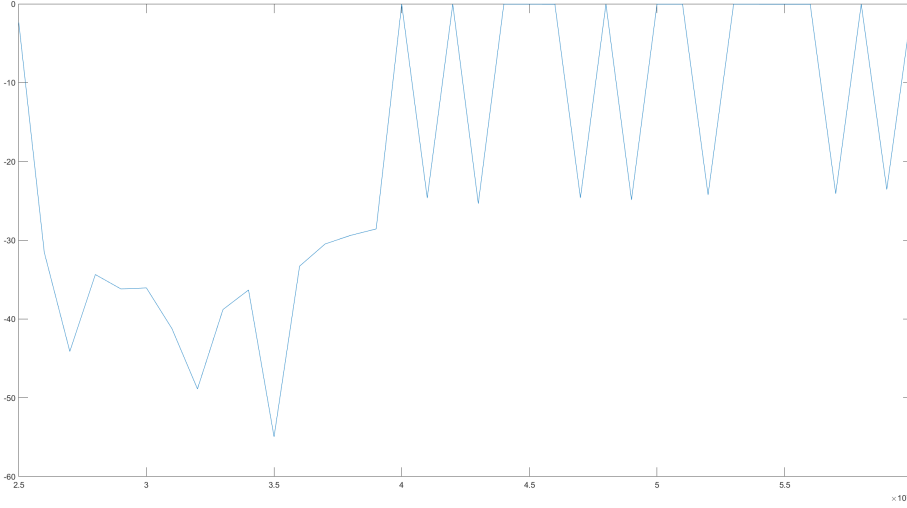


Figure 2: Minimum of the reflection coefficient $\Gamma [dB]$ in the frequency range $2.0 \div 2.2 GHz$ depending on the varying mesh density level

Overall array performance evaluation

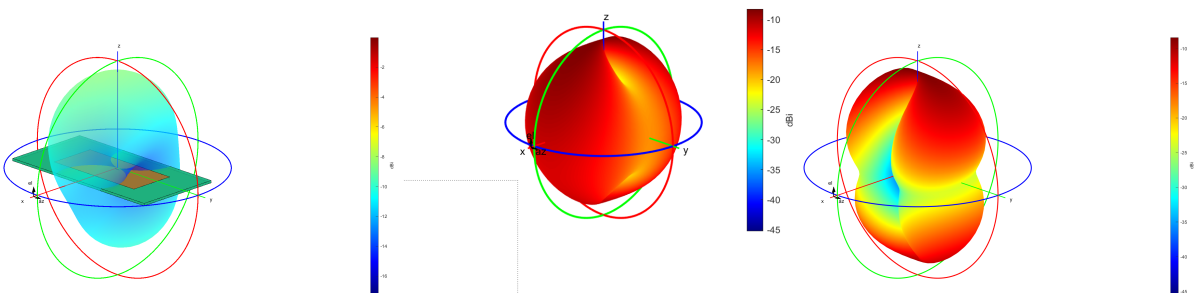


Figure 3: Gain pattern (left), gain pattern with vertical polarization (center) and with the horizontal one (right)

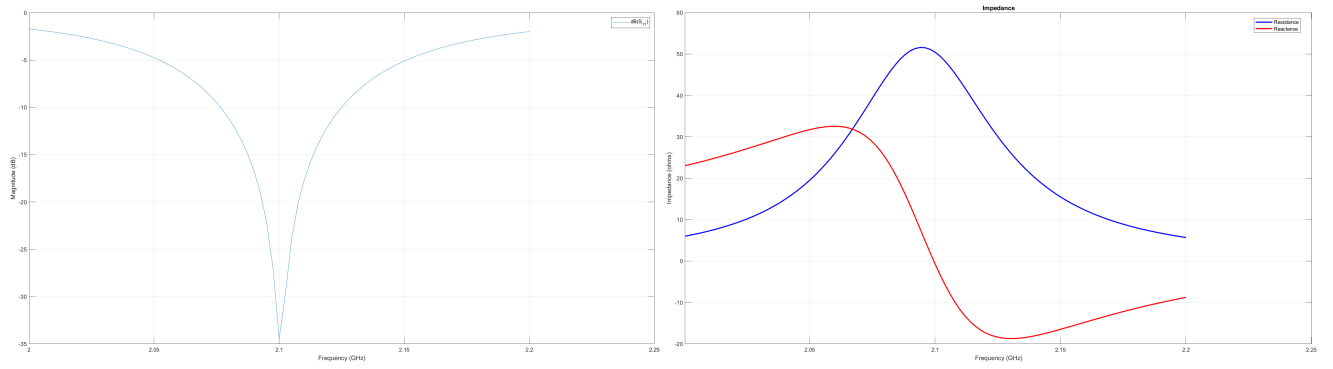


Figure 4: Reflection coefficient (left) and impedances (right) plots depending on $f \in 2.0 \div 2.1 \text{ GHz}$