

```
graph TD; In[ ] --> SW[SOFTWARE]; In --> HW[HARDWARE]; SW --> SI[Software Innovation]; SI --> SD[Software Diversity<br/>Programming languages, execution environment, software platforms, etc]; SD --> Out[ ]; HW --> HI[Hardware Innovation]; HI --> HH[Hardware Heterogeneity<br/>New CPU arch, 2x faster, more RAM, smaller, etc]; HH --> Out; Out --> Out;
```

SOFTWARE

Software Innovation

Software Diversity

Programming languages, execution environment, software platforms, etc

HARDWARE

Hardware Innovation

Hardware Heterogeneity

New CPU arch, 2x faster, more RAM, smaller, etc