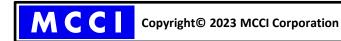
Revision History				
Level	Date	Date Description		
Rev A	28-Oct-2020	Initial Design		
Rev C	01-Feb-2023	Final Revision		

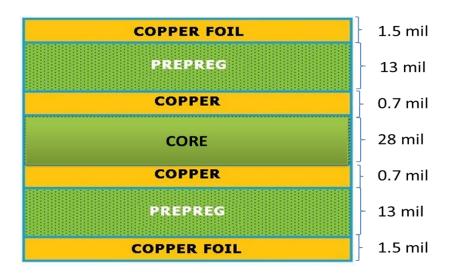
Catena 4801 PCB Layout Fabrication Drawing



Title: Catena 4801 Shield PCB Layout Fabrication Drawing

Date: 01-Feb-2020

Drawing number: 234001117



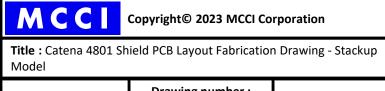
Stackup:

Thickness	Thickness (mm)	Layer	Tolerance	Tolerance (mm)
1 mil	.025	Solder Mask	±0.25mil	±0.01
1.5 mil	.04	1 oz copper	+0.5mil	±0.02
13 mil	.33	FR4 Prepreg		
0.7 mil	.02	0.5 oz copper		
28 mil	.7	FR4 Core		
0.7 mil	.02	0.5 oz copper		
13 mil	.33	FR4 Prepreg		
1.5 mil	.04	1 oz copper	+0.5mil	±0.02
1 mil	.025	Solder Mask	±0.25mil	±0.01

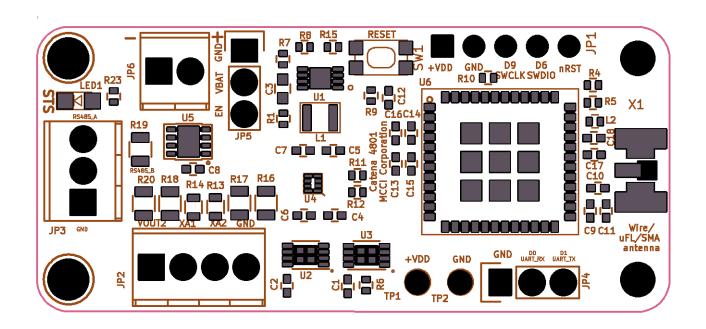
PCB Substrate dielectric constant of 3.95 @ 1GHz

4-Layer PCB Stackup Model

Note : All the Measurements in mils



Date: 01-Feb-2023 Drawing number: 234001117

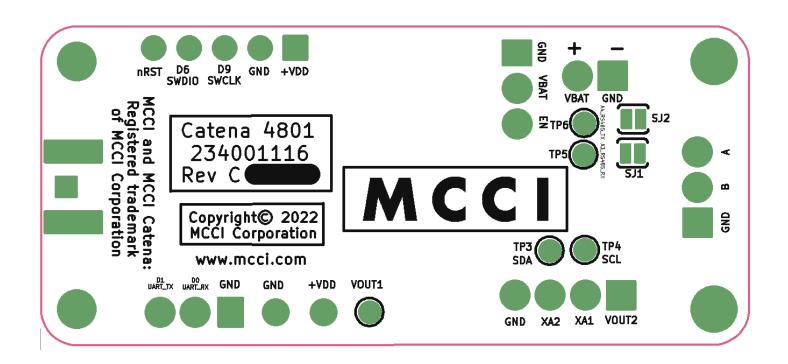


Components Assembly Top

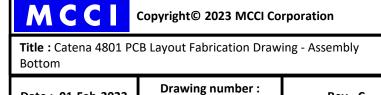


Date: 01-Feb-2023

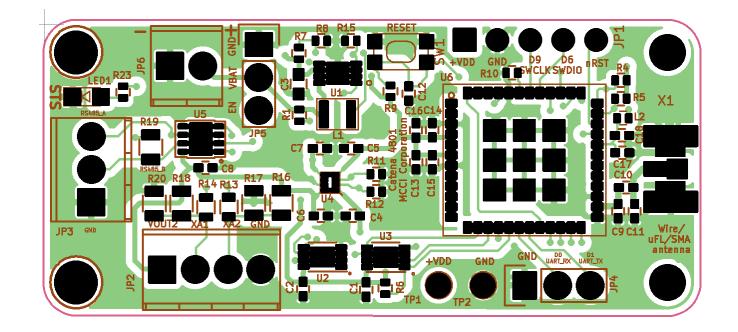
Drawing number: 234001117



Components Assembly Bottom



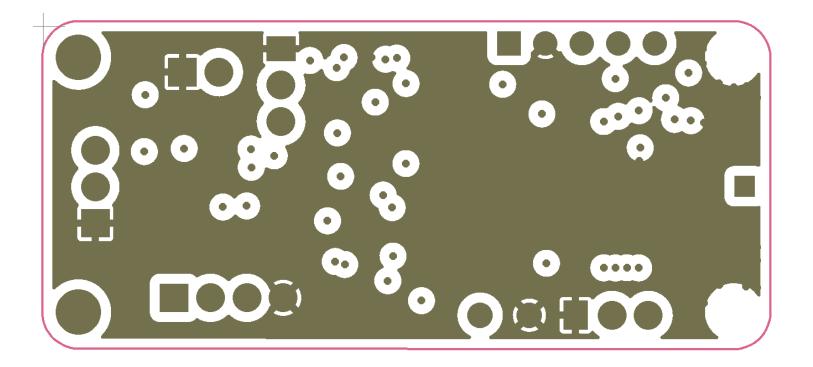
Date: 01-Feb-2023 234001117



Top Layer



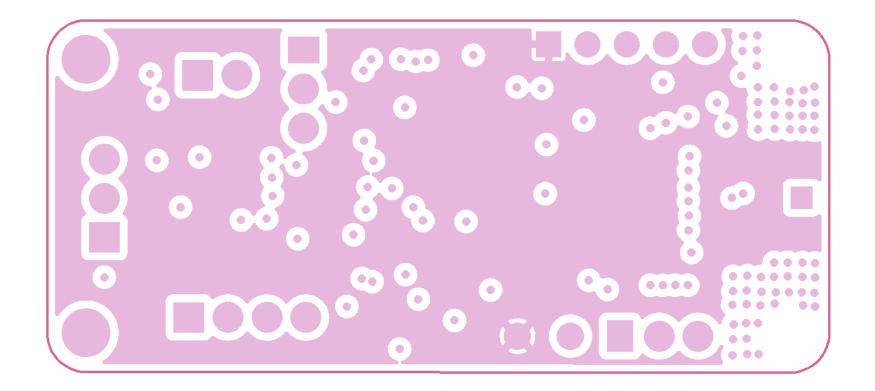
Title : Catena 4801 PCB Layout Fabrication Drawing - Top Layer



Ground Layer



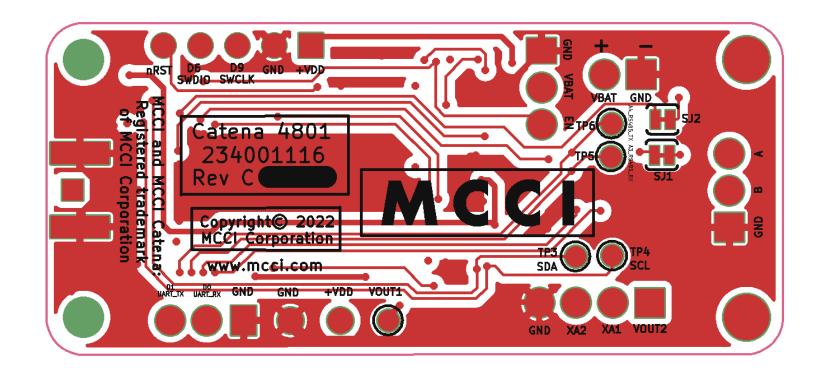
Title: Catena 4801 PCB Layout Fabrication Drawing - Ground Layer



Power Layer



Title: Catena 4801 PCB Layout Fabrication Drawing - Power Layer

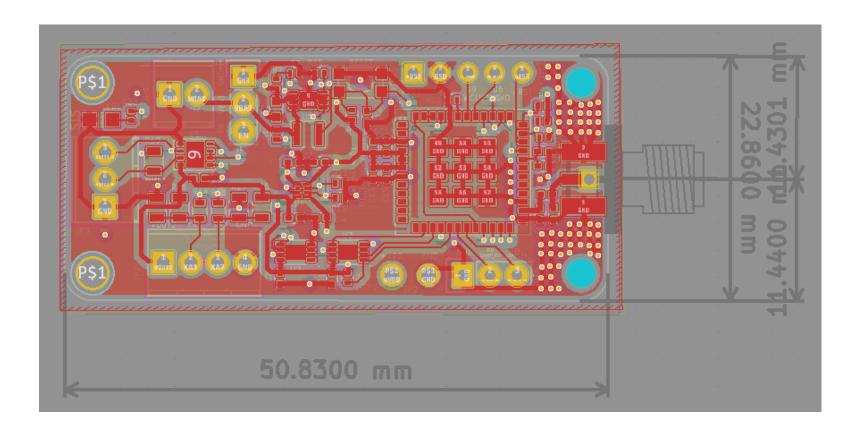


Bottom Layer



Date: 01-Feb-2023

Drawing number: 234001117



Board Dimension

