Revision History				
Level	evel Date Description			
Rev E	01-Feb-2023	Final Revision		

Catena 4610 PCB Layout Fabrication Drawing

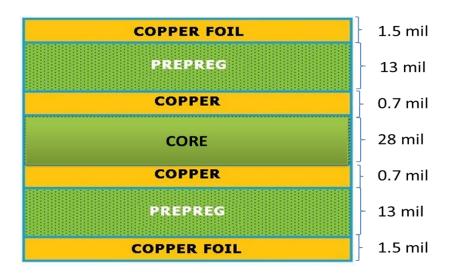


Title: Catena 4610 PCB Layout Fabrication Drawing

Date: 01-Feb-2023

Drawing number: 234001128

Rev - E



Stackup:

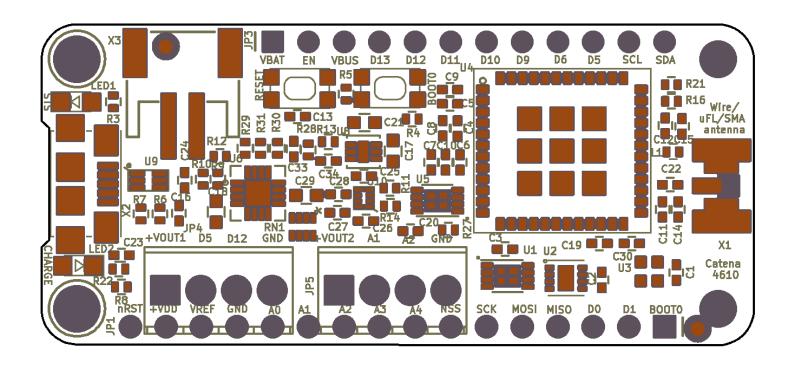
Thickness	Thickness (mm)	Layer	Tolerance	Tolerance (mm
1 mil	.025	Solder Mask	±0.25mil	±0.01
1.5 mil	.04	1 oz copper	+0.5mil	±0.02
13 mil	.33	FR4 Prepreg		
0.7 mil	.02	0.5 oz copper		
28 mil	.7	FR4 Core		
0.7 mil	.02	0.5 oz copper		
13 mil	.33	FR4 Prepreg		
1.5 mil	.04	1 oz copper	+0.5mil	±0.02
1 mil	.025	Solder Mask	±0.25mil	±0.01

PCB Substrate dielectric constant of 3.95 @ 1GHz

4-Layer PCB Stackup Model

Note : All the Measurements in mils





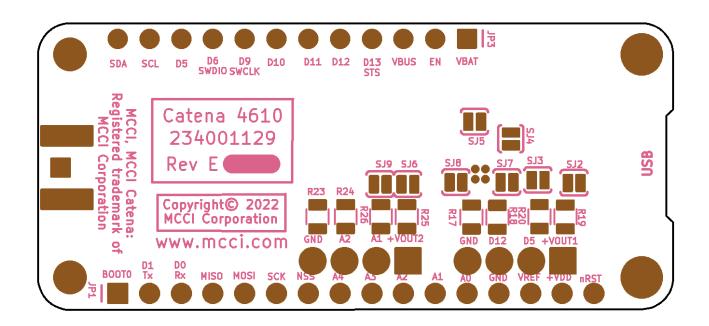
Components Assembly Top



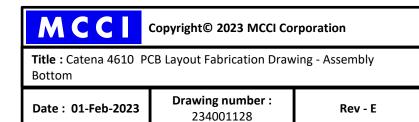
Date: 01-Feb-2023

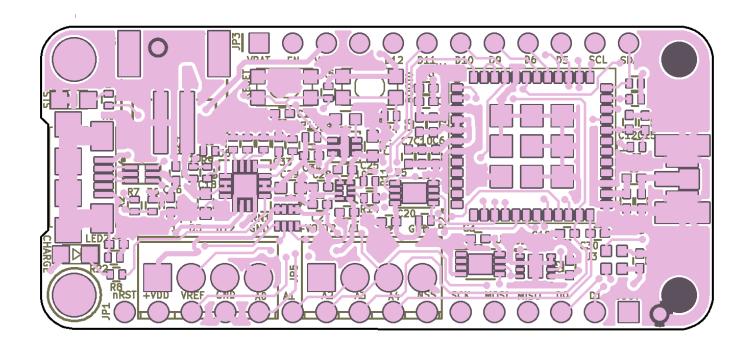
Drawing number: 234001128

Rev - E

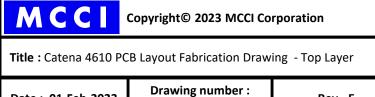


Components Assembly Bottom



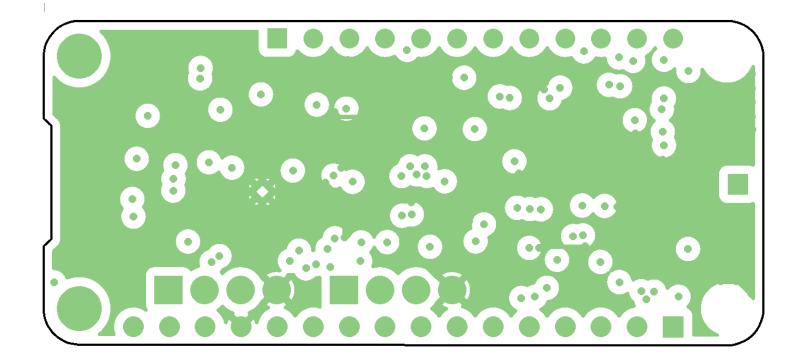


Top Layer



Date: 01-Feb-2023 234001128

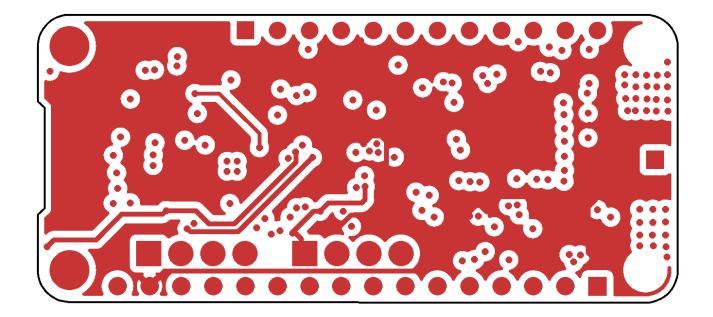
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Ground Layer



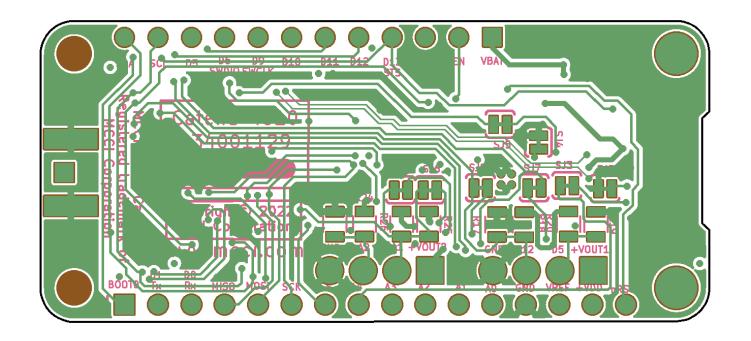
Title: Catena 4610 PCB Layout Fabrication Drawing - Ground Layer



Power Layer



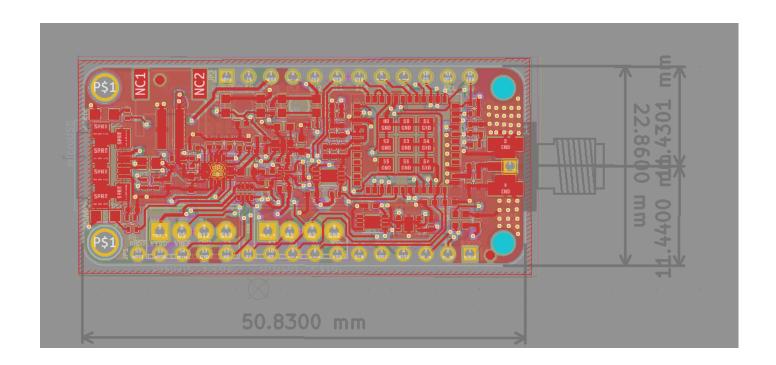
Title: Catena 4610 PCB Layout Fabrication Drawing - Power Layer



Bottom Layer



Title: Catena 4610 PCB Layout Fabrication Drawing - Bottom Layer



Board Dimension

