



**MicroVAX,
NO MCU**

Assemble Block A,
Do Not Assemble R13, R16
(Fake BPOK, BDCOK Signals, no MCU, no LTC)

**PDP11,
NO MCU**

For Fully Analog Option Including LTC Clock (No
Microcontroller): Assemble Blocks A, B.
Do Not Assemble R13, R16

**ALL
PDP11**

For Systems Requiring the LTC Signal (50/60Hz clock), Must
Assemble Blocks B and/or C

**MCU
DOES ALL**

For Fully Digital Option Including Artificial LTC Clock:
Assemble Block C, R13, R16

For preservation of TOY (Time of Year) clock
on the KA630, and/or other battery-backed up
RAM on other CPUs, blocks B and C must be
assembled.
Blocks A and C are mutually exclusive.

Title			H7864-C Schematic
Size	Number	Revision	
A		Sheet 2 of 2	
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