NORTH

VDDIO

RXI_LCLK_P — TXO_LCLK_P
RXI_LCLK_N — TXO_LCLK_N
RXI_FRAME_P — TXO_FRAME_P
RXI_FRAME_N — TXO_FRAME_N
RXO_RD_WAIT_P — TXI_RD_WAIT_P
RXO_RD_WAIT_N — TXI_RD_WAIT_N
RXO_WR_WAIT_P — TXI_WR_WAIT_P
RXO_WR_WAIT_N — TXI_WR_WAIT_N

RXI_DATA_P[0..7] — TXO_DATA_P[0..7]
RXI_DATA_N[0..7] — TXO_DATA_N[0..7]

File: paracard-elink.kicad_sch

SOUTH

VDDIO

RXI_LCLK_P — TXO_LCLK_P
RXI_LCLK_N — TXO_LCLK_N
RXI_FRAME_P — TXO_FRAME_P
RXI_FRAME_N — TXO_FRAME_N
RXO_RD_WAIT_P — TXI_RD_WAIT_P
RXO_RD_WAIT_N — TXI_RD_WAIT_N
RXO_WR_WAIT_P — TXI_WR_WAIT_P
RXO_WR_WAIT_N — TXI_WR_WAIT_N

RXI_DATA_P[0..7] — TXO_DATA_P[0..7]
RXI_DATA_N[0..7] — TXO_DATA_N[0..7]

File: paracard-elink.kicad_sch

MountingHoles

PWR_FLAG — SYS_5P0V

File: paracard-mtg.kicad_sch

P9 (1, 3, 5)
M4
100nF/16V
D1
C1
100uF/6V3
C2
GND

P10 (SATA7-67491-1030)

1	GND	
2	A_SYNC_P	GPIO_N1
3	A_SYNC_N	GPIO_P1
4	GND	
5	B_SYNC_P	GPIO_P8
6	B_SYNC_N	GPIO_N8
7	GND	

GPIO_N[0..23]
GPIO_P[0..23]

GPIO

VGPIO
GPIO_N[0..23]
GPIO_P[0..23]
GPIO[0..47]

File: paracard-gpio.kicad_sch

POWER

SYS_5P0V
DSP_YID[0..3]
DSP_XID[0..3]
REG_EN[1..4]
1P0V
VDD_DSP
1P35V
1P8V
VDD_ADJ
VDD_GPIO
2P5V
3P3V
JTAG_BOOT_EN
JTAG_TDI
JTAG_TDO
JTAG_TMS
JTAG_TCK

I2C_SDA
I2C_SCL
PROG_IO
USER_LED
DSP_FLAG
UART_RX
UART_TX
RESET_N
VADC_P
VADC_N
SPDIF
TURBO_MODE
SPARE

SW1
GND

File: paracard-power.kicad_sch

P5A

ROW A — SAS_mini_1888174-6

GPIO_N0	U17	GPIO_0N	A1
GPIO_P0	T16	GPIO_0P	A3
GPIO_N2	P16	GPIO_2N	A4
GPIO_P2	P15	GPIO_2P	A6
GPIO_N4	R14	GPIO_N4	A7
GPIO_P4	P14	GPIO_P4	A9
GPIO_N10	U12	GPIO_10N	A12
GPIO_P10	T12	GPIO_10P	A14
GPIO_N6	U15	GPIO_6N	A15
GPIO_P6	U14	GPIO_6P	A17

GND: A1, A5, A8, A12, A15, A18
A10, A11 — no connect

P5B

ROW B — SAS_mini_1888174-6

GPIO_P3	U18	GPIO_3P	B17
GPIO_N3	U19	GPIO_3N	B16
GPIO_P11	T11	GPIO_11P	B14
GPIO_N11	U12	GPIO_11N	B13
GPIO_P9	V12	GPIO_9P	B9
GPIO_N9	V13	GPIO_9N	B8
GPIO_P7	W14	GPIO_7P	B6
GPIO_N7	Y14	GPIO_7N	B5
GPIO_P5	T14	GPIO_5P	B3
GPIO_N5	T15	GPIO_5N	B2

GND: B18, B12, B7, B4, B1
B11, B10 — no connect

R1 100
C3 100nF/16V
GND

P6A

ROW A — SAS_mini_1888174-6

GPIO_N12	Y13	GPIO_12N	A1
GPIO_P12	Y12	GPIO_12P	A3
GPIO_N14	V10	GPIO_14N	A4
GPIO_P14	V11	GPIO_14P	A6
GPIO_N16	W9	GPIO_16N	A7
GPIO_P16	W10	GPIO_16P	A9
GPIO_N18	Y9	GPIO_18N	A12
GPIO_P18	W8	GPIO_18P	A14
GPIO_N20	U7	GPIO_20N	A15
GPIO_P20	Y6	GPIO_20P	A17

GND: A1, A5, A8, A12, A15, A18
A10, A11 — no connect

P6B

ROW B — SAS_mini_1888174-6

GPIO_P21	V7	GPIO_21P	B17
GPIO_N21	V6	GPIO_21N	B16
GPIO_P19	Y8	GPIO_19P	B14
GPIO_N19	Y7	GPIO_19N	B13
GPIO_P17	U9	GPIO_17P	B9
GPIO_N17	U8	GPIO_17N	B8
GPIO_P15	T9	GPIO_15P	B6
GPIO_N15	U10	GPIO_15N	B5
GPIO_P13	W11	GPIO_13P	B3
GPIO_N13	Y11	GPIO_13N	B2

GND: B18, B12, B7, B4, B1
B11, B10 — no connect

R2 100
C4 100nF/16V
GND

P6A and P6B Mini-SAS connectors
are available only with Zynq XC7Z020.
Therefore could be used only with
Parallella Embedded (P1602).

Parallella Back Side (Connector Side)

3.40" × 2.15"

GPIO — NORTH — SDCARD — ETHERNET — POWER — SOUTH

Daughtercard Top Side (Connectors on Back)

POWER PAD

GPIO — NORTH — POWER — SOUTH

COMMENT0

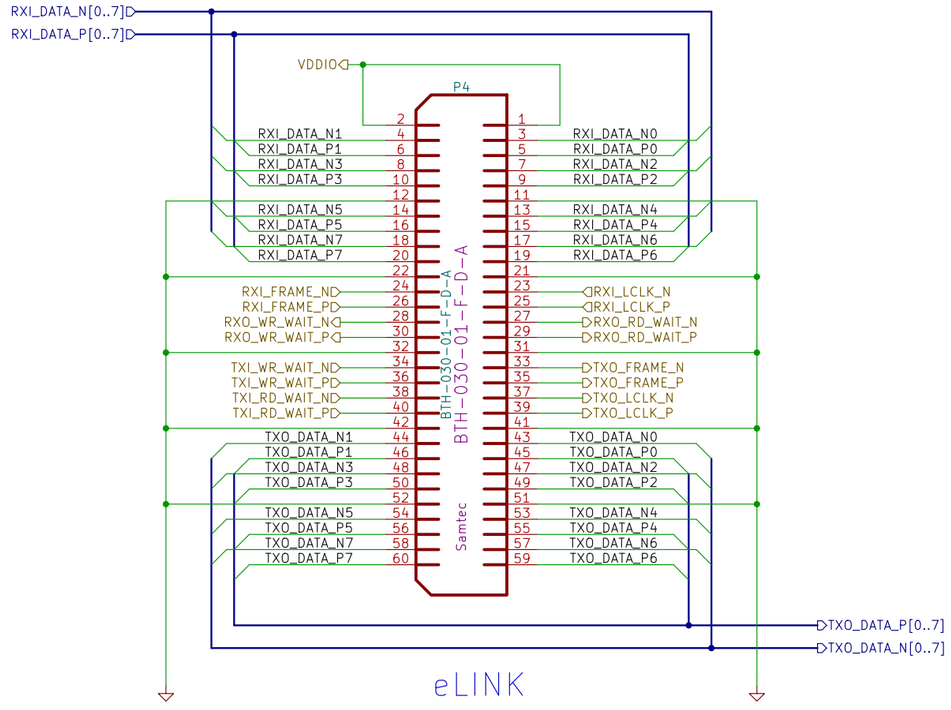
Paralella MLAB interface board

Title: MPPB01B

Author: MLAB.cz

Size: B	Page: 1/6	Date: 2022-03-24

mlab

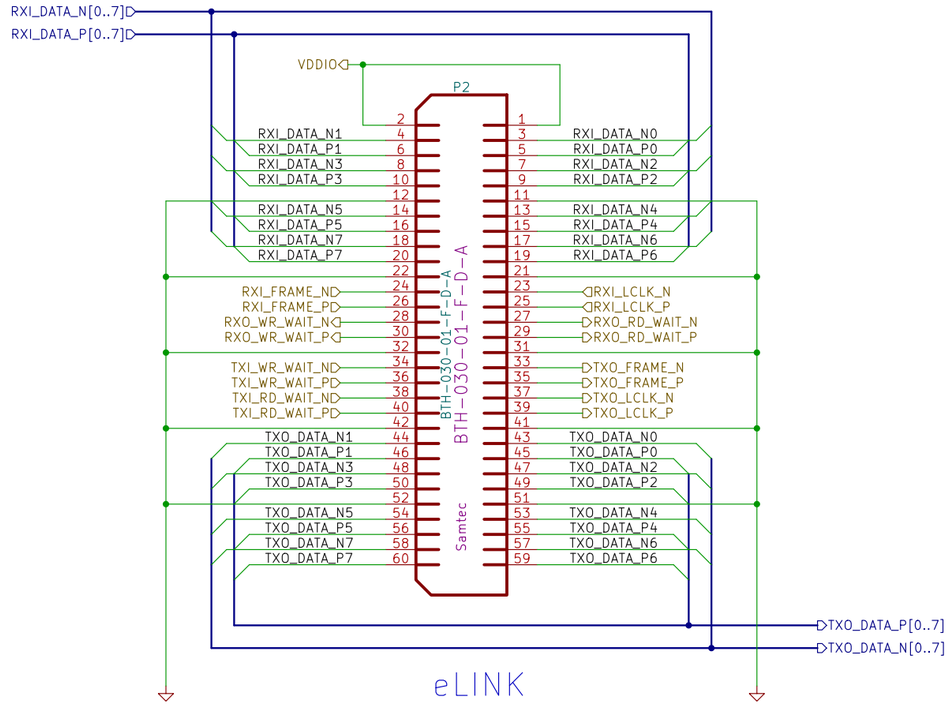


COMMENT0
VERSION 1
Paralella MLAB interface board

Title: MPPB01B

Author: MLAB.cz

Size: B	Page: 2/6	Date: 2022-03-24

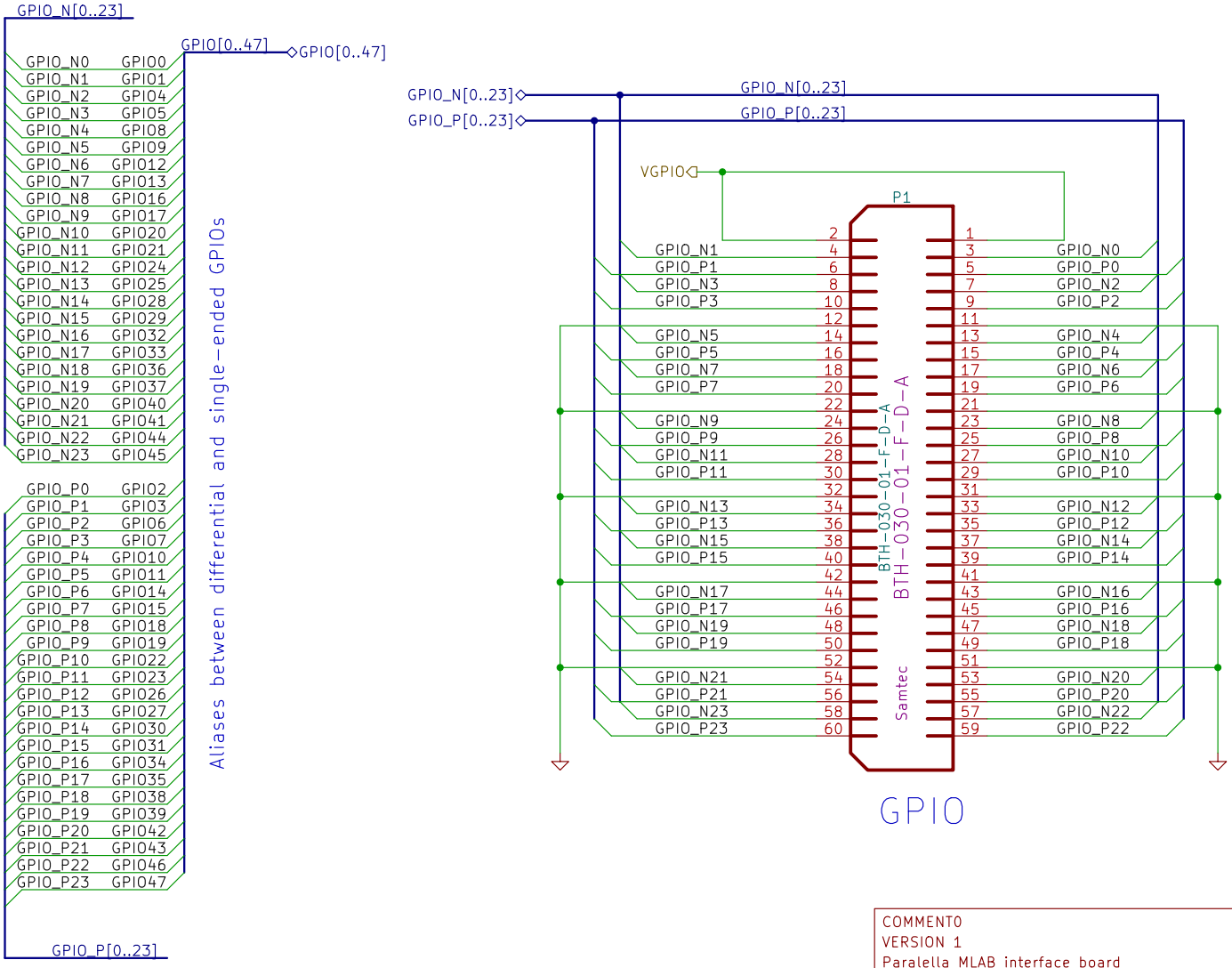


COMMENT0
VERSION 1
Paralella MLAB interface board

Title: MPPB01B

Author: MLAB.cz

Size: B	Page: 3/6	Date: 2022-03-24

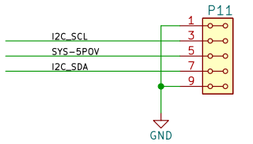


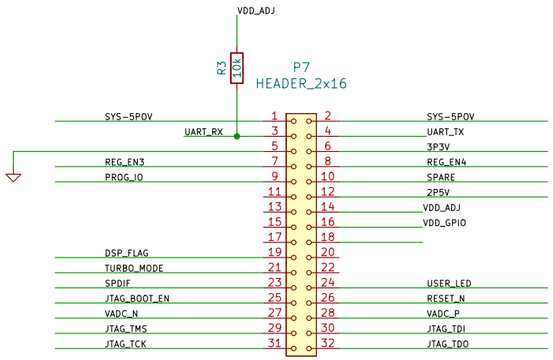
COMMENTO
VERSION 1
Paralella MLAB interface board

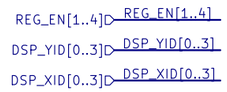
Title: MPPB01B

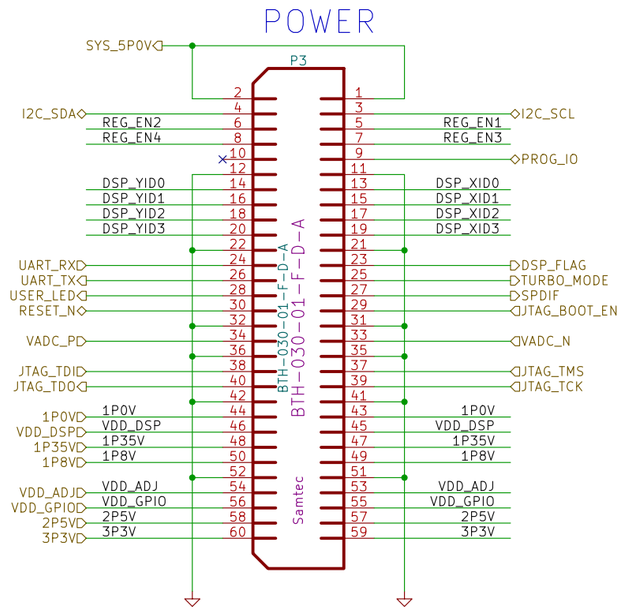
Author: MLAB.cz

Size: A4	Page: 4/6	Date: 2022-03-24







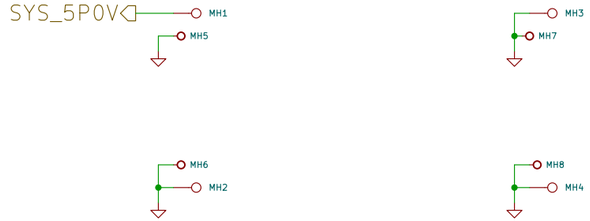


COMMENT0
VERSION 1
Paralella MLAB interface board

Title: MPPB01B

Author: MLAB.cz

Size: B | Page: 5/6 | Date: 2022-03-24



COMMENT0
VERSION 1
Paralella MLAB interface board

Title: MPPB01B

Author: MLAB.cz

Size: B	Page: 6/6	Date: 2022-03-24