NORTH

VDDIO

RXI_LCLK_P	TXO_LCLK_PD
RXI_LCLK_N	TXO_LCLK_ND
RXI_FRAME_P	TXO_FRAME_PD
RXI_FRAME_N	TXO_FRAME_ND
RXO_RD_WAIT_P	TXI_RD_WAIT_P
RXO_RD_WAIT_N	TXI_RD_WAIT_N
RXO_WR_WAIT_P	TXI_WR_WAIT_P
RXO_WR_WAIT_N	TXI_WR_WAIT_N
RXI_DATA_P[0..7]	TXO_DATA_P[0..7]
RXI_DATA_N[0..7]	TXO_DATA_N[0..7]

File: paracard-elink.kicad_sch

SOUTH

VDDIO

RXI_LCLK_P	TXO_LCLK_PD
RXI_LCLK_N	TXO_LCLK_ND
RXI_FRAME_P	TXO_FRAME_PD
RXI_FRAME_N	TXO_FRAME_ND
RXO_RD_WAIT_P	TXI_RD_WAIT_P
RXO_RD_WAIT_N	TXI_RD_WAIT_N
RXO_WR_WAIT_P	TXI_WR_WAIT_P
RXO_WR_WAIT_N	TXI_WR_WAIT_N
RXI_DATA_P[0..7]	TXO_DATA_P[0..7]
RXI_DATA_N[0..7]	TXO_DATA_N[0..7]

File: paracard-elink.kicad_sch

MountingHoles

PWR_FLAG — SYS-5P0V — SYS_5P0V

File: paracard-mtg.kicad_sch

P9 — M4 — D1 — C1 100nF/16V — C2 100uF/6V3 — GND

P10 — SATA7-67491-1030

1	GND	
2	A_SYNC_P	GPIO_N1
3	A_SYNC_N	GPIO_P1
4	GND	
5	B_SYNC_P	GPIO_P8
6	B_SYNC_N	GPIO_N8
7	GND	

GPIO

VGPIO

GPIO_N[0..23]
GPIO_P[0..23]
GPIO[0..47]

File: paracard-gpio.kicad_sch

POWER

SYS-5P0V	SYS_5P0V	I2C_SDA	I2C_SDA
		I2C_SCL	I2C_SCL
DSP_YID[0..3]	DSP_YID[0..3]	PROG_IO	PROG_IO
DSP_XID[0..3]	DSP_XID[0..3]	USER_LED	USER_LED
REG_EN[1..4]	REG_EN[1..4]	DSP_FLAG	DSP_FLAG
1P0V	1P0V		
VDD_DSP	VDD_DSP	UART_RX	UART_RX
1P35V	1P35V	UART_TX	UART_TX
1P8V	1P8V		
VDD_ADJ	VDD_ADJ	RESET_N	RESET_N
VDD_GPIO	VDD_GPIO		
2P5V	2P5V	VADC_P	VADC_P
3P3V	3P3V	VADC_N	VADC_N
JTAG_BOOT_EN	JTAG_BOOT_EN	SPDIF	SPDIF
JTAG_TDI	JTAG_TDI		
JTAG_TDO	JTAG_TDO	TURBO_MODE	TURBO_MODE
JTAG_TMS	JTAG_TMS	SPARE	SPARE
JTAG_TCK	JTAG_TCK		

File: paracard-power.kicad_sch

SW1 — GND

P5A / P5B — SAS_mini_1888174-6

P5A ROW A			P5B ROW B		
GPIO_N0	U17	GPIO_0N	GPIO_P3	U18	GPIO_3P
GPIO_P0	T16	GPIO_0P	GPIO_N3	U19	GPIO_3N
GPIO_N2	P16	GPIO_2N	GPIO_P11	T11	GPIO_11P
GPIO_P2	P15	GPIO_2P	GPIO_N11	U12	GPIO_11N
GPIO_N4	R14	GPIO_N4	GPIO_P9	V12	GPIO_9P
GPIO_P4	P14	GPIO_P4	GPIO_N9	V13	GPIO_9N
GPIO_N10	U12	GPIO_10N	GPIO_P7	W14	GPIO_7P
GPIO_P10	T12	GPIO_10P	GPIO_N7	Y14	GPIO_7N
GPIO_N6	U15	GPIO_6N	GPIO_P5	T14	GPIO_5P
GPIO_P6	U14	GPIO_6P	GPIO_N5	T15	GPIO_5N

R1 100 — C3 100nF/16V — GND

P6A / P6B — SAS_mini_1888174-6

P6A ROW A			P6B ROW B		
GPIO_N12	Y13	GPIO_12N	GPIO_P21	V7	GPIO_21P
GPIO_P12	Y12	GPIO_12P	GPIO_N21	V6	GPIO_21N
GPIO_N14	V10	GPIO_14N	GPIO_P19	Y8	GPIO_19P
GPIO_P14	V11	GPIO_14P	GPIO_N19	Y7	GPIO_19N
GPIO_N16	W9	GPIO_16N	GPIO_P17	U9	GPIO_17P
GPIO_P16	W10	GPIO_16P	GPIO_N17	U8	GPIO_17N
GPIO_N18	Y9	GPIO_18N	GPIO_P15	T9	GPIO_15P
GPIO_P18	W8	GPIO_18P	GPIO_N15	U10	GPIO_15N
GPIO_N20	U7	GPIO_20N	GPIO_P13	W11	GPIO_13P
GPIO_P20	Y6	GPIO_20P	GPIO_N13	Y11	GPIO_13N

R2 100 — C4 100nF/16V — GND

P6A and P6B Mini-SAS connectors
are available only with Zynq XC7Z020.
Therefore could be used only with
Parallella Embedded (P1602).

Parallella Back Side (Connector Side)

3.40" × 2.15"

DC: GPIO (0 59 60 1 2) — NORTH (59 60 1 0 2) — SDCARD

ETHERNET: POWER (59 0 60 1 2) — SOUTH (59 60 1 2 0)

POWER PAD

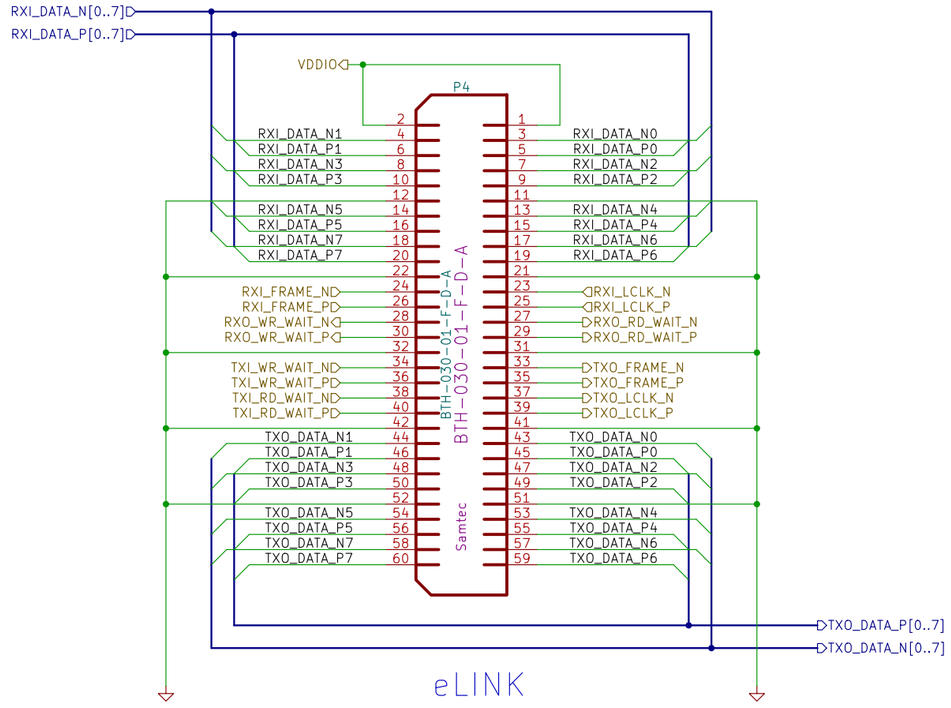
Daughtercard Top Side (Connectors on Back)

GPIO (0 59 60 1 2) — NORTH (59 60 1 0 2)

POWER (59 0 60 1 2) — SOUTH (59 60 1 2 0)

COMMENT0	Paralella MLAB interface board
Title: MPPB01B	
Author: MLAB.cz	
Size: B	Page: 1/6
Date: 2022-03-24	

mlab

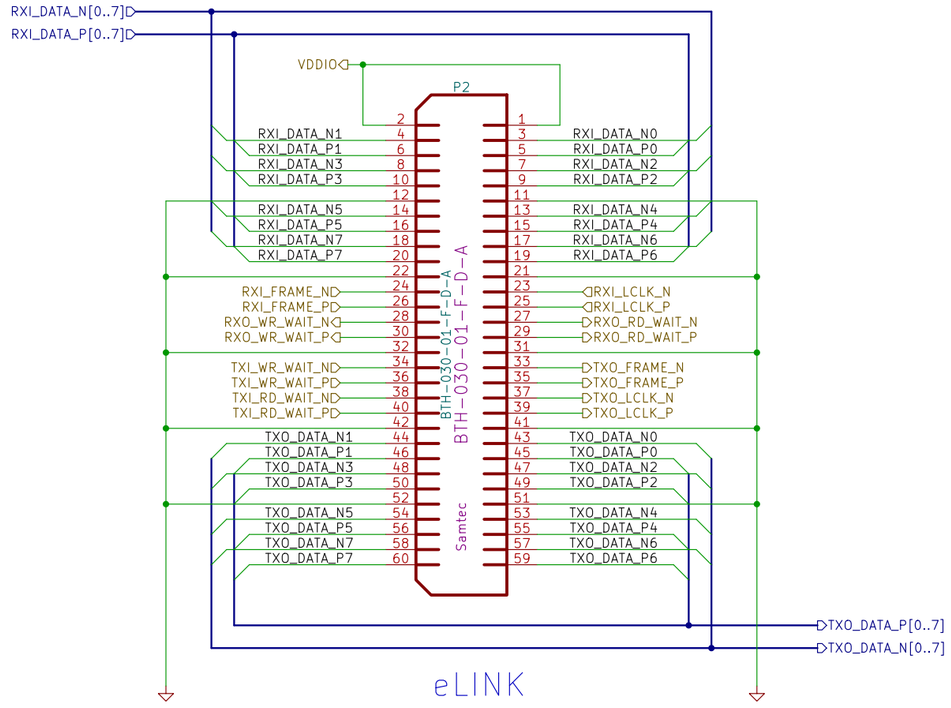


COMMENT0
VERSION 1
Paralella MLAB interface board

Title: MPPB01B

Author: MLAB.cz

Size: B	Page: 2/6	Date: 2022-03-24

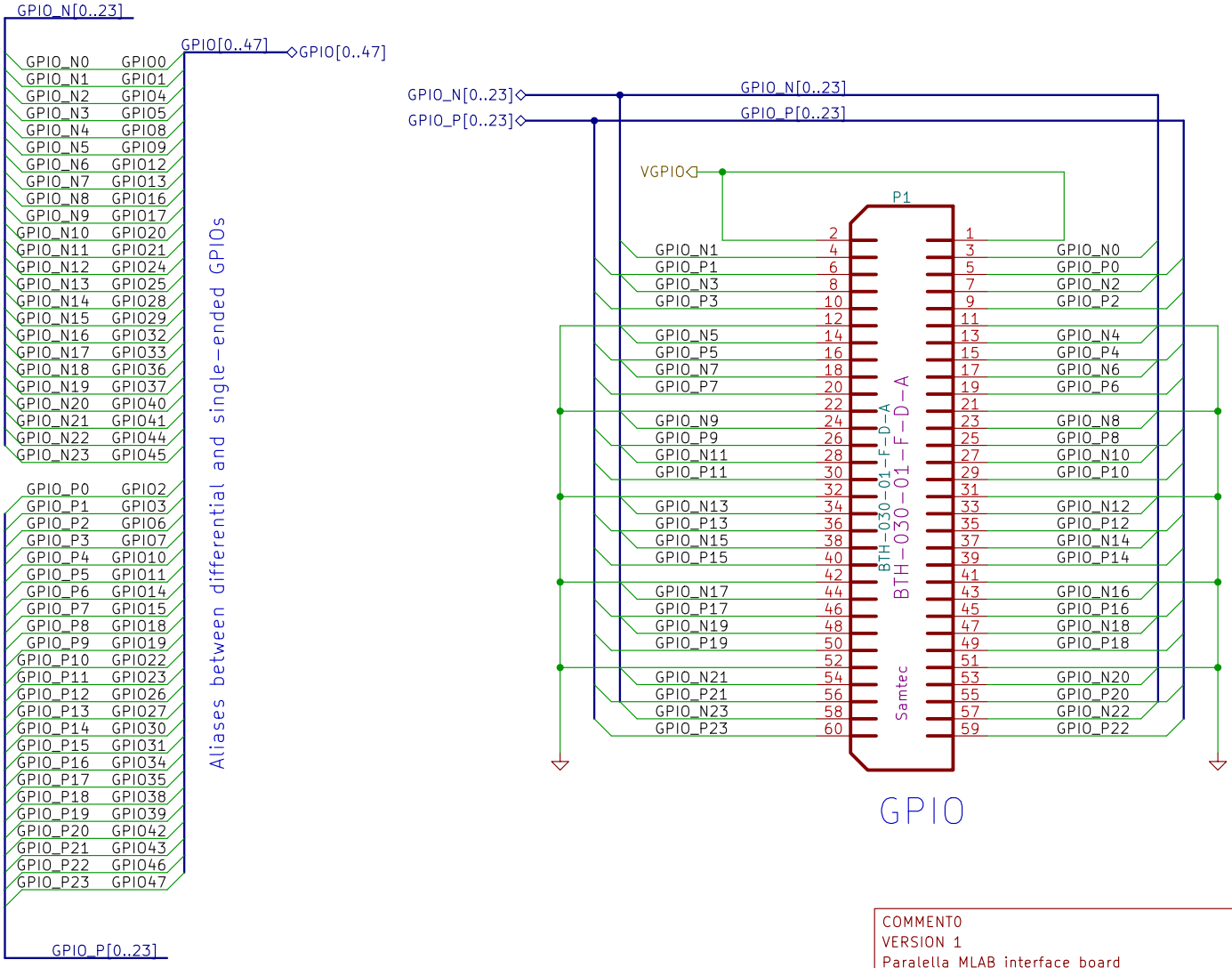


COMMENT0
VERSION 1
Paralella MLAB interface board

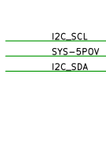
Title: MPPB01B

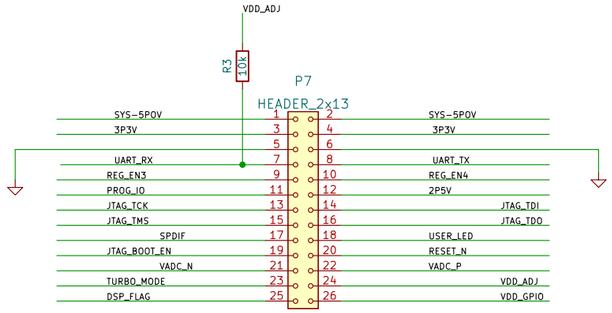
Author: MLAB.cz

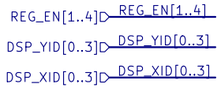
Size: B	Page: 3/6	Date: 2022-03-24

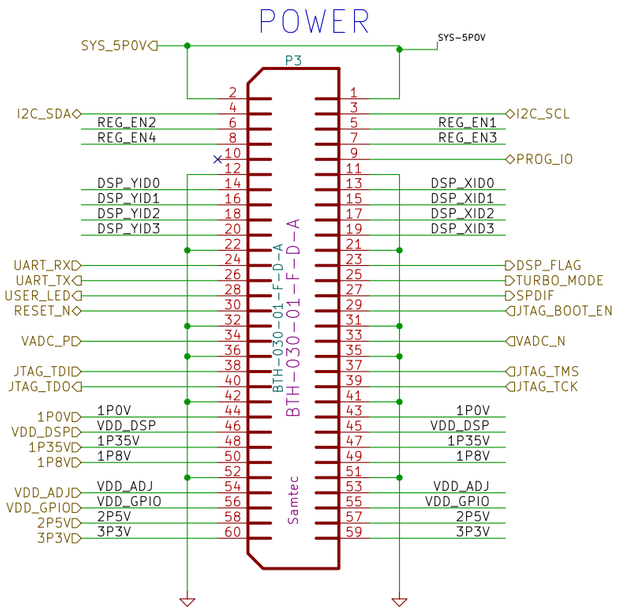


COMMENTO VERSION 1 Paralella MLAB interface board		
Title: MPPB01B Author: MLAB.cz		
Size: A4	Page: 4/6	Date: 2022-03-24







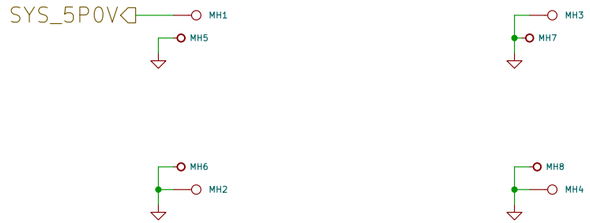


COMMENT0
VERSION 1
Paralella MLAB interface board

Title: MPPB01B

Author: MLAB.cz

Size: B | Page: 5/6 | Date: 2022-03-24



COMMENT0
VERSION 1
Paralella MLAB interface board

Title: MPPB01B

Author: MLAB.cz

Size: B	Page: 6/6	Date: 2022-03-24