

NORTH

QVDDIO

DRXLCLK_P	TXO_LCLK_PD
DRXLCLK_N	TXO_LCLK_ND
DRXLFRAME_P	TXO_FRAME_PD
DRXLFRAME_N	TXO_FRAME_ND
QRXO_RD_WAIT_P	TXL_RD_WAIT_PD
QRXO_RD_WAIT_N	TXL_RD_WAIT_ND
QRXO_WR_WAIT_P	TXL_WR_WAIT_PD
QRXO_WR_WAIT_N	TXL_WR_WAIT_ND

DRXL_DATA_P[0..7] TXO_DATA_P[0..7]
DRXL_DATA_N[0..7] TXO_DATA_N[0..7]

File: paracard-elink.kicad_sch

SOUTH

QVDDIO

DRXLCLK_P	TXO_LCLK_PD
DRXLCLK_N	TXO_LCLK_ND
DRXLFRAME_P	TXO_FRAME_PD
DRXLFRAME_N	TXO_FRAME_ND
QRXO_RD_WAIT_P	TXL_RD_WAIT_PD
QRXO_RD_WAIT_N	TXL_RD_WAIT_ND
QRXO_WR_WAIT_P	TXL_WR_WAIT_PD
QRXO_WR_WAIT_N	TXL_WR_WAIT_ND

DRXL_DATA_P[0..7] TXO_DATA_P[0..7]
DRXL_DATA_N[0..7] TXO_DATA_N[0..7]

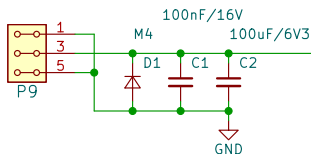
File: paracard-elink.kicad_sch

MountingHoles

PWR_FLAG <SYS-5P0V

QSYS_5P0V

File: paracard-mtg.kicad_sch



GPIO

QVGPIO

GPIO_N[0..23]
GPIO_P[0..23]
GPIO[0..47]

File: paracard-gpio.kicad_sch

POWER

SYS-5P0V <SYS_5P0V

DSP_YID[0..3] >DSP_YID[0..3]
DSP_XID[0..3] >DSP_XID[0..3]

REG_EN[1..4] >REG_EN[1..4]

1P0V >1P0V
VDD_DSP >VDD_DSP
1P35V >1P35V
1P8V >1P8V
VDD_ADJ >VDD_ADJ
VDD_GPIO >VDD_GPIO
2P5V >2P5V
3P3V >3P3V

JTAG_BOOT_EN >JTAG_BOOT_EN
JTAG_TDI >JTAG_TDI
JTAG_TDO >JTAG_TDO
JTAG_TMS >JTAG_TMS
JTAG_TCK >JTAG_TCK

I2C_SDA <I2C_SDA
I2C_SCL <I2C_SCL

PROG_IO <PROG_IO
USER_LEDD <USER_LED
DSP_FLAGD <DSP_FLAG

UART_RX <UART_RX
UART_TXD <UART_TX

RESET_N <RESET_N

VADC_P <VADC_P
VADC_N <VADC_N

SPDIFD <SPDIF

TURBO_MODED <TURBO_MODE
SPARE <SPARE

File: paracard-power.kicad_sch

GPIO_N[0..23]
GPIO_P[0..23]

GPIO_N0 U17 GPIO_0N
GPIO_P0 T16 GPIO_0P

GPIO_N2 P16 GPIO_2N
GPIO_P2 P15 GPIO_2P

GPIO_N4 R14 GPIO_4N
GPIO_P4 P14 GPIO_4P

GPIO_N10 U12 GPIO_10N
GPIO_P10 T12 GPIO_10P

GPIO_N6 U15 GPIO_6N
GPIO_P6 U14 GPIO_6P

GPIO_P3 U18 GPIO_3P
GPIO_N3 U19 GPIO_3N

GPIO_P11 T11 GPIO_11P
GPIO_N11 U12 GPIO_11N

GPIO_P9 V12 GPIO_9P
GPIO_N9 V13 GPIO_9N

GPIO_P7 W14 GPIO_7P
GPIO_N7 Y14 GPIO_7N

GPIO_P5 T14 GPIO_5P
GPIO_N5 T15 GPIO_5N

GPIO_N12 Y13 GPIO_12N
GPIO_P12 Y12 GPIO_12P

GPIO_N14 V10 GPIO_14N
GPIO_P14 V11 GPIO_14P

GPIO_N16 W9 GPIO_16N
GPIO_P16 W10 GPIO_16P

GPIO_N18 Y9 GPIO_18N
GPIO_P18 W8 GPIO_18P

GPIO_N20 U7 GPIO_20N
GPIO_P20 Y6 GPIO_20P

GPIO_P21 V7 GPIO_21P
GPIO_N21 V6 GPIO_21N

GPIO_P19 Y8 GPIO_19P
GPIO_N19 Y7 GPIO_19N

GPIO_P17 U9 GPIO_17P
GPIO_N17 U8 GPIO_17N

GPIO_P15 T9 GPIO_15P
GPIO_N15 U10 GPIO_15N

GPIO_P13 W11 GPIO_13P
GPIO_N13 Y11 GPIO_13N

P5A

ROW A

SAS_mini_1888174-6

A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18

P5B

ROW B

SAS_mini_1888174-6

B1 B2 B3 B4 B5 B6 B7 B8 B9 B10 B11 B12 B13 B14 B15 B16 B17 B18

P6A

ROW A

SAS_mini_1888174-6

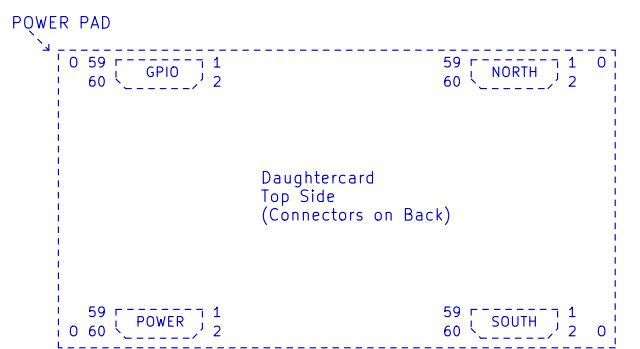
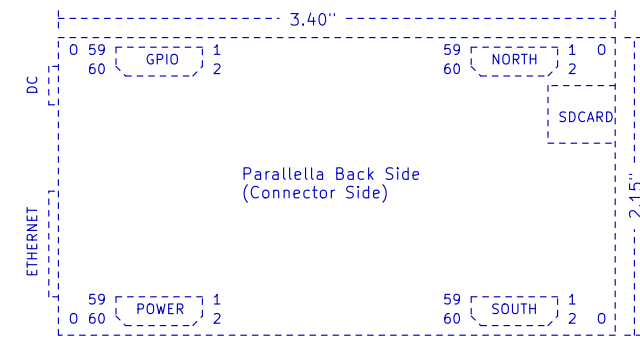
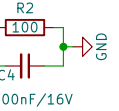
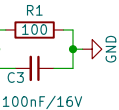
A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15 A16 A17 A18

P6B

ROW B

SAS_mini_1888174-6

B1 B2 B3 B4 B5 B6 B7 B8 B9 B10 B11 B12 B13 B14 B15 B16 B17 B18



P6A and P6B Mini-SAS connectors are available only with Zynq XC7Z020. Therefore could be used only with Parallella Embedded (P1602).

COMMENTO

Parallella MLAB interface board

Title: MPPB01B

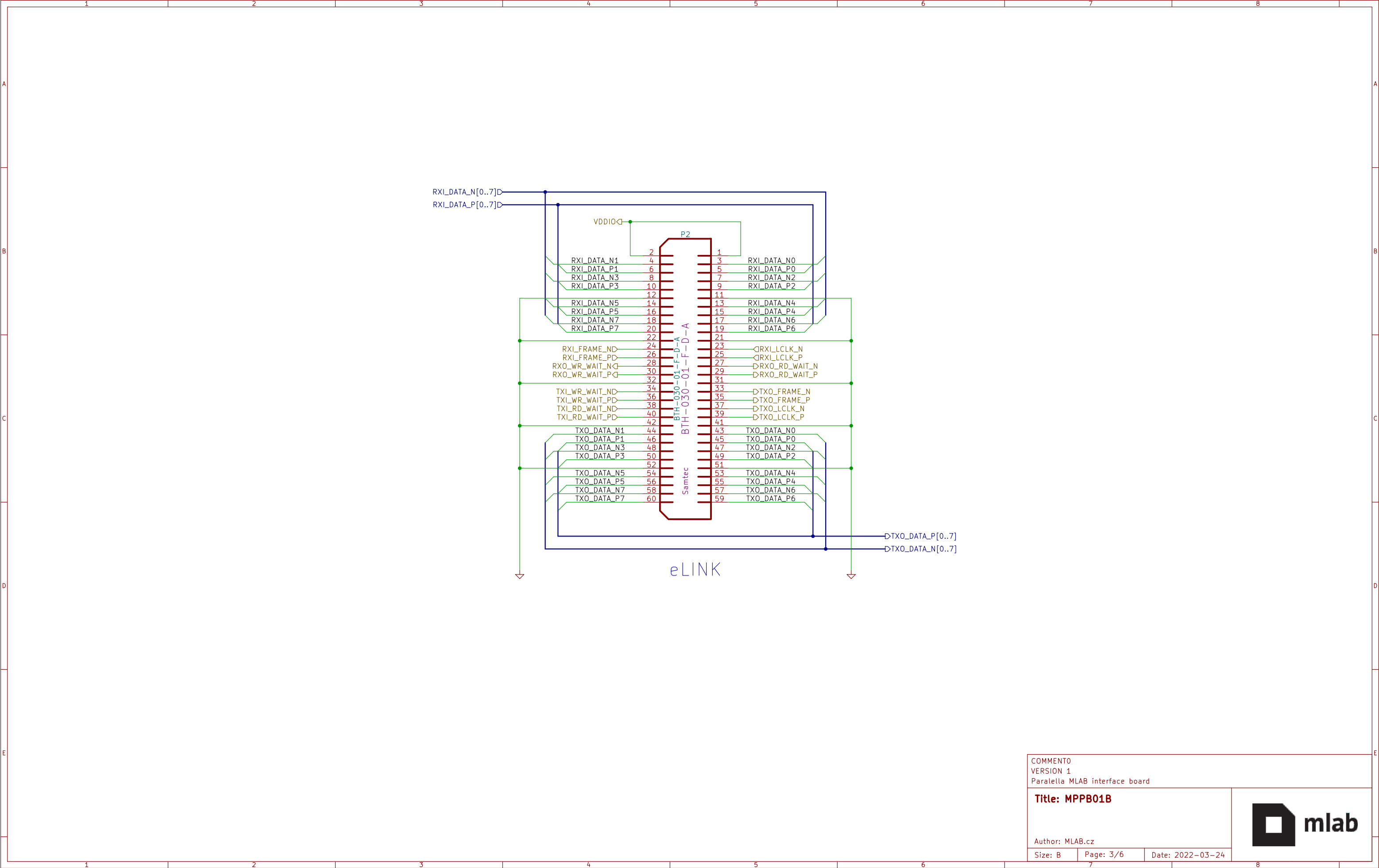
Author: MLAB.cz

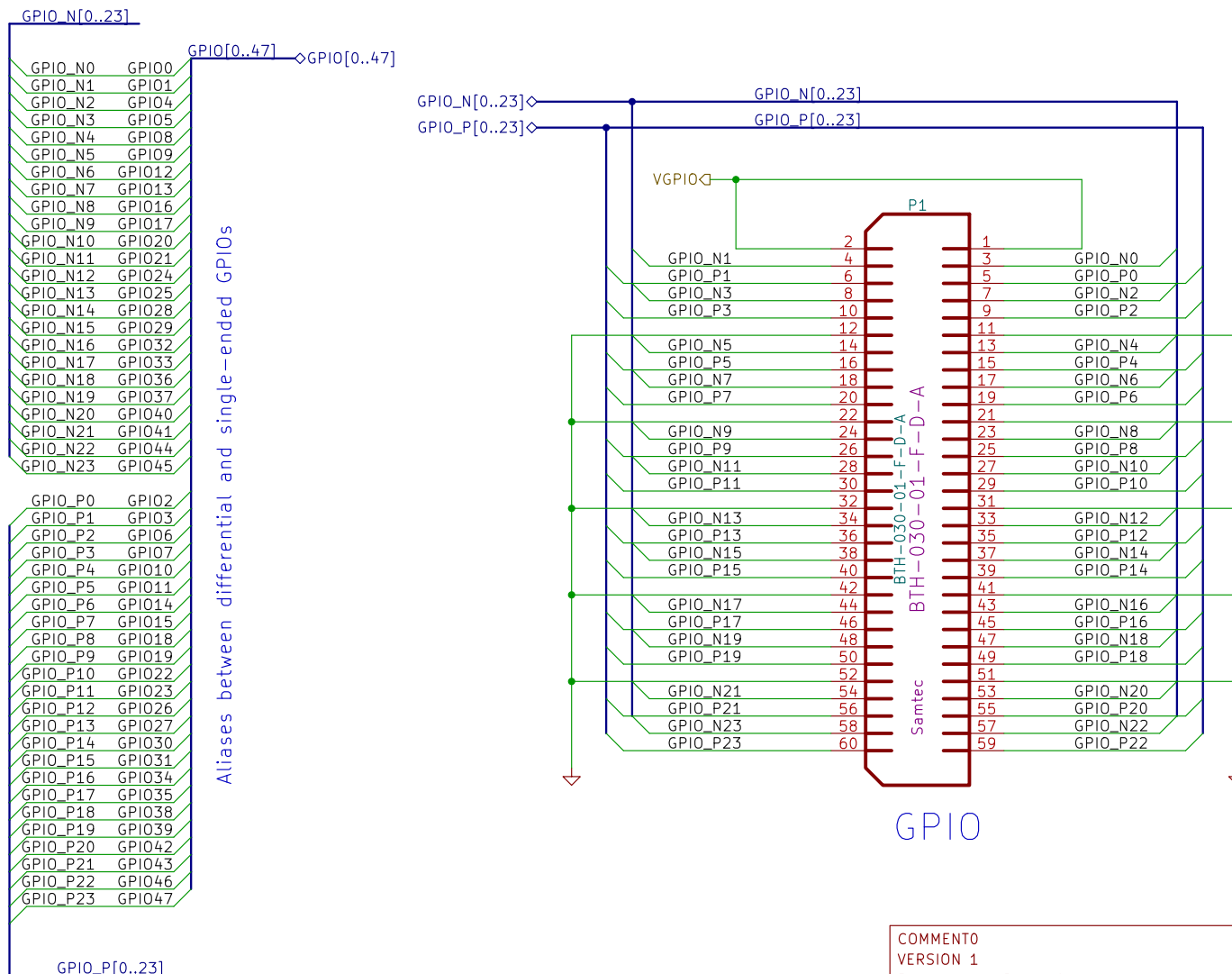
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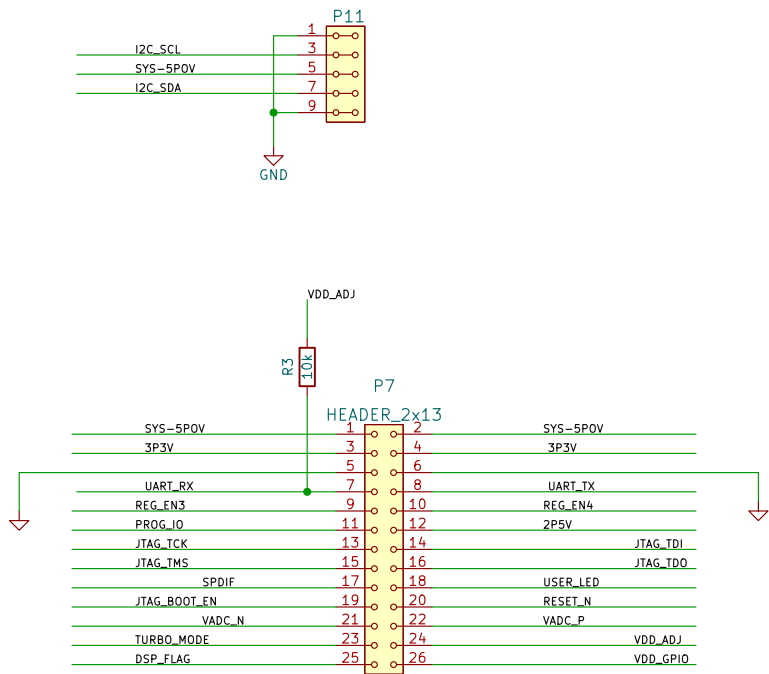
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Date: 2022-03-24

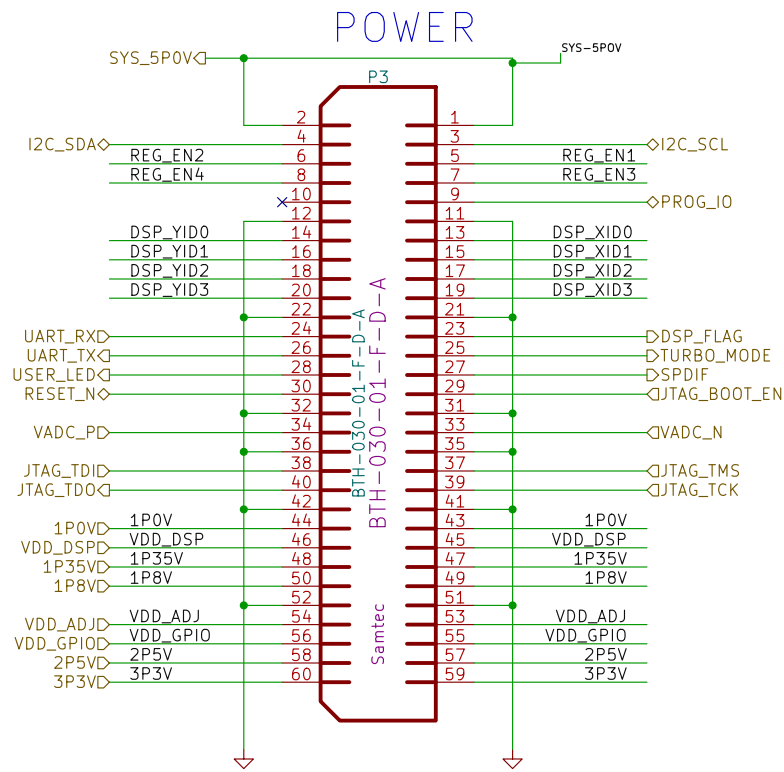








REG_EN[1..4] → REG_EN[1..4]
DSP_YID[0..3] → DSP_YID[0..3]
DSP_XID[0..3] → DSP_XID[0..3]



COMMENTO
VERSION 1
Paralella MLAB interface board

Title: MPPB01B

Author: MLAB.cz

Size: B

Page: 5/6

Date: 2022-03-24



