

Programs:

- DIA, INKSCAPE (SCHEMATICS)
- WAVEDROM (TIMINGS)

Font: Comic Sans

Colors:

- Black, Blue, Red, Orange, Grey
- ↓ ↓ ↓ ↓ ↓
- Base Ctrl Highlight Data Comments
- ↳ RGB color to be defined

Blocks:

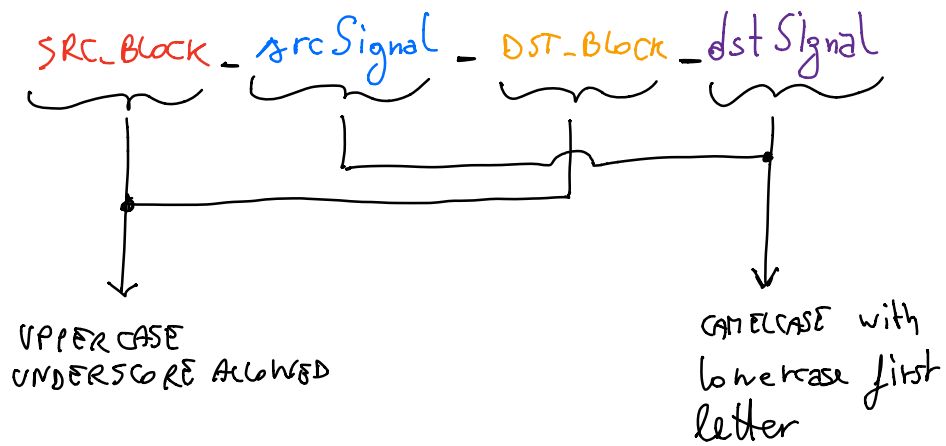
- Reg file (RF)
- Main CV (CV)
- Memory (I-MEM, D-MEM)
- ALU (ALU)
- Others (PC, IF-ID, ID-EX, EX-MEM, MEM-WB)
- Hazard detection unit (HDU)
- Forwarding unit (FWD)
- If (mux) → (to be decided)

Language:

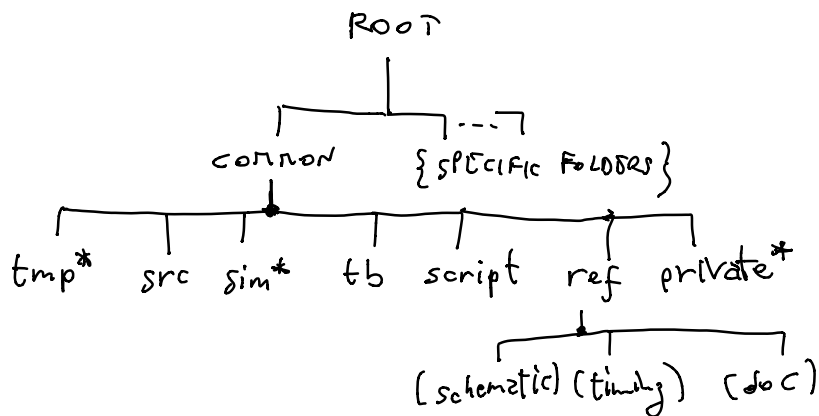
- System Verilog

Rules:

- Keep the names of the signals of the book, when possible
- Names:



Folder tree:



* to be added in .gitignore