



## 1. Description

### 1.1. Project

Project Name	OroLink
Board Name	custom
Generated with:	STM32CubeMX 6.8.0
Date	03/08/2023

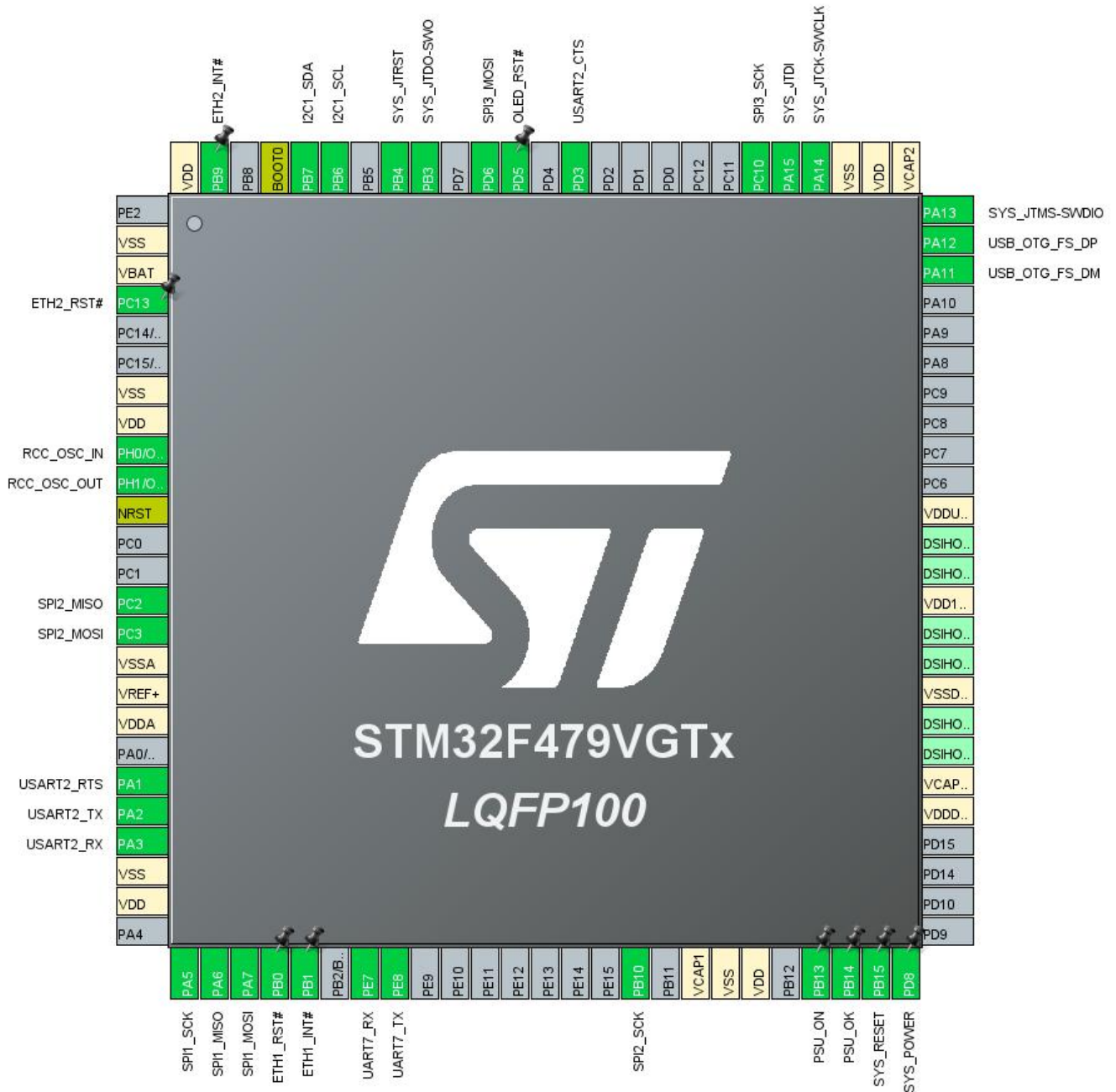
### 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F469/479
MCU name	STM32F479VGTx
MCU Package	LQFP100
MCU Pin number	100

### 1.3. Core(s) information

Core(s)	Arm Cortex-M4
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## 2. Pinout Configuration



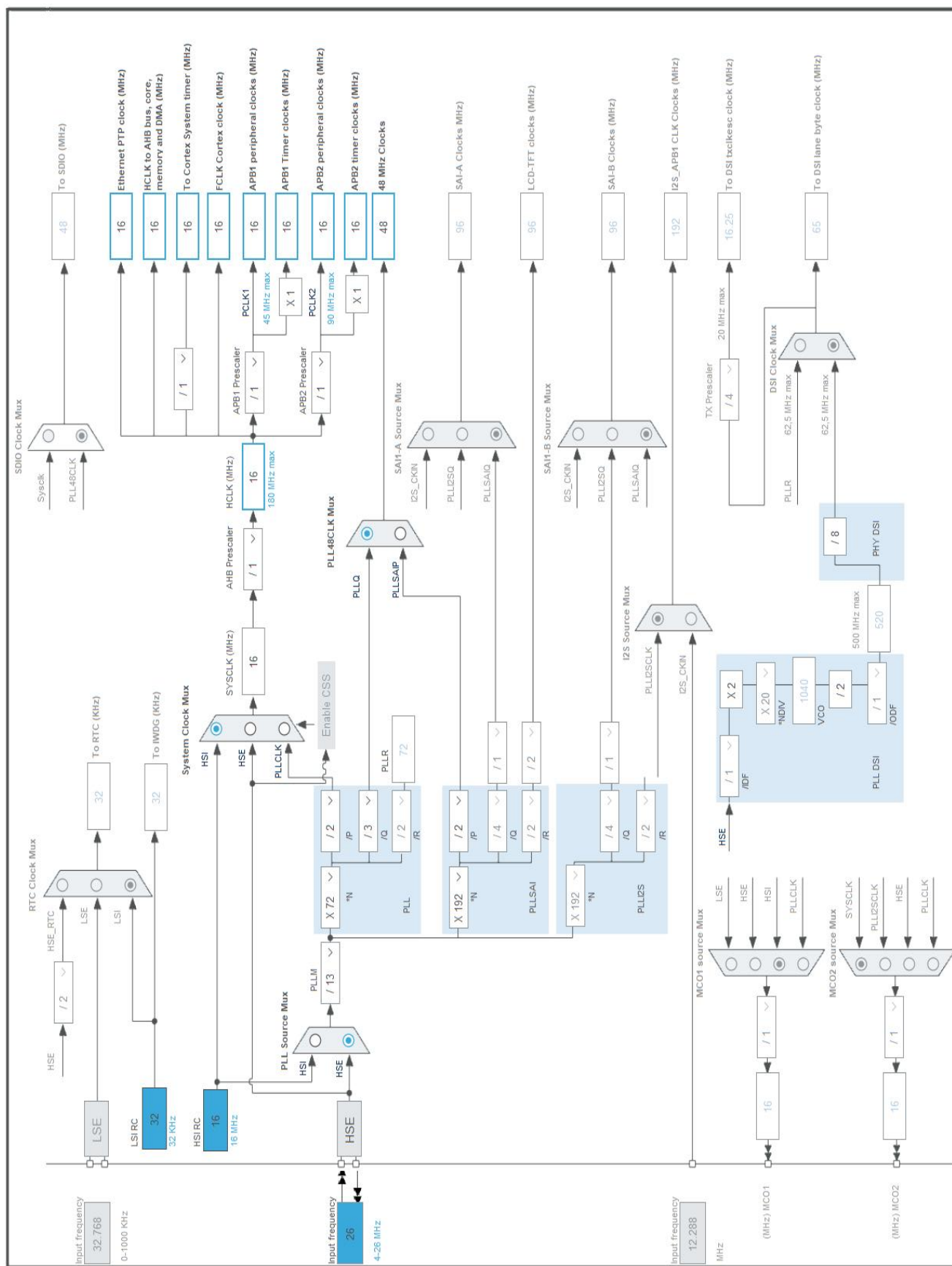
### 3. Pins Configuration

Pin Number LQFP100	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
2	VSS	Power		
3	VBAT	Power		
4	PC13 *	I/O	GPIO_Output	ETH2_RST#
7	VSS	Power		
8	VDD	Power		
9	PH0/OSC_IN	I/O	RCC_OSC_IN	
10	PH1/OSC_OUT	I/O	RCC_OSC_OUT	
11	NRST	Reset		
14	PC2	I/O	SPI2_MISO	
15	PC3	I/O	SPI2_MOSI	
16	VSSA	Power		
17	VREF+	Power		
18	VDDA	Power		
20	PA1	I/O	USART2_RTS	
21	PA2	I/O	USART2_TX	
22	PA3	I/O	USART2_RX	
23	VSS	Power		
24	VDD	Power		
26	PA5	I/O	SPI1_SCK	
27	PA6	I/O	SPI1_MISO	
28	PA7	I/O	SPI1_MOSI	
29	PB0 *	I/O	GPIO_Output	ETH1_RST#
30	PB1	I/O	GPIO_EXTI1	ETH1_INT#
32	PE7	I/O	UART7_RX	
33	PE8	I/O	UART7_TX	
41	PB10	I/O	SPI2_SCK	
43	VCAP1	Power		
44	VSS	Power		
45	VDD	Power		
47	PB13 *	I/O	GPIO_Output	PSU_ON
48	PB14 *	I/O	GPIO_Input	PSU_OK
49	PB15 *	I/O	GPIO_Output	SYS_RESET
50	PD8 *	I/O	GPIO_Output	SYS_POWER
55	VDDDSI	Power		
56	VCAPDSI	Power		
59	VSSDSI	Power		

Pin Number LQFP100	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
62	VDD12DSI	Power		
65	VDDUSB	Power		
73	PA11	I/O	USB_OTG_FS_DM	
74	PA12	I/O	USB_OTG_FS_DP	
75	PA13	I/O	SYS_JTMS-SWDIO	
76	VCAP2	Power		
77	VDD	Power		
78	VSS	Power		
79	PA14	I/O	SYS_JTCK-SWCLK	
80	PA15	I/O	SYS_JTDI	
81	PC10	I/O	SPI3_SCK	
87	PD3	I/O	USART2_CTS	
89	PD5 *	I/O	GPIO_Output	OLED_RST#
90	PD6	I/O	SPI3_MOSI	
92	PB3	I/O	SYS_JTDO-SWO	
93	PB4	I/O	SYS_JTRST	
95	PB6	I/O	I2C1_SCL	
96	PB7	I/O	I2C1_SDA	
97	BOOT0	Boot		
99	PB9	I/O	GPIO_EXTI9	ETH2_INT#
100	VDD	Power		

\* The pin is affected with an I/O function

## 4. Clock Tree Configuration



## 5. Software Project

### 5.1. Project Settings

Name	Value
Project Name	OroLink
Project Folder	C:\Users\Anonymous\STM32CubeIDE\workspace_1.12.0\OroLink
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F4 V1.27.1
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

### 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Add necessary library files as reference in the toolchain project configuration file
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

### 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_I2C1_Init	I2C1
4	MX_SPI1_Init	SPI1
5	MX_SPI2_Init	SPI2
6	MX_SPI3_Init	SPI3
7	MX_UART7_Init	UART7
8	MX_USART2_UART_Init	USART2
9	MX_USB_DEVICE_Init	USB_DEVICE
10	MX_WWDG_Init	WWDG





## 6. Power Consumption Calculator report

### 6.1. Microcontroller Selection

Series	STM32F4
Line	STM32F469/479
MCU	STM32F479VGTx
Datasheet	DS11118_Rev4

### 6.2. Parameter Selection

Temperature	25
Vdd	3.3

### 6.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

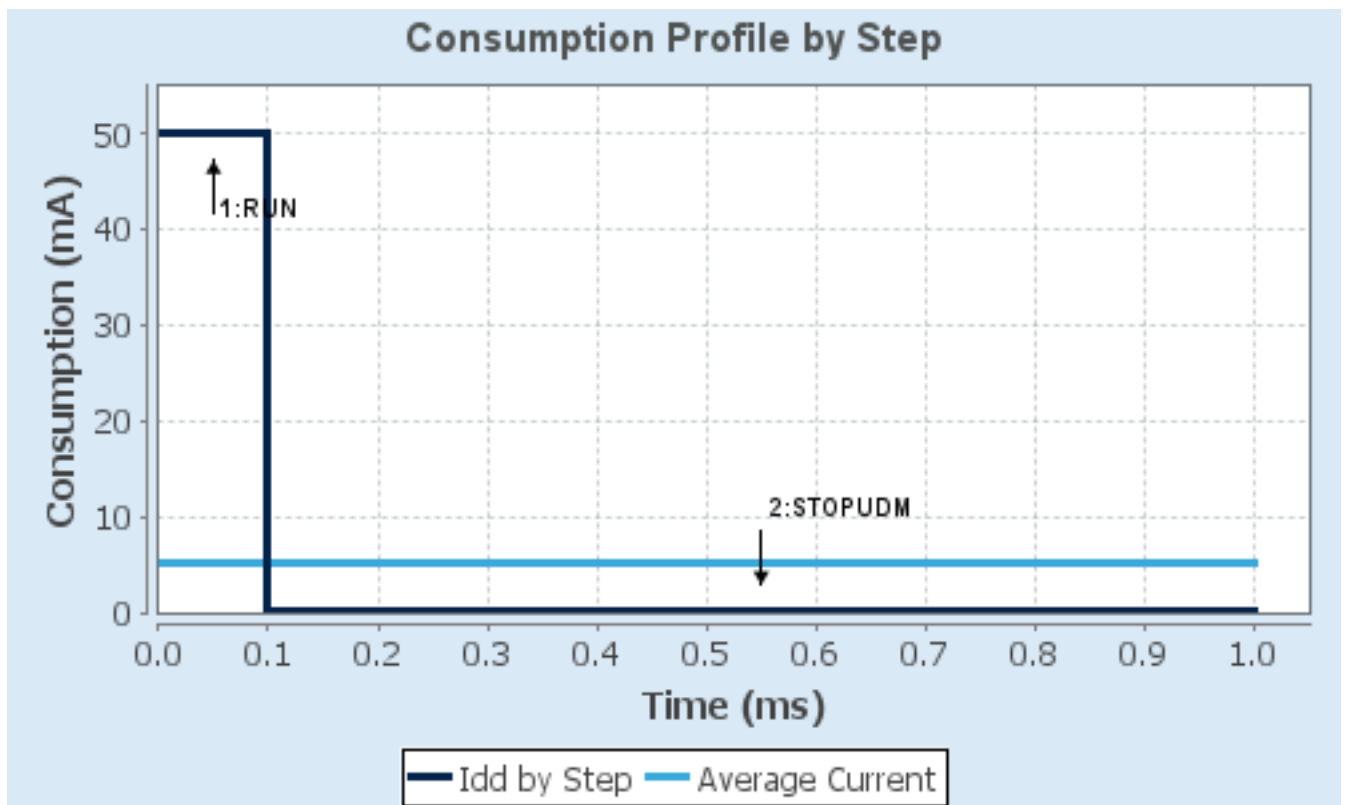
#### 6.4. Sequence

<b>Step</b>	Step1	Step2
<b>Mode</b>	RUN	STOP UDM (Under Drive)
<b>Vdd</b>	3.3	3.3
<b>Voltage Source</b>	Battery	Battery
<b>Range</b>	Scale1-High	No Scale
<b>Fetch Type</b>	RAM/FLASH/ART/REGON	n/a
<b>CPU Frequency</b>	180 MHz	0 Hz
<b>Clock Configuration</b>	HSE PLL	Regulator LP Flash-PwrDwn
<b>Clock Source Frequency</b>	4 MHz	0 Hz
<b>Peripherals</b>		
<b>Additional Cons.</b>	0 mA	0 mA
<b>Average Current</b>	50 mA	140 $\mu$ A
<b>Duration</b>	0.1 ms	0.9 ms
<b>DMIPS</b>	225.0	0.0
<b>Ta Max</b>	97.9	104.98
<b>Category</b>	In DS Table	In DS Table

#### 6.5. Results

Sequence Time	1 ms	Average Current	5.13 mA
Battery Life	27 days, 14 hours	Average DMIPS	225.0 DMIPS

#### 6.6. Chart



## 7. Peripherals and Middlewares Configuration

### 7.1. I2C1

#### I2C: I2C

##### 7.1.1. Parameter Settings:

###### Master Features:

I2C Speed Mode	Standard Mode
I2C Clock Speed (Hz)	100000

###### Slave Features:

Clock No Stretch Mode	Disabled
Primary Address Length selection	7-bit
Dual Address Acknowledged	Disabled
Primary slave address	0
General Call address detection	Disabled

### 7.2. RCC

#### High Speed Clock (HSE): Crystal/Ceramic Resonator

##### 7.2.1. Parameter Settings:

###### System Parameters:

VDD voltage (V)	3.3
Instruction Cache	Enabled
Prefetch Buffer	Enabled
Data Cache	Enabled
Flash Latency(WS)	0 WS (1 CPU cycle)

###### RCC Parameters:

HSI Calibration Value	16
TIM Prescaler Selection	Disabled
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

###### Power Parameters:

Power Regulator Voltage Scale	Power Regulator Voltage Scale 3
Power Over Drive	Disabled

### 7.3. SPI1

## Mode: Full-Duplex Master

### 7.3.1. Parameter Settings:

#### Basic Parameters:

Frame Format	Motorola
Data Size	8 Bits
First Bit	MSB First

#### Clock Parameters:

Prescaler (for Baud Rate)	2
Baud Rate	<b>8.0 MBits/s *</b>
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

#### Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Software

## 7.4. SPI2

### Mode: Full-Duplex Master

#### 7.4.1. Parameter Settings:

#### Basic Parameters:

Frame Format	Motorola
Data Size	8 Bits
First Bit	MSB First

#### Clock Parameters:

Prescaler (for Baud Rate)	2
Baud Rate	<b>8.0 MBits/s *</b>
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

#### Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Software

## 7.5. SPI3

### Mode: Transmit Only Master

### 7.5.1. Parameter Settings:

#### **Basic Parameters:**

Frame Format	Motorola
Data Size	8 Bits
First Bit	MSB First

#### **Clock Parameters:**

Prescaler (for Baud Rate)	2
Baud Rate	<b>8.0 MBits/s *</b>
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

#### **Advanced Parameters:**

CRC Calculation	Disabled
NSS Signal Type	Software

## **7.6. SYS**

**Debug: JTAG (5 pins)**

**Timebase Source: TIM3**

## **7.7. UART7**

**Mode: Asynchronous**

### 7.7.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

#### **Advanced Parameters:**

Data Direction	Receive and Transmit
Over Sampling	16 Samples

## **7.8. USART2**

**Mode: Asynchronous**

**Hardware Flow Control (RS232): CTS/RTS**

### 7.8.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate	115200
Word Length	8 Bits (including Parity)
Parity	None
Stop Bits	1

#### **Advanced Parameters:**

Data Direction	Receive and Transmit
Over Sampling	16 Samples

## **7.9. USB\_OTG\_FS**

### **Mode: Device\_Only**

#### 7.9.1. Parameter Settings:

Speed	Full Speed 12MBit/s
Low power	Disabled
Link Power Management	Disabled
VBUS sensing	Disabled
Signal start of frame	Disabled

## **7.10. WWDG**

### **mode: Activated**

#### 7.10.1. Parameter Settings:

#### **Watchdog Clocking:**

WWDG counter clock prescaler	1
WWDG window value	64
WWDG free-running downcounter value	64

#### **Watchdog Interrupt:**

Early wakeup interrupt	Disable
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## **7.11. FREERTOS**

### **Interface: CMSIS\_V2**

### 7.11.1. Config parameters:

#### **API:**

FreeRTOS API CMSIS v2

#### **Versions:**

FreeRTOS version 10.3.1

CMSIS-RTOS version 2.00

#### **MPU/FPU:**

ENABLE\_MPU Disabled

ENABLE\_FPU Disabled

#### **Kernel settings:**

USE\_PREEMPTION Enabled

CPU\_CLOCK\_HZ SystemCoreClock

TICK\_RATE\_HZ 1000

MAX\_PRIORITIES 56

MINIMAL\_STACK\_SIZE 128

MAX\_TASK\_NAME\_LEN 16

USE\_16\_BIT\_TICKS Disabled

IDLE\_SHOULD\_YIELD Enabled

USE\_MUTEXES Enabled

USE\_RECURSIVE\_MUTEXES Enabled

USE\_COUNTING\_SEMAPHORES Enabled

QUEUE\_REGISTRY\_SIZE 8

USE\_APPLICATION\_TASK\_TAG Disabled

ENABLE\_BACKWARD\_COMPATIBILITY Enabled

USE\_PORT\_OPTIMISED\_TASK\_SELECTION Disabled

USE\_TICKLESS\_IDLE Disabled

USE\_TASK\_NOTIFICATIONS Enabled

RECORD\_STACK\_HIGH\_ADDRESS Disabled

#### **Memory management settings:**

Memory Allocation Dynamic / Static

TOTAL\_HEAP\_SIZE 15360

Memory Management scheme heap\_4

#### **Hook function related definitions:**

USE\_IDLE\_HOOK Disabled

USE\_TICK\_HOOK Disabled

USE\_MALLOC\_FAILED\_HOOK Disabled

USE\_DAEMON\_TASK\_STARTUP\_HOOK Disabled

CHECK\_FOR\_STACK\_OVERFLOW Disabled

#### **Run time and task stats gathering related definitions:**

GENERATE\_RUN\_TIME\_STATS Disabled



USE_TRACE_FACILITY	Enabled
USE_STATS_FORMATTING_FUNCTIONS	Disabled

**Co-routine related definitions:**

USE_CO_ROUTINES	Disabled
MAX_CO_ROUTINE_PRIORITIES	2

**Software timer definitions:**

USE_TIMERS	Enabled
TIMER_TASK_PRIORITY	2
TIMER_QUEUE_LENGTH	10
TIMER_TASK_STACK_DEPTH	256

**Interrupt nesting behaviour configuration:**

LIBRARY_LOWEST_INTERRUPT_PRIORITY	15
LIBRARY_MAX_SYSCALL_INTERRUPT_PRIORITY	5

**Added with 10.2.1 support:**

MESSAGE_BUFFER_LENGTH_TYPE	size_t
USE_POSIX_ERRNO	Disabled

**CMSIS-RTOS V2 flags:**

USE_OS2_THREAD_SUSPEND_RESUME	Enabled
USE_OS2_THREAD_ENUMERATE	Enabled
USE_OS2_EVENTFLAGS_FROM_ISR	Enabled
USE_OS2_THREAD_FLAGS	Enabled
USE_OS2_TIMER	Enabled
USE_OS2_MUTEX	Enabled

7.11.2. Include parameters:

**Include definitions:**

vTaskPrioritySet	Enabled
uxTaskPriorityGet	Enabled
vTaskDelete	Enabled
vTaskCleanUpResources	Disabled
vTaskSuspend	Enabled
vTaskDelayUntil	Enabled
vTaskDelay	Enabled
xTaskGetSchedulerState	Enabled
xTaskResumeFromISR	Enabled
xQueueGetMutexHolder	Enabled
xSemaphoreGetMutexHolder	Disabled
pcTaskGetTaskName	Disabled
uxTaskGetStackHighWaterMark	Enabled
xTaskGetCurrentTaskHandle	Enabled

eTaskGetState	Enabled
xEventGroupSetBitFromISR	Disabled
xTimerPendFunctionCall	Enabled
xTaskAbortDelay	Disabled
xTaskGetHandle	Disabled
uxTaskGetStackHighWaterMark2	Disabled

### 7.11.3. Advanced settings:

**Newlib settings (see parameter description first):**

USE_NEWLIB_REENTRANT	Enabled *
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**Project settings (see parameter description first):**

Use FW pack heap file	Enabled
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## 7.12. USB\_DEVICE

## Class For FS IP: Human Interface Device Class (HID)

### 7.12.1. Parameter Settings:

### Class Parameters:

HID\_FS\_BINTERVAL 0xA\*

### Basic Parameters:

USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces)	1
USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration)	1
USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors)	512
USBD_SELF_POWERED (Enabled self power)	Enabled
USBD_DEBUG_LEVEL (USBD Debug Level)	0: No debug message
USBD_LPM_ENABLED (Link Power Management)	1: Link Power Management supported

### 7.12.2. Device Descriptor:

### Device Descriptor:

VID (Vendor Identifier)	1155
LANGID_STRING (Language Identifier)	English(United States)
MANUFACTURER_STRING (Manufacturer Identifier)	STMicroelectronics

### Device Descriptor FS:

PID (Product Identifier)	22315
PRODUCT_STRING (Product Identifier)	STM32 Human interface
CONFIGURATION_STRING (Configuration Identifier)	HID Config

INTERFACE\_STRING (Interface Identifier)

HID Interface

**\* User modified value**

## 8. System Configuration

### 8.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
I2C1	PB6	I2C1_SCL	Alternate Function Open Drain	No pull-up and no pull-down	Very High *	
	PB7	I2C1_SDA	Alternate Function Open Drain	No pull-up and no pull-down	Very High *	
RCC	PH0/OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1/OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PA6	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PA7	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
SPI2	PC2	SPI2_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PC3	SPI2_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PB10	SPI2_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
SPI3	PC10	SPI3_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PD6	SPI3_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
	PA15	SYS_JTDI	n/a	n/a	n/a	
	PB3	SYS_JTDO-SWO	n/a	n/a	n/a	
	PB4	SYS_JTRST	n/a	n/a	n/a	
UART7	PE7	UART7_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PE8	UART7_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
					*	
USART2	PA1	USART2_RTS	Alternate Function Push Pull	No pull-up and no pull-down	<b>Very High</b> *	
	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	<b>Very High</b> *	
	PA3	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	<b>Very High</b> *	
	PD3	USART2_CTS	Alternate Function Push Pull	No pull-up and no pull-down	<b>Very High</b> *	
USB_OTG_FS	PA11	USB_OTG_FS_DM	Alternate Function Push Pull	No pull-up and no pull-down	<b>Very High</b> *	
	PA12	USB_OTG_FS_DP	Alternate Function Push Pull	No pull-up and no pull-down	<b>Very High</b> *	
GPIO	PC13	GPIO_Output	Output Push Pull	<b>Pull-up</b> *	Low	ETH2_RST#
	PB0	GPIO_Output	Output Push Pull	<b>Pull-up</b> *	Low	ETH1_RST#
	PB1	GPIO_EXTI1	<b>External Interrupt Mode with Falling edge trigger detection</b>	No pull-up and no pull-down	n/a	ETH1_INT#
	PB13	GPIO_Output	Output Push Pull	<b>Pull-down</b> *	Low	PSU_ON
	PB14	GPIO_Input	Input mode	<b>Pull-down</b> *	n/a	PSU_OK
	PB15	GPIO_Output	Output Push Pull	<b>Pull-down</b> *	Low	SYS_RESET
	PD8	GPIO_Output	Output Push Pull	<b>Pull-down</b> *	Low	SYS_POWER
	PD5	GPIO_Output	Output Push Pull	<b>Pull-up</b> *	Low	OLED_RST#
	PB9	GPIO_EXTI9	<b>External Interrupt Mode with Falling edge trigger detection</b>	No pull-up and no pull-down	n/a	ETH2_INT#

## 8.2. DMA configuration

nothing configured in DMA service

### 8.3. NVIC configuration

#### 8.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	15	0
System tick timer	true	15	0
RCC global interrupt	true	5	0
EXTI line1 interrupt	true	5	0
EXTI line[9:5] interrupts	true	5	0
TIM3 global interrupt	true	15	0
USB On The Go FS global interrupt	true	5	0
Window watchdog interrupt	unused		
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
I2C1 event interrupt	unused		
I2C1 error interrupt	unused		
SPI1 global interrupt	unused		
SPI2 global interrupt	unused		
USART2 global interrupt	unused		
SPI3 global interrupt	unused		
FPU global interrupt	unused		
UART7 global interrupt	unused		

#### 8.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	false	false
Debug monitor	false	true	false
Pendable request for system service	false	false	false

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
System tick timer	false	false	true
RCC global interrupt	false	true	false
EXTI line1 interrupt	false	true	true
EXTI line[9:5] interrupts	false	true	true
TIM3 global interrupt	false	true	true
USB On The Go FS global interrupt	false	true	true

\* User modified value

## 9. System Views

### 9.1. Category view

#### 9.1.1. Current

Middleware						
<div>FREERTOS </div> <div>USB_DEVICE </div>						
System Core	Analog	Timers	Connectivity	Multimedia	Security	Computing
DMA			I2C1			
GPIO			SPI1			
NVIC			SPI2			
RCC			SPI3			
SYS			UART7			
WWDG			USART2			
			USB_FS			



## 10. Docs & Resources

Type	Link
BSDL files	<a href="https://www.st.com/resource/en/bsdl_model/stm32f469-479_bsd.zip">https://www.st.com/resource/en/bsdl_model/stm32f469-479_bsd.zip</a>
IBIS models	<a href="https://www.st.com/resource/en/ibis_model/stm32f469_479_ibis.zip">https://www.st.com/resource/en/ibis_model/stm32f469_479_ibis.zip</a>
System View Description	<a href="https://www.st.com/resource/en/svd/stm32f4_svd.zip">https://www.st.com/resource/en/svd/stm32f4_svd.zip</a>
BSDL files	<a href="https://www.st.com/resource/en/bsdl_model/stm32f469-479_bsd.zip">https://www.st.com/resource/en/bsdl_model/stm32f469-479_bsd.zip</a>
IBIS models	<a href="https://www.st.com/resource/en/ibis_model/stm32f469_479_ibis.zip">https://www.st.com/resource/en/ibis_model/stm32f469_479_ibis.zip</a>
System View Description	<a href="https://www.st.com/resource/en/svd/stm32f4_svd.zip">https://www.st.com/resource/en/svd/stm32f4_svd.zip</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf">https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf">https://www.st.com/resource/en/product_presentation/stm32_stm8_functional-safety-packages.pdf</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf</a>
Training Material	<a href="https://www.st.com/resource/en/sales_guide/sg_sc2154.pdf">https://www.st.com/resource/en/sales_guide/sg_sc2154.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstm32f469.pdf">https://www.st.com/resource/en/flyer/flstm32f469.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstm32nucleo.pdf">https://www.st.com/resource/en/flyer/flstm32nucleo.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstmcsuite.pdf">https://www.st.com/resource/en/flyer/flstmcsuite.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstm32trust.pdf">https://www.st.com/resource/en/flyer/flstm32trust.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstm32gui.pdf">https://www.st.com/resource/en/flyer/flstm32gui.pdf</a>
Product Certifications	<a href="https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf">https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an1181-electrostatic-discharge-sensitivity-measurement-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf</a>

Application Notes [https://www.st.com/resource/en/application\\_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2639-soldering-recommendations-and-package-information-for-leadfree-ecopack-mcus-and-mpus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2834-how-to-get-the-best-adc-accuracy-in-stm32-microcontrollers-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an2867-oscillator-design-guide-for-stm8afals-stm32-mcus-and-mpus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2867-oscillator-design-guide-for-stm8afals-stm32-mcus-and-mpus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3070-managing-the-driver-enable-signal-for-rs485-and-iolink-communications-with-the-stm32s-usart-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3997-audio-playback-and-recording-using-the-stm32f4discovery-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3997-audio-playback-and-recording-using-the-stm32f4discovery-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3998-pdm-audio-software-decoding-on-stm32-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3998-pdm-audio-software-decoding-on-stm32-microcontrollers-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4013-stm32-crossseries-timer-overview-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4013-stm32-crossseries-timer-overview-stmicroelectronics.pdf)

- Application Notes [https://www.st.com/resource/en/application\\_note/an4031-using-the-stm32f2-stm32f4-and-stm32f7-series-dma-controller-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4031-using-the-stm32f2-stm32f4-and-stm32f7-series-dma-controller-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4229-how-to-implement-a-vocoder-solution-using-stm32-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4229-how-to-implement-a-vocoder-solution-using-stm32-microcontrollers-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4230-stm32-microcontroller-random-number-generation-validation-using-the-nist-statistical-test-suite-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4230-stm32-microcontroller-random-number-generation-validation-using-the-nist-statistical-test-suite-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4277-using-stm32-device-pwm-shutdown-features-for-motor-control-and-digital-power-conversion-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4277-using-stm32-device-pwm-shutdown-features-for-motor-control-and-digital-power-conversion-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4286-spi-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4286-spi-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4488-getting-started-with-stm32f4xxx-mcu-hardware-development-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4488-getting-started-with-stm32f4xxx-mcu-hardware-development-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4566-extending-the-dac-performance-of-stm32-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4566-extending-the-dac-performance-of-stm32-microcontrollers-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4640-peripherals-interconnections-on-stm32f4057xx-stm32f4157xx-stm32f42xxx-stm32f43xxx-stm32f446xx-and-stm32f469479xx-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4640-peripherals-interconnections-on-stm32f4057xx-stm32f4157xx-stm32f42xxx-stm32f43xxx-stm32f446xx-and-stm32f469479xx-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4739-stm32cube-firmware-examples-for-stm32f4-series-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4739-stm32cube-firmware-examples-for-stm32f4-series-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf)
- Application Notes [https://www.st.com/resource/en/application\\_note/an4759-using-the-hardware-realtime-clock-rtc-and-the-tamper-management-unit-tamp-with-stm32-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4759-using-the-hardware-realtime-clock-rtc-and-the-tamper-management-unit-tamp-with-stm32-microcontrollers-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4776-generalpurpose-timer-cookbook-for-stm32-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4776-generalpurpose-timer-cookbook-for-stm32-microcontrollers-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4803-highspeed-si-simulations-using-ibis-and-boardlevel-simulations-using-hyperlynx-si-on-stm32-mcus-and-mpus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4803-highspeed-si-simulations-using-ibis-and-boardlevel-simulations-using-hyperlynx-si-on-stm32-mcus-and-mpus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4838-managing-memory-protection-unit-in-stm32-mcus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4838-managing-memory-protection-unit-in-stm32-mcus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4850-stm32-mcus-spreadspectrum-clock-generation-principles-properties-and-implementation-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4850-stm32-mcus-spreadspectrum-clock-generation-principles-properties-and-implementation-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4861-lcdtft-display-controller-ltdc-on-stm32-mcus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4861-lcdtft-display-controller-ltdc-on-stm32-mcus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4879-usb-hardware-and-pcb-guidelines-using-stm32-mcus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4879-usb-hardware-and-pcb-guidelines-using-stm32-mcus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4908-stm32-usart-automatic-baud-rate-detection-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4908-stm32-usart-automatic-baud-rate-detection-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4989-stm32-microcontroller-debug-toolbox-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4989-stm32-microcontroller-debug-toolbox-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4995-using-an-electromyogram-technique-to-detect-muscle-activity-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4995-using-an-electromyogram-technique-to-detect-muscle-activity-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an5020-digital-camera-interface-dcml-on-stm32-mcus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an5020-digital-camera-interface-dcml-on-stm32-mcus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an5027-interfacing-pdm-digital-microphones-using-stm32-mcus-and-mpus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an5027-interfacing-pdm-digital-microphones-using-stm32-mcus-and-mpus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an5036-thermal-management-guidelines-for-stm32-applications-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an5036-thermal-management-guidelines-for-stm32-applications-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an5073-receiving-spdif-audio-stream-with-the-stm32f4f7h7-series-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an5073-receiving-spdif-audio-stream-with-the-stm32f4f7h7-series-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an5225-usb-typec-power-delivery-using-stm32-mcus-and-mpus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an5225-usb-typec-power-delivery-using-stm32-mcus-and-mpus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an5253-migrating-from-stm32f469479-line-to-stm32mp151-stm32mp153-and-stm32mp157-lines](https://www.st.com/resource/en/application_note/an5253-migrating-from-stm32f469479-line-to-stm32mp151-stm32mp153-and-stm32mp157-lines)

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Application Notes [https://www.st.com/resource/en/application\\_note/an5543-enhanced-methods-to-handle-spi-communication-on-stm32-devices-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an5543-enhanced-methods-to-handle-spi-communication-on-stm32-devices-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4760-quadspi-interface-on-stm32-microcontrollers-and-microprocessors-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4760-quadspi-interface-on-stm32-microcontrollers-and-microprocessors-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4899-stm32-microcontroller-gpio-hardware-settings-and-lowpower-consumption-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4899-stm32-microcontroller-gpio-hardware-settings-and-lowpower-consumption-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an5612-esd-protection-of-stm32-mcus-and-mpus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an5612-esd-protection-of-stm32-mcus-and-mpus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an5156-introduction-to-stm32-microcontrollers-security-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an5156-introduction-to-stm32-microcontrollers-security-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an4860-dsi-host-on-stm32-mcus-and-mpus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4860-dsi-host-on-stm32-mcus-and-mpus-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an1202\\_freertos\\_guide-for\\_related\\_Tools\\_freertos-guide-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an1202_freertos_guide-for_related_Tools_freertos-guide-stmicroelectronics.pdf)

& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an1602\\_semihosting\\_in\\_for\\_related\\_Tools\\_truestudio-how-to-do-semihosting-in-truestudio-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an1602_semihosting_in_for_related_Tools_truestudio-how-to-do-semihosting-in-truestudio-stmicroelectronics.pdf)

& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an1801\\_stm32cubeprog\\_for\\_related\\_Tools\\_rammer\\_in\\_truestudio-installing-stm32cubeprogrammer-in-truestudio-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an1801_stm32cubeprog_for_related_Tools_rammer_in_truestudio-installing-stm32cubeprogrammer-in-truestudio-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/atollic\\_editing\\_keyboard\\_for\\_related\\_Tools\\_shortcuts-atollic-editing-keyboard-shortcuts-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/atollic_editing_keyboard_for_related_Tools_shortcuts-atollic-editing-keyboard-shortcuts-stmicroelectronics.pdf)

& Software

Application Notes [https://www.st.com/resource/en/application\\_note/iar\\_to\\_atollic\\_truestudio\\_for\\_related\\_Tools\\_migration\\_guide-truestudio-for-arm-migration-guide-iar-embedded-workbench-to-truestudio-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/iar_to_atollic_truestudio_for_related_Tools_migration_guide-truestudio-for-arm-migration-guide-iar-embedded-workbench-to-truestudio-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/stm32cubemx\\_installation\\_in\\_truestudio-stm32cubemx-installation-in-truestudio-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/stm32cubemx_installation_in_truestudio-stm32cubemx-installation-in-truestudio-stmicroelectronics.pdf)

& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an2656-stm32f10xxx-lcd-glass-driver-firmware-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2656-stm32f10xxx-lcd-glass-driver-firmware-stmicroelectronics.pdf)  
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& Software

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for related Tools  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an3078-stm32-inapplication-programming-over-the-ic-bus-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3078-stm32-inapplication-programming-over-the-ic-bus-stmicroelectronics.pdf)  
for related Tools  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an3116-stm32s-adc-modes-and-their-applications-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3116-stm32s-adc-modes-and-their-applications-stmicroelectronics.pdf)  
for related Tools  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an3174-implementing-receivers-for-infrared-remote-control-protocols-using-stm32f10xxx-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3174-implementing-receivers-for-infrared-remote-control-protocols-using-stm32f10xxx-microcontrollers-stmicroelectronics.pdf)  
for related Tools  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an3241-qvga-tftlcd-direct-drive-using-the-stm32f10xx-fsmc-peripheral-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3241-qvga-tftlcd-direct-drive-using-the-stm32f10xx-fsmc-peripheral-stmicroelectronics.pdf)  
for related Tools  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an3307-guidelines-for-obtaining-iec-60335-class-b-certification-for-any-stm32-application-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3307-guidelines-for-obtaining-iec-60335-class-b-certification-for-any-stm32-application-stmicroelectronics.pdf)  
for related Tools  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3364-migration-and-compatibility-guidelines-for-stm32-microcontroller-applications-stmicroelectronics.pdf)  
for related Tools  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an3965-stm32f40xstm32f41x-inapplication-programming-using-the-usart-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3965-stm32f40xstm32f41x-inapplication-programming-using-the-usart-stmicroelectronics.pdf)  
for related Tools  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an3969-eeeprom-emulation-in-stm32f40xstm32f41x-microcontrollers-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3969-eeeprom-emulation-in-stm32f40xstm32f41x-microcontrollers-stmicroelectronics.pdf)  
for related Tools  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an3990-upgrading-stm32f4discovery-board-firmware-using-a-usb-key-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3990-upgrading-stm32f4discovery-board-firmware-using-a-usb-key-stmicroelectronics.pdf)  
for related Tools  
& Software

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for related Tools and-recording-using-the-stm32f4discovery-stmicroelectronics.pdf  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an3998-pdm-audio-](https://www.st.com/resource/en/application_note/an3998-pdm-audio-software-decoding-on-stm32-microcontrollers-stmicroelectronics.pdf)  
for related Tools software-decoding-on-stm32-microcontrollers-stmicroelectronics.pdf  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an4044-floating-point-](https://www.st.com/resource/en/application_note/an4044-floating-point-unit-demonstration-on-stm32-microcontrollers-stmicroelectronics.pdf)  
for related Tools unit-demonstration-on-stm32-microcontrollers-stmicroelectronics.pdf  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an4323-getting-started-](https://www.st.com/resource/en/application_note/an4323-getting-started-with-stemwin-library-stmicroelectronics.pdf)  
for related Tools with-stemwin-library-stmicroelectronics.pdf  
& Software

Application Notes [https://www.st.com/resource/en/application\\_note/an4435-guidelines-for-](https://www.st.com/resource/en/application_note/an4435-guidelines-for-obtaining-ulsaiec-607301603351-class-b-certification-in-any-stm32-application-stmicroelectronics.pdf)  
for related Tools obtaining-ulsaiec-607301603351-class-b-certification-in-any-stm32-  
& Software application-stmicroelectronics.pdf

Application Notes [https://www.st.com/resource/en/application\\_note/an4457-implementing-](https://www.st.com/resource/en/application_note/an4457-implementing-an-emulated-uart-on-stm32f4-microcontrollers-stmicroelectronics.pdf)  
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