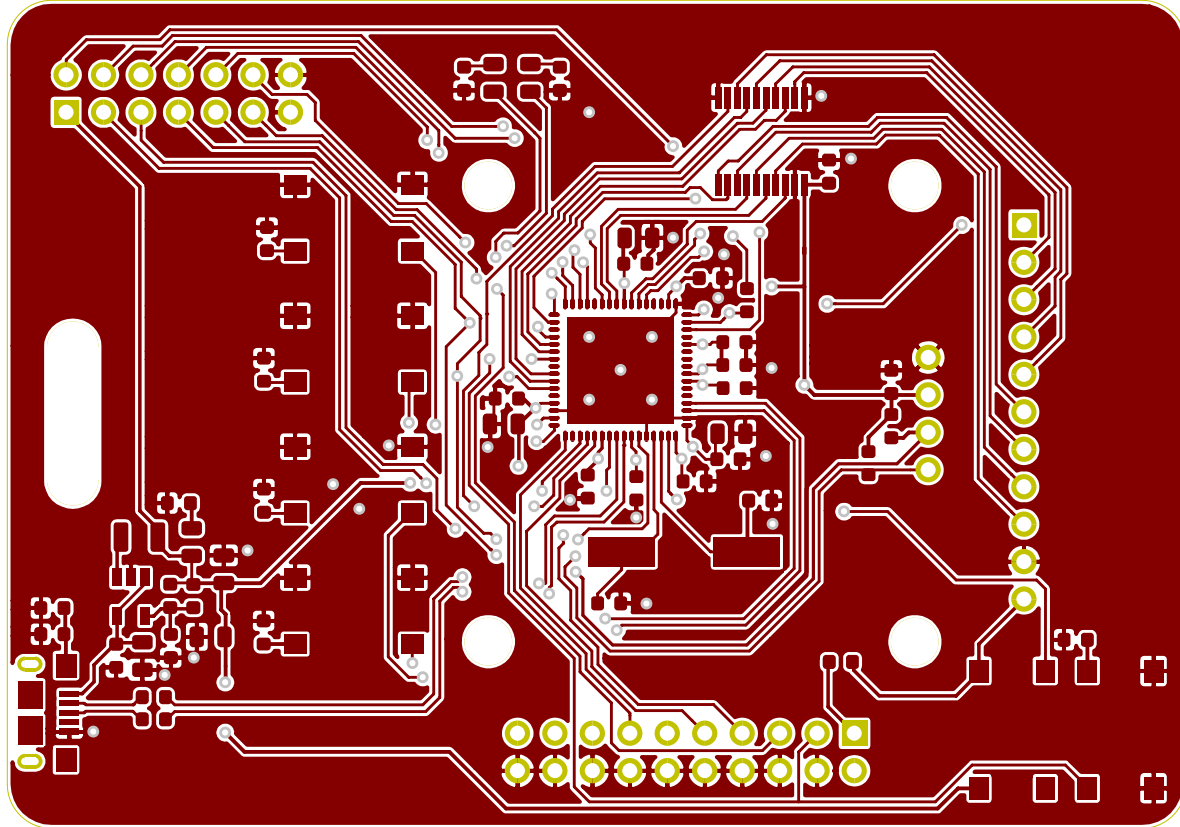
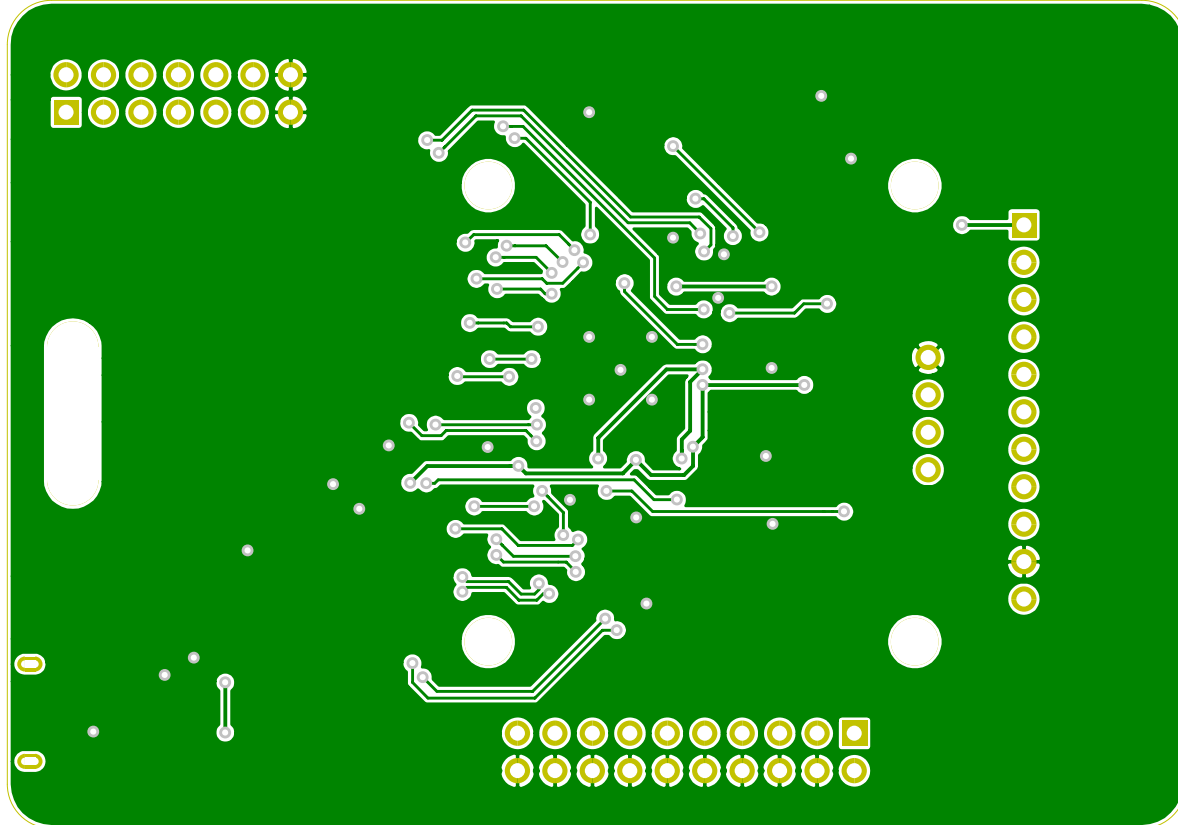
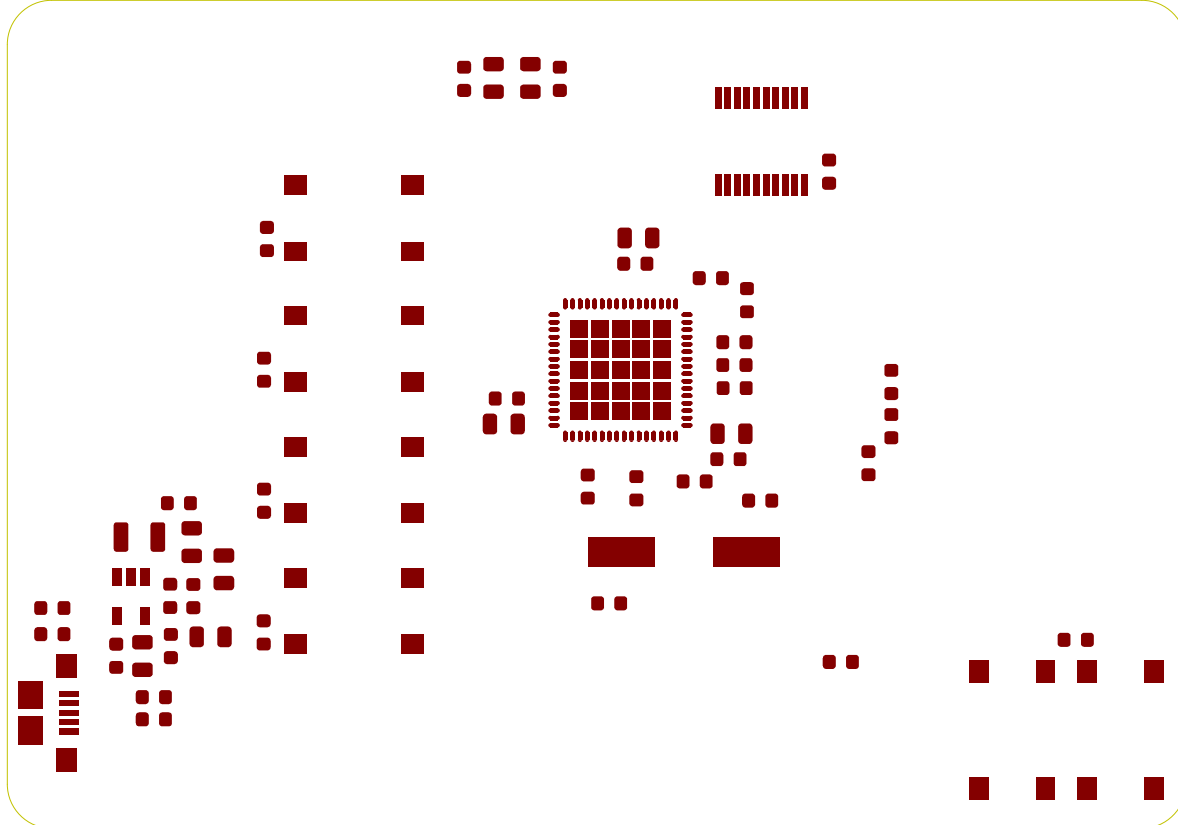


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Pinout of the Digispark Pro

by Digi-Key Electronics

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