# Meltdown and Spectre Samples

Written in Assembly

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# 1 Introduction

### 1.1 Overview

TBD

#### 1.2 Nasm

```
5 \langle preamble 5 \rangle \equiv (8a) bits 64 global _start pagesize equ 4096
```

# 2 Cache Access Timing

#### 2.1 Introduction

```
TBD
          \langle \textit{tsc-64bit} \ \textit{7} \mathbf{a} \rangle {\equiv}
7a
                                                                                                                                         (7b)
                      rdtsc
                      shl
                                        RDX,32
                      add
                                        RAX,RDX
             TBD
          \langle calculate\text{-}cache\text{-}access\text{-}time 7b \rangle \equiv
7b
                                                                                                                                         (8a)
             _calccachetime:
                                        RAX,RAX
                      xor
                                        RDX,RDX
                      xor
                      lfence
             \langle tsc\text{-}64bit 7a \rangle
                      mov
                                        R8,RAX
                                        RCX,[RDI]
                      mov
                      lfence
             \langle tsc\text{-}64bit \ 7a \rangle
                                        RAX,R8
                      sub
                      ret
```

#### 2.2 Detect Cache Access Time

```
TBD
        \langle cachetiming.asm 8a \rangle \equiv
8a
           \langle preamble 5 \rangle
           \langle cachetiming-rodata \ 8c \rangle
           \langle cachetiming-uninitialized-data 8b \rangle
           section .text
           _start:
                                  RDI, suncached
                   call
                                   _print
           \langle exitProgram 11b \rangle
           \langle calculate\text{-}cache\text{-}access\text{-}time \text{ 7b} \rangle
           \langle xorshift\text{-}prng \ 11c \rangle
           ⟨utilities 11a⟩
           TBD
8b
        \langle cachetiming-uninitialized-data~8b \rangle \equiv
                                                                                                                        (8a)
           section .bss
                   measures:
                                           resq 2048
                   align pagesize
                   data:
                                           times 2 resb pagesize
           TBD
        \langle cachetiming-rodata \ 8c \rangle \equiv
8c
                                                                                                                        (8a)
           section .rodata
                   suncached:
                                           db "Uncached Access Time: ",0x00
```

### 2.3 Read Array via Cache Access Time

# 3 Signals

## 3.1 Basics

TBD

## 3.2 Detecting Signals

TBD

## 3.3 Handling Signals

### 4 Utilities

#### 4.1 Introduction

TBD

11a 
$$\langle utilities \ 11a \rangle \equiv \langle print \ 12c \rangle$$
 (8a)

### 4.2 Exit Program

TBD

11b 
$$\langle exitProgram \ 11b \rangle \equiv$$
 (8a)

xor RDI,RDI

mov RAX,60

syscall

#### 4.3 Random Number Generator

To initialize the data a random number generator (RNG) is used. The sample programs use  $xorshift^1$  as RNG.

The start of the memory area to fill with random numbers is given in RDI and the number of bytes to fill in RSI. RSI must be a multiple of 4. The seed of the RNG is given in EDX.

First we move the number of values to be generated to RCX (which is a counter in x86 processors) and divide it by 4 (because we use a 32bit RNG).

```
11c \langle xorshift\text{-}prng \ 11c \rangle \equiv (8a) 12a > _xorshift: mov RCX,RSI shr RCX,2
```

<sup>&</sup>lt;sup>1</sup>https://en.wikipedia.org/wiki/Xorshift

Now we can generate the next 32bit random number.

12a  $\langle xorshift\text{-}prng \ 11c \rangle + \equiv$ (8a) ⊲11c 12b⊳ mov EAX, EDX .next\_random: EBX, EAX mov EAX,13 shl xor EAX, EBX EBX, EAX mov EAX,17 shr EAX, EBX xor EBX, EAX mov EAX,5 shl EAX, EBX xor

Because we want to use multiple random numbers we store the value of EAX to [RDI] and loop for the next random number.

12b 
$$\langle xorshift\text{-}prng \ 11c \rangle + \equiv$$
 (8a)  $\triangleleft 12a$  stosd loop .next\_random ret

#### 4.4 Printing Text

The routine \_print prints a null-terminated string to the terminal (stdout). The only argument passed in to the routine (in RDI) is the address of the string to print.

So first we start with clearing AL (setting it to null) and saving the address of the string to RSI. We're using RSI because we later need the address to calculate the length of the string and also RSI is the register that we need to use for the string address in the systemcall.

Next we search for the terminating null ('\0') character. For this we use the instruction scasb (scan string byte) which compares the byte at the address [RDI] with the value in AL and sets the flags accordingly. When the byte at [RDI] is not the value of AL the the next instruction (jne) jumps to the given label (.next\_char in this case).

scasb additionally increments RDI so that we go through the string until \0 is found.

```
12d ⟨print 12c⟩+≡ (11a) ⊲12c 13a⊳
.next_char:
scasb
jne .next_char
```

After we have found the string termination we calculate the number of bytes that the string has. For this we copy the value of the last byte read (which is in RDI) to RDX and subtract the start of the string (which we saved to RSI).

Now we have the address of the string in RSI and the length of the string in RDX which are the 2nd and 3rd argument in a systemcall. The 1st argument (in RDI) to the systemcall is the file descriptor (1 is stdout). Additionally the number of the systemcall (1) is passed in RAX. The systemcall (syscall) now prints RDX bytes from [RSI] to the file descriptor RDI.

Now that we are done and can return to the caller.

13b 
$$\langle print | 12c \rangle + \equiv$$
 (11a)  $\triangleleft$  13a ret

#### 4.5 Printing Numbers

# **A** Acronyms

 $\boldsymbol{\mathsf{RNG}}\,$  random number generator 11

# **B** Code Chunks

```
 \begin{array}{l} \langle cachetiming\text{-}rodata \ 8c \rangle \\ \langle cachetiming\text{-}uninitialized\text{-}data \ 8b \rangle \\ \langle cachetiming.asm \ 8a \rangle \\ \langle calculate\text{-}cache\text{-}access\text{-}time \ 7b \rangle \\ \langle exitProgram \ 11b \rangle \\ \langle preamble \ 5 \rangle \\ \langle print \ 12c \rangle \\ \langle tsc\text{-}64bit \ 7a \rangle \\ \langle utilities \ 11a \rangle \\ \langle xorshift\text{-}prng \ 11c \rangle \end{array}
```