Tabla de Proyecto

Entradas Lógicas				Salidas Lógicas							
Decimal	Α	В	С	D	а	b	С	d	е	f	g
0	0	0	0	0	0	0	0	0	0	0	1
1	0	0	0	1	1	0	0	1	1	1	1
2	0	0	1	0	0	0	1	0	0	1	0
3	0	0	1	1	0	0	0	0	1	1	0
4	0	1	0	0	1	0	0	1	1	0	0
5	0	1	0	1	0	1	0	0	1	0	0
6	0	1	1	0	0	1	0	0	0	0	0
7	1	1	1	1	0	0	0	1	1	1	1
8	1	0	0	0	0	0	0	0	0	0	0
9	1	0	0	1	0	0	0	0	1	0	0
10	1	0	1	0	Х	Х	Х	Χ	Х	Х	Χ
11	1	0	1	1	Х	Х	Х	Χ	Х	Х	Х
12	1	1	0	0	Χ	X	X	Χ	Х	Х	Χ
13	1	1	0	1	X	Х	X	Х	Х	Х	Х
14_6=8:	1	31.0	11 = E	0	Х	X	Х	Х	Х	Х	Х
15	1	1	1	1	Х	Х	Х	Х	Х	Х	Х

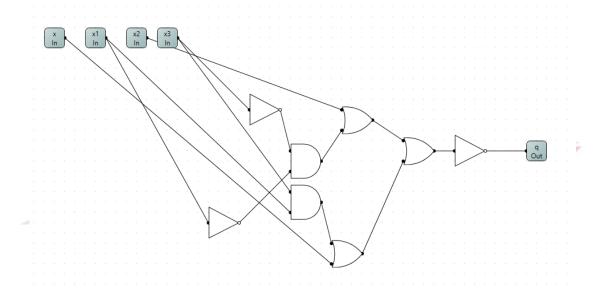


Segmento A

	_	$\overline{}$				
CD\A	3	00	01	11	10	
00		0	1	X	0	
01		1	9	X	0	
11		þ	0	X	X	
10		0	0	X	X	

Formula

BD+A+BD+C



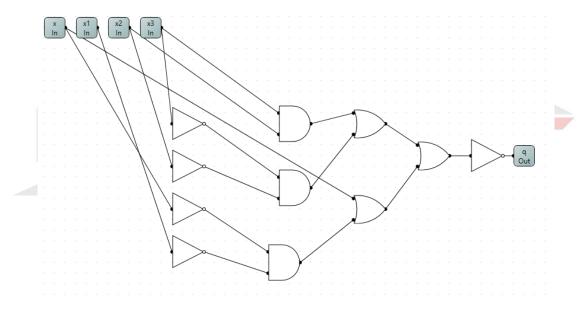


Segmento B

CD\AB	00	01	11	10
00	0	0	X	0
01	0	1	Х	0
11 [0	0	Χ	X
10	0	1	X	X

Formula

ĀĒ+A+CD+C̄D̄



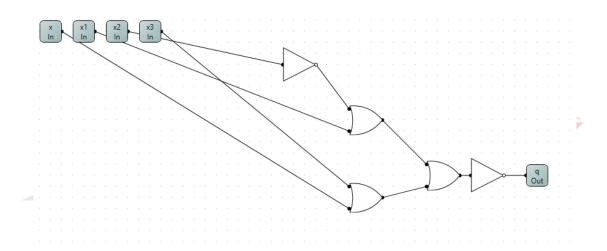


Segmento C

CD\AB	00	01	11	10
00	0	0	X	0
01	0	0	Х	0
11	0	0	Χ	X
10	1	0	X	Χ

Formula

D+A+B+C



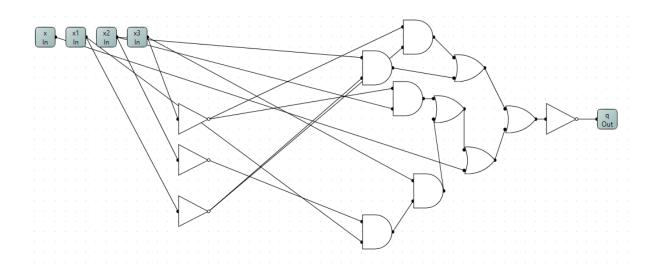


Segmento D

()			
CD\	4B	00	01	11	10	
00		\circ	1	X	0	
01		1	0	X	0	
11		0	1	X	Х	
10		0	0	X	X	
	Ţ					

Formula

BD+A+BC+CD+BCD



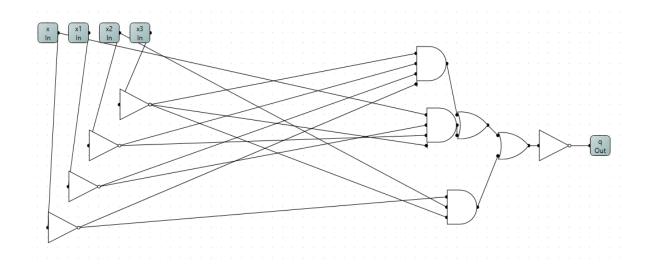


Segmento E

CD\AB	00	01	11	10
00	0	1	Χ	0
01	1	1	Χ	1
11	1	1	Χ	Χ
10	0	0	Х	Χ

Formula

ĀBCD+ABCD+ĀCD





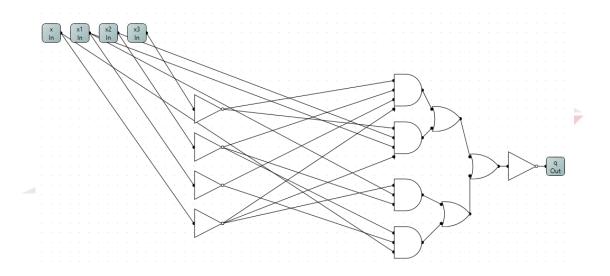
Segmento F

CD\AB	00	01	11	10
00	0	0	Χ	0
01	1	0	Χ	0
11	1	1	Χ	Χ
10	1	0	Χ	Χ

Formula

\$\overline{A}\overline{B}\overline{\overline{C}} + \overline{A}\overline{B}\overline{C} + \overline{A}\overline{B}\overline{C}\$

Circuito





Segmento G

CD\AB	00	01	11	10	
00	1	0	X	0	
 01	1	0	X	0	
11	0	1	Х	X	
10	0	0	X	X	

Formula

A+BC+CD+ABC

