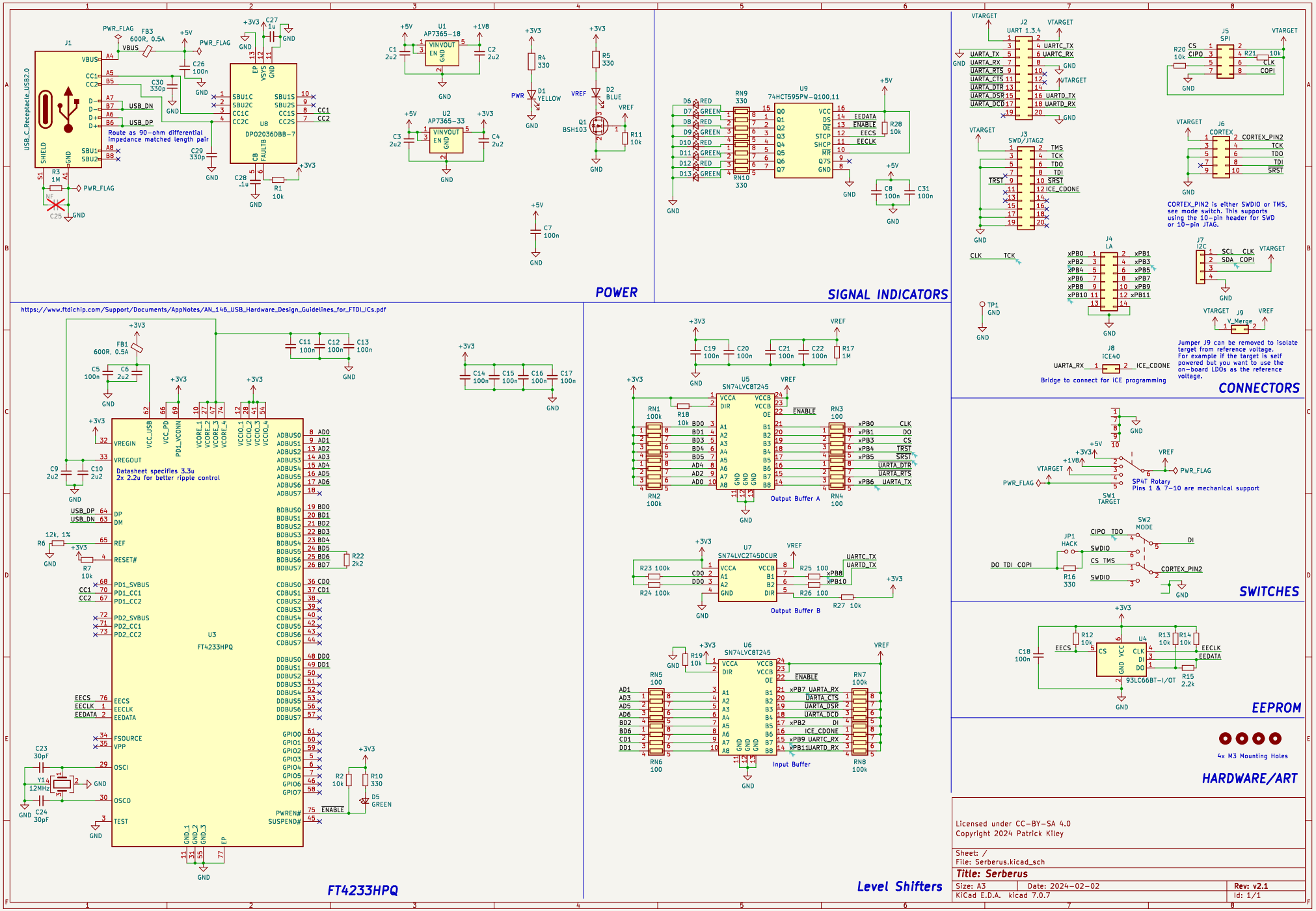
POWER
SIGNAL INDICATORS
CONNECTORS
SWITCHES
EEPROM
HARDWARE/ART
FT4233HPQ
Level Shifters
https://www.ftdichip.com/Support/Documents/AppNotes/AN_146_USB_Hardware_Design_Guidelines_for_FTDI_ICs.pdf
Route as 90-ohm differential impedance matched length pair
Datasheet specifies 3.3u
2x 2.2u for better ripple control
CORTEX_PIN2 is either SWDIO or TMS, see mode switch. This supports using the 10-pin header for SWD or 10-pin JTAG.
Jumper J9 can be removed to isolate target from reference voltage. For example if the target is self powered but you want to use the on-board LDOs as the reference voltage.
Bridge to connect for ICE programming
SP4T Rotary
Pins 1 & 7-10 are mechanical support
Output Buffer A
Output Buffer B
Input Buffer
4x M3 Mounting Holes
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Sheet: /
File: Serberus.kicad_sch
Title: Serberus
Size: A3
Date: 2024-02-02
Rev: v2.1
KiCad E.D.A. kicad 7.0.7
Id: 1/1