

A

A

B

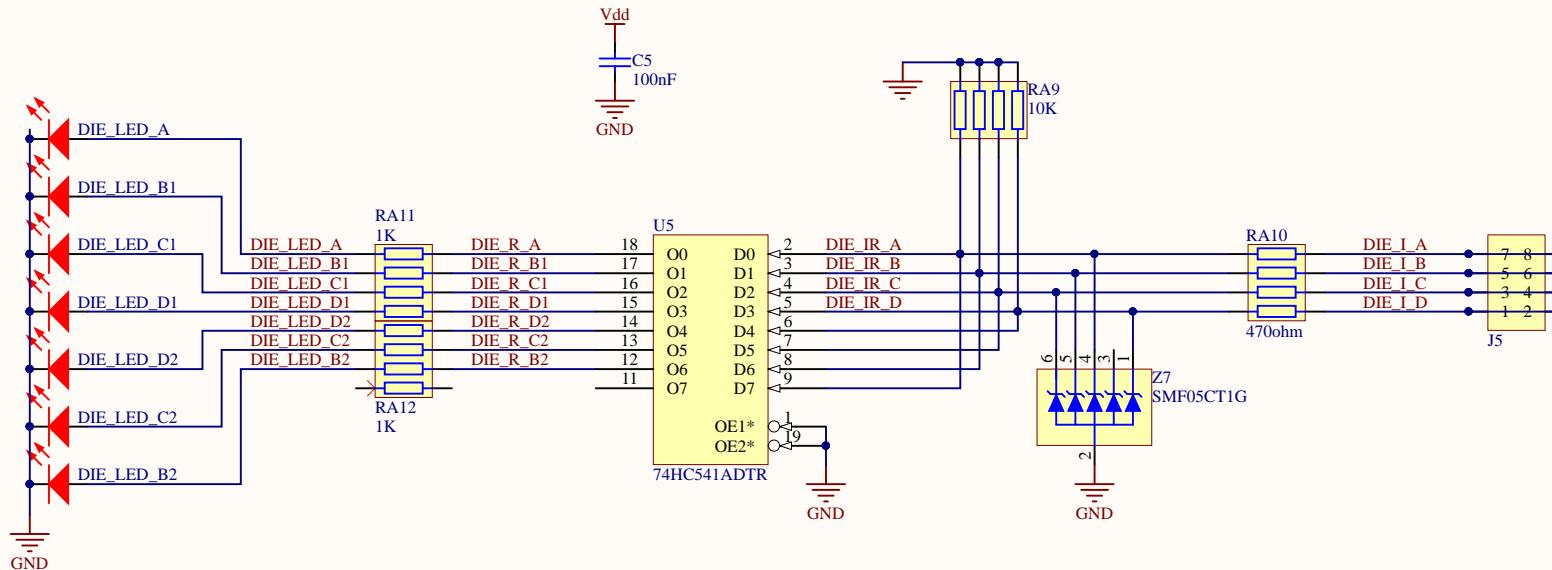
B

C

C

D

D



Title		
Size A4	Number	Revision
Date: 2/11/2019	Sheet	of
File: C:\Users\.\DigitalLabBoard_Die.SchDoc	Drawn By:	

A

A

B

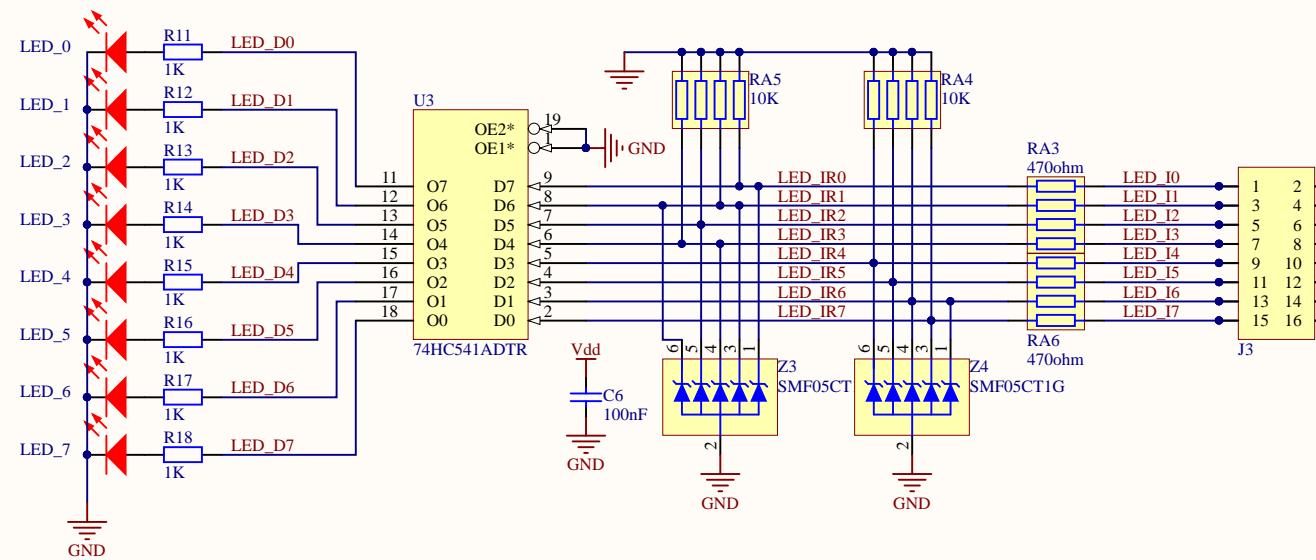
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C

C

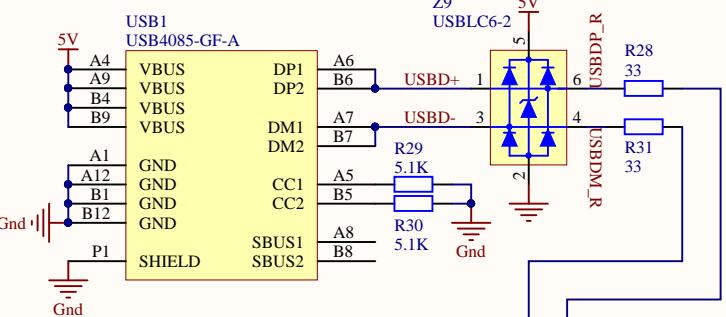
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D

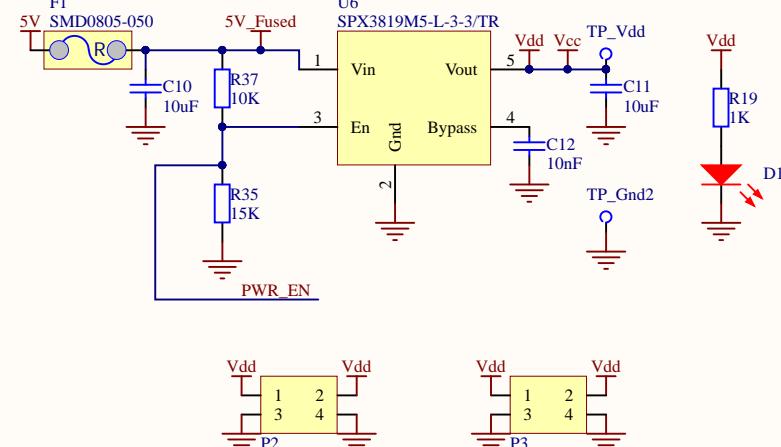


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Date: 2/11/2019	Sheet	of
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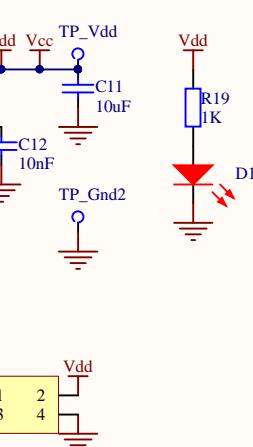
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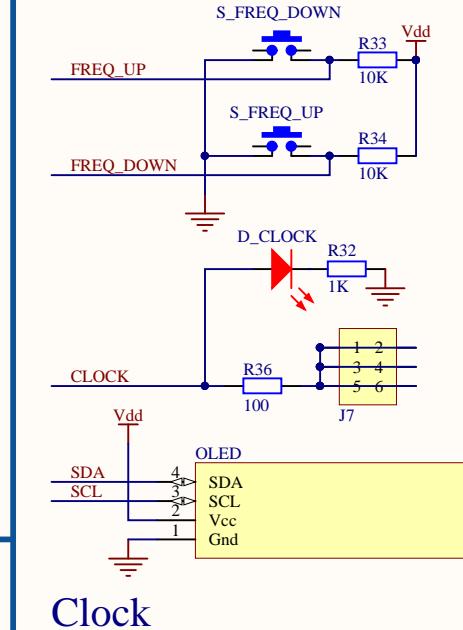
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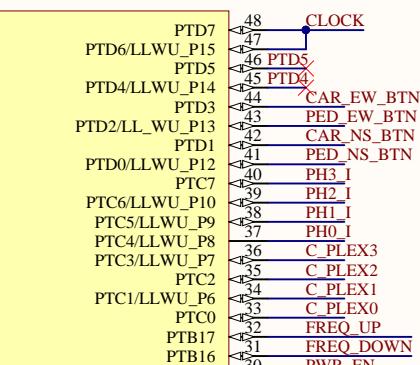
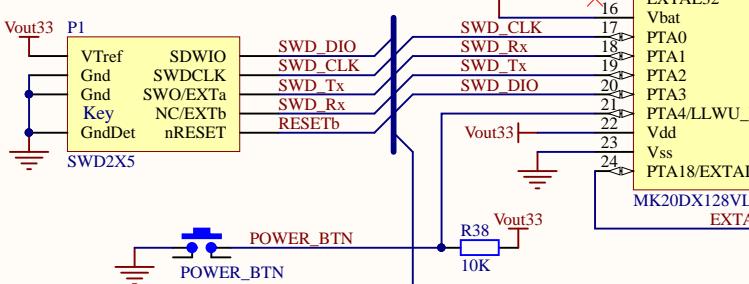
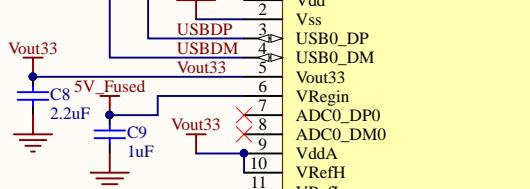
3



4



B



open source hardware

Title

Size

A4

Number

Revision

Date: 2/11/2019

Sheet of

File: C:\Users...\DigitalLabBoard_Processor.SchDrawn By:

1

2

3

4

A

4

B

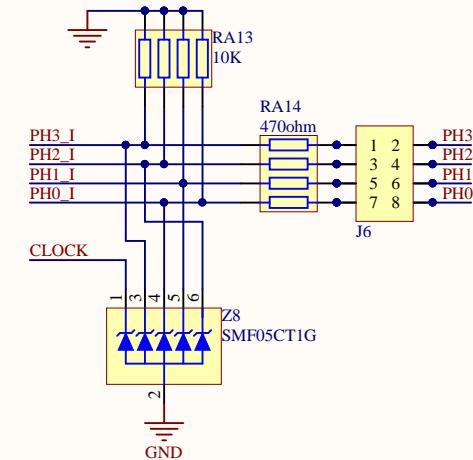
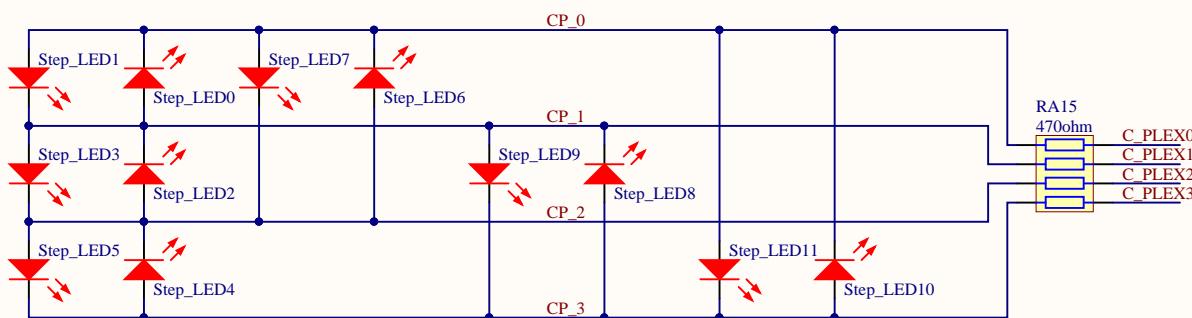
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6

6

B

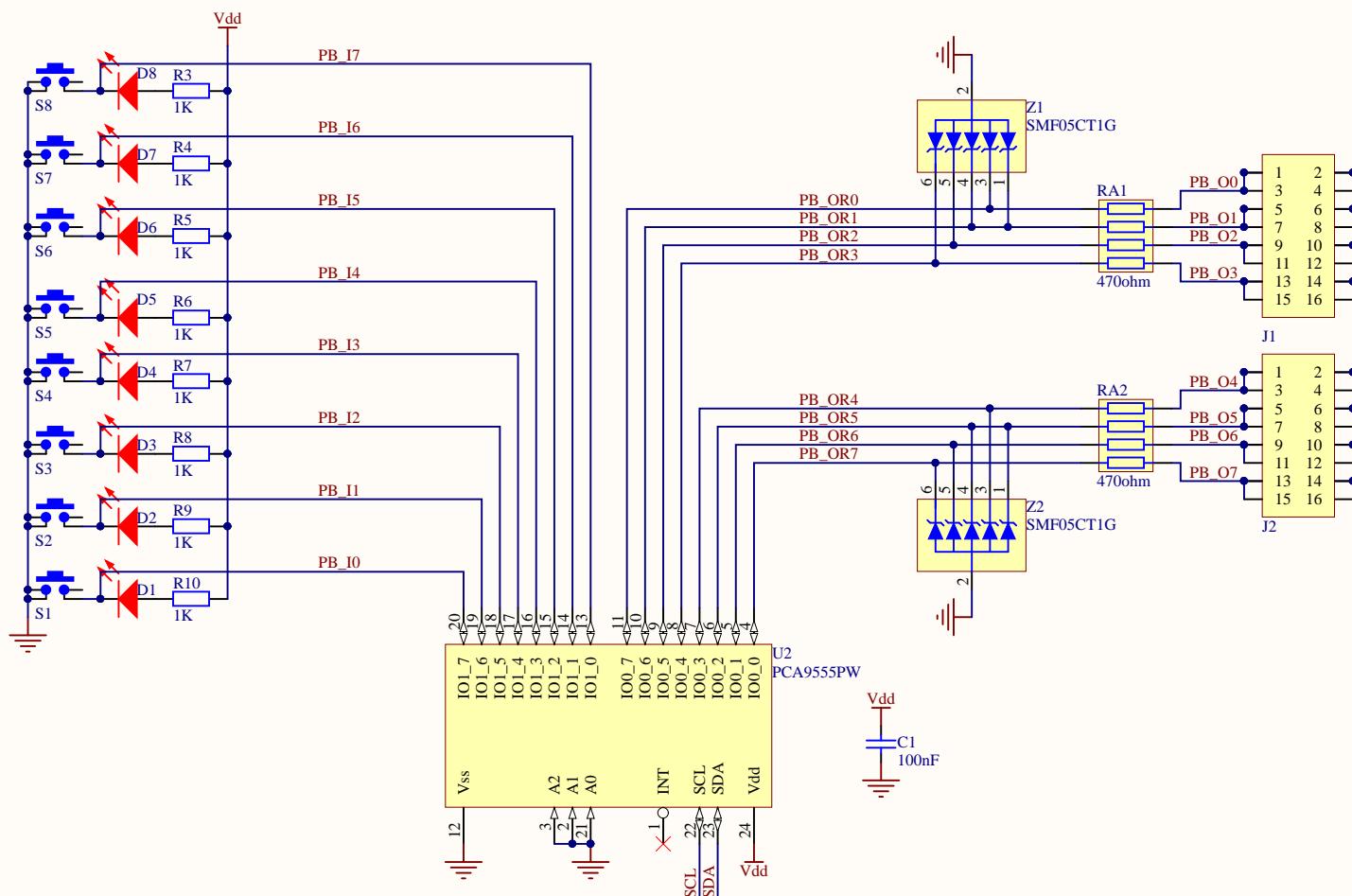
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Title		
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Date:	2/11/2019	Sheet of
File:	C:\Users\..\DigitalLabBoard Stepper.Sch	Drawn By:

A

A



B

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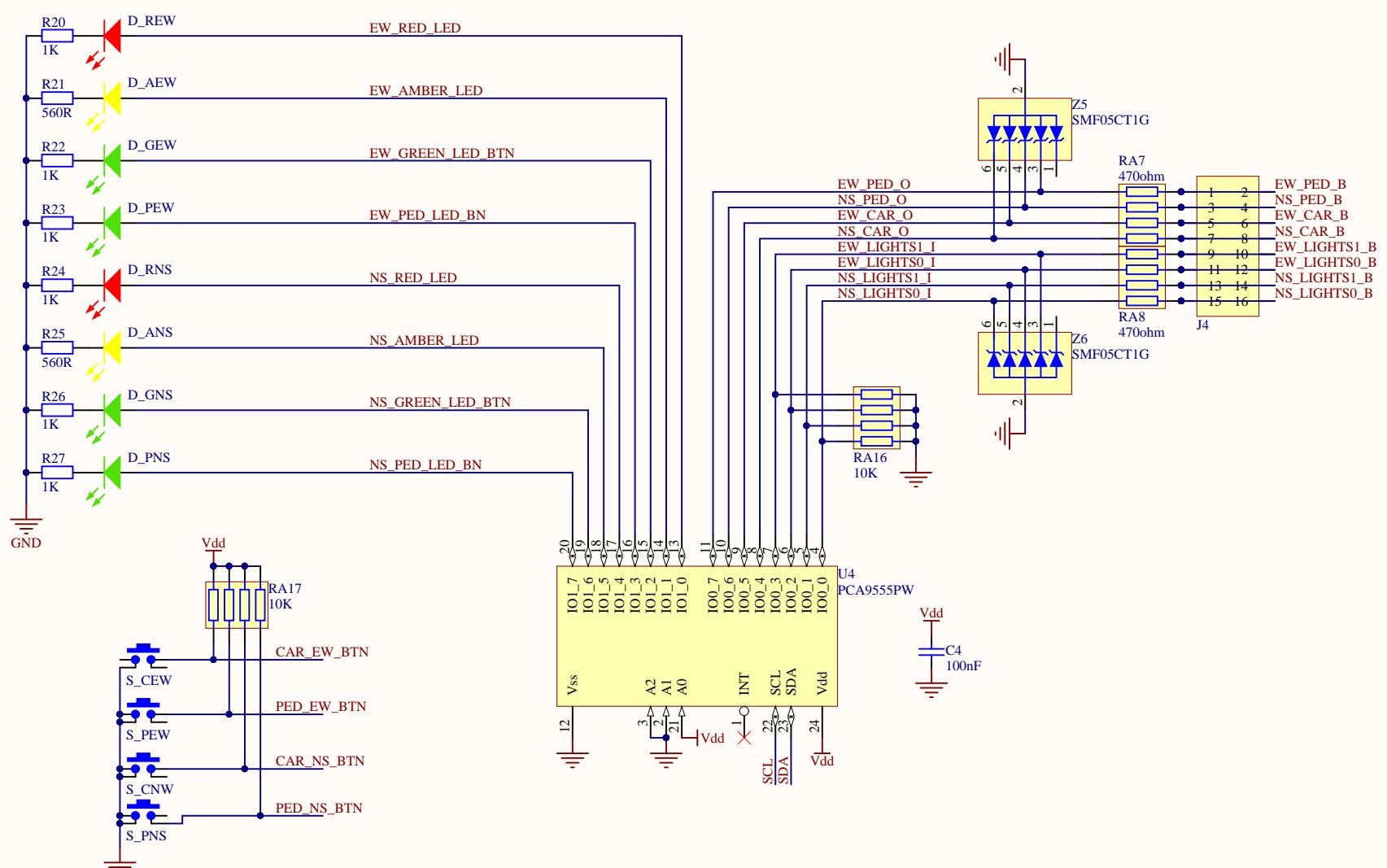
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D

Title		
Size	Number	Revision
A4		
Date:	2/11/2019	Sheet of
File:	C:\Users\...\DigitalLabBoard_Switches.Sch	Drawn By:

A



Title		
Size	Number	Revision
A4		
Date: 2/11/2019	Sheet	of
File: C:\Users...\DigitalLabBoard_Traffic.SchDoc		

-150.00—

The image shows a red PCB with four main functional areas highlighted by green boxes:

- Clock:** Contains an OLED display and a digital clock component.
- Dice:** Features a 6x6 grid of pads labeled A through F.
- Traffic:** Depicts a street intersection with traffic lights and arrows.
- Stepper:** Shows a circular stepper motor with phase and enable pins.

Various green highlights and callouts point to specific components like the OLED, power switch, and motor.



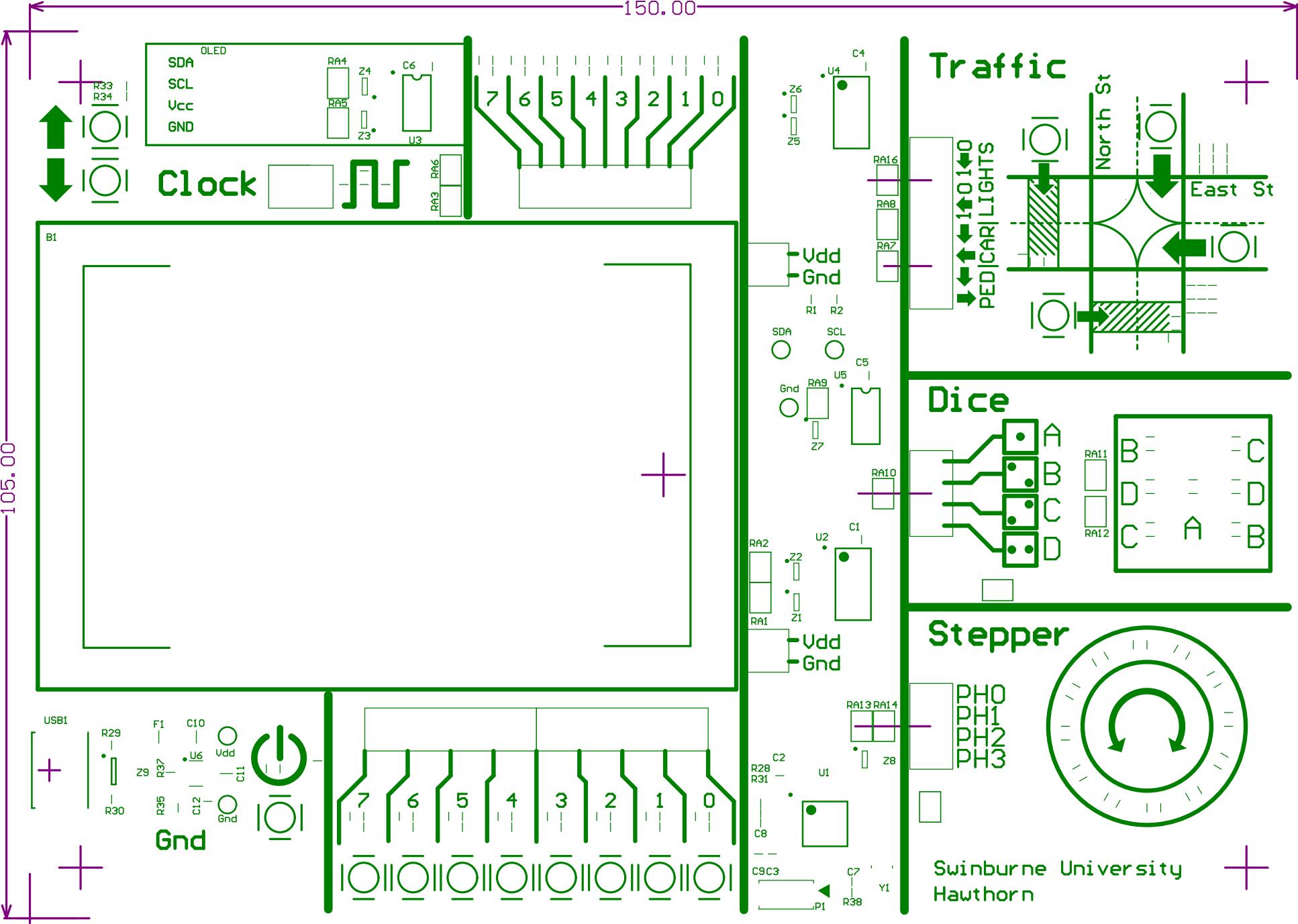
DIGITAL ELECTRONICS DESIGN
SWINBURNE UNIVERSITY
OF TECHNOLOGY



REV. 0.1

*Based on a design by
Jordan Boekel, Ben Gregg, Patrick Curtain*

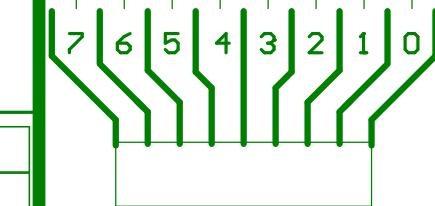
If found please call (+613) 9214 8319 .



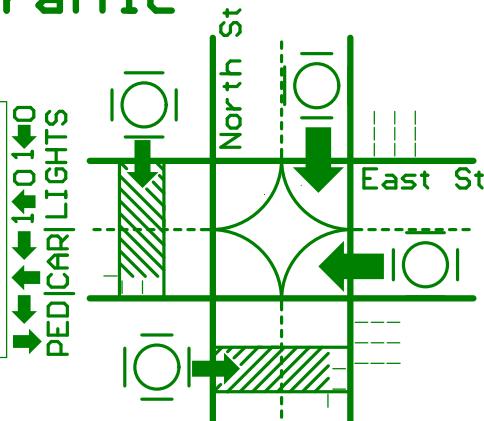
SDA
SCL
Vcc
GND



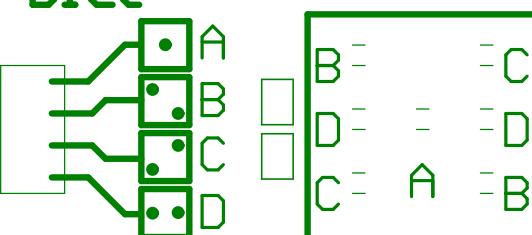
Clock



Traffic

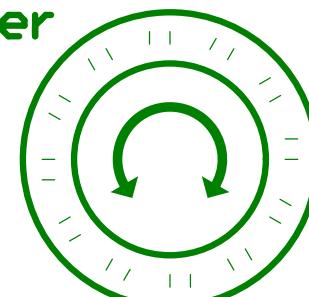


Dice



Stepper

PH0
PH1
PH2
PH3



Gnd

