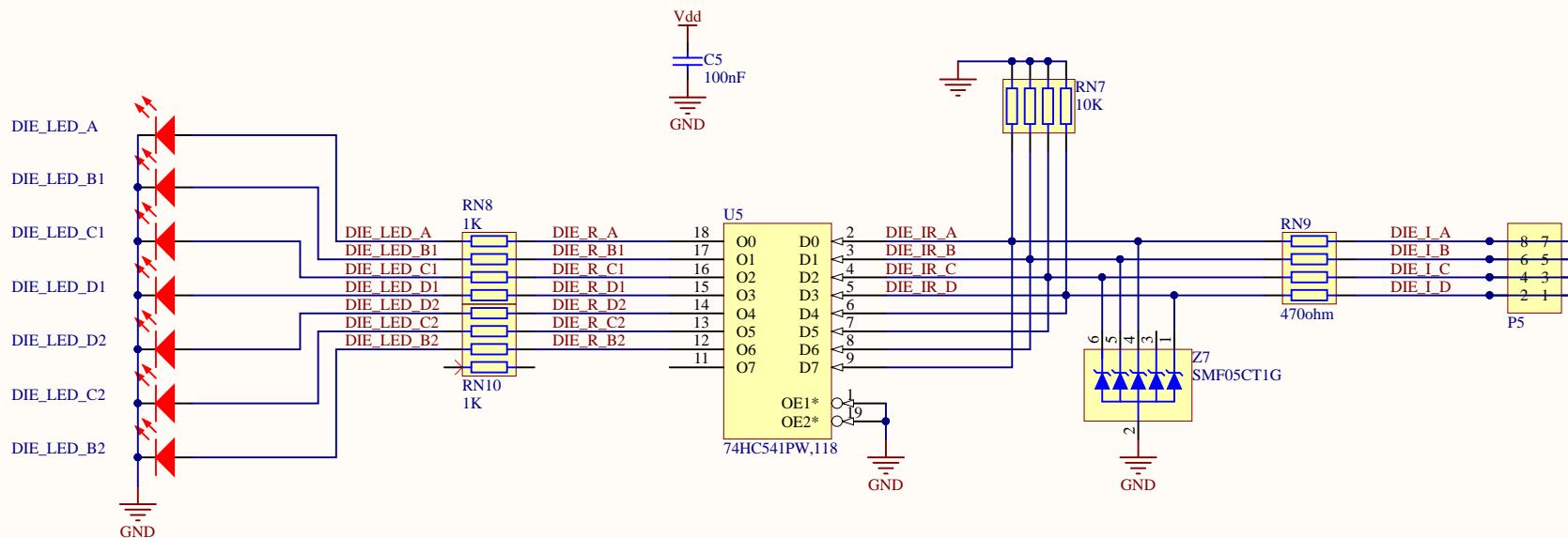


A

A



B

B

C

C

D

D

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Date: 4/11/2019	Sheet	of
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B

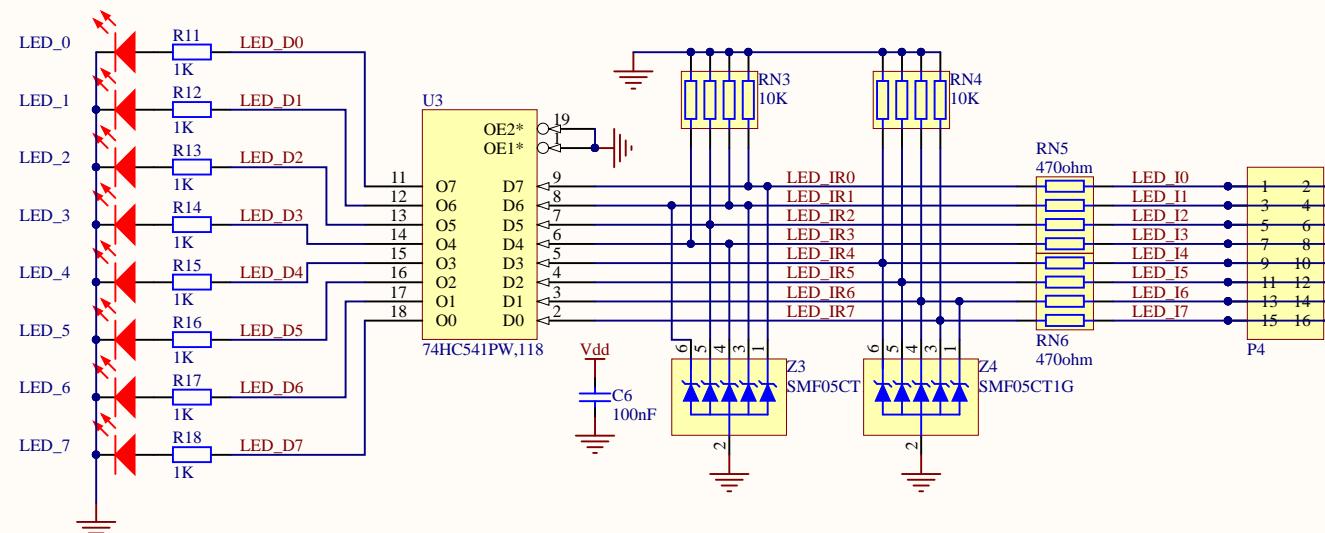
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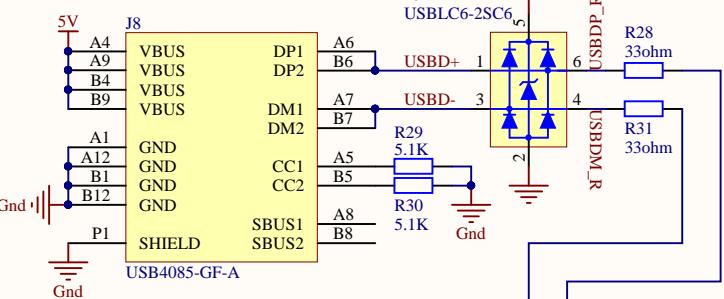
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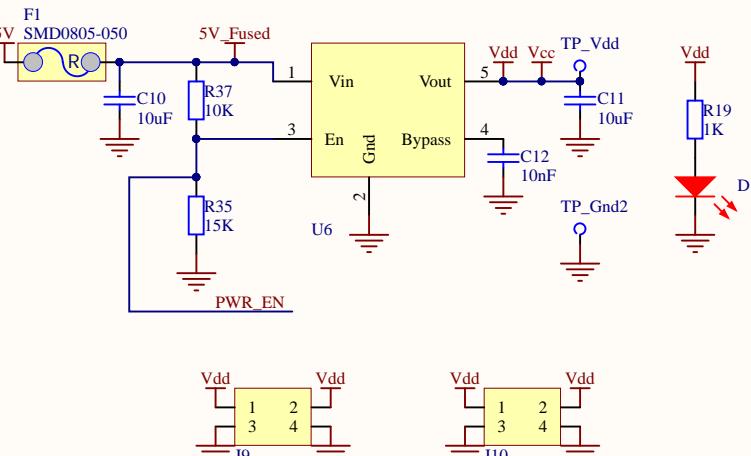


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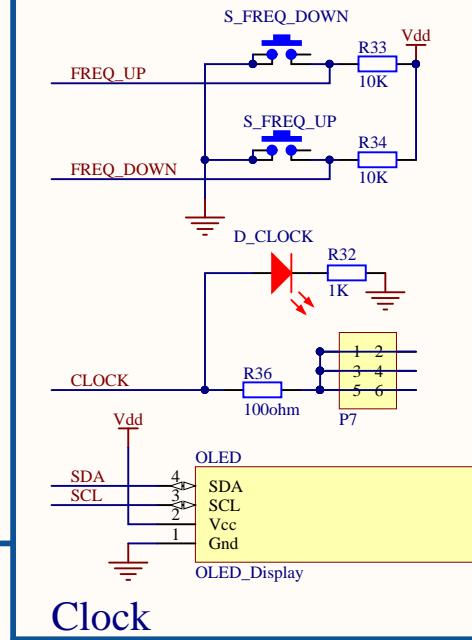
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2



3



S_FREQ_DOWN

FREQ_UP

FREQ_DOWN

D_CLOCK

CLOCK

OLED

SDA

SCL

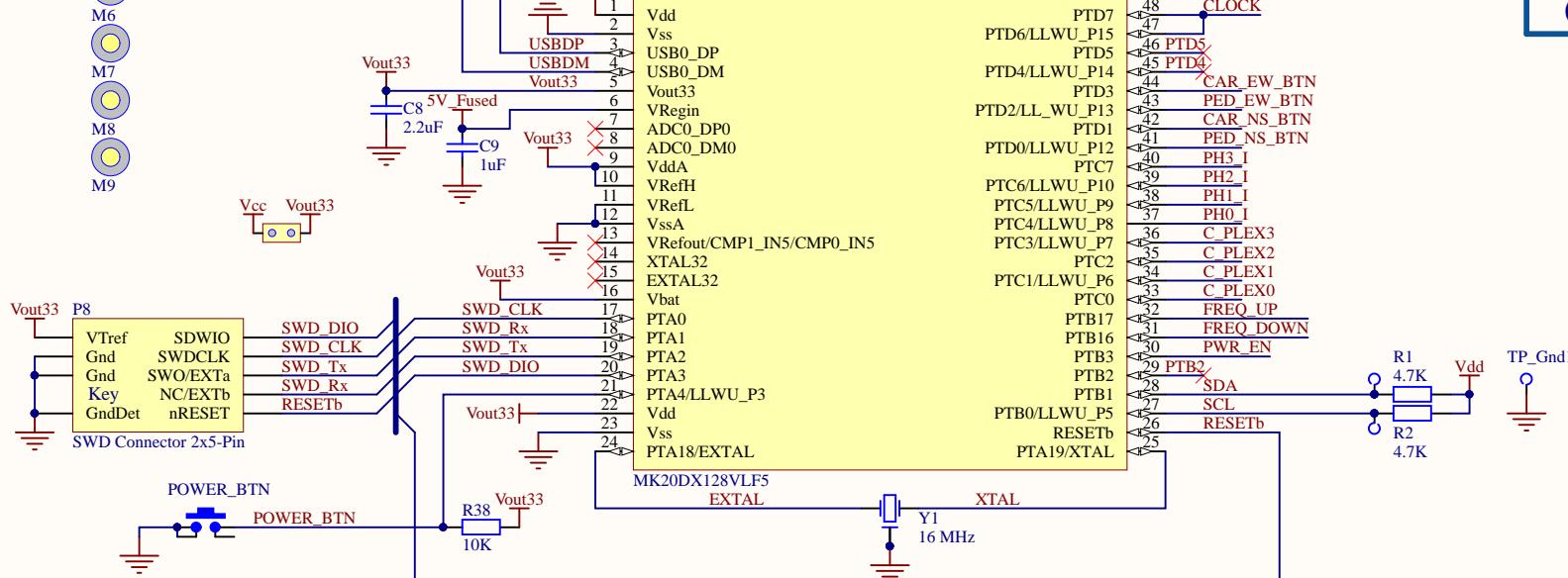
Vcc

Gnd

OLED_Display

Clock

4



Title

Size

A4

Number

Revision

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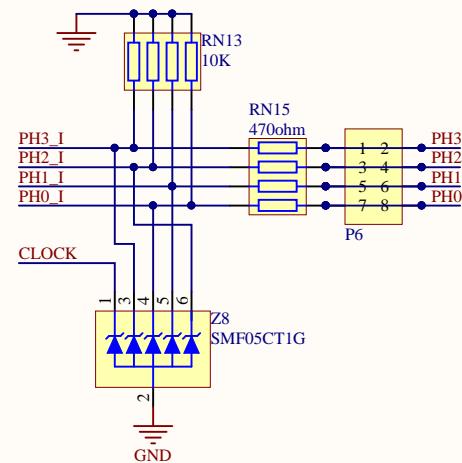
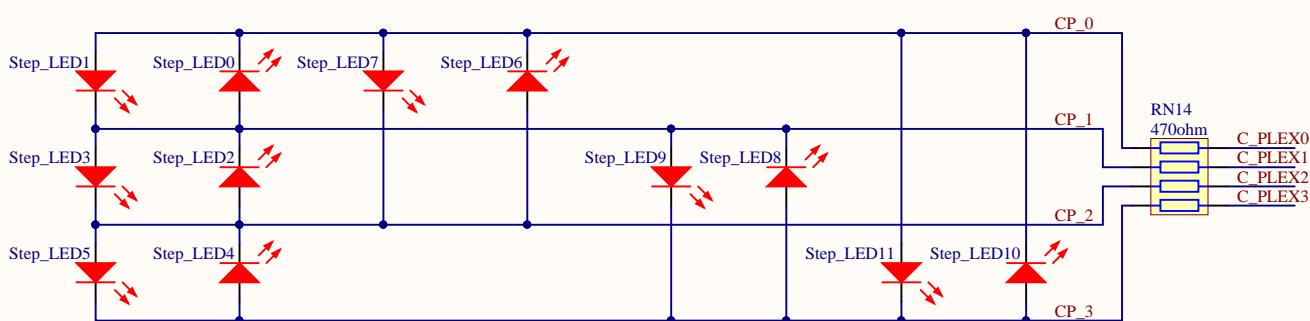
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Title

Size
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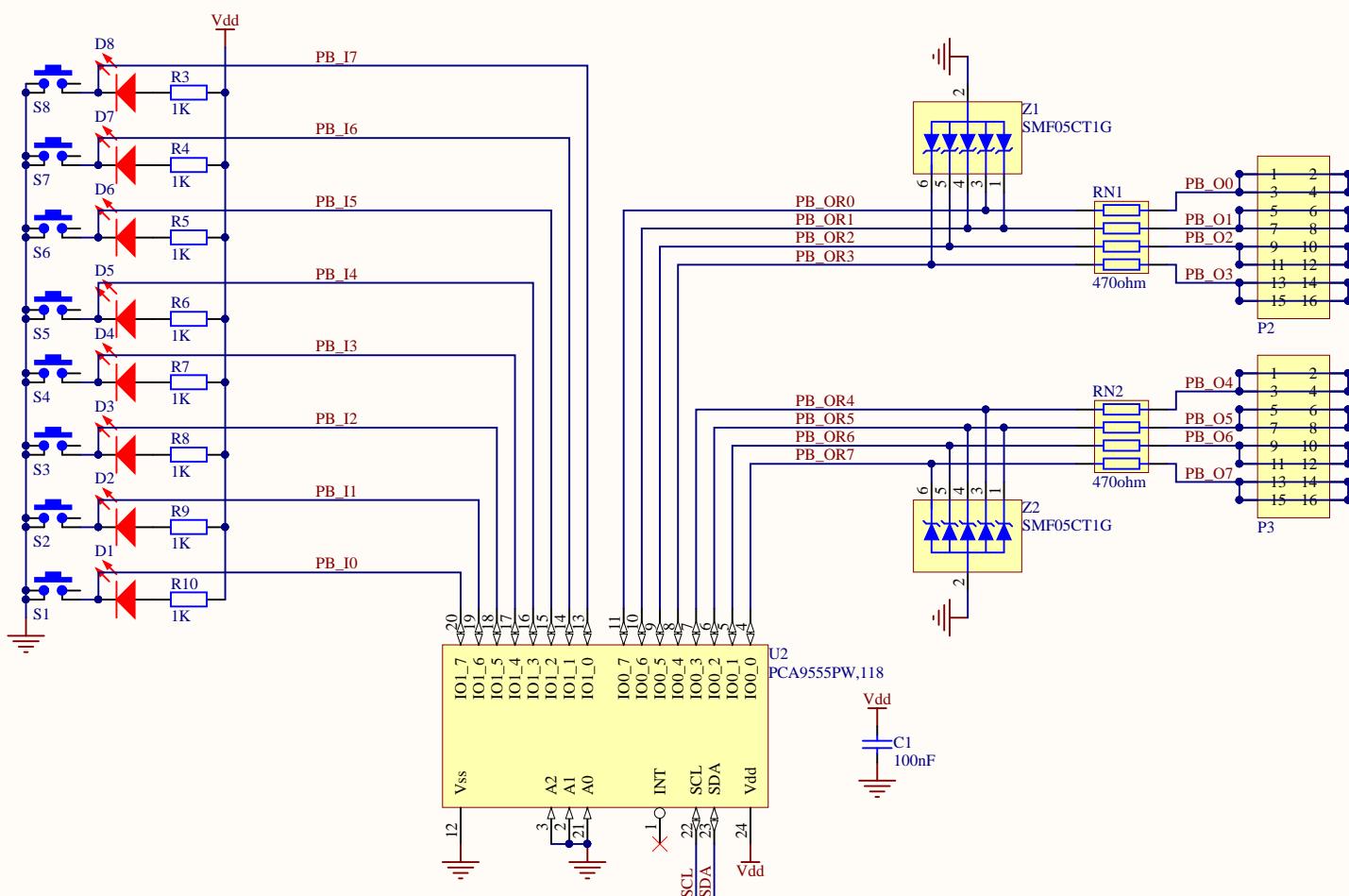
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Date: 4/11/2019

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of

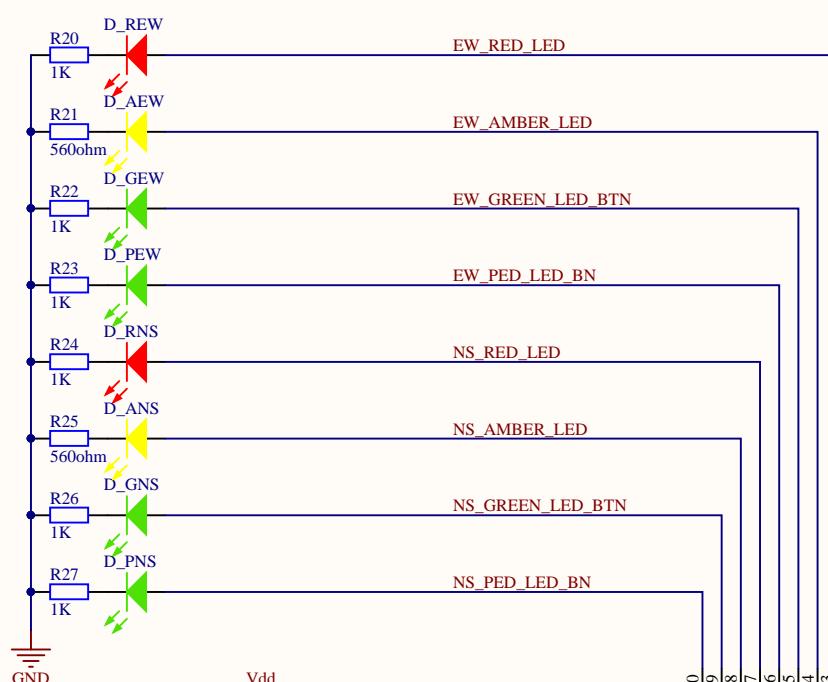
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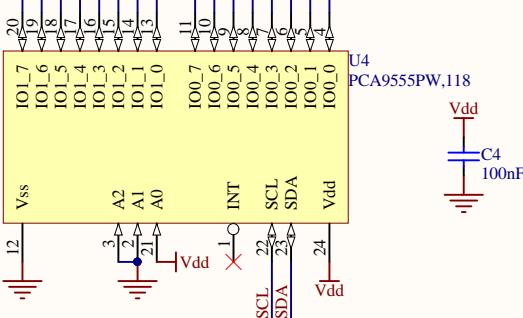
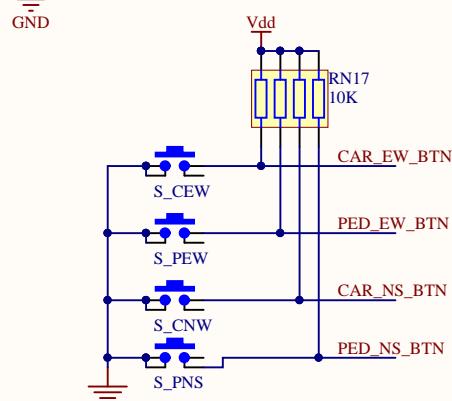


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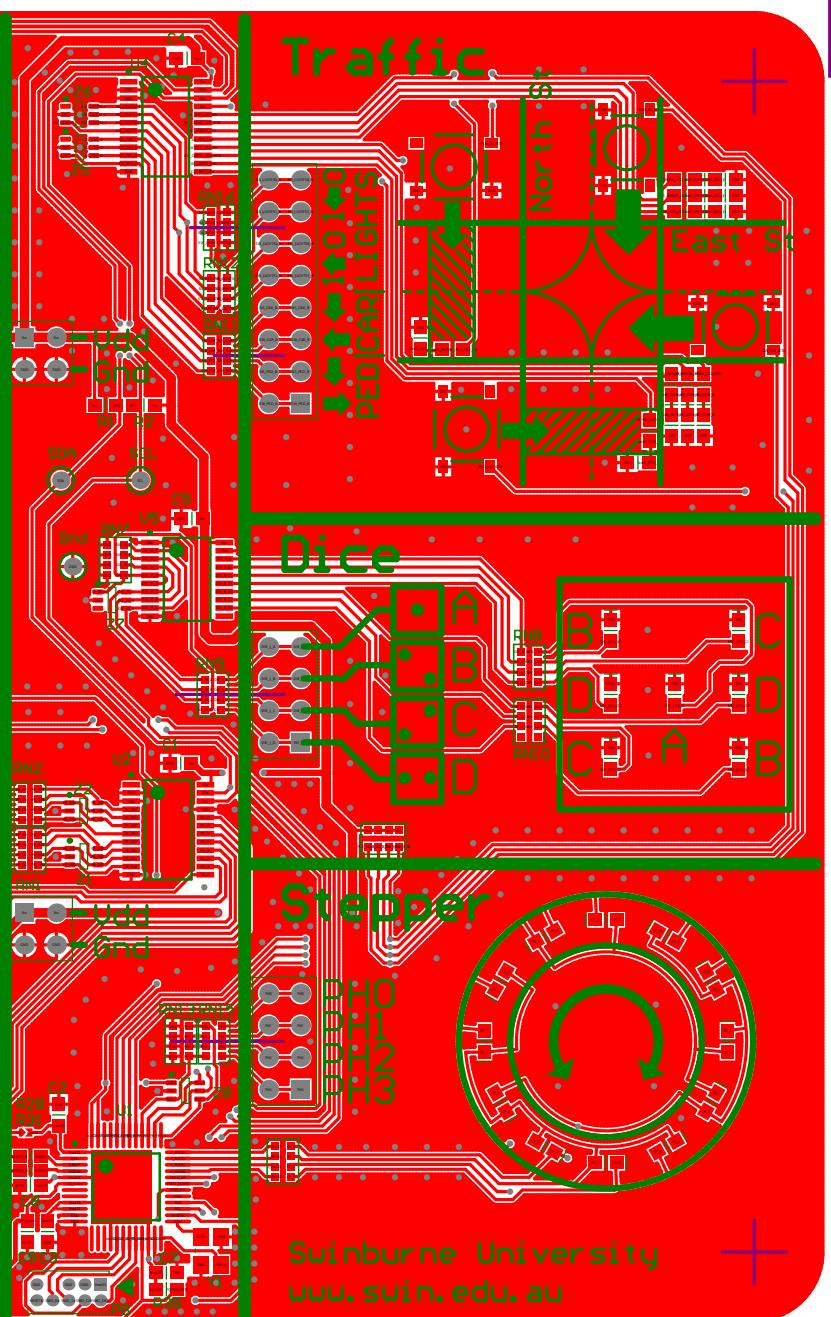
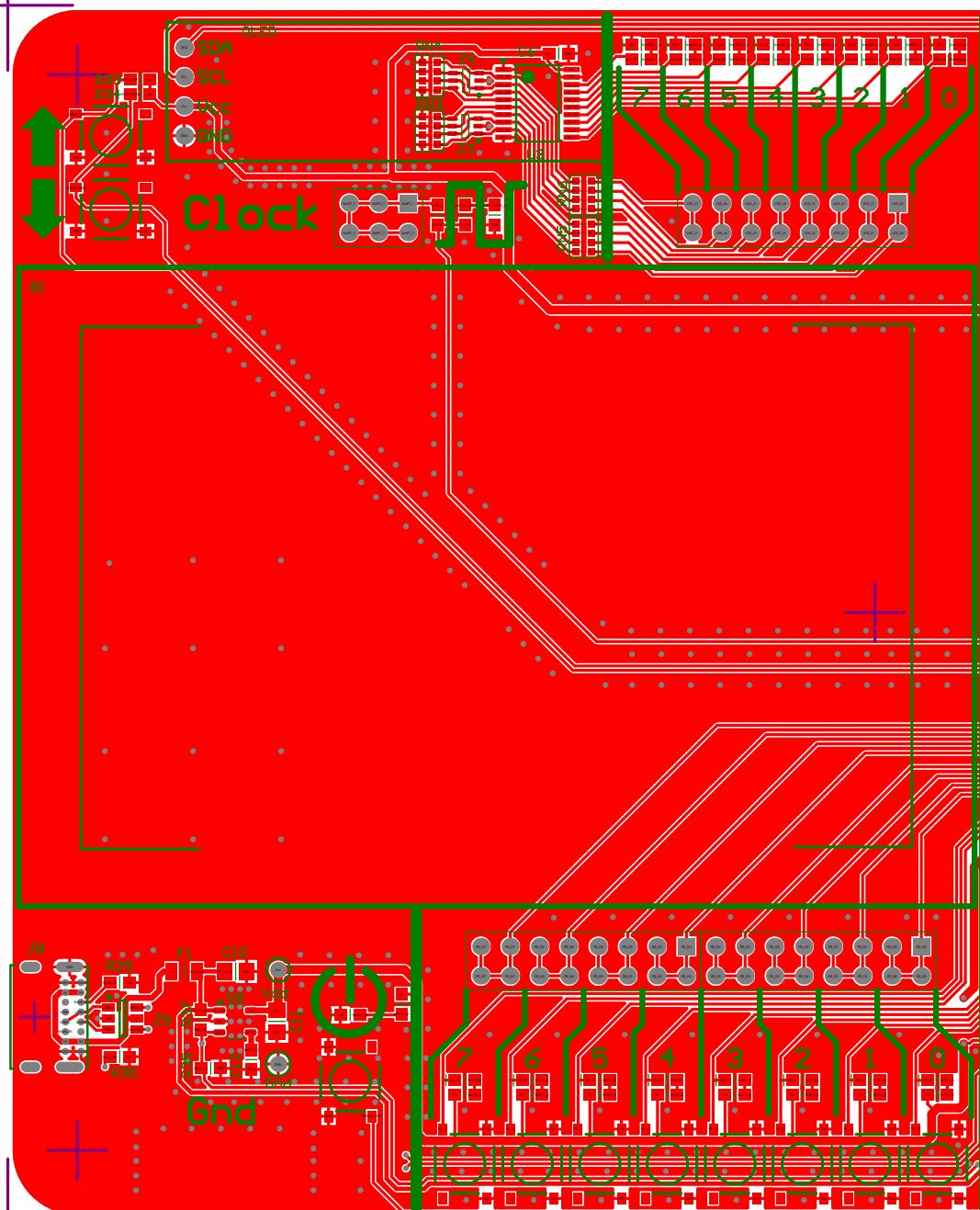
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D

Title		
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Date: 4/11/2019	Sheet	of
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150.00





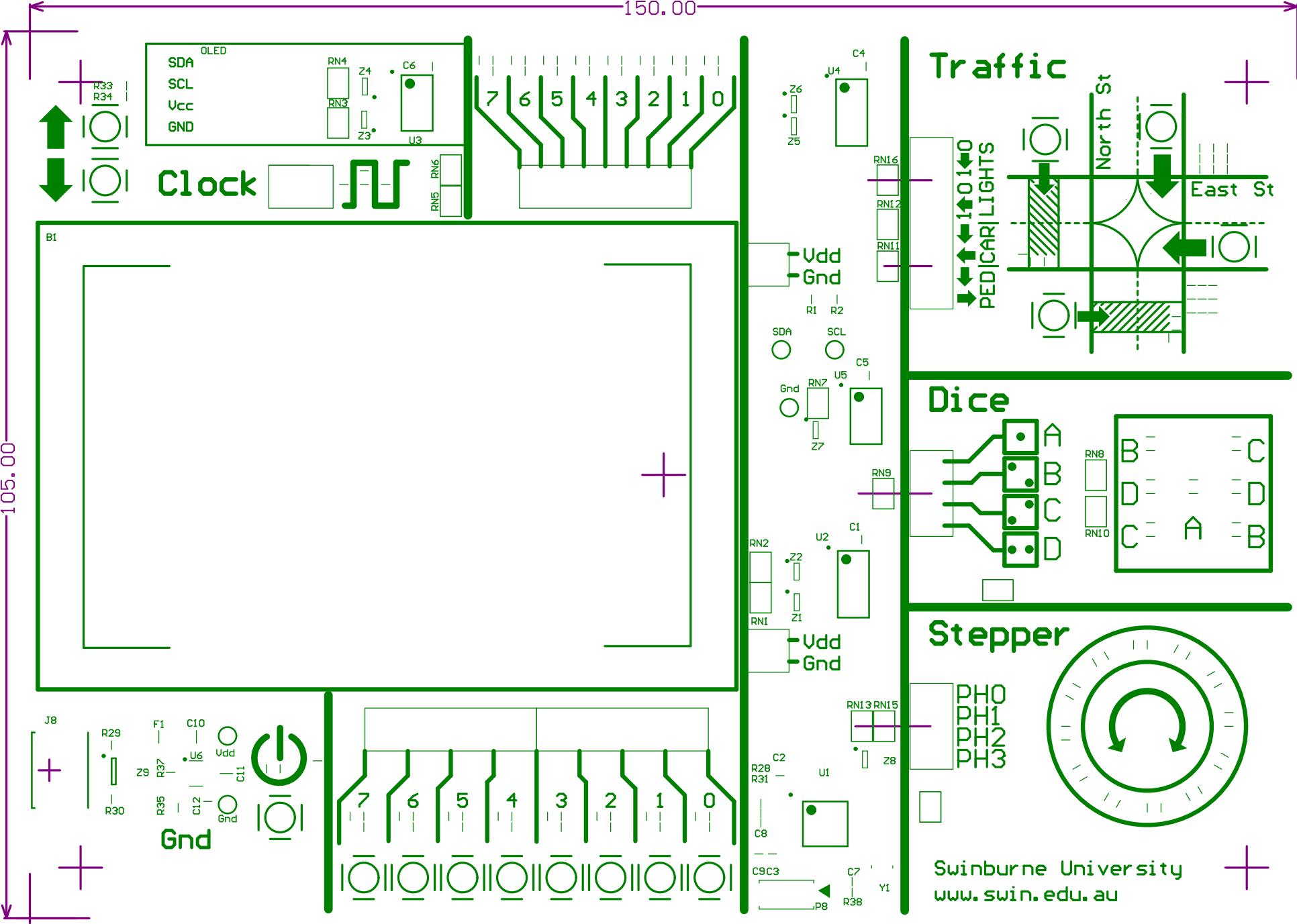
DIGITAL ELECTRONICS DESIGN
SWINBURNE UNIVERSITY
OF TECHNOLOGY



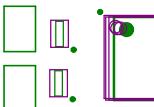
REV. 0.1

*Based on a design by
Jordan Boekel, Ben Gregg, Patrick Curtain*

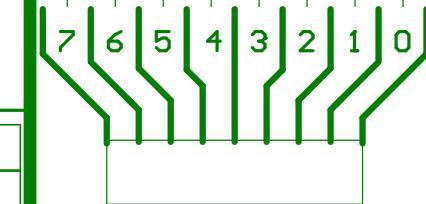
If found please call (+613) 9214 8319 .



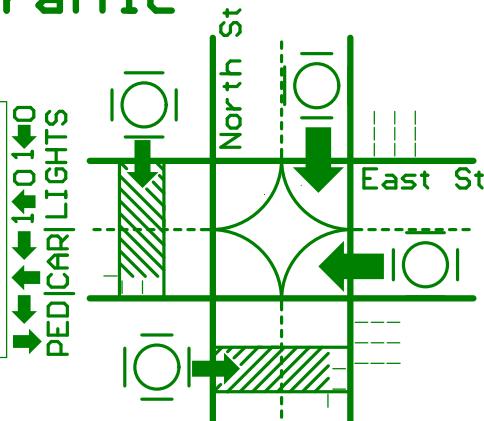
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SCL
Vcc
GND



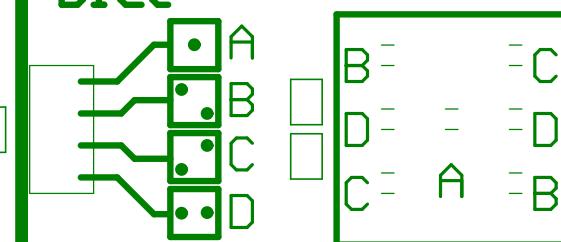
Clock



Traffic

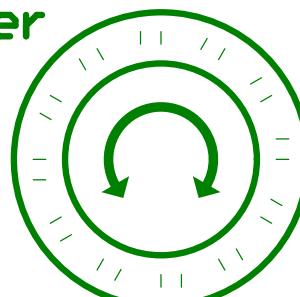


Dice



Stepper

PH0
PH1
PH2
PH3



Gnd

