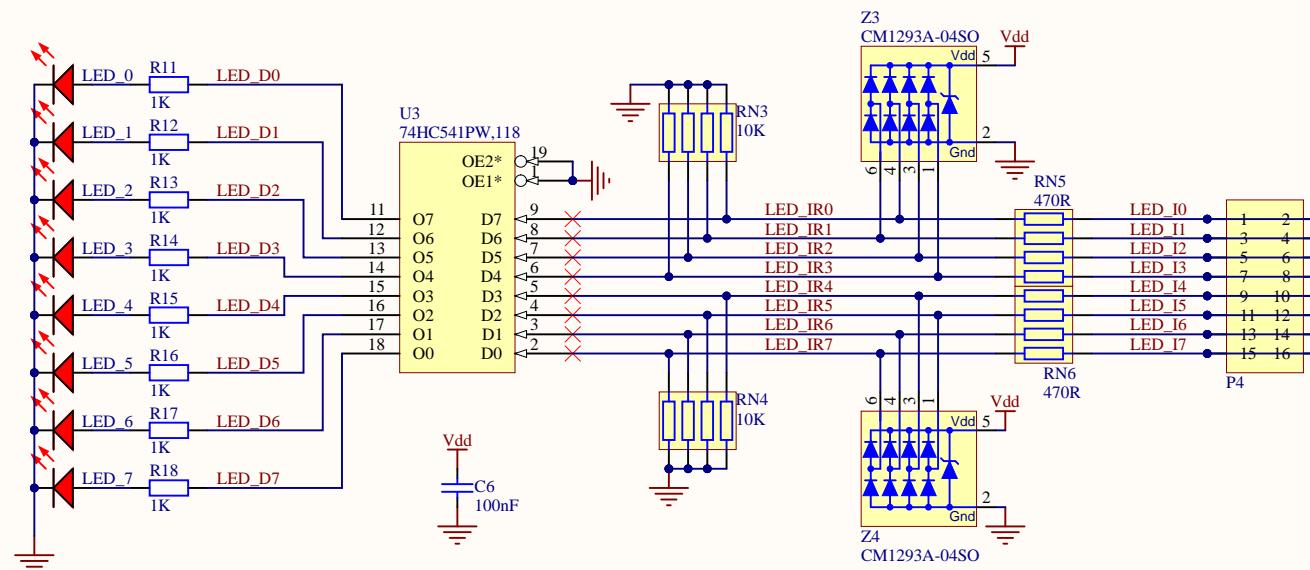
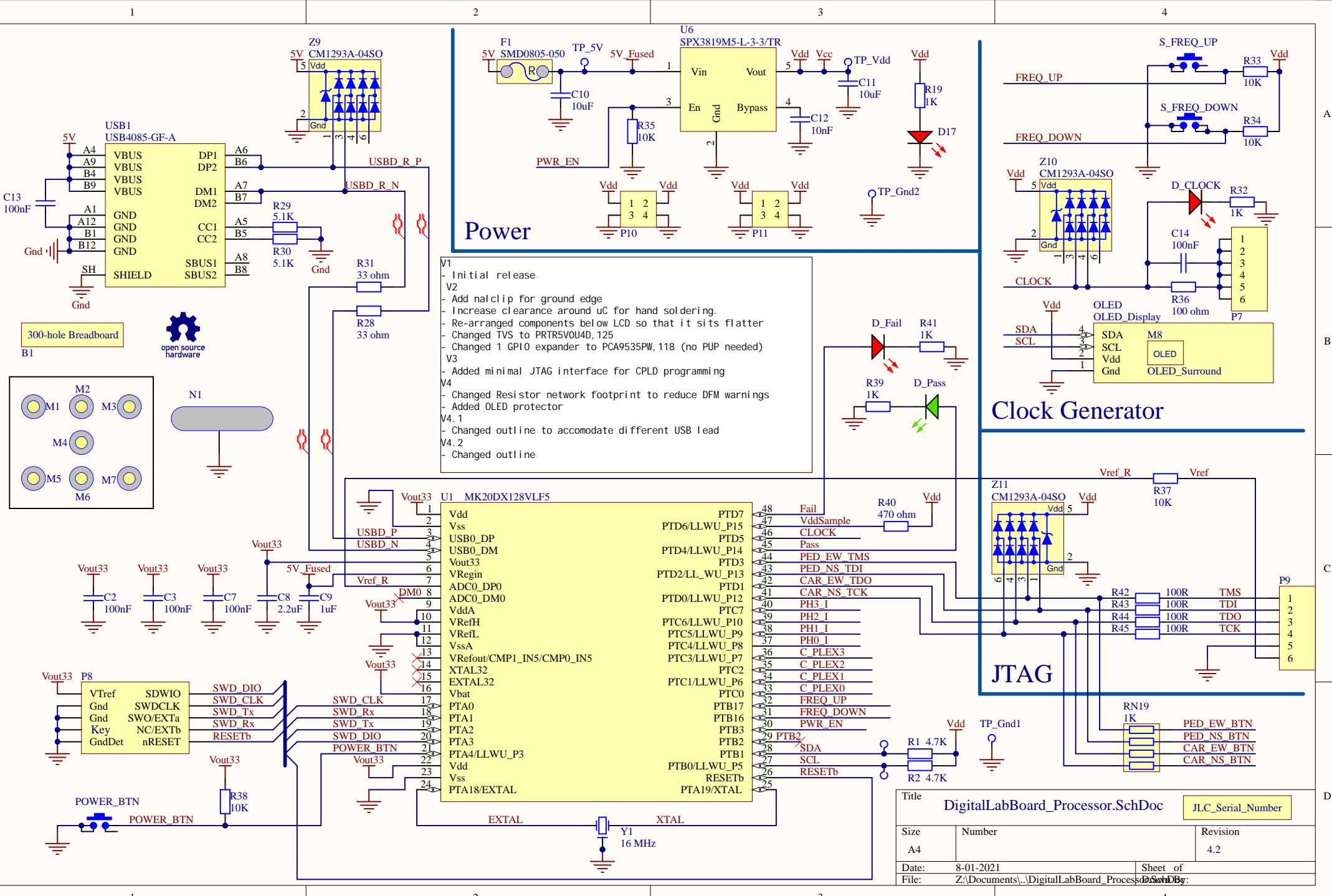


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| Size A4 | Number | Revision 4.2 |
| Date: 8-01-2021 | Sheet of | |
| File: Z:\Documents\..\DigitalLabBoard_Die.SchDoc | | Drawn By: |



Title **DigitalLabBoard_LEDs.SchDoc**

| Size | Number | Revision |
|-------|---|--------------|
| A4 | | 4.2 |
| Date: | 8-01-2021 | Sheet of |
| File: | Z:\Documents\..\DigitalLabBoard_LEDs.SchDoc | Download By: |



A

A

B

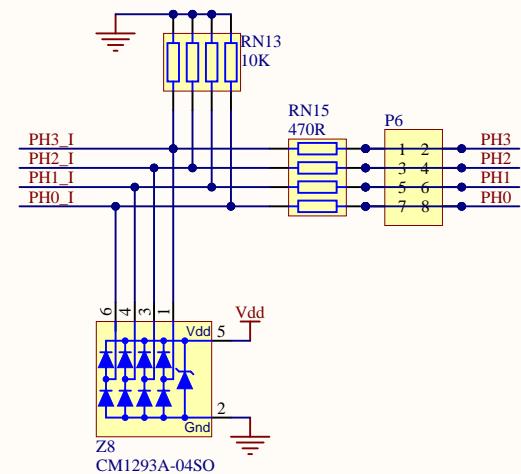
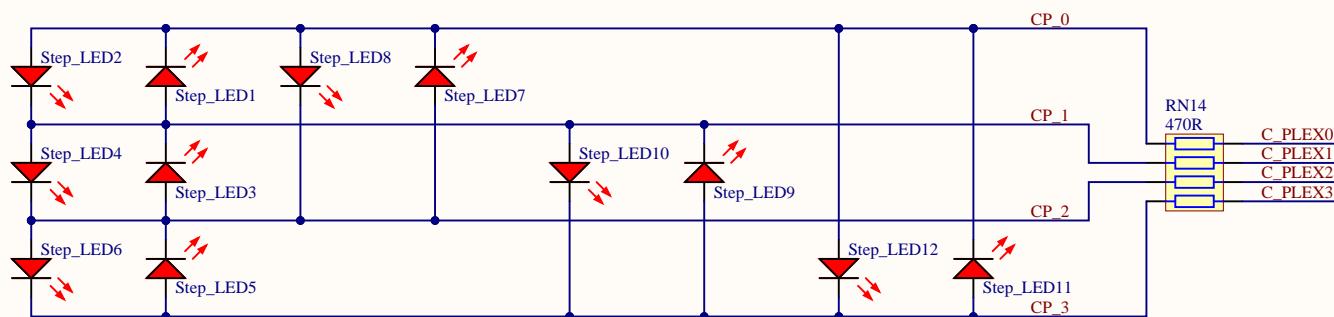
B

C

C

D

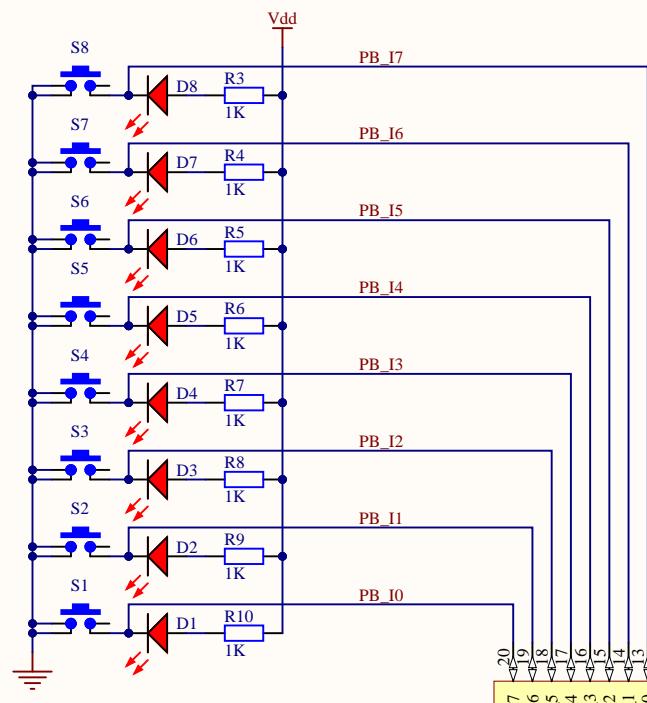
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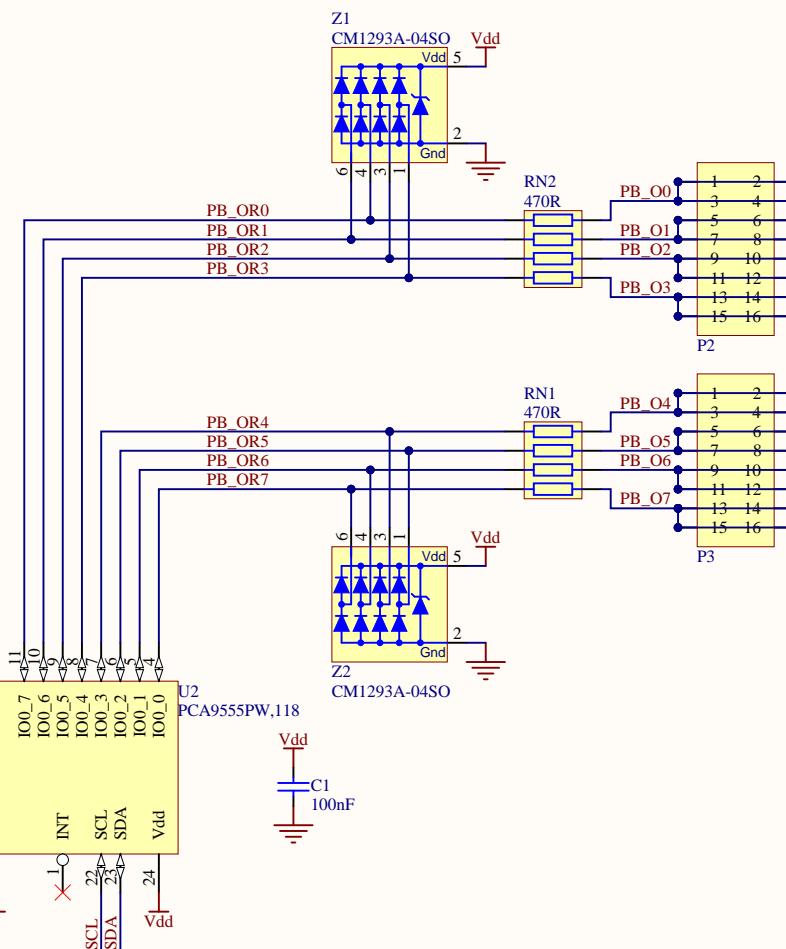
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| Size | Number | Revision |
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A



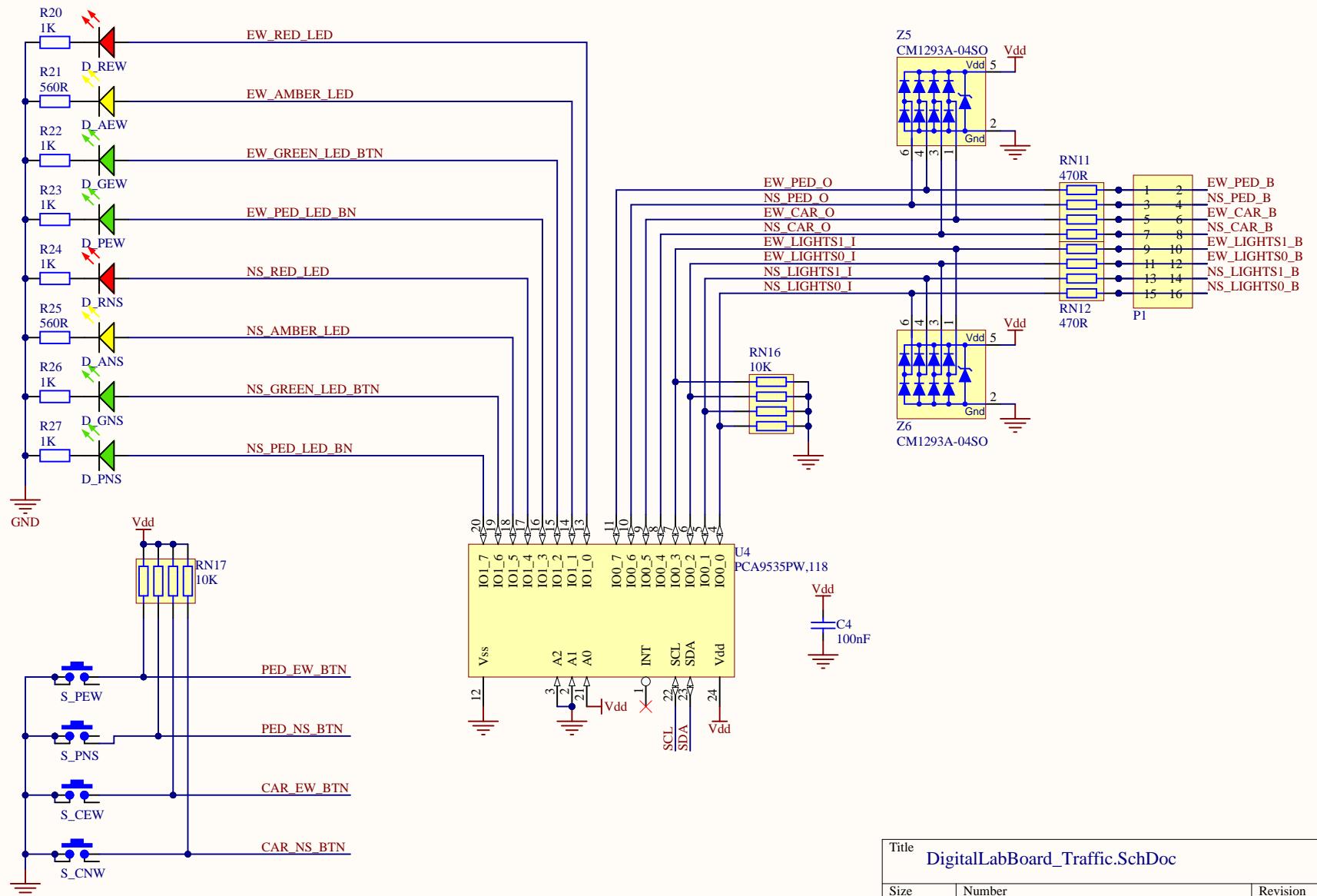
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C

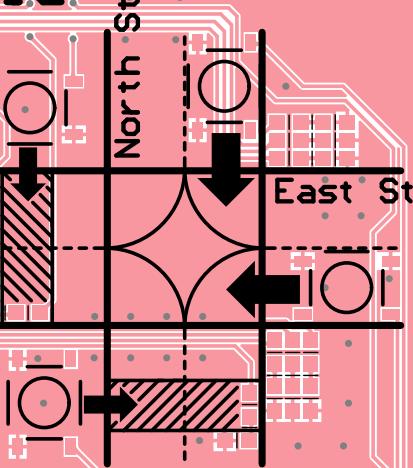
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| Title DigitalLabBoard_Traffic.SchDoc | | |
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| Size A4 | Number | Revision 4.2 |
| Date: 8-01-2021 | | Sheet of |
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Traffic



Clock



Pass

Fail

VDD
GND
TCK
TDO
TDI
TMS

R42
R43
R44
R45

USB1
F1
C10
C11
5V
Udd
Gnd
R29
Z9
R30
R35
R36
012
Gnd

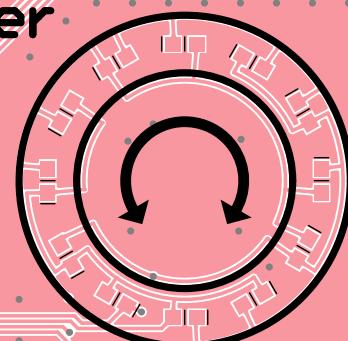
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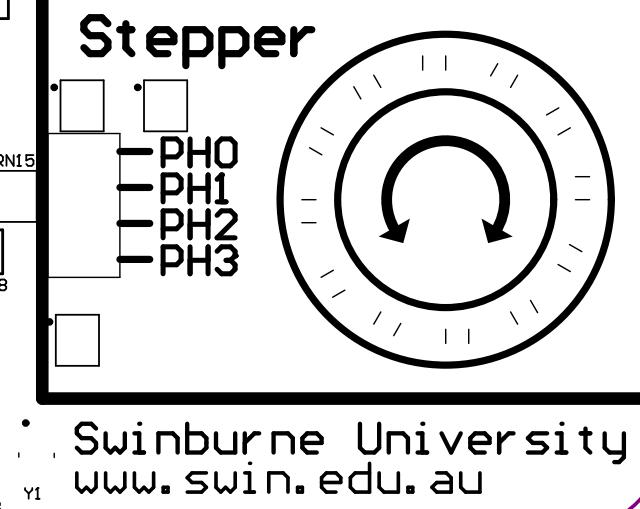
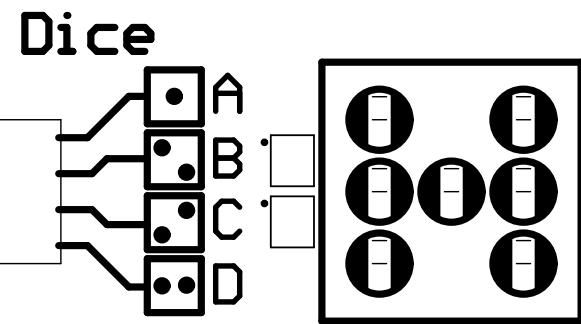
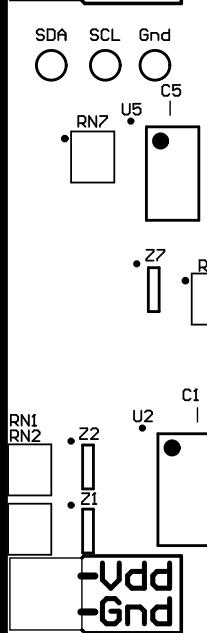
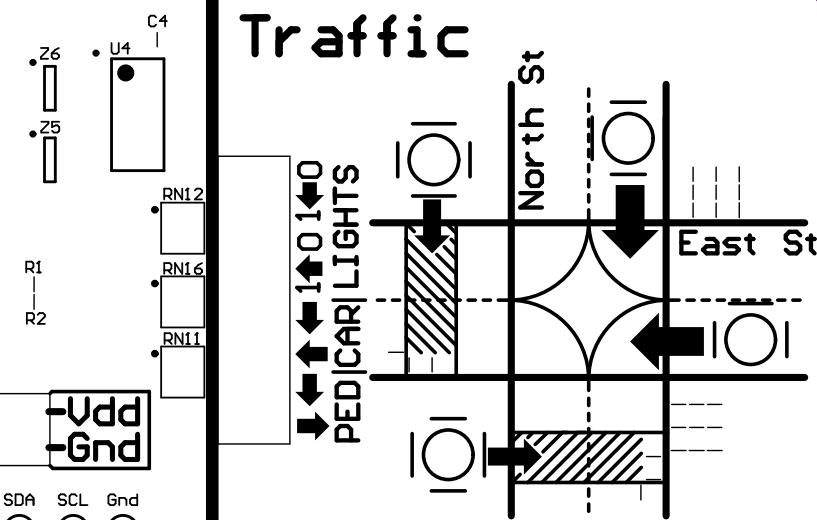
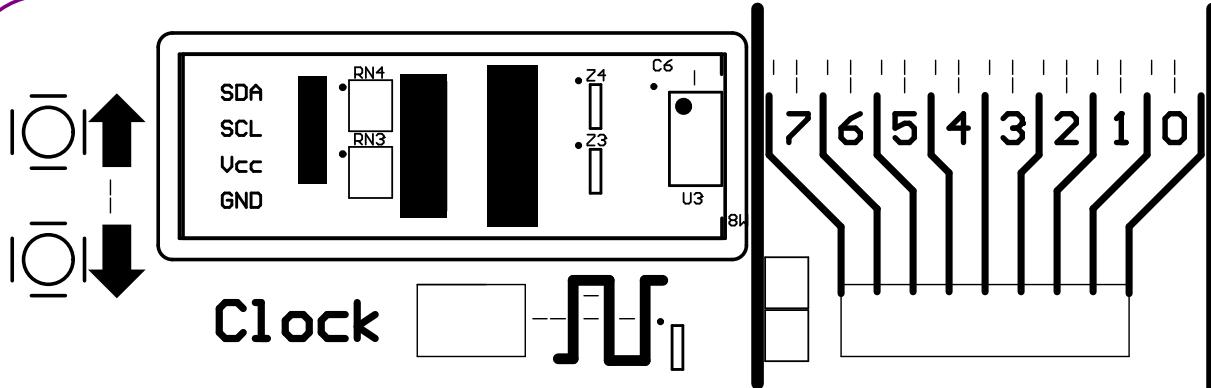
JLCJLCJLCJLC

Dice

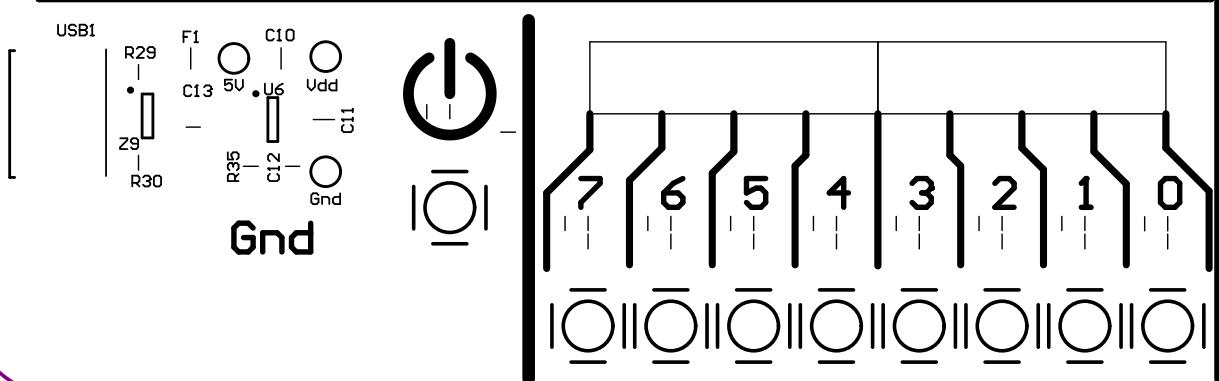
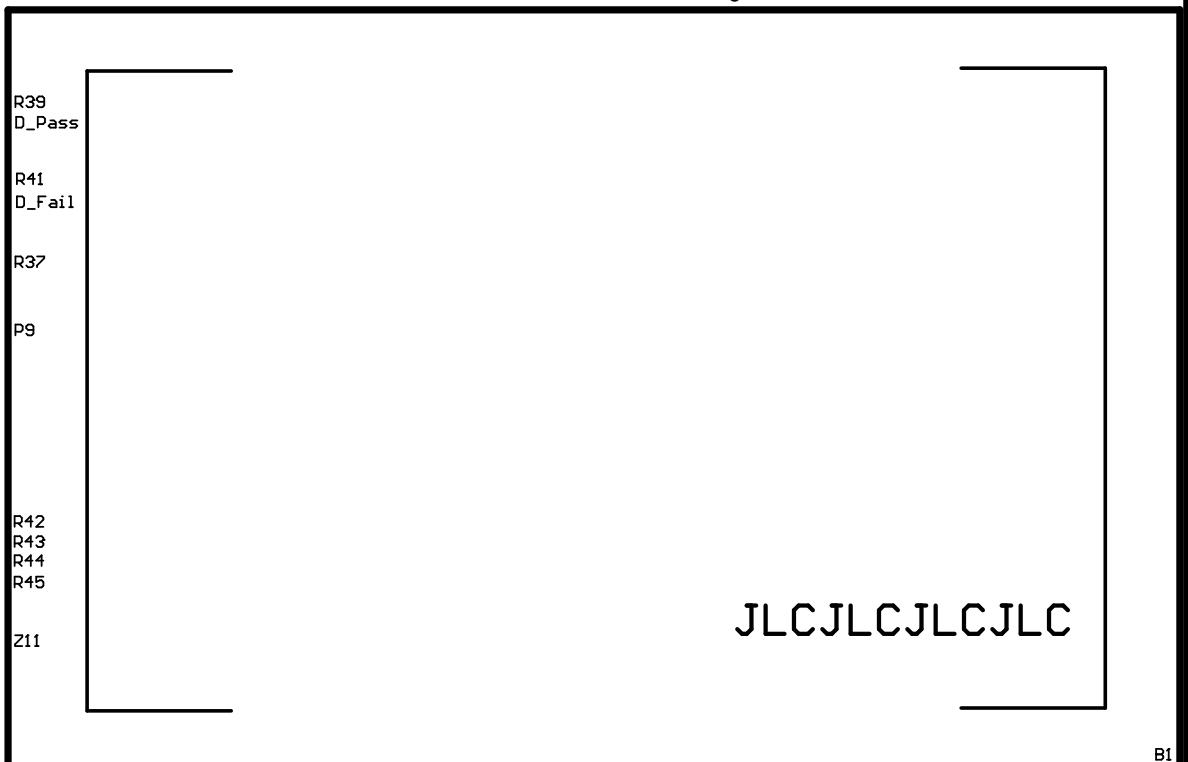
Stepper

PH0
PH1
PH2
PH3





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OSU

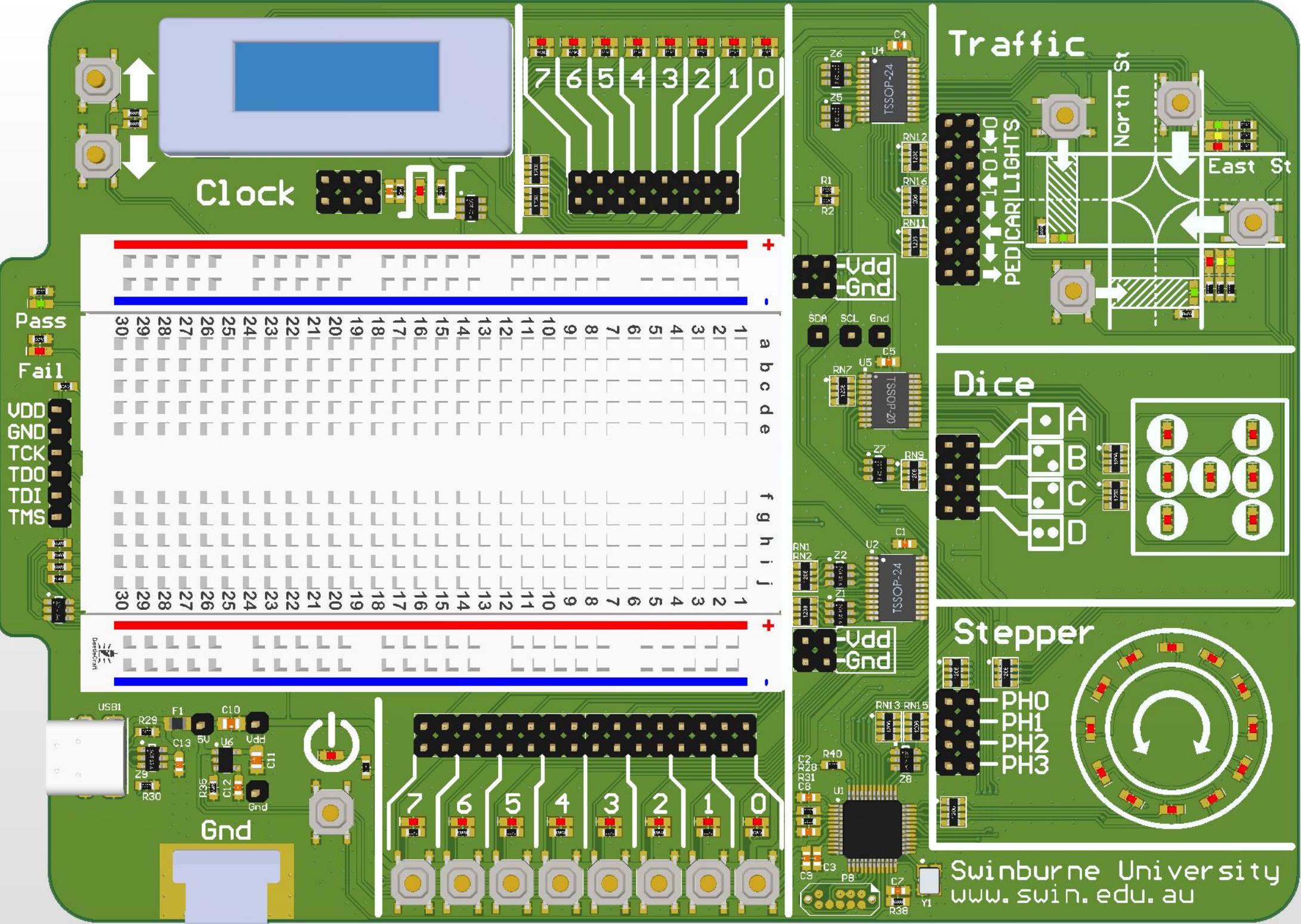


Based on a design by
Jordan Boekel, Ben Gregg,
Patrick Curtain

Ver = 4.2

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