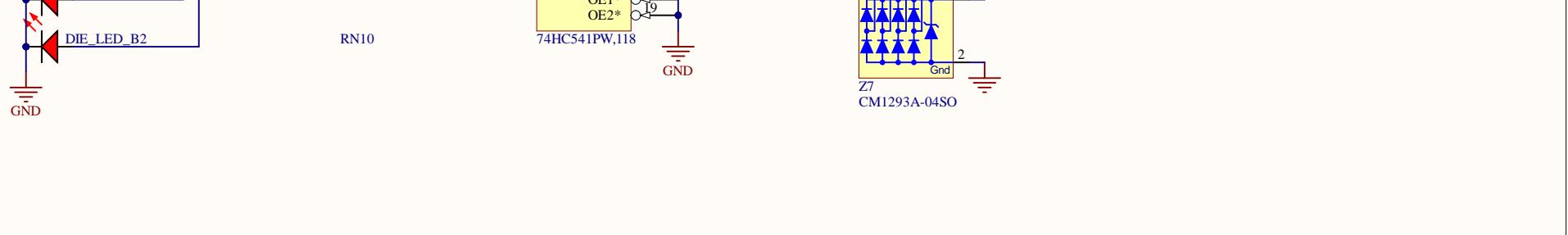


A A

B B

C C

D D



Title DigitalLabBoard_Die.SchDoc		
Size A4	Number	Revision 4.2
Date: 1/19/2021	Sheet of	
File: H:\Documents\..\DigitalLabBoard_Die.Sch	Drawn By:	

A

B

C

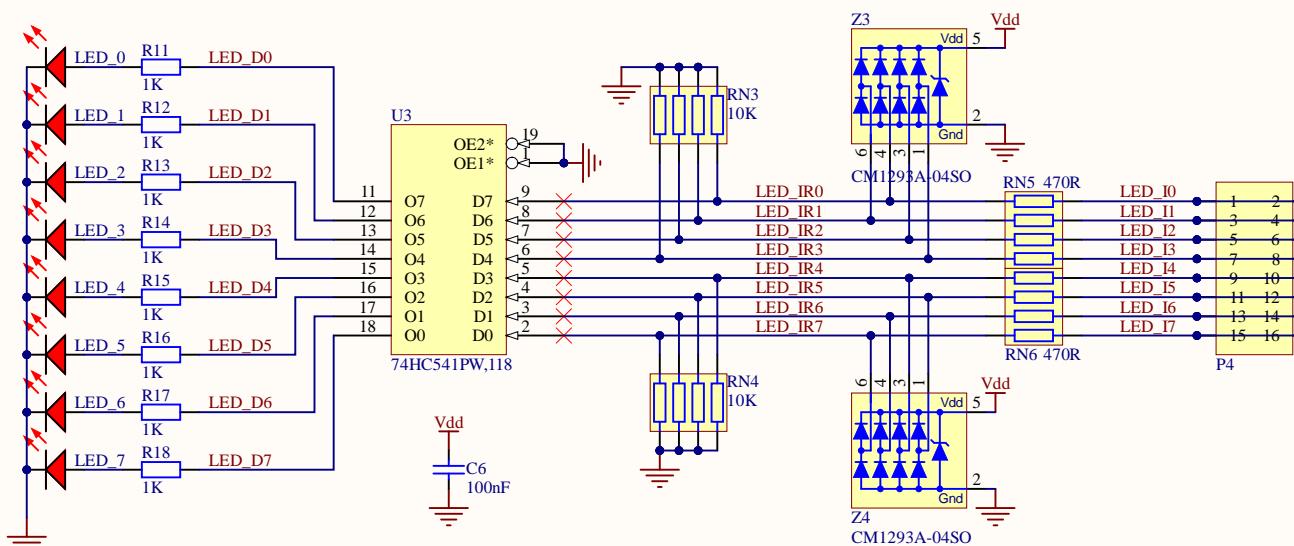
D

A

B

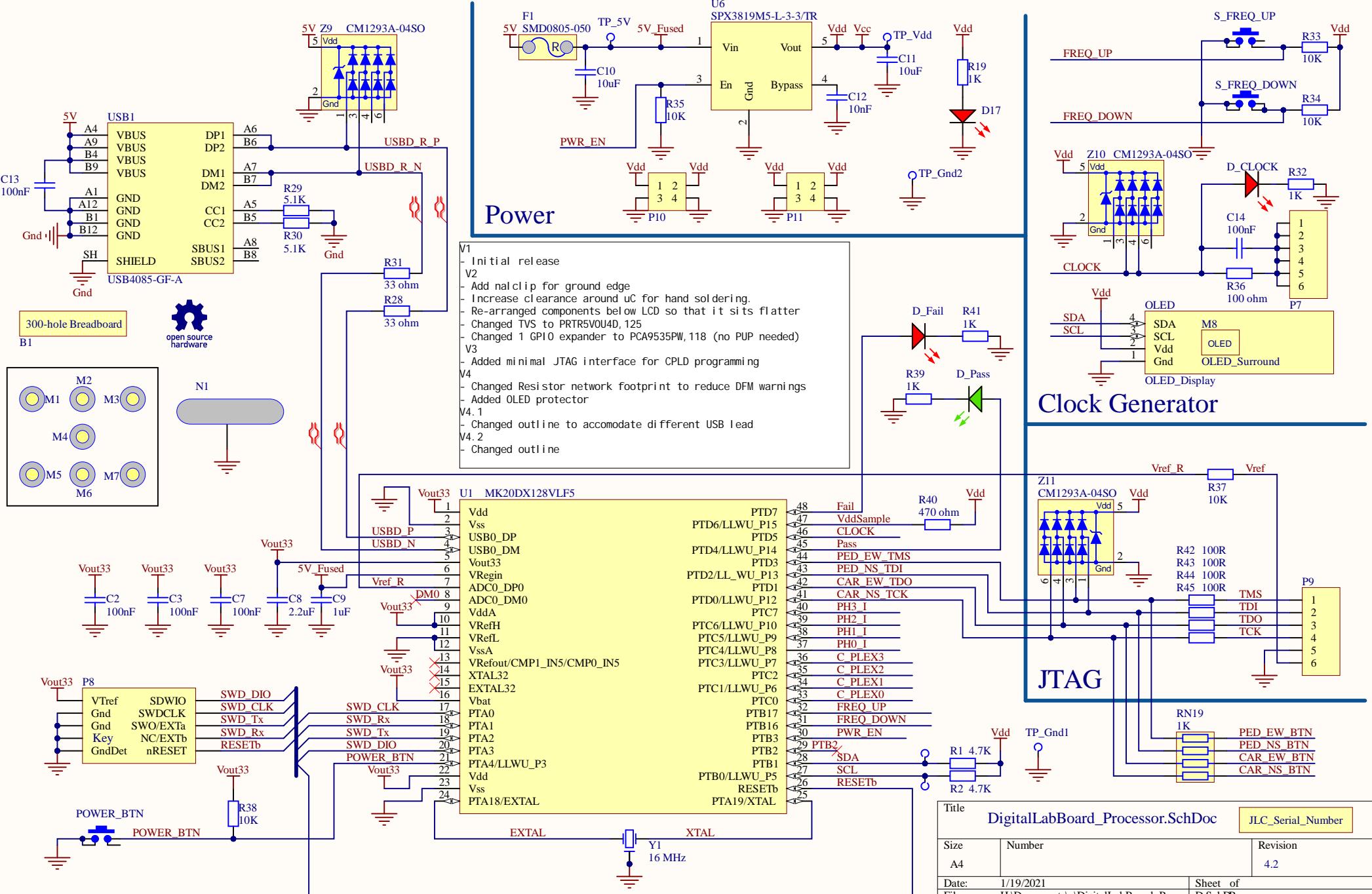
C

D

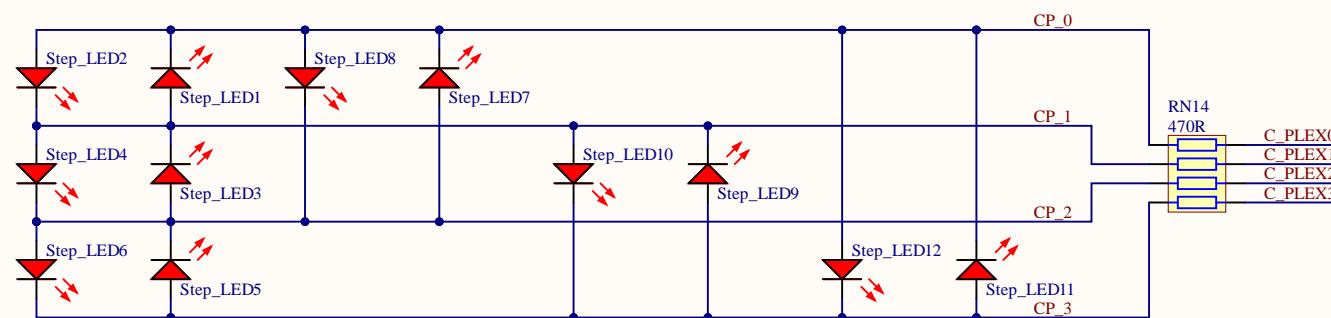


Title: DigitalLabBoard_LEDs.SchDoc

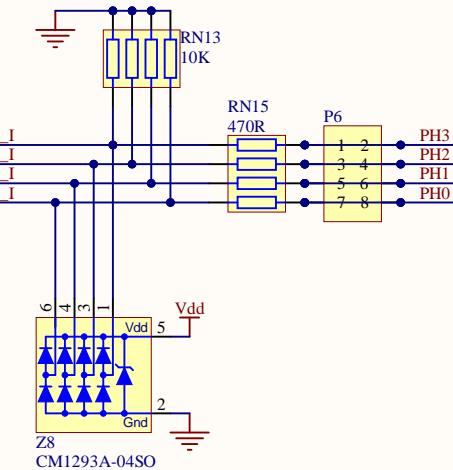
Size	Number	Revision
A4		4.2
Date:	1/19/2021	Sheet of
File:	H:\Documents\..\DigitalLabBoard_LEDs.SchDoc	Drawn By:



A



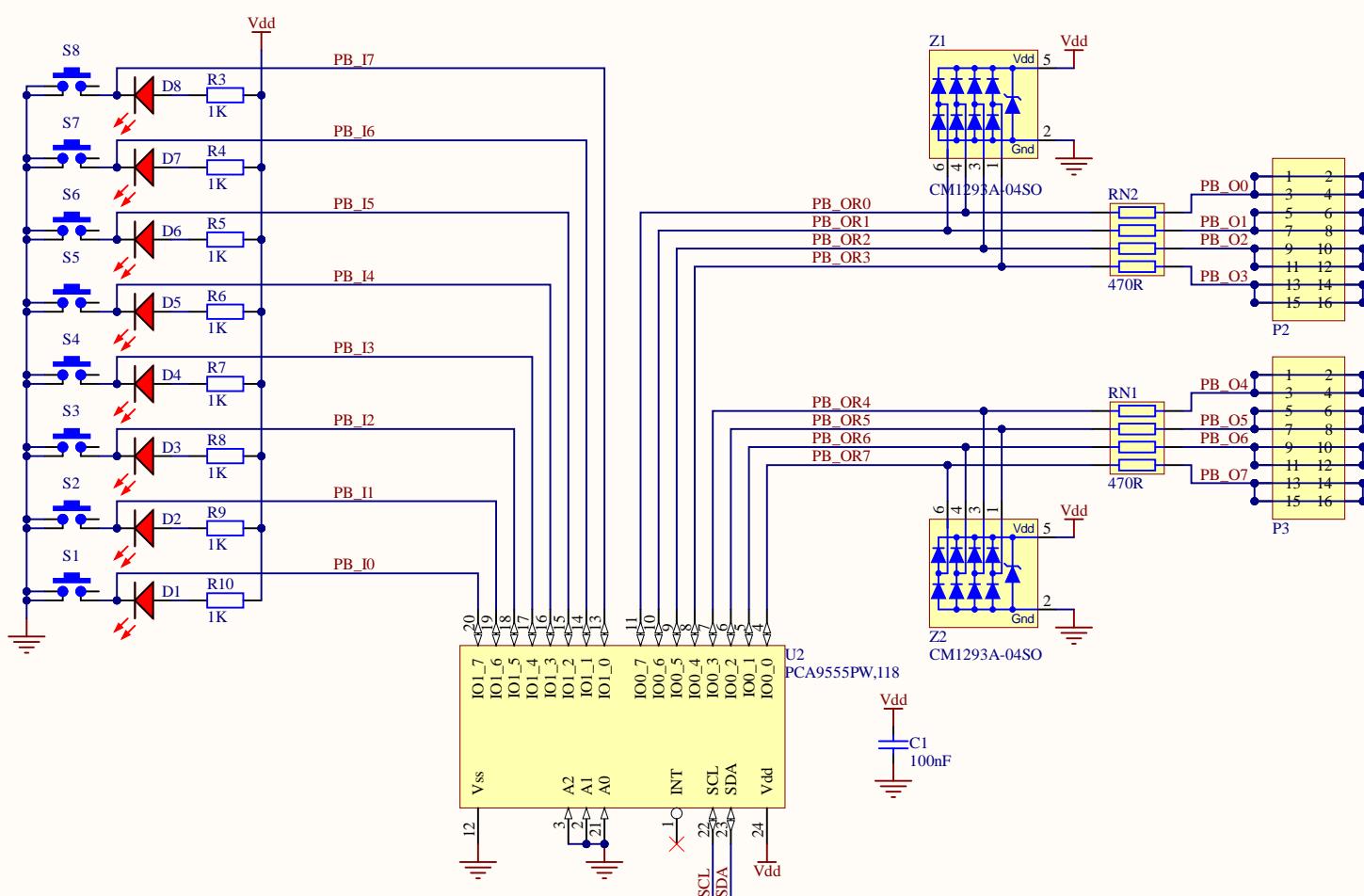
B



C

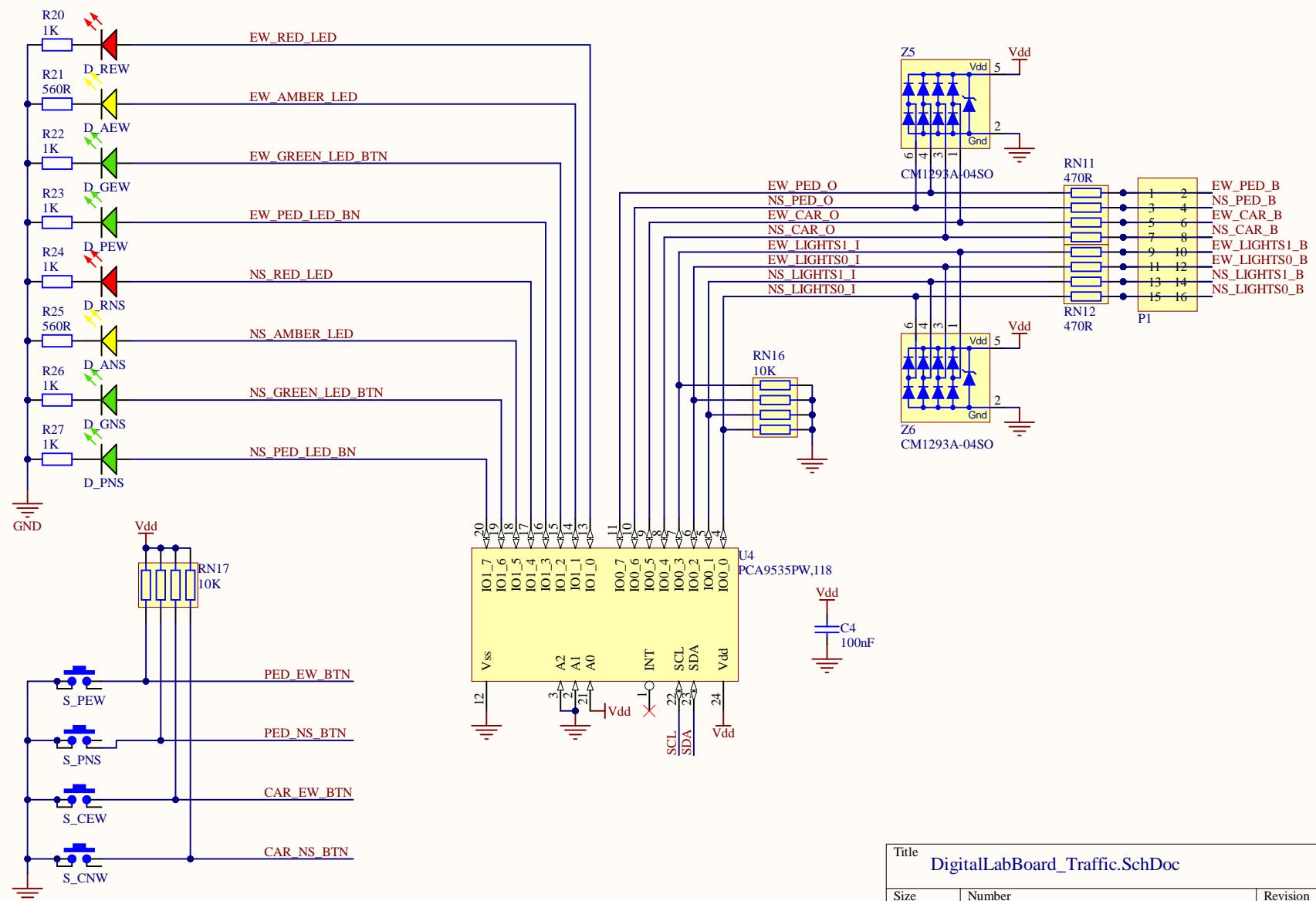
Title DigitalLabBoard_Stepper.SchDoc		
Size A4	Number	Revision 4.2
Date: 1/19/2021	Sheet of	
File: H:\Documents\..\DigitalLabBoard_Stepper.SchDoc	8	

A



Title: DigitalLabBoard_Switches.SchDoc

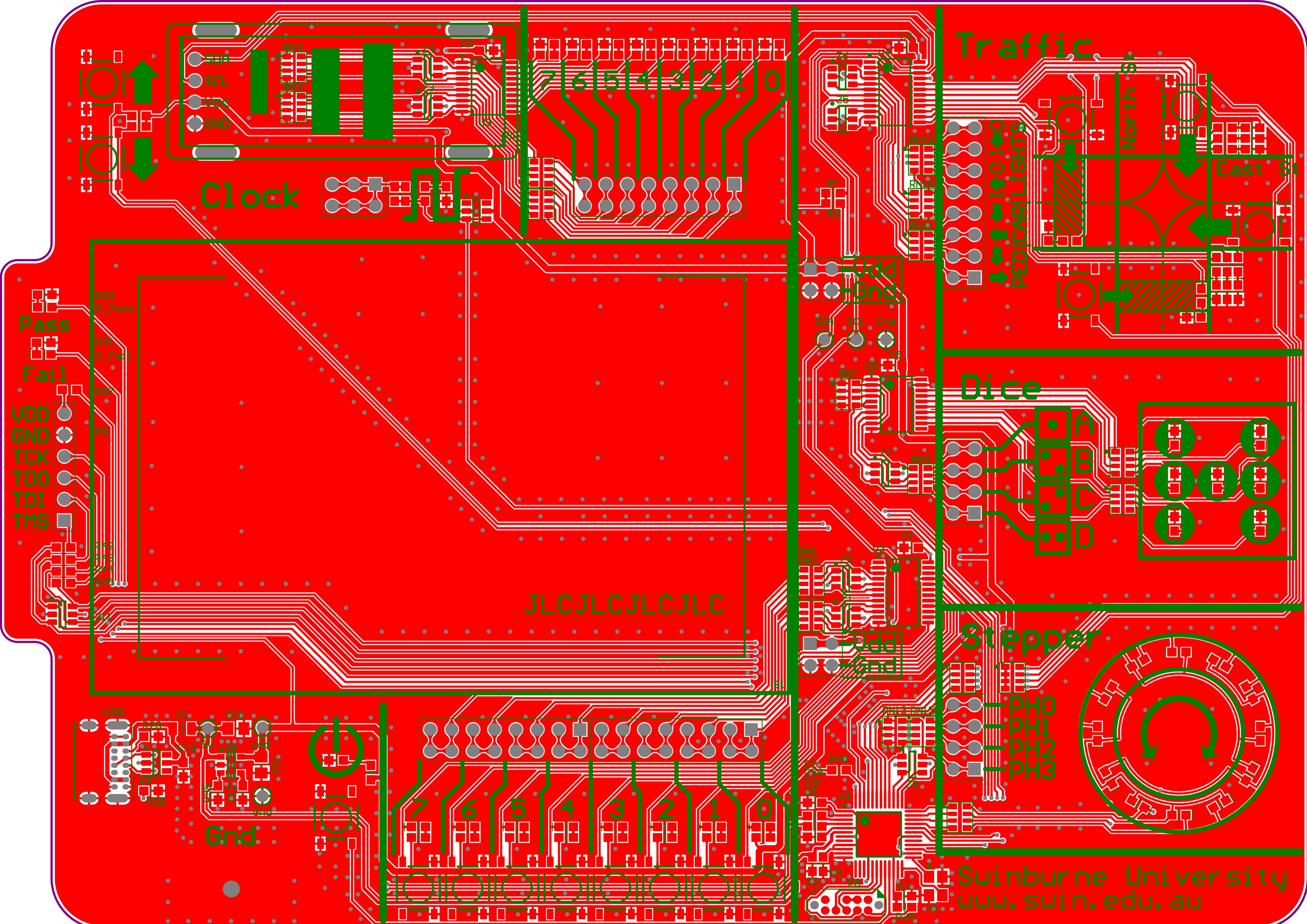
Size	Number	Revision
A4		4.2
Date:	1/19/2021	Sheet of
File:	H:\Documents\DigitalLabBoard_Switches.SchDoc	1

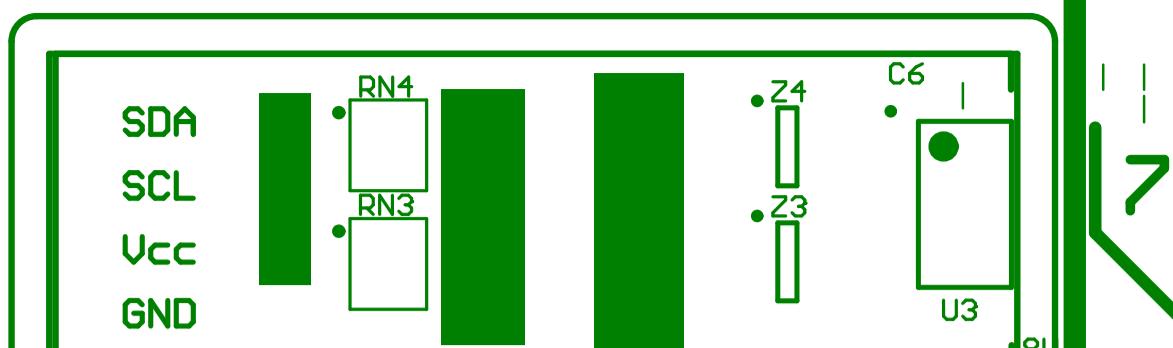
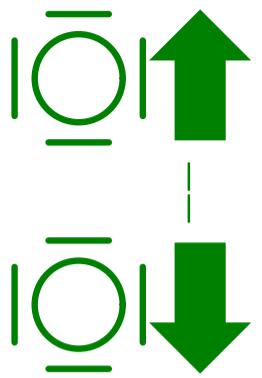


Title: DigitalLabBoard_Traffic.SchDoc

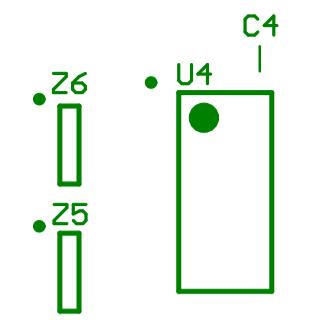
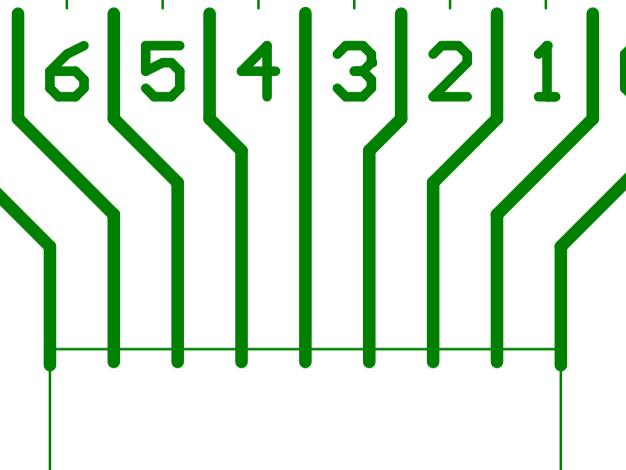
Size	Number	Revision
A4		4.2

Date:	1/19/2021	Sheet of
File:	H:\Documents\..\DigitalLabBoard_Traffic.SchDoc	10

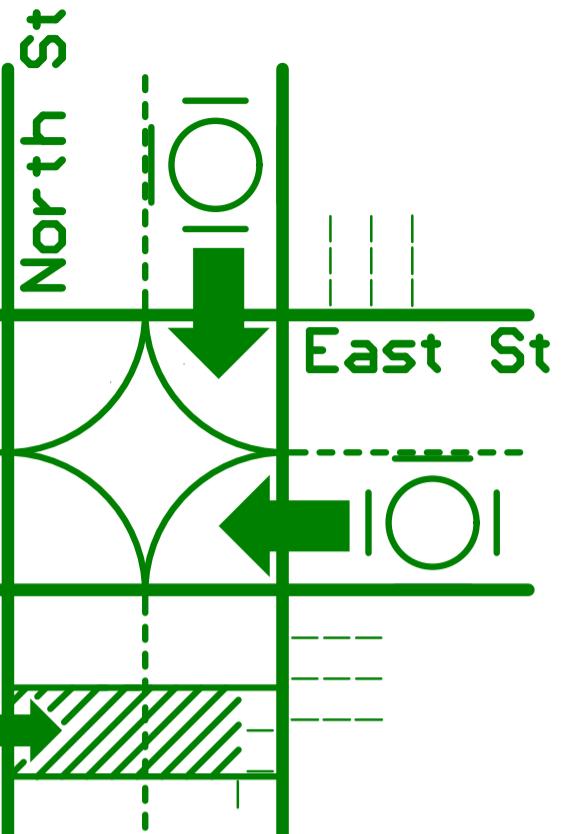




Clock



Traffic



Pass

Fail

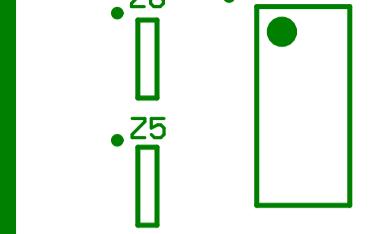
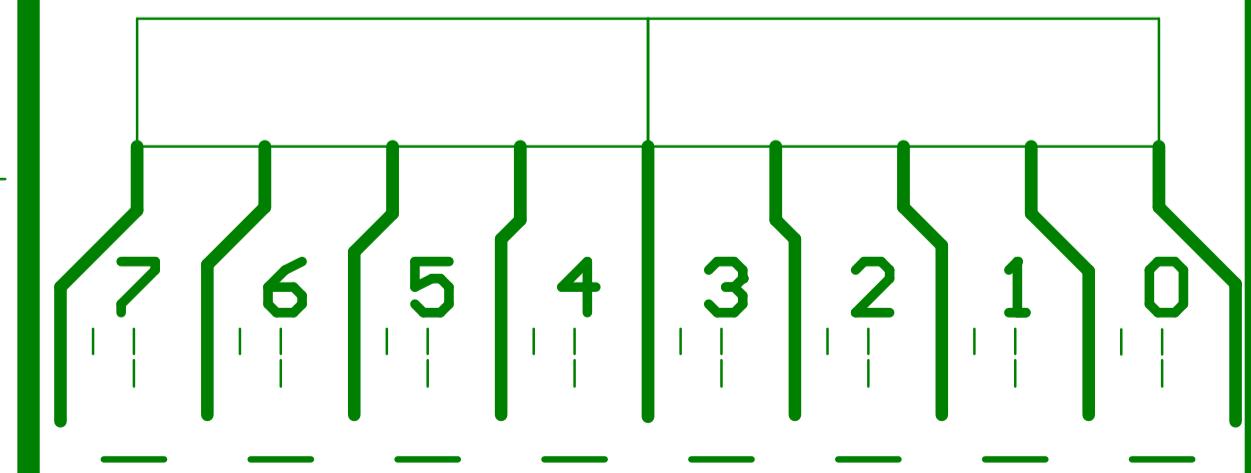
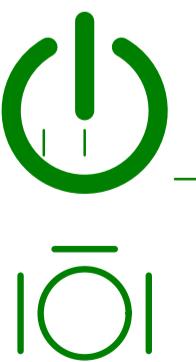
VDD
GND
TCK
TDO
TDI
TMS

•

USB1

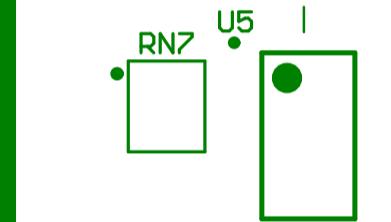
R29
Z9
R30

Gnd



-Vdd
-Gnd

SDA SCL Gnd



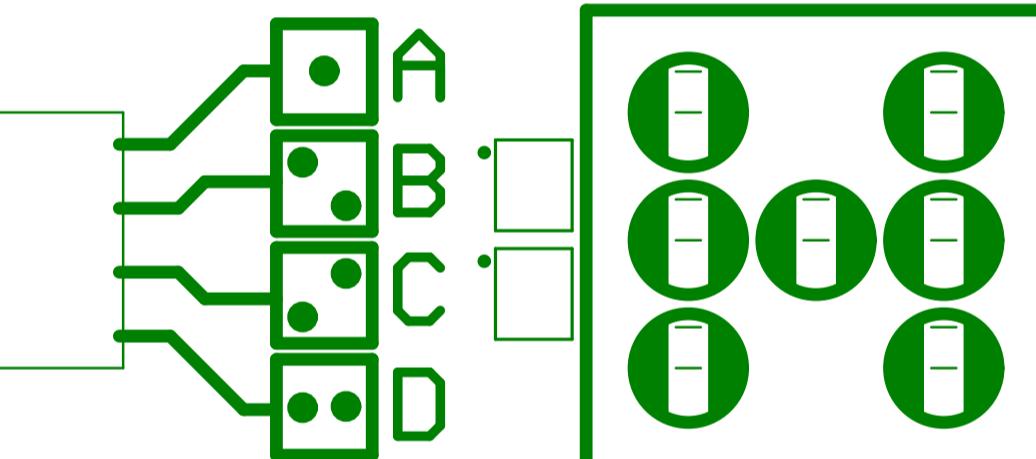
RN1 RN2
Z2
Z1
U2
C1

-Vdd
-Gnd

RN13 RN15
R40
Z8
U1

C2 R28
R31 C8
C3 P8
C7 R38
Y1

Dice



Stepper



Swinburne University
www.swin.edu.au

DIGITAL ELECTRONICS DESIGN
SWINBURNE UNIVERSITY
OF TECHNOLOGY



Based on a design by
Jordan Boekel, Ben Gregg,
Patrick Curtain

Ver = 4.2

If found please call (+613) 9214 8319

Vref
Gnd
TCK
TDO
TDI
TMS

