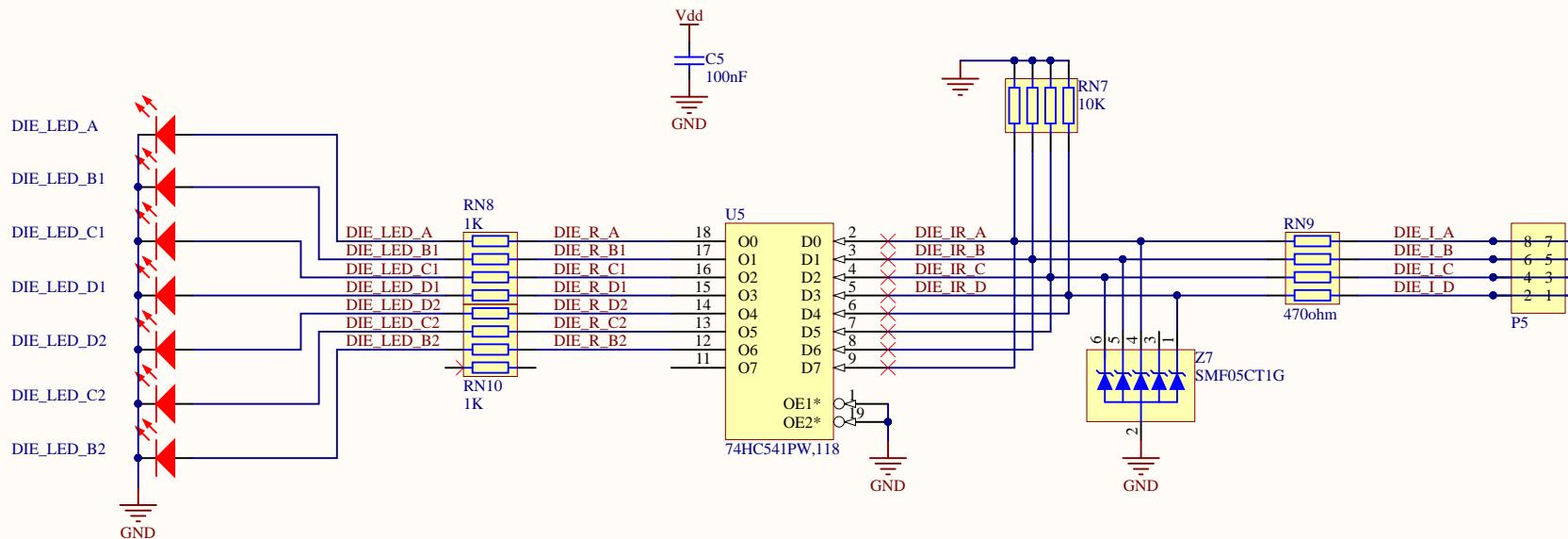


A

A



B

B

Title
DigitalLabBoard_Die.SchDoc

Size	Number	Revision
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Date:	5/11/2019	Sheet of
File:	C:\Users\.\DigitalLabBoard_Die.SchDoc	Drawn By:

A

A

B

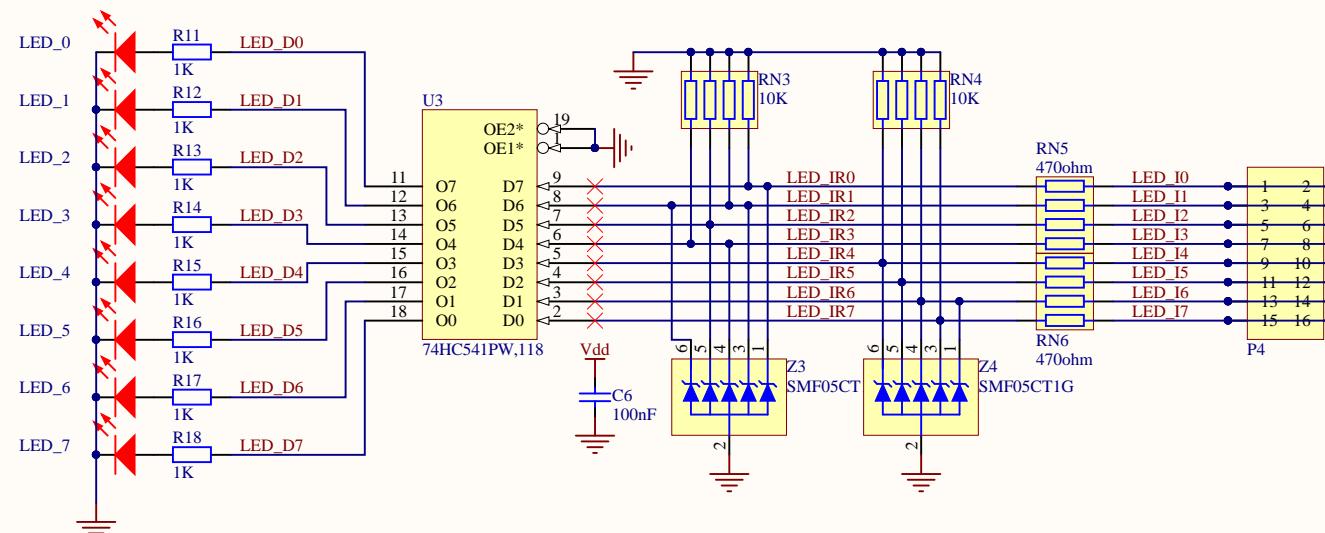
B

C

C

D

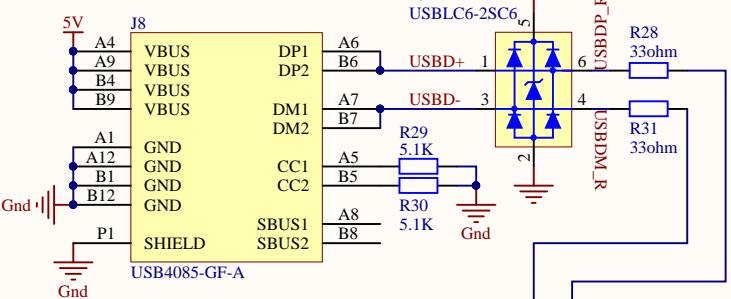
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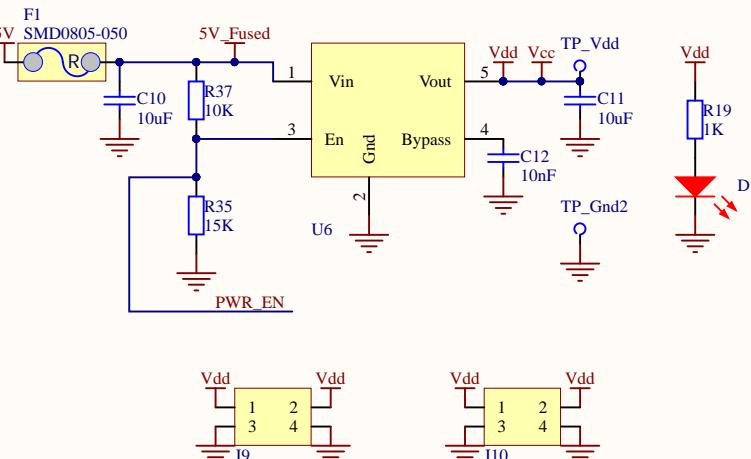
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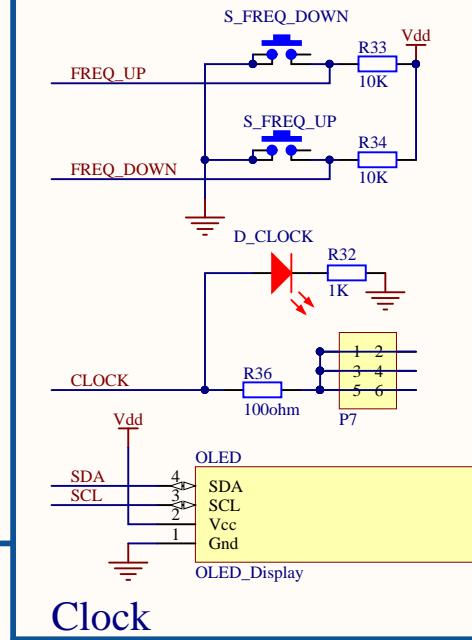
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2



3



S_FREQ_DOWN

FREQ_UP

FREQ_DOWN

D_CLOCK

CLOCK

SDA

SCL

Vcc

Gnd

OLED

Display

Clock

A

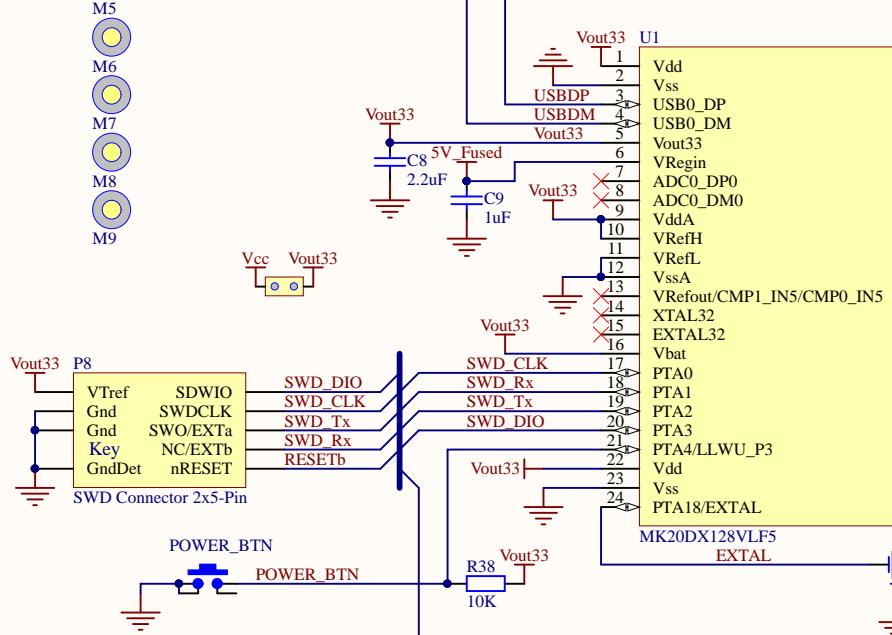
open source
hardware

B

M5
M6
M7
M8
M9

300-hole Breadboard

C



Title: DigitalLabBoard_Processor.SchDoc
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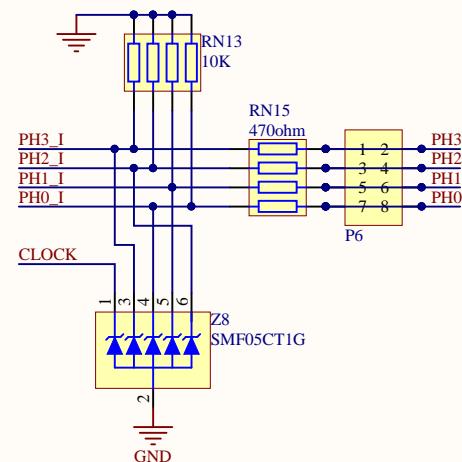
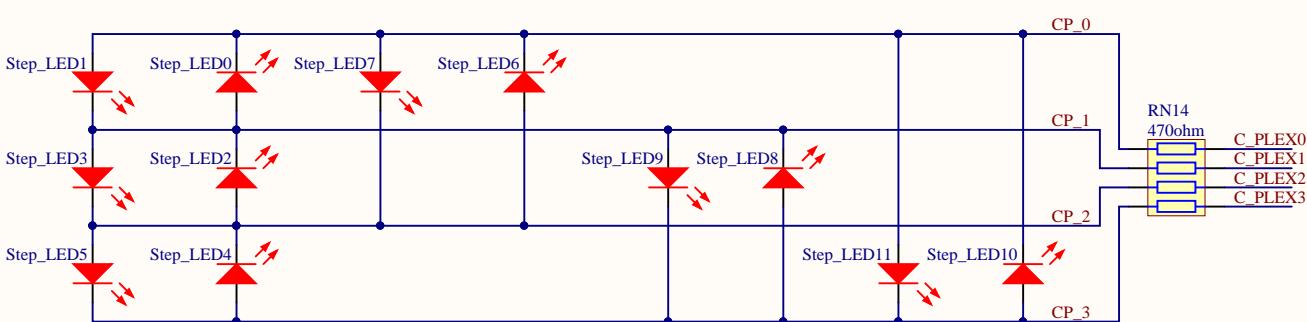
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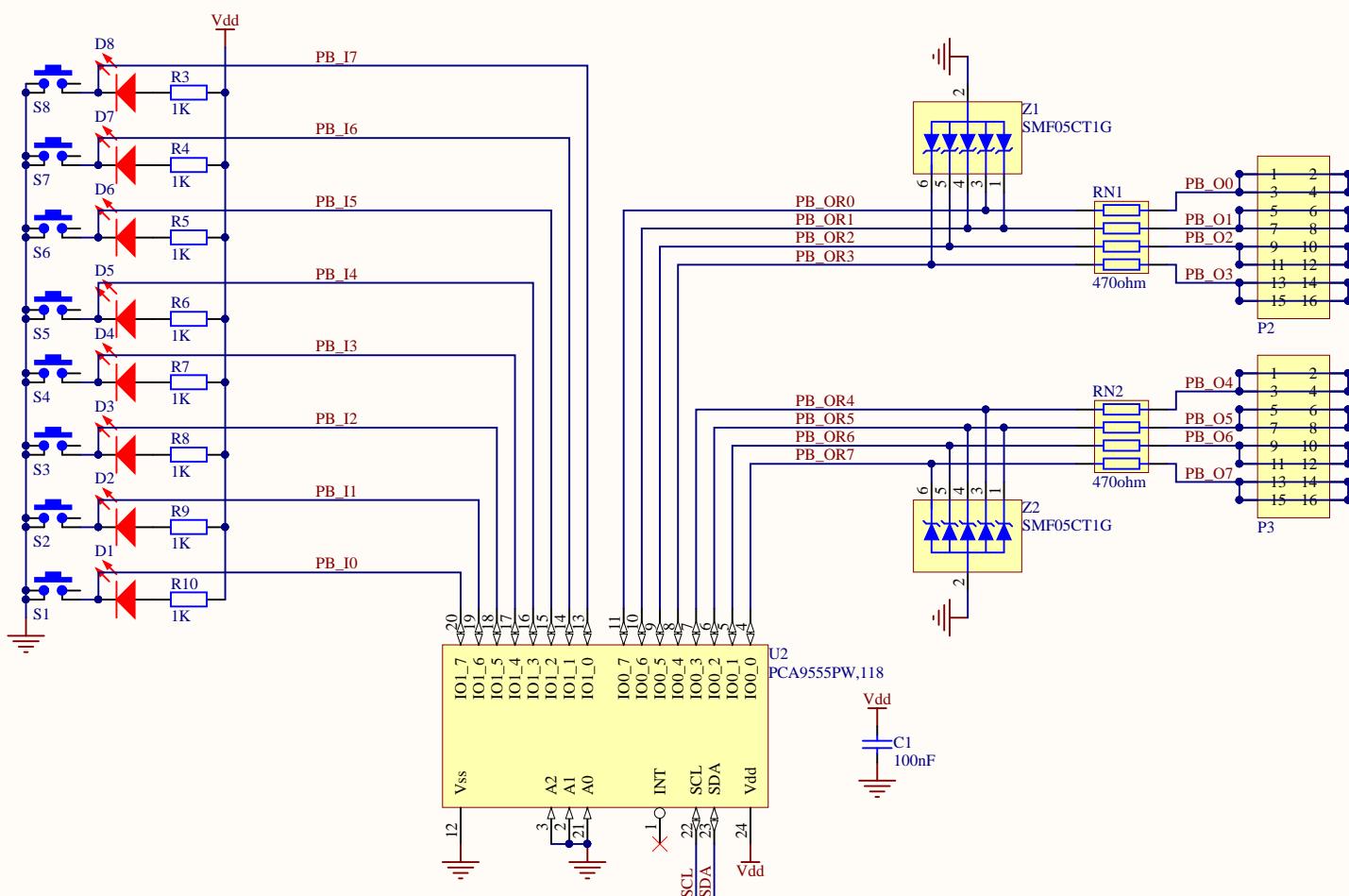
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4

A



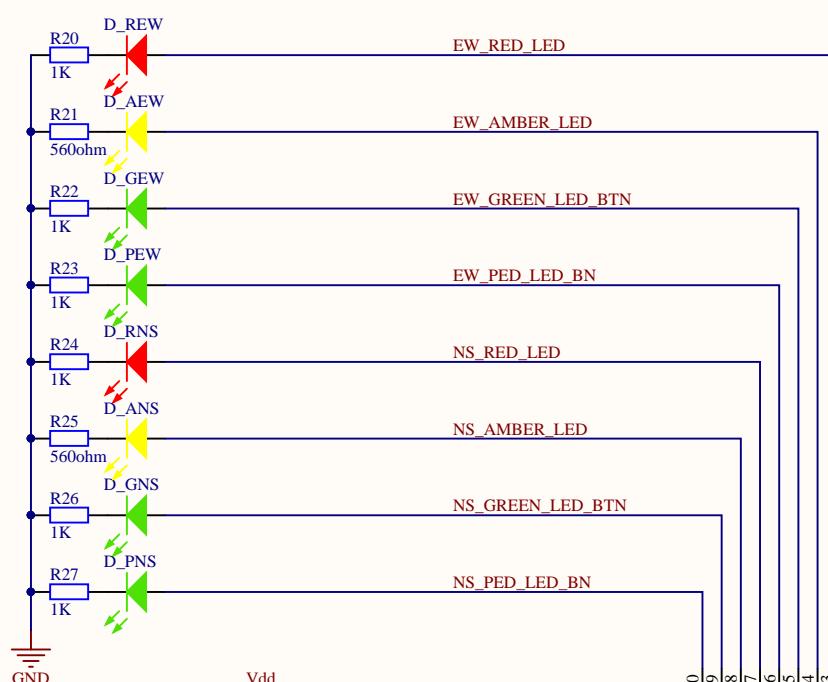
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Size A4	Number	Revision 0.1
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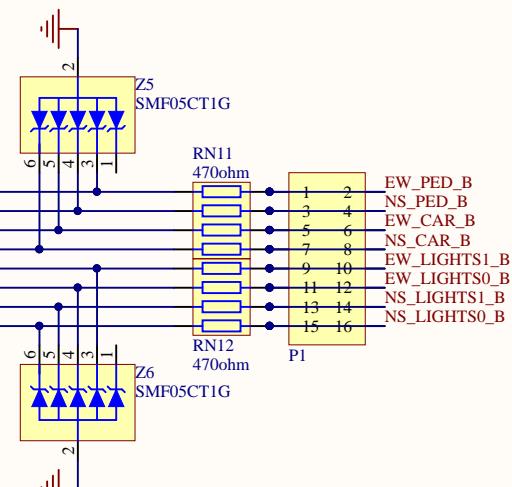
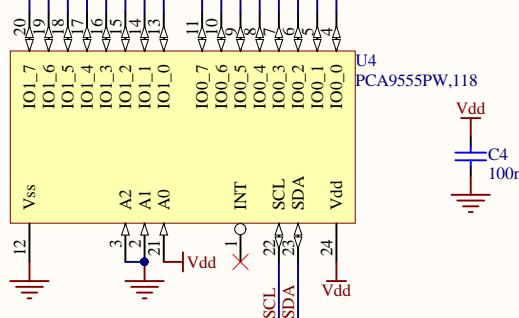
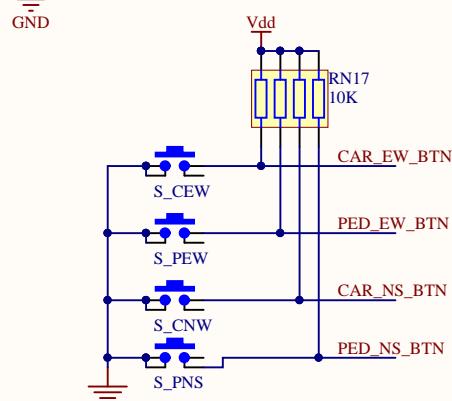
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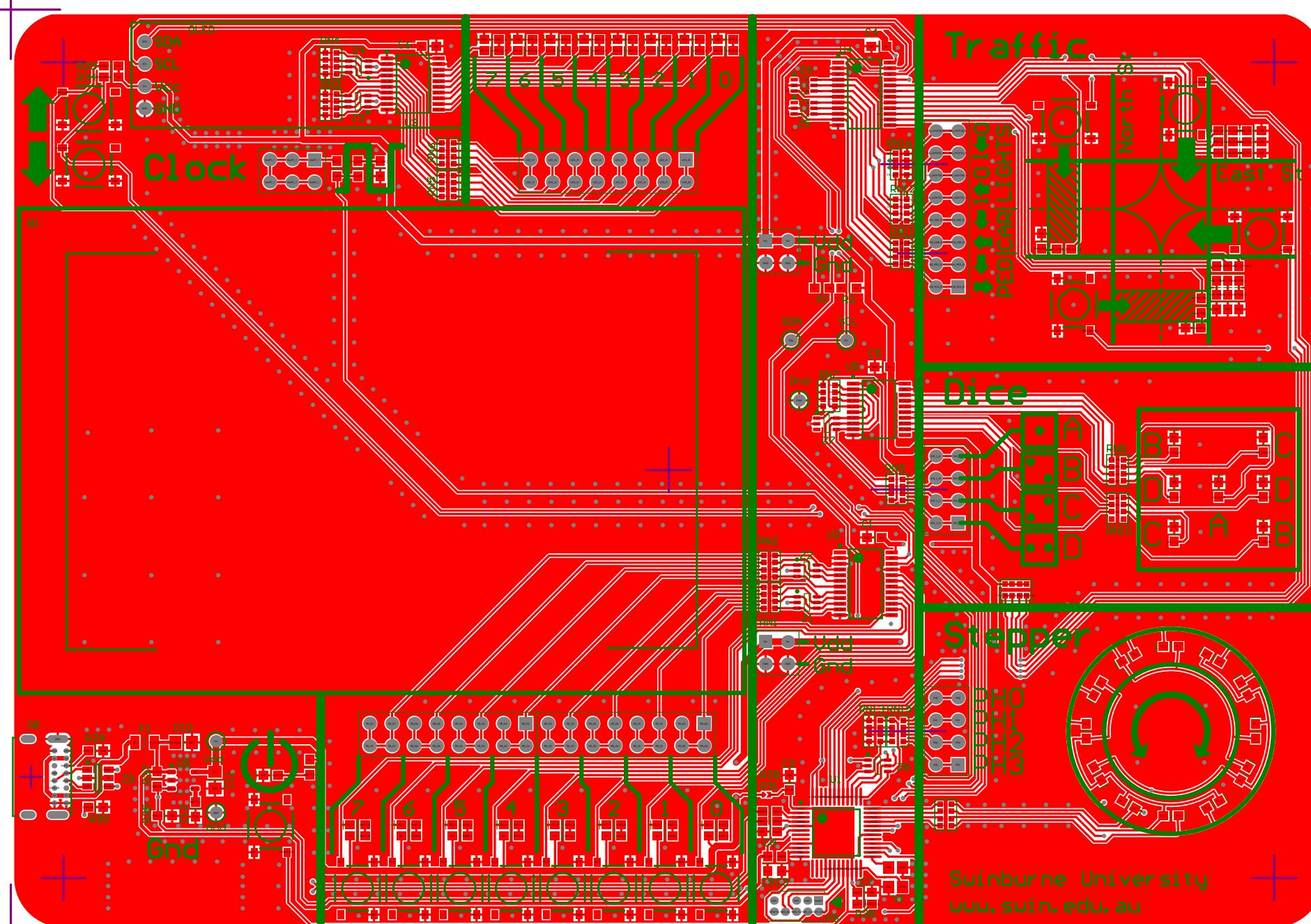


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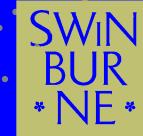


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Size	Number	Revision
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Date:	5/11/2019	Sheet of
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DIGITAL ELECTRONICS DESIGN
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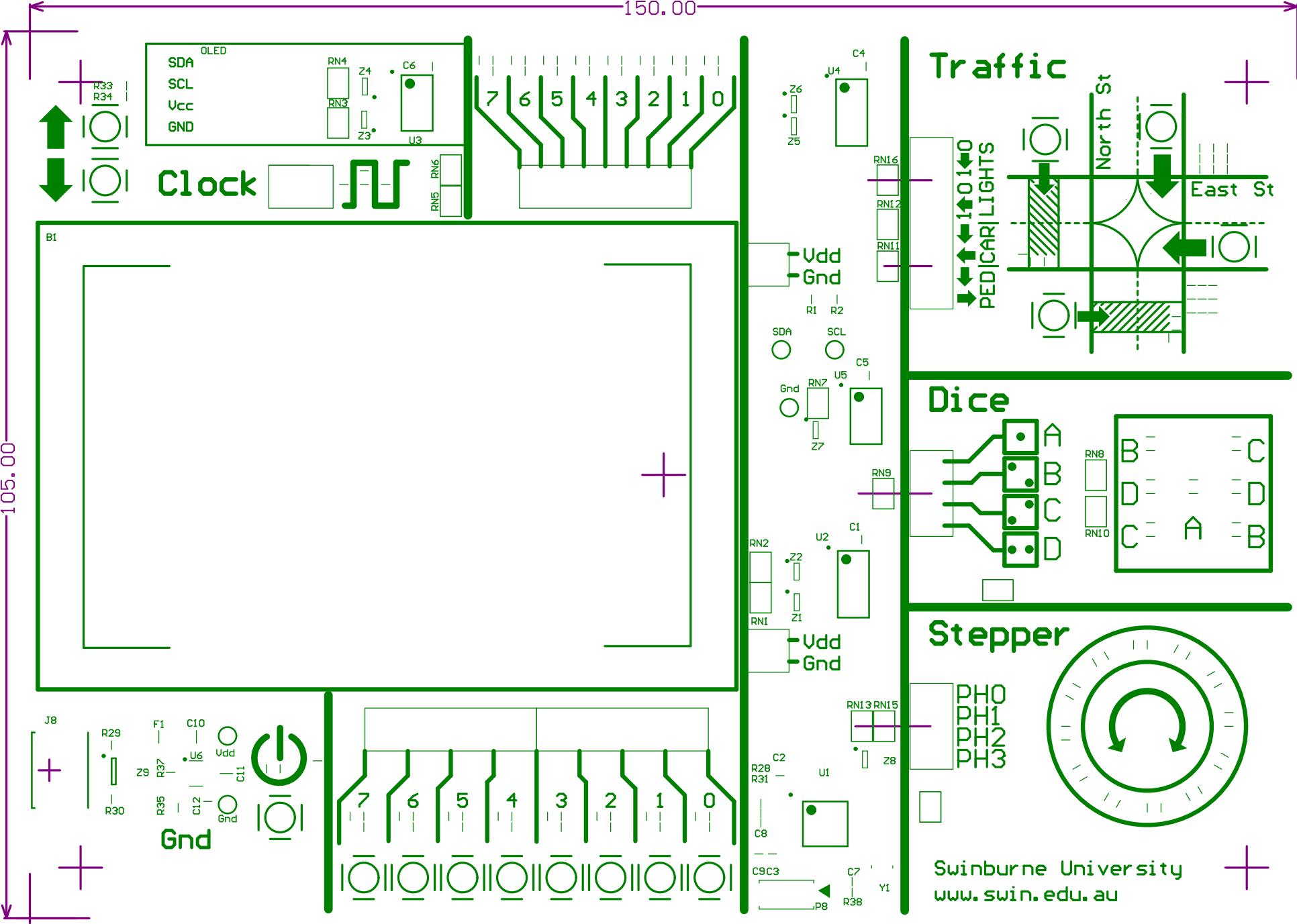


Based on a design by
Jordan Boekel, Ben Gregg,
Patrick Curtaín



Ver = 0.1

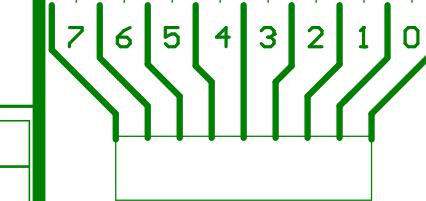
If found please call (+613) 9214.8319



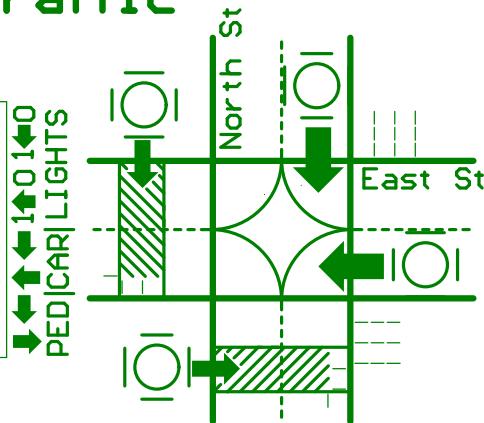
SDA
SCL
Vcc
GND



Clock



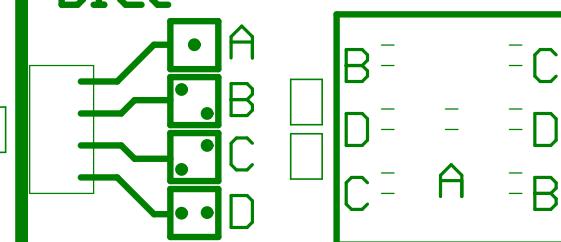
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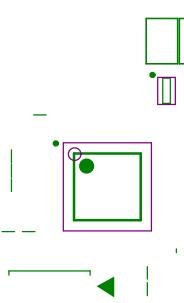
Vdd
Gnd
SDA
SCL



Dice

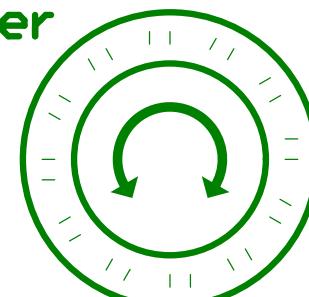


Vdd
Gnd



Stepper

PH0
PH1
PH2
PH3



Gnd

