Codigos fuente

Arnix

Modo protegido con GRUB

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1. Codigos fuente

include

1.1. defs.h

```
/*********************
          Defs.h
      #ifndef _defs_
#define _defs_
 6
      #define byte unsigned char
#define word short int
#define dword int
1\,1
12
      /* Flags para derechos de acceso de los segmentos */
#define ACS_PRESENT 0x80 /* segmento
memoria */
13
                                                                              /* segmento presente en ↔
14
      memoria */
#define ACS_CSEG
#define ACS_DSEG
#define ACS_READ
#define ACS_WRITE
#define ACS_IDT
#define ACS_INT_386
#define ACS_INT
                                                 0 \ge 18
                                                                              /* segmento de codigo */
                                                 0 \times 10
                                                                              /* segmento de datos */
                                                                              /* segmento de lectura */
17
                                                 0 \ge 0 2
                                                 0 x 0 2
ACS_DSEG
18
                                                                              /* segmento de escritura */
19
                                                 OxOE /* Interrupt GATE 32 bits */
( ACS_PRESENT | ACS_INT_386 )
20
^{22}
^{23}
                                                  \begin{array}{c|cccc} (ACS\_PRESENT & ACS\_CSEG & ACS\_READ) \\ (ACS\_PRESENT & ACS\_DSEG & ACS\_WRITE) \\ (ACS\_PRESENT & ACS\_DSEG & ACS\_WRITE) \\ \end{array} 
24
      #define ACS_CODE
#define ACS_DATA
#define ACS_STACK
25
26
27
       #pragma pack (1)
                                                 /* Alinear las siguiente estructuras a 1 byte \hookleftarrow
29
       /* Descriptor de segmento */
typedef struct {
  word limit ,
30
31
32
33
                  base_1;
34
          byte base_m ,
35
                  access ,
\frac{36}{37}
                   attribs,
                  base_h;
38
      } DESCR_SEG;
39
40
       /* Descriptor de interrupcion */
typedef struct {
  word          offset_1 ,
41
42
43
44
                            selector;
45
          byte
                            cero,
^{46}
47
          word
                            offset_h;
       } DESCR_INT;
48
49
       /* IDTR */
typedef struct {
  word limit;
50
51
52
53
          dword base;
\frac{54}{55}
       } IDTR;
56
```

1.2. kasm.h

```
***************
5
   #include "defs.h"
6
   unsigned int
                  _read_msw();
10
                   _lidt (IDTR *idtr);
11
   v o i d
12
               _mascaraPIC1 (byte mascara); /* Escribe mascara de PIC1 \leftarrow
   void
13
               _mascaraPIC2 (byte mascara); /* Escribe mascara de PIC2 \Leftarrow
14
   v o i d
15
               16
   v o i d
                                 /* Deshabilita interrupciones */
17
   void
18
                                 /* Timer tick */
19
   v o i d
               _int_08_hand();
^{20}
21
   void
               \_debug (void);
```

1.3. stdarg.h

```
* stdarg.h
 2
 3
      * Provides facilities for stepping through a list of function \hookleftarrow
 4
           arguments of
      * an unknown number and type.
 5
 6
      * NOTE: Gcc should provide stdarg.h, and I believe their version will\leftrightarrow
 7
 8
                with crtdll. If necessary I think you can replace this with \hookleftarrow
           the GCC
                stdarg.h (or is it vararg.h).
9
10
      * Note that the type used in va_arg is supposed to match the actual \hookleftarrow
11
      * *after default promotions *. Thus, va_arg (..., short) is not valid.
12
13
      * This file is part of the Mingw32 package.
14
15
16
          Created by Colin Peters < colin@bird.fu.is.saga-u.ac.jp>
17
18
         THIS SOFTWARE IS NOT COPYRIGHTED
19
20
          This source code is offered for use in the public domain. You may use, modify or distribute it freely.
21
22
          This code is distributed in the hope that it will be useful but WITHOUT ANY WARRANTY. ALL WARRANTIES, EXPRESS OR IMPLIED ARE \hookleftarrow
^{24}
^{25}
           HERERY
          DISCLAMED. This includes but is not limited to warranties of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE.
26
27
28
         $Revision: 1.1.1.1 $
29
      * $Author: brandon6684 $

* $Date: 2001/12/18 22:53:51 $
30
31
32
33
      * /
```

```
/* Appropriated for Reactos Crtdll by Ariadne */
 35
             #ifndef STDARG_H
#define STDARG_H
 36
 37
  38
 39
               * Don't do any of this stuff for the resource compiler.
 40
 41
             #ifndef RC INVOKED
 42
 43
  44
               * I was told that Win NT likes this.
  ^{45}
  46
             #ifndef _VA_LIST_DEFINED
#define _VA_LIST_DEFINED
  47
 48
  ^{49}
             #endif
 50
            #ifndef _VA_LIST #define _VA_LIST typedef char* va_list; #endif
 51
 52
 53
 54
 55
 56
  57
               * Amount of space required in an argument list (ie. the stack) for an
 58
 59
              * argument of type t.
  60
  61
             #define
                       \begin{array}{lll} & & & & \\ & & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & \\ & & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\
  62
  63
  64
 65
               * Start variable argument list processing by setting AP to point to \hookleftarrow
 66
                          the
  67
                * argument after pN.
  68
             #ifdef __GNUC__
  69
  70
               * In GNU the stack is not necessarily arranged very neatly in order \hookleftarrow
 71
                * pack shorts and such into a smaller argument list. Fortunately a
  72
               * neatly arranged version is available through the use of \leftarrow __builtin_next_arg.
             #define va_start(ap, pN) \
    ((ap) = ((va_list) __builtin_next_arg(pN)))
#else
 74
 75
  76
 77
 78
 79
               st For a simple minded compiler this should work (it works in GNU too \hookleftarrow
                           for
               \ast vararg lists that don't follow shorts and such).
  80
             81
  82
  83
  84
  85
  86
 87
 88
               * End processing of variable argument list. In this case we do \hookleftarrow
                           nothing.
  89
  90
             #define va_end(ap) ((void)0)
 91
 92
 93
               * Increment ap to the next argument in the list while returing a
 94
              * pointer to what ap pointed to first, which is of type t.
 95
  96
              ^{\ast} We cast to void* and then to t* because this avoids a warning about * increasing the alignment requirement.
 97
 98
 99
100
            101
```

1.4. varargs.h

```
/* $NetBSD: varargs.h,v 1.11 2005/12/11 12:16:16 christos Exp $ */
2
3
     *-
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 4
 5
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          distribution
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28
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29
          GOODS
      * OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS ↔
30
          INTERRUPTION)
       HOWEVER CAUSED AND ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, \hookleftarrow
31
           STRICT
      * LIABILITY, OR TORT (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN \hookleftarrow
32
          ANY WAY
      * OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY↔
33
^{34}
      * SUCH DAMAGE.
35
36
         @(\#) \text{ varargs.h} 8.2 (Berkeley) 3/22/94
37
38
39
    #ifndef VARARGS H
    #define VARARGS H
40
41
    #if !__GNUC_PREREQ
42
    #define __va_ellipsis
43
    #else
```

```
#define __va_ellipsis
46
    #endif
47
48
           GNUC PREREQ
    #define va_alist_t
                                  __builtin_va_alist_t
^{49}
50
                                  long
    #define __va_alist_t
51
     #endif
52
53
    #define va_alist
#define va_dcl
                             __builtin_va_alist
__va_alist_t __builtin_va_alist; __va_ellipsis
54
^{56}
57
58
     #endif
```

 src

kernel

1.5. kernel.c

```
#include "../include/kasm.h"
#include "kernel/driver/screen.h"
#include "kernel/system/idt.h"
#include "kernel/driver/keyboard.h"
#include "kernel/system/keyboardlisteners.h"
 3
 5
6
     kmain()
9
     Punto de entrada de codigo C.
10
     1\,1
12
     {\tt kmain}\,(\,)
13
14
           // Init system.
15
           init_descriptor_tables();
16
          init_int80();
17
          init_in_out();
          init_keyboard();
18
19
          init_timer_tick();
20
          init_screen();
^{21}
           // Start Shell
^{22}
23
           shell_start();
24
```

1.6. lib.asm

```
GLOBAL
              _read_msw ,_lidt
     GLOBAL
               _int_08_hand
 3
     GLOBAL
              _mascaraPIC1 , _mascaraPIC2 , _Cli , _Sti
 \frac{4}{5}
     GLOBAL
               _debug
 6
     EXTERN int_08
9
     {\tt SECTION} \quad . \ {\tt text}
10
11
12
13
     _Cli:
         cli
                         ; limpia flag de interrupciones
14
15
```

```
_Sti:
17
18
19
                     ; habilita interrupciones por flag
20
        ret
21
22
    _mascaraPIC1:
                              ; Escribe mascara del PIC 1
        push ebp
mov
23
             24
25
26
^{27}
             рор
28
             retn
29
                               ; Escribe mascara del PIC 2
30
    \verb|_mascaraPIC2:|
            sh ebp mov ebp, esp mov ax, [ss:ebp+8]; ax = mascara de 16 bits out 0\,\mathrm{A1h}, al
31
       push
32
33
^{34}
35
             рор
                      ebp
36
             retn
37
38
    _read_msw:
39
                              ; Obtiene la Machine Status Word
40
             retn
41
42
                           ; Carga el IDTR
43
    _lidt:
44
                      ebp
             push
^{45}
             mov
                      ebp, esp
^{46}
             push
                       ebx
                       \verb"ebx", [ss: ebp + 6] ; ds:bx = \verb"puntero" a IDTR" \\
47
             mοv
        \verb"rol" ebx", 16"
48
        lidt [ds: ebx]
pop ebx
                                     ; carga IDTR
49
50
51
             pop
                       ebp
52
             retn
53
54
55
56
    ; Debug para el BOCHS, detiene la ejecucion para continuar ; colocar \hookleftarrow
        en el BOCHSDBG: set $eax=0
58
    _debug:
59
             push
60
             mοv
                      bp, sp
61
            push
                     аx
62
    vuelve: mov
                     ax , 1
63
             \mathtt{cmp} \ \mathtt{ax} \ , \quad 0
64
         jne vuelve
65
         pop ax
66
         рор
67
             retn
```

1.7. loader.asm

```
; making entry point visible to linker ; end of kernel land
     global _loader
global eokl
 3
                               ; _main is defined elsewhere
     ; setting up the Multiboot header - see GRUB docs for details MODULEALIGN equ 1{<}{<}0 ; align loaded modules
 6
                                                               ; align loaded modules on page \hookleftarrow
             boundaries
     MEMINFO equ
                             1{<<}1 ; provide memory map MODULEALIGN | MEMINFO ; this is the Multiboot 'flag' \hookleftarrow
 9
     FLAGS
                      equ
     field field 0x1BADB002 bootloader find the header
                                                               ; 'magic number' lets \hookleftarrow
1.0
```

```
11
     CHECKSUM
                 \mathtt{equ} \ -(\mathtt{MAGIC} \ + \ \mathtt{FLAGS})
                                                        ; checksum required
12
13
     section .text
14
     align 4
     MultiBootHeader:
15
16
          dd MAGIC
17
          dd FLAGS
          dd CHECKSUM
18
19
20
            reserve initial kernel stack space
          STACKSIZE equ 0x4000
                                       ; that 's 16k.
^{22}
23
          mov esp, stack+STACKSIZE; set up the stack push eax; pass Multiboot magic number push ebx; pass Multiboot info structure
24
25
26
27
28
          call kmain
                               ; call kernel proper
29
          hlt
                   ; halt machine should kernel return
30
31
              {\tt dd} \;\; {\tt STACKSIZE} \; + \; {\tt stack}
     eok l
32
         section .bss
          align 32
33
^{34}
35
          resb STACKSIZE
                                   ; reserve 16k stack on a quadword boundary
```

driver

1.8. keyboard.c

```
#include "../system/isr.h"
#include "../system/in_out.h"
#include "../system/keyboardlisteners.h"
      #define KEYBOARD 0x60
 5
 6
      #define BUFFER_SIZE 100
      #define LSHIFT_KEY_PRESED_SCAN_CODE 42
#define LSHIFT_KEY_RELESED_SCAN_CODE 170
#define RSHIFT_KEY_PRESED_SCAN_CODE 54
#define RSHIFT_KEY_RELESED_SCAN_CODE 182
 8
10
1\,1
12
      #define BLOQ MAYUS SCAN CODE 58
13
14
      char array[BUFFER_SIZE];
15
17
      buffer_t stdin;
18
19
      char * actual_scan_code_table;
     20
23
      \begin{array}{ll} \textbf{int} & \textbf{shift} \ ; \\ \textbf{int} & \textbf{bloq\_mayusc} \ ; \end{array}
24
25
26
27
      int bloq_mayusc_unpresed();
28
      int bloq_mayusc_presed();
29
      \color{red} \textbf{int} \quad \texttt{bloq\_mayusc\_presed} \; (\;) \; \{
30
31
            \verb|bloq_mayusc|=0;
```

```
32
                return 0;
 33
 ^{34}
      }
 35
36
      \verb|int bloq_mayusc_unpresed|()| \{
37
           \verb|bloq_mayusc|=1;
                \verb"add_key_listener" (-1, BLOQ_MAYUS_SCAN_CODE", bloq_mayusc_presed);
38
           return 0;
39
40
      }
 41
 ^{42}
      _{\hbox{int shift\_presed}}\left(\,\right)\,\{
 43
           \mathtt{shift} ++;
           \verb"actual_scan_code_table=SHIFT_SCAN_CODE_TABLE";
 44
45
           return 0;
 ^{46}
      }
 47
 48
      int shift_relesed(){
 ^{49}
           shift --;
if (shift==0){
50
                actual_scan_code_table=SCAN_CODE_TABLE;
51
52
 53
           return 0;
 54
      }
55
56
      void IRQ1_handler(registers_t reg){
57
           int tmp;
int i=inb(KEYBOARD);
58
           if (activate(i)){
    tmp=(stdin.end+1)%stdin.size;
59
 60
                if (tmp!=stdin.start) {
 61
                      char c=actual_scan_code_table[i];
62
                      \begin{array}{c} \text{if (bloq_mayusc)} \{ \\ \text{if (c>='a' &\& c<='z')} \{ \\ \text{c=c+'A'-'a'}; \\ \} \, \text{else if (c>='A' &\& c<='Z')} \{ \end{array} 
 63
 64
 65
 66
 67
                                c=c+ \( \bar{a} '- 'A' ;
 68
 69
                      stdin.array[stdin.end]=c;
 70
 71
                      stdin.end=tmp;
                } else {
 72
 73
                                //TODO: beep
                }
 74
\frac{75}{76}
           }
      }
 77
      static void reset(){
 78
 79
           outb (0 x64,0 xFE);
 80
 81
      {\tt static int cnrl_alt_supr_manager()} \{
 82
          reset();
return 0;
 83
 84
 85
 86
 87
88
      void init_keyboard(){
           register_interrupt_handler(IRQ1,IRQ1_handler);
stdin.start=stdin.end=0;
 89
90
 91
           stdin.array=array;
 92
           stdin.size=BUFFER_SIZE;
93
           \verb"add_in_out" (0, \& \verb"stdin")"
           \verb"actual_scan_code_table=SCAN_CODE_TABLE";
94
95
                \verb|bloq_mayusc|=0;
 96
                init_key_listeners();
                \verb"add_key_listener" (-1, LSHIFT_KEY_PRESED_SCAN_CODE", shift_presed") \leftarrow
97
                \verb"add_k\'ey_listener" (-1, \verb"RSHIFT_KEY_PRESED_SCAN_CODE", shift_presed") \leftarrow \\
98
                \verb"add_k'ey_listener" (-1, LSHIFT_KEY_RELESED_SCAN_CODE", \; \hookleftarrow \;
99
                100
                      shift_relesed);
```

1.9. keyboard.h

```
1  #ifndef KEYBOARD_H
2  #define KEYBOARD_H
3
4  void init_keyboard();
5  #endif /* KEYBOARD_H */
```

1.10. screen.c

```
#include "screen.h"
#include "../system/isr.h"
#include "../system/in_out.h"
      #include "timer.h"
      int16_t * video_memory = (int16_t *) 0xB8000;
      #define BUFFER_SIZE 1000
char array_out[BUFFER_SIZE];
buffer_t stdout;
 8
1\,1
      #define ESC '\x1B'
#define BELL '\x07
12
13
14
      #define DEFAULT SETTINGS 0x07
15
16
      #define SCREEN SIZE X 80
18
      #define SCREEN_SIZE_Y 25
19
      uint8_t screen_state = 0; // 0=normal, 1=scaped, 2=parameters.
20
      #define SCREEN_MAX_PARAM_COUNT 16 uint8_t screen_param_count = 0;
21
      int screen_param [SCREEN_MAX_PARAM_COUNT];
^{24}
25
      uint8_t screen_cursor_x = 0;
26
      \verb"uint8_t screen_cursor_y = 0;
      {\tt uint8\_t~screen\_settings} \ = \ {\tt DEFAULT\_SETTINGS} \ ;
27
      #define VGA_HIGH_CURSOR_BYTE 14
      #define VGA_LOW_CURSOR_BYTE 15
#define VGA_MODE_PORT 0x3D4
#define VGA_IO_PORT 0x3D5
30
31
32
33
      static void update_cursor() {
  int16_t cursorLocation = screen_cursor_y * SCREEN_SIZE_X + \leftarrow
34
35
            screen_cursor_x;
outb(VGA_MODE_PORT, VGA_HIGH_CURSOR_BYTE);
36
            outb(VGA_IO_PORT , cursorLocation >> 8);
outb(VGA_MODE_PORT , VGA_LOW_CURSOR_BYTE);
outb(VGA_IO_PORT , cursorLocation);
37
38
39
40
      }
41
      static void scroll() {
  int16_t blank = ' | (DEFAULT_SETTINGS << 8);
  if (screen_cursor_y >= SCREEN_SIZE_Y) {
^{42}
43
44
45
                  int i:
```

```
46
47
48
             ^{49}
50
51
                 {\tt video\_memory[i]} \ = \ \dot{\tt blank} \ ;
52
53
             screen_cursor_y = (lastLine);
54
        }
55
56
    static void print(char c) {
   int16_t *location;
57
58
59
         location = video\_memory + (screen\_cursor\_y * SCREEN\_SIZE\_X + \leftarrow
             screen_cursor_x);
60
        if (c != '\b') {
    *location = (c | (screen_settings << 8));
    if (++screen_cursor_x >= SCREEN_SIZE_X) {
        screen_cursor_x = 0;
        screen_v++:
61
62
63
64
65
66
        } else {
67
             68
69
70
    }
71
     static void do_bell() {
    // TODO
73
74
75
     static void do_backspace() {
76
77
        if (screen_cursor_x) {
78
             screen_cursor_x --
          else if (screen_cursor_y) {
   screen_cursor_x = SCREEN_SIZE_X - 1;
79
80
             \verb|screen_cursor_y|--;
81
82
83
        print('\b');
84
    }
85
     {\tt static \ void \ do\_lineFeed()} \ \{
86
87
        screen\_cursor\_x = 0;
88
        \verb"screen_cursor_y++;
89
    }
90
     91
92
    }
93
94
    static void do_return() {
    screen_cursor_x = 0;
95
96
97
98
    99
100
101
        int i;
for (i = 0; i < SCREEN_SIZE_X * SCREEN_SIZE_Y; i++) {</pre>
102
103
             video_memory[i] = blank;
104
105
         screen\_cursor\_x = screen\_cursor\_y = 0;
106
        update_cursor();
107
    }
108
     109
110
111
             screen_clear();
112
113
    }
114
     /* Map from ANSI colors to the attributes used by the PC */
115
116 | static uint8_t ansi_colors[8] = \{0, 4, 2, 6, 1, 5, 3, 7\};
```

```
117
       static void do_scape_m() {
    int i;
    for (i = 0; i < screen_param_count; i++) {</pre>
118
119
120
                  \begin{array}{lll} & \text{int dec} = \text{screen\_param[i]} / 10; \\ & \text{int u} = \text{screen\_param[i]} \% 10; \end{array}
121
122
                   123
124
                              case 0:
125
126
                                    screen_settings = DEFAULT_SETTINGS;
127
                                    break;
128
                               case 1:
129
                                     \verb|screen_settings| = 0 x 0 8;
130
                                    break;
131
                               case 4:
                                     {\tt screen\_settings} \ \&= \ 0\,{\tt xBB} \ ;
132
133
                                    break;
134
                               case 5:
135
                                     \verb|screen_settings| = 0 x80;
136
                   } else if (dec == 3) { /* foreground */
137
                         //print('3');
138
                         screen_settings = (0 \text{ xFO } \& \text{ screen_settings}) \mid (0 \text{ xOF } \& \leftarrow)
139
                    ansi_colors[u]);
else if (dec == 4) { /* background */
screen_settings = (0x0F & screen_settings) | (ansi_colors[←
140
141
                              u | << 4);
142
                  }
143
            }
1\,4\,4
       }
145
       {\tt static \ void \ do\_scape(char \ c) \ \{}
146
            switch \quad (\, \mathtt{screen\_state} \,) \quad \{\,
147
148
                  case 1:
149
                        if (c == '[') {
150
                              screen_state = 2;
151
                               {\tt screen\_param\_count} \ = \ 1 \, ;
                              \begin{array}{lll} \mbox{int } \mbox{i = 0}; \\ \mbox{for } (; \mbox{i <= SCREEN_MAX_PARAM_COUNT}; \mbox{i++}) \end{array} \{
152
153
                                    screen_param[i] = 0;
154
155
                        } else {
156
157
                              screen_state = 0;
158
                        break;
159
                   case 2:
160
                         if (c >= '0' && c <= '9') {
161
                              screen_param[screen_param_count - 1] = 10 * \leftarrow
162
                        screen_param[screen_param_count - 1] + (c - '0');
} else if (c == ';') {
163
                        sci.
} else {
    switch (c) {
        case 'm' do_s
164
                              screen_param_count++;
165
166
                                             'm':
167
168
                                          do_scape_m();
169
                                          break;
170
                                     c\,a\,s\,e^{-+}\,J^{-\!+} :
171
                                          {\tt do\_scape\_J()}\;;
172
                                          break:
173
174
                               screen\_state = 0;
175
                         break;
176
177
            }
178
      }
179
       void screen_put(char c)
180
            if (screen_state > 0) {
181
182
                  {\tt do\_scape(c)}\;;
            183
184
185
186
187
                              screen_state = 1;
```

```
return;
case '\0':
return;
case BELL:
188
189
190
191
192
                              do_bell();
                             return;
193
194
                        case \ ^{+} \setminus b ^{+}:
                             do_backspace();
195
196
                             break;
197
198
                              do_lineFeed();
                             break;
199
200
                              {\tt do\_tab}\;(\;)\;;
201
202
                             break;
203
                        case
204
                              do_return();
205
                             break;
206
                        default:
                             print(c);
break;
207
208
209
210
                  scroll();
211
                  update_cursor();
212
            }
213
      }
214
215
      void screen_write(char *string) {
           int i = 0;
216
217
            while (string[i]) {
218
                screen_put(string[i++]);
219
220
      }
221
222
       static void timer_print(registers_t reg) {
223
224
            225
                  screen_put(stdout.array[stdout.start]);
226
                  \mathtt{stdout.start} \ = \ \big(\,\mathtt{stdout.start} \ + \ 1\big) \ \ \% \ \ \mathtt{stdout.size} \, ;
227
            }
228
      }
229
230
      void init_screen() {
            \tt register\_tick\_subhandler(timer\_print);
231
232
            \mathtt{stdout.start} \ = \ \mathtt{stdout.end} \ = \ 0\,;
233
            stdout.array = array_out;
stdout.size = BUFFER_SIZE;
234
            \begin{array}{lll} \textbf{add_in_out} \ (1 \ , \ \& \textbf{stdout}) \ ; \\ \textbf{screen\_write} \ (" \setminus x1B \ [2 \ J") \ ; \end{array}
235
236
237
```

1.11. screen.h

```
* screen.h | Interfaz para manejo de pantalla.
2
3
    #include "../system/common.h"
    #ifndef SCREEN_H
 6
    #define SCREEN_H
 8
     * Escribe un caracter en pantalla.
* @param char c: el caracter a escribir.
* Los siguientes ANSI scape Characters fueron implementados:
9
10
11
13
             Esc [2 J
                                   Borra la pantalla y mueve el cursor a (line 0,←
            column 0).
                                Cambia el modo de graficos segun los ←
           Esc[#;#;...m Cassiguientes atributos:
14
```

```
* Text attributes
16
     * 0
              All attributes off
17
18
     * 1
              Bold on
19
              Underscore (on monochrome display adapter only)
20
     * 5
              Blink on
21
     * Foreground colors
* 30 Black
                                 Background colors
40 Black
41 Red
22
23
24
     * 31
              Red
^{25}
     * 32
              G\,re\,e\,n
                                      G\,r\,ee\,n
^{26}
     * 33
              Yellow
                                 43
                                     Yellow
27
     * 34
              Blue
                                 44
                                          Blue
28
     * 35
              Magenta
                                 4.5
                                           Magenta
29
     * 36
              Cvan
                                 46
                                           Cyan
              White
                                           White
30
     * 37
                                 47
31
32
     * Ej: Esc[34;47m (azul en fondo blanco)
33
    void screen_put(char c);
34
35
36
    #endif
```

1.12. timer.c

```
#include "../system/isr.h"
#include "../system/int80.h"
 3
      #define SUB_FUNC_VEC_SIZE 10
 4
 5
      int80_t sub_handler_vec[SUB_FUNC_VEC_SIZE];
 9
      int count_ticks;
10
      {\color{red} \textbf{int}} \quad {\color{red} \texttt{sub\_func\_count}} \; ;
11
      unsigned long k;
12
      void register_tick_subhandler(int80_t func) {
   if (sub_func_count < SUB_FUNC_VEC_SIZE - 1) {
      sub_handler_vec[sub_func_count] = func;
}</pre>
13
14
15
16
                   \verb|sub_func_count++;|
17
            }
      }
18
19
20
      void IRQO_handler(registers_t regs) {
^{21}
             if (count_ticks) {
^{22}

\begin{array}{ll}
\mathbf{if} & (\mathbf{ticks} \stackrel{-}{=} \stackrel{'}{0}) \\
\mathbf{k} &= \mathbf{getRDTSC}();
\end{array}

23
24
25
26
                   ticks++;
^{27}
             for (i = 0; i < sub_func_count; i++) {
28
29
                   sub_handler_vec[i](regs);
30
31
      }
32
33
      void cpu_speed(registers_t regs) {
^{34}
            count_ticks = 1;
35
            \mathtt{ticks} \; = \; -1 \, ;
            _Sti();
while (ticks < 30);
36
37
            k = getRDTSC() - k;
_Cli();
38
39
40
             count_ticks = 0;
41
             *((unsigned\ long*)\ regs.ebx) = (k / ticks)*18 + k / (ticks * 5);
      }
42
43
```

```
44 | void init_timer_tick() {
45 | sub_func_count = 0;
46 | count_ticks = 0;
47 | register_interrupt_handler(IRQO, IRQO_handler);
48 | register_functionality(5, cpu_speed);
49 | }
```

1.13. timer.h

```
#include "../system/int80.h"

#ifndef TIMER_H

#define TIMER_H

void register_tick_subhandler(int80_t func);

void init_timer_tick();

void start_ticks();

void stop_ticks();

int get_ticks();

#endif /* TIMER_H */
```

system

1.14. common.h

```
#ifndef COMMON H
      #define COMMON_H
 3
      // Exact-width integer types typedef signed char int8_typedef unsigned char uint8_
 4
 5
                                          int8_t;
                     insigned char uint8_t;
signed short int16_t;
 6
      typedef
      typedef unsigned short uint16_t;
 9
      typedef
                     signed int
                                           int32_t;
      typedef unsigned int
10
                                           uint32_t;
11
      #define NULL ((void*)0)
12
13
14
      #define PORT_PIC1 0x20
#define PORT_PIC2 0xA0
#define SIGNAL_EOI 0x20
15
16
17
18
      extern void outw(uint16_t port, uint16_t value);
extern void outb(uint16_t port, uint8_t value);
19
20
      extern uint8_t inb(uint16_t port);
extern uint16_t inw(uint16_t port);
^{21}
\frac{22}{23}
      extern uint32_t getRDTSC();
24
      #endif // COMMON_H
```

1.15. idt.c

```
1 #include "common.h"
```

```
#include "idt.h"
#include "isr.h"
 3
     static void init_idt();
     static void idt_set_gate(uint8_t, uint32_t, uint16_t, uint8_t);
     \verb|idt_entry_t idt_entries||[256]|;
 9
     \verb"idt_ptr_t" idt_ptr";
10
11
      / Extern the ISR handler array so we can nullify them on startup.
     extern isr_t interrupt_handlers[];
13
     extern void idt_flush(uint32_t);
14
15
     void init_descriptor_tables()
16
          /* Habilito interrupcion de timer tick*/
17
          _Cli();
18
          \verb|_mascaraPIC1(0xFE);
19
^{20}
         _mascaraPIC2(0xFF);
21
          _Sti();
22
23
         init_idt();
^{24}
     }
^{25}
26
27
     static void init_idt()
28
29
          idt_ptr.limit = sizeof(idt_entry_t) * 256 -1;
         idt_ptr.base = (uint32_t)&idt_entries;
30
31
^{32}
           / Remap the irq table.
          outb (0 \times 20, 0 \times 11);
33
34
          outb (0 xA0, 0x11);
          outb (0 x21, 0 x20);
35
36
          outb (0 xA1 , 0 x28)
37
          outb (0 x21 , 0 x04)
38
          outb (0 xA1 , 0x02)
39
          outb (0 x21 , 0 x01)
40
          \verb"outb" (0 x A 1 , 0 x 0 1)
          outb (0 x21 , 0x0);
41
42
         outb (0xA1, 0x0);
43
44
          idt_set_gate(0, (uint32_t)isr0,
                                                     0 \times 08 , 0 \times 8E );
                           1,
                                                     0 \times 08,
45
          idt_set_gate(
                               (uint32_t)isr1
                                                            0 x 8 E )
                           2,
                                                     0 x 08 ,
46
          idt_set_gate(
                                (uint32_t)isr2
                                                            0 x 8 E )
                           3,
                               (uint32_t) isr3
(uint32_t) isr4
                                                     0 \times 08,
47
          idt_set_gate(
                                                            0 x 8 E
48
                                                     0x08.
                                                            0 x 8 E )
          idt set gate (
                           4.
^{49}
         idt_set_gate(
                               (uint32_t)isr5
                                                     0x08, 0x8E
50
          idt_set_gate(
                           6,
                               (uint32_t)isr6
                                                     0 \times 08,
                                                            0 x 8 E
51
          idt_set_gate(
                                (uint32_t)isr7
                                                     0 x 08 ,
                                                            0 x 8 E
                                                     0 \times 08,
52
          idt_set_gate(
                               (uint32_t)isr8
                                                            0 x 8 E
                           9.
                                                     0 \times 08,
53
          \verb|idt_set_gate||
                               (uint32_t)isr9
                                                            0 x 8 E )
          \verb|idt_set_gate| (10),
                                                   0 x 08
54
                               (uint32_t)isr10
                                                            0 x 8 E
                               (uint32_t)isr11,
55
          idt_set_gate(11,
                                                     0 x 08,
                                                            0 x 8 E
56
                                (uint32_t) isr12
                                                     0 x 08 ,
                                                            0 x 8 E
          idt_set_gate(12,
57
          idt_set_gate(13)
                                (uint32_t) isr13
                                                     0 \times 08,
                                                            0 x 8 E
58
          idt_set_gate(14)
                                (uint32_t)isr14
                                                     0 \times 08,
                                                            0 x 8 E
                               (uint32_t) isr15
                                                     0 x 08 ,
59
          idt_set_gate(15)
                                                            0 x 8 E
60
          idt_set_gate(16,
                               (uint32_t) isr16
                                                     0x08,
                                                            0 x 8 E
                               (uint32_t) isr17,
61
          \verb"idt_set_gate" (17",
                                                     0 x 08 ,
                                                            0 x 8 E
62
          idt_set_gate(18,
                                (uint32_t)isr18
63
          idt_set_gate(19,
                                (uint32_t) isr19
                                                     0 \times 08
                                                            0 x 8 E
                                                     0 x 08 ,
64
          \verb"idt_set_gate" (20",
                                (uint32_t) isr20
                                                            0 x 8 E
                                                     0 \times 08,
65
          idt_set_gate(21)
                                (uint32_t) isr21,
                                                            0 x 8 E )
                               (uint32_t)isr22,
(uint32_t)isr23,
66
          \verb"idt_set_gate" (22\ ,
                                                     0 \times 08 ,
                                                            0 x 8 E
67
          idt_set_gate(23,
                                                     0 x 08 ,
                                                            0 x 8 E )
68
          idt_set_gate(24,
                               (uint32_t) isr24, 0x08, 0x8E
69
          idt_set_gate(25,
                                (uint32_t) isr25,
                                                     0x08,
                                                            0 x 8 E
                                                     0 \times 08,
70
          idt_set_gate(26)
                                (uint32_t)isr26
                                                            0 x 8 E
                                                     0 x 08 ,
71
          \verb"idt_set_gate" (27",
                                (uint32_t) isr27,
                                                            0 x 8 E
                                                     0 \times 08,
72
          \verb"idt_set_gate" (28",
                                (uint32_t)isr28,
                                                            0 x 8 E )
          idt_set_gate(29,
                               (uint32_t)isr29, 0x08, 0x8E)
73
          idt_set_gate(30,
                               (uint32_t)isr30, 0x08, 0x8E)
          idt_set_gate(31, (uint32_t)isr31, 0x08, 0x8E)
```

```
76
           \verb"idt_set_gate" (32, (uint32_t) irq0, 0x08,
 77
                                                              0x8E):
           idt_set_gate(33, (uint32_t)irq1,
idt_set_gate(34, (uint32_t)irq2,
 78
                                                      0 \times 08,
                                                              0x8E);
 79
                                                      0 x 08 ,
                                                              0 x 8 E ) ;
                                                      0 x 0 8
 80
           idt_set_gate(35, (uint32_t)irq3,
                                                      0 x 08 ,
 81
           idt_set_gate(36, (uint32_t)irq4,
                                                              0 x 8 E
                                                      0 \times 08,
                                 (uint32_t) irq5,
 82
           idt_set_gate(37)
                                                              0x8E);
                                                      0 \times 08,
           idt_set_gate(38,
 83
                                 (uint32_t)irq6,
                                                              0x8E);
           idt_set_gate(39,
                                 (uint32_t)irq7,
 84
                                                      0 x 0 8 ,
                                                              0x8E):
                                 (uint32_t)irq8,
 85
           idt_set_gate(40,
                                                      0 \times 08,
                                                              0x8E):
           idt_set_gate(41,
                                  (uint32_t)irq9
                                                      0 x 0 8 ,
                                                              0 x 8 E )
 87
           idt_set_gate(42,
                                  (uint32_t)irq10, 0x08, 0x8E)
                                                       0 \times 08,
 88
           \verb"idt_set_gate" (43",
                                  (uint32_t)irq11,
                                                               0 x 8 E )
                                                        0 \times 08 ,
 89
           \verb"idt_set_gate" (44",
                                  (uint32_t)irq12
                                                               0 x 8 E )
90
           idt_set_gate(45)
                                 (uint32_t)irq13, 0x08, 0x8E);
 91
           idt_set_gate(46, (uint32_t)irq14, 0x08, 0x8E);
 92
           idt_set_gate(47, (uint32_t)irq15, 0x08, 0x8E);
 93
 94
95
           \mathtt{idt\_set\_gate} \left( \, 0 \, \mathtt{X80} \, , \quad \left( \, \mathtt{uint32\_t} \, \right) \, \mathtt{isr80h} \, \, , \quad 0 \, \mathtt{x08} \, , \quad 0 \, \mathtt{x8E} \, \right) \, ;
96
97
98
           idt_flush((uint32_t)&idt_ptr);
99
      }
100
101
      static \ void \ idt\_set\_gate(uint8\_t \ num \,, \ uint32\_t \ base \,, \ uint16\_t \ sel \,, \ \hookleftarrow
           uint8_t flags)
102
      {
           103
104
105
106
           idt_entries[num].sel
107
           idt_entries[num].always0 = 0;
108
109
           idt_entries[num].flags = flags;
110
```

1.16. idt.h

```
#include "common.h"
3
    void init_descriptor_tables();
 5
    // interrupt gate descriptor
    struct idt_entry_struct {
 6
        uint16_t base_lo;
 8
         uint16_t sel;
9
         uint8_t always0;
         uint8_t flags;
uint16_t base_hi;
10
11
    } _{\tt _attribute_{\tt _a}((packed));}
12
13
    typedef struct idt_entry_struct idt_entry_t;
14
15
    // array of interrupt handlers descriptor (for lidt).
16
    struct idt_ptr_struct {
    uint16_t limit;
    uint32_t base;
17
18
19
20
    } __attribute__((packed));
^{21}
22
    typedef struct idt_ptr_struct idt_ptr_t;
23
24
    #define IDT_SIZE 256
^{25}
26
    // interrupciones default del procesador.
27
    extern void isr0();
28
    extern void isr1();
29
    extern void isr2()
    extern void isr3();
```

```
extern void isr4();
32
    extern void isr5();
33
    extern void isr6
    extern void isr7
35
    extern void
36
    extern void
                 isr9 (
37
    extern void isr10()
38
    extern void
                 isr11(
39
    extern void
                 isr12(
40
    extern void
                 isr13 (
41
    extern void
                 isr14
^{42}
    extern
           v o i d
                 isr15
43
    extern void
                 isr16
44
    extern void isr17
45
                 isr18
    extern void
46
    extern void
                 isr19
47
    extern void
                 isr20
48
    extern void isr21
^{49}
    extern void isr22
50
    extern void isr23
    extern void
51
                 isr24
52
    extern void
                 isr25
    extern void
53
                 isr26
    extern void
55
    extern void isr28 (
56
    extern void isr29
    extern void isr30
57
    extern void isr31();
58
59
    extern void irq0()
60
    extern void
                 irq1(
61
    extern void irq2
62
    extern void irq3 (
63
    extern void
                 irq4(
64
    extern void
                 irq5(
65
    extern void
                 irq6 (
66
    extern void
                 irq7 (
67
    extern void
                 irq8()
68
    extern void
69
    extern void
                 irq10()
70
    extern void
                 irq11();
71
    extern void irq12();
    extern void irq13();
73
    extern void
                 irq14()
74
    extern void irq15();
75
    extern void isr80h();
```

1.17. in out.c

```
#include "int80.h"
#include "in out.h"
      buffer_t * in_out_vector[10];
 5
      void READ_INTERRUPT_handler(registers_t regs){
 6
            int i;
buffer_t * buff=in_out_vector[regs.ebx];
for(i=0;i<regs.edx && buff->start!=buff->end;i++){
 7
 8
                        buff->start=(buff->start+1) %buff->size;
10
1\,1
12
            if (i<regs.edx) {
13
                  *((char*)(regs.ecx+i))='\0';
14
15
16
17
18
      {\color{red}\mathbf{void}} \quad \mathtt{WRITE\_INTERRUPT\_handler} \left( \, \mathtt{registers\_t} \quad \mathtt{regs} \, \right) \left\{
            int i;
int tmp;
19
20
```

```
buffer_t * buff=in_out_vector[regs.ebx];
tmp=(buff->end+1) %ouff->size;
21
22
23
                                                                                            \vec{for} \; (\; \vec{i} = 0\;; \vec{i} < \texttt{regs.edx} \;\; \&\& \;\; \texttt{tmp!} = \texttt{buff-} > \texttt{start}\; ; \; \vec{i} + +, \texttt{tmp} = (\; \texttt{buff-} > \texttt{end} + 1) \; \% \\ \textit{uff} \leftarrow \texttt{or} \; (\; \vec{i} = 0\;; \vec{i} < \texttt{regs.edx} \;\; \&\& \;\; \texttt{tmp!} = \texttt{buff-} > \texttt{start}\; ; \; \vec{i} + +, \texttt{tmp} = (\; \texttt{buff-} > \texttt{end} + 1) \; \% \\ \textit{uff} \leftarrow \texttt{or} \; (\; \vec{i} = 0\;; \vec{i} < \texttt{regs.edx} \;\; \&\& \;\; \texttt{tmp!} = \texttt{ouff-} > \texttt{ord} + 1) \; \% \\ \textit{uff} \leftarrow \texttt{or} \; (\; \vec{i} = 0\;; \vec{i} < \texttt{regs.edx} \;\; \&\& \;\; \texttt{tmp!} = \texttt{ouff-} > \texttt{ord} + 1) \; \% \\ \textit{uff} \leftarrow \texttt{or} \; (\; \vec{i} = 0\;; \vec{i} < \texttt{regs.edx} \;\; \&\& \;\; \texttt{tmp!} = \texttt{ouff-} > \texttt{ord} + 1) \; \% \\ \textit{uff} \leftarrow \texttt{or} \; (\; \vec{i} = 0\;; \vec{i} < \texttt{regs.edx} \;\; \&\& \;\; \texttt{tmp!} = \texttt{ouff-} > \texttt{ord} + 1) \; \% \\ \textit{uff} \leftarrow \texttt{or} \; (\; \vec{i} = 0\;; \vec{i} < \texttt{ord} \;\; \&\& \;\; \texttt{tmp!} = \texttt{ouff-} > \texttt{ord} + 1) \; \% \\ \textit{uff} \leftarrow \texttt{or} \; (\; \vec{i} = 0\;; \vec{i} < \texttt{ord} \;\; \&\& \;\; \texttt{tmp!} = \texttt{ouff-} > \texttt{ord} + 1) \; \% \\ \textit{uff} \leftarrow \texttt{ord} \; (\; \vec{i} = 0\;; \vec{i} < \texttt{ord} \;\; \&\& \;\; \texttt{tmp!} = \texttt{ouff-} > \texttt{ord} + 1) \; \&\& \;\; \texttt{tmp!} = \texttt{ouff-} = \texttt{ord} + 1) \; \&\& \;\; \texttt{tmp!} = \texttt{ord} + 1) \; \&\& \;\; \texttt{tmp!} = \texttt{ouff-} = \texttt{ord} + 1) \; \&\& \;\; \texttt{tmp!} = \texttt{ord} + 1) \; \&\&\;\; \texttt{tmp!} = \texttt{ord} + 1) 
                                                                                                                                             ->size){
 ^{24}
                                                                                                                                      buff \rightarrow array [buff \rightarrow end] = *((char*)(regs.ecx+i));
25
                                                                                                                                      \mathtt{buff} = \mathtt{end} = \mathtt{tmp} ;
26
27
                                           }
28
                                            void add_in_out(int n, buffer_t * buff){
 29
 30
                                                                                       in_out_vector[n]=buff;
 31
32
33
34
                                           \label{locality} \begin{array}{l} \verb|init_in_out()| \{ \\ \verb|register_functionality(3,READ_INTERRUPT_handler); \\ \verb|register_functionality(4,WRITE_INTERRUPT_handler); \\ \end{aligned}
35
 36
37
```

1.18. in out.h

```
#ifndef IN_H
#define IN_H
 2
3
       \begin{array}{ll} \textbf{struct} & \textbf{buffer\_struct} \\ \{ \end{array}
 5
 6
 7
               \quad \hbox{int size} \; ;
               char * array;
int start;
int end;
 8
 9
10
1\,1
       };
12
       {\tt typedef\ struct\ buffer\_struct\ buffer\_t;}
13
14
       #endif // IN OUT H
```

1.19. int80.c

```
#include "isr.h"
#include "int80.h"
 3
       #define SUB_FUNC_VEC_SIZE 10
 4
5
 8
       \verb|int80_t sub_funcs_vec[SUB_FUNC_VEC_SIZE||;|
 9
10
       void register_functionality(uint8_t n, int80_t func) {
   if(n<SUB_FUNC_VEC_SIZE){
      sub_funcs_vec[n] = func;
}</pre>
11
12
13
14
15
       }
16
       void int80_handler(registers_t regs) {
    if(regs.eax<SUB_FUNC_VEC_SIZE) {
        sub_funcs_vec[regs.eax](regs);
}</pre>
17
18
19
20
^{21}
22
23
       void nofunc (registers_t regs) {
```

1.20. int80.h

```
#include "isr.h"

#ifndef INT80_H

#define INT80_H

typedef void (*int80_t)(registers_t);

void register_functionality(uint8_t n, int80_t func);

void init_int80();

#endif /* INT80_H */
```

1.21. isr.c

```
#include "common.h"
#include "isr.h"
#include "idt.h"
3
4
5
    {\tt isr\_t \ interrupt\_handlers[IDT\_SIZE];}
6
    void register_interrupt_handler(uint8_t n, isr_t handler) {
 8
        interrupt_handlers[n] = handler;
9
10
    11
12
13
14
         if (interrupt_handlers[regs.int_no] != NULL) {
15
             isr_t handler = interrupt_handlers[regs.int_no];
handler(regs);
16
17
18
        }
19
    }
\frac{20}{21}
    void irq_handler(registers_t regs) {
22
        if (regs.int_no >= IRQ8) {
   outb(PORT_PIC2, SIGNAL_EOI);
23
24
25
        outb (PORT_PIC1 , SIGNAL_EOI) ;
^{26}
        isr_handler(regs);
27
```

1.22. isr.h

```
#include "common.h"
     #ifndef ISR_H
#define ISR_H
 3
     #define IRQ0 32
 6
     #define IRQ1 33
     #define IRQ2 34
     #define IRQ3 35
 9
     #define IRQ4 36
10
1\,1
     #define IRQ5 37
12
     #define IRQ6 38
     #define IRQ7 39
13
     #define IRQ8 40
#define IRQ9 41
#define IRQ10 42
#define IRQ11 43
14
15
16
     #define IRQ12 44
19
     #define IRQ13 45
#define IRQ14 46
#define IRQ15 47
20
21
22
     typedef struct registers
23
^{24}
25
          uint32_t ds;
26
          \verb"uint32_t" edi", esi", ebp", esp", ebx", edx", ecx", eax"; ~//~ pusha ~pushs \;.
          uint32_t int_no, err_code;
uint32_t eip, cs, eflags, useresp, ss; // processor automatic \hookleftarrow
27
28
                pushs.
29
     } registers_t;
30
     {\tt typedef\ void\ (*isr\_t)(registers\_t)};\\
31
32
     void register_interrupt_handler(uint8_t n, isr_t handler);
33
     #endif //ISR_H
```

1.23. keyboardlisteners.c

```
#ifndef KEYBOARDLISTENER H
     #define KEYBOARDLISTENER H
     #define MAX_SCAN_CODE 300
     #define CTRL_KEY_PRESED_SCAN_CODE 29
#define CTRL_KEY_RELESED_SCAN_CODE 157
8
     #define ALT_KEY_PRESED_SCAN_CODE 56
#define ALT_KEY_RELESED_SCAN_CODE 184
10
11
     typedef int (*key_listener)();
13
14
     int activate(int scan_code);
15
     void add_key_listener(int mode, int scan_code, key_listener listener);
16
     {\color{red} \textbf{void}} \quad {\color{blue} \textbf{init\_key\_listeners}} \; (\;) \; ;
17
     #endif //KEYBOARDLISTENER H
```

1.24. keyboardlisteners.h

```
#ifndef KEYBOARDLISTENER_H

#define KEYBOARDLISTENER_H

#define MAX SCAN CODE 300
```

```
#define CTRL_KEY_PRESED_SCAN_CODE 29
#define CTRL_KEY_RELESED_SCAN_CODE 157
    #define ALT_KEY_PRESED_SCAN_CODE 56
#define ALT_KEY_RELESED_SCAN_CODE 184
9
10
11
     typedef int (*key_listener)();
12
13
14
     int activate(int scan_code);
     void add_key_listener(int mode, int scan_code, key_listener listener);
16
     void init_key_listeners();
17
     #endif //KEYBOARDLISTENER H
18
```

asm

1.25. idt.asm

```
[GLOBAL idt_flush] ; Allows the C code to call idt_flush().
3
    \verb|idt_flush|:
         mov eax, [\mathtt{esp}+4] ; Get the pointer to the IDT, passed as a \hookleftarrow
 4
         parameter.
lidt [eax]
                             ; Load the IDT pointer.
 5
6
         ret
 8
    \verb§macro ISR_NOERRCODE 1 
9
      global isr\%1
       isr \%1:
10
                                           ; Disable interrupts firstly.; Push a dummy error code.
        cli
11
         push byte 0
12
         push byte %1
                                           ; Push the interrupt number.
13
         jmp isr_common_stub
                                           ; Go to our common handler code.
14
15
    %endmacro
16
    ; This macro creates a stub for an ISR which passes it's own
17
    ; error code. \%macro ISR_ERRCODE 1
18
19
20
      global isr %1
^{21}
      isr %1:
                                           ; Disable interrupts.
22
         cli
23
         push byte \%1
                                           ; Push the interrupt number
24
         jmp isr_common_stub
^{25}
    %endmacro
26
27
    ; This macro creates a stub for an IRQ - the first parameter is
      the IRQ number, the second is the \overrightarrow{ISR} number it is remapped to.
28
29
     %macro IRQ 2
      global irq %1 irq %1 cli
30
31
32
         push byte 0
push byte %2
33
34
35
         jmp irq_common_stub
    %endmacro
36
37
    ISR_NOERRCODE O
38
39
    ISR_NOERRCODE
40
    ISR_NOERRCODE 2
41
    ISR_NOERRCODE
    ISR_NOERRCODE
ISR_NOERRCODE
42
43
    ISR_NOERRCODE
44
45
    ISR_NOERRCODE
^{46}
    ISR_ERRCODE
    ISR_NOERRCODE 9
47
    ISR_ERRCODE 10
ISR_ERRCODE 11
48
49
```

```
ISR_ERRCODE
                                                                                             12
                       ISR_ERRCODE
ISR_ERRCODE
                                                                                            13
   51
   52
                                                                                             14
   53
                        ISR_NOERRCODE 15
                        ISR_NOERRCODE 16
   55
                        ISR_NOERRCODE
                                                                                            17
   56
                        ISR_NOERRCODE 18
                       ISR_NOERRCODE
   57
                                                                                           19
                        ISR_NOERRCODE 20
   58
                        ISR_NOERRCODE
   59
                        ISR_NOERRCODE
    61
                        ISR_NOERRCODE 23
   62
                        ISR_NOERRCODE
                                                                                           24
                       ISR_NOERRCODE 25
   63
                        ISR_NOERRCODE
                                                                                             26
   64
    65
                        ISR_NOERRCODE
    66
                        ISR_NOERRCODE 28
   67
                        ISR_NOERRCODE 29
    68
                        ISR_NOERRCODE 30
   69
                       ISR_NOERRCODE 31
   70
    71
                        IRQ
    72
                        IRQ
                                                      1,
    73
                        IRQ
   74
                       IRQ
                                                      3,
                                                                                   35
   75
76
77
                       IRO
                                                      4,
                                                                                   36
                        IRO
                                                      5,
                                                                                    37
                        IRO
                                                                                    38
                                                      6.
   78
                        IRQ
                                                                                    39
    79
                        IRQ
                                                                                     40
                                                     9,
    80
                       IRQ
                                                                                     41
   81
                       IRO
                                                 10,
                                                                                    42
   82
                       IRO
                                                 11,
                                                                                    43
                                                 12,
   83
                        IRO
                                                                                    44
    84
                        IRQ
                                                 13,
                                                                                     45
    85
                                                 14,
                                                                                     46
   86
                       IRQ
                                               15,
                                                                                   47
    87
                                   global isr80h
   88
   89
                                 isr80h:
   90
                                                                                                                                                                                        ; Disable interrupts firstly.
                                       cli
                                                                                                                                                                                       ; Push a dummy error code.
; Push the interrupt number
   91
                                           {\tt push \ byte} \ 0
   92
                                           push byte 80h
   93
                                           jmp isr_common_stub
                                                                                                                                                                                         ; Go to our common handler code.
   94
   95
                       ; In isr.c
   96
   97
                       extern isr_handler
   98
                       ; ISR stub. It saves the processor state, sets ; up for kernel mode segments, calls the C-level fault handler,
   99
100
101
                        ; and finally restores the stack frame.
102
                        isr_common_stub:
                                          pusha
103
                                                                                                                                                                          ; Pushes edi, esi, ebp, esp, ebx, edx, ecx, eax
104
105
                                           \verb"mov" ax", & ds"
                                                                                                                                                                          ; Lower 16-{\tt bits} of eax ={\tt ds}.
106
                                           push eax
                                                                                                                                                                           ; save the data segment descriptor % \left( 1\right) =\left( 1\right) \left( 1
107
108
                                           mov ax, 0 \times 10 ; load the kernel data segment descriptor
                                           mov ds, ax
mov es, ax
109
110
                                           mov fs, ax mov gs, ax
111
112
113
                                           call isr_handler
114
115
116
                                           pop ebx
                                                                                                                      ; reload the original data segment descriptor
                                           mov ds, bx
                                           mov es, bx mov fs, bx
118
119
120
                                           \verb"mov gs", bx"
121
122
                                           popa
                                                                                                                                                                         ; Pops edi,esi,ebp,...
```

```
υμ, 8
number
sti
123
                                 ; Cleans up the pushed error code and pushed ISR \leftarrow
124
125
                                 ; pops 5 things at once: CS, EIP, EFLAGS, SS, and \hookleftarrow
            iret
                  ESP
126
127
       ; In isr.c
       extern irq_handler
128
129
      ; IRQ stub. It saves the processor state, sets ; up for kernel mode segments, calls the C-\mbox{level} fault handler,
130
131
132
         and finally restores the stack frame.
133
       \verb"irq_common_stub":
134
            pusha
                                                ; Pushes edi, esi, ebp, esp, ebx, edx, ecx, eax
135
            mov ax, ds
push eax
136
                                                ; Lower 16-{	t bits} of eax ={	t ds}.
137
                                                 ; save the data segment descriptor
138
139
            mov ax, 0\,\mathrm{x}10 ; load the kernel data segment descriptor
140
            \verb"mov" ds", \verb"ax"
            \verb"mov" es , \verb"ax"
141
            mov fs, ax
142
143
            mov gs, ax
1\,4\,4
            call irq_handler
145
146
            \begin{array}{cccc} \textbf{pop} & \textbf{ebx} \\ \textbf{mov} & \textbf{ds} \ , & \textbf{bx} \end{array}
                                 ; reload the original data segment descriptor
147
148
            mov es, bx
mov fs, bx
149
150
151
            mov gs, bx
152
                                 ; Pops edi,esi,ebp...; Cleans up the pushed error code and pushed ISR \hookleftarrow
153
            popa
            \verb"add" esp , 8"
154
                 number
155
            sti
156
            iret
                                  ; pops 5 things at once: CS, EIP, EFLAGS, SS, and \hookleftarrow
                  ESP
```

1.26. common.asm

```
global outb
         global outw
global inb
 3
         global inw
         global getRDTSC
  6
         getRDTSC:
 7
 8
                  rdtsc
 9
                   ret
10
11
          outb:
                 \begin{array}{cccc} \texttt{mov} & \texttt{dx} \;, & \texttt{[esp+4]} \\ \texttt{mov} & \texttt{al} \;, & \texttt{[esp+8]} \end{array}
13
                   out dx, al
14
15
                   ret
16
17
         outw:
                  \begin{array}{lll} & \texttt{mov} & \texttt{dx} \;, & \texttt{[esp}+4] \\ & \texttt{mov} & \texttt{ax} \;, & \texttt{[esp}+8] \\ & \texttt{out} & \texttt{dx} \;, & \texttt{ax} \end{array}
18
19
20
21
                   ret
22
23
         inb:
                 24
^{25}
26
                   ret
27
28
        inw:
```

 std

1.27. getchar.c

```
#include "stdio.h"
                      #define STREAM_SIZE 500
    3
                      typedef int (*flusher)(char * streampointer);
     5
                      \begin{array}{ll} \textbf{char} & \texttt{stream} \; [\; \texttt{STREAM\_SIZE} \; ] \; ; \end{array}
    8
9
                       char * streamout=stream;
 10
                      \begin{array}{lll} & \verb|intro_flush| (\verb|char| * | streampointer|) \{ & & & & \\ & & & | & & \\ & & & | & & \\ & & & | & & \\ & & & | & \\ & & & | & \\ & & & | & \\ & & & | & \\ & & & | & \\ & & & | & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\
 11
12
                                                                  -1){}
 13
                                                                  return 1;
 14
15
16
                                             return 0;
 17
                     }
 18
                      \begin{array}{c} {\tt char} \ \ {\tt getchar} \ (\ ) \ \{ \\ {\tt char} \ \ {\tt c=*streamout} \ ; \\ {\tt if} \ (\ {\tt c==' \setminus 0'} \ ) \ \{ \end{array}
 19
\frac{20}{21}
22
                                                                  streamout=stream;
23
                                                                  char * streamin=stream;
                                                                   int i,j;
^{25}
                                                                  \label{eq:formal_size} \begin{array}{l} \textbf{for} \; (\; \textbf{i} = 0 \, ; \textbf{i} \! < \! \textbf{STREAM\_SIZE} \; ; \; \textbf{i} \! + \! + \! ) \{ \end{array}
\frac{26}{27}
                                                                                       stream[i] = | \setminus 0 |;
                                                                   \begin{array}{c} \begin{array}{c} \textbf{w} \ \textbf{hile} \ (\ \texttt{!intro\_flush} \ (\ \texttt{streamin} \ )\ ) \ \{ \\ & \quad \textbf{if} \ (\ \texttt{*streamin!} = \ \ \backslash \ 0 \ \ ) \end{array} \end{array} 
28
 29
30
                                                                                                          streamin++;
                                                                                         31
^{32}
                                                                                                             printf(streamin);
33
                                                                                        } else if(*streamin=='\b'){
34
35
                                                                                                               if ( streamin > stream ) {
    printf("\b");
36
 37
                                                                                                                                     *streamin=(0);
38
39
                                                                                                                                     {\tt streamin} --;
40
                                                                                                              \begin{tabular}{ll} \tt J \\ * & \texttt{streamin} = \tt ' \setminus 0 \tt '; \\ & \texttt{else} & \texttt{if} \left( * & \texttt{streamin} = \tt = \tt ' \setminus t \tt ' \right) \{ \end{tabular}
41
42
 43
                                                                                                                                                                                *streamin='\0';
 44
 45
\frac{46}{47}
                                                                  c=*streamout;
 48
                                             streamout++;
 49
                                            return c;
50
51
```

1.28. printf.c

```
#include "stdio.h"
 2
 3
      static void prints(char * string);
      static char * numberBaseNtoString(unsigned int number, int base, char \hookleftarrow
            * out);
 6
      \begin{array}{c} {\tt void} \  \  \, {\tt putchar} \, \left( \, {\tt char} \  \  \, {\tt c} \, \right) \, \, \left\{ \\ {\tt \_write} \, \left( \, 1 \, , \, \, \&c \, , \, \, \, 1 \, \right) \, ; \end{array} \right.
 7
8
 9
      }
10
11
      void printf(char * formatString, ...) {
           int integer;
unsigned int unsigenedInteger;
char * string;
char out [40];
12
13
14
15
16
17
18
            va_list args;
19
20
            va_start(args, formatString);
21
            23
24
25
                        {\tt formatString} ++;
26
27
                        switch (*formatString) {
28
                               case 'c':
29
                                     c = va_arg(args, int);
30
                                     putchar(c);
31
                                     break;
32
                               case 's':
33
                                     string = va_arg(args, char *);
                                     prints (string);
34
35
                                     break;
36
                               \mathbf{c}\,\mathbf{a}\,\mathbf{s}\,\mathbf{e}^{-1}\,\mathbf{d}^{+}:
                                     integer = va_arg(args, int);
if (integer < 0) {
   integer = -integer;
   putchar('-');</pre>
37
38
39
40
41
^{42}
                                     prints (numberBaseNtoString(integer, 10, out));
43
                                     break;
44
                               case 'u':
                                     unsigenedInteger = va_arg(args, unsigned int); prints(numberBaseNtoString(unsigenedInteger, 10, \hookleftarrow
4.5
46
                                          out ) ) ;
                                     break;
48
                               case
                                     integer = va_arg(args, unsigned int);
prints(numberBaseNtoString(integer, 8, out));
49
50
51
                                     break:
52
53
                                     unsigenedInteger = va_arg(args, unsigned int);
54
                                     {\tt prints} \, (\, {\tt numberBaseNtoString} \, (\, {\tt unsigenedInteger} \, \, , \, \, \, 16 \, , \, \, \, \hookleftarrow \, \,
                                           out));
                                     break;
55
56
                               case
                                     putchar('%');
break;
57
59
                    else {
60
                        putchar (*formatString);
61
62
63
                  formatString++;
64
65
            va_end(args);
66
     }
67
      68
69
70
71
                  string++;
```

```
\frac{72}{73}
            }
       }
74
75
       static char * numberBaseNtoString(unsigned int number, int base, char \leftarrow
76
77
             int digits [40];
            int position = 0;
char * numbers = "0123456789ABCDEF";
78
79
             int index = 0;
80
 81
            82
83
 84
85
                               number = 0;
 86
 87
 88
                               digits [position] = number % base;
 89
                               number /= base;
90
91
                         position++;
92
                   }
93
                   \begin{array}{lll} \mbox{for (index} = 0; \mbox{ position} > 0; \mbox{ position} --, \mbox{ index} ++) \ \{ \mbox{ out[index]} = \mbox{ numbers[digits[position} -1] \ \% \mbox{ base]}; \end{array}
 94
95
96
                   \verb"out[index] = \verb"\0";
97
98
               else
                   out [0] = '0';
out [1] = '\0';
99
100
101 \\
102
103
             {\tt return} \  \  \, {\tt out} \ ;
104
```

1.29. scanf.c

```
#include "../../src/std/string.h"
#include "stdio.h"
 3
        4
 5
       }
 6
 7
 8
       int \  \  \, sscanf\left(\, ch\, ar \  \, *stream \,\, , \  \  \, ch\, ar \  \, *format \,\, , \  \, \ldots \, \right) \  \, \left\{
               va_list ap;
10
                va_start(ap, format);
11
               int i = 0;
                int j = 0;
12
               int converted;
13
14
15
               int *integer , iTmp , iTmp2;
               char* string;
char *chr;
unsigned int *uinteger;
16
17
18
19
               \begin{array}{ccc} \mathbf{w}\,\mathbf{hile} & (\,\mathbf{format}\,[\,\mathbf{i}\,]\,) & \{\\ & \mathbf{if} & (\,\mathbf{format}\,[\,\mathbf{i}\,] \ == \ \ \ '\,\%'\,) & \{ \end{array}
20
^{21}
^{22}
\frac{23}{24}
                                case
                                               \begin{array}{lll} \mathtt{chr} &=& \mathtt{va\_arg}\,(\,\mathtt{ap}\;,\;\; \mathbf{char}\,*)\;;\\ *\,\mathtt{chr} &=& \mathtt{stream}\,[\,\mathtt{j}\,+\,+\,]; \end{array}
25
26
27
                                                break;
28
^{29}
                                               integer = va_arg(ap, int *);
                                               iTmp = 0;

iTmp2 = 1;
30
31
32
                                                if (stream [j] == '-') {
```

```
iTmp2 = -1;
33
34
                                                      j++;
35
                                               36
37
38
39
40
                                               *integer = iTmp*iTmp2;
41
                                       case^{-1}u^+:
42
                                               {\tt uinteger} \ = \ {\tt va\_arg} \left( \, {\tt ap} \; , \; \; {\tt unsigned} \quad int \quad * \, \right) \; ;
                                               iTmp = 0;
while (isNumber(stream[j])) {
   iTmp = iTmp * 10 + (stream[j] - '0');
43
44
^{45}
\frac{46}{47}
                                                      j++;
48
                                               \dot{*}uinteger = iTmp;
^{49}
                                               break;
50
                                       case^{-1}s^+:
51
                                               \begin{array}{lll} \mathtt{string} &=& \mathtt{va\_arg} \left( \mathtt{ap} \;,\;\; \mathtt{char} \;\; * \right) \;; \\ \mathtt{iTmp} &=& 0 \;; \\ \mathtt{while} \;\; \left( \mathtt{stream} \left[ \; \mathsf{j} \; \right] \;\; ! = \;\; ' \;\; ' \; \right) \;\; \left\{ \end{array}
52
53
                                                    string[iTmp++] = stream[j++];
54
55
56
                                               string[iTmp] = ' \setminus 0';
57
                                               break;
                                       default:
    // WRONG %
    return converted;
58
59
60
61
62
63
                                     (format[i] == stream[j]) {
64
                                       i++;
                               j++;
} else {
65
66
67
                                       //WRONG FORMAT STRING
68
                                       return converted;
69
\begin{array}{c} 70 \\ 71 \end{array}
                       }
               }
72
       }
```

1.30. stdio.h

```
#include "../../include/varargs.h"
#include "../../include/stdarg.h"

#ifndef STDIO_H
#define STDIO_H

char getchar();
void putchar(char c);
void printf( char * formatString, ...);
int sscanf(char *formatString, char *format, ...);
#endif //STDIO_H
#include ".../include/varargs.h"
#define STDIO_H

#define STDIO_H

#erdif //STDIO_H

#endif //
```

1.31. string.c

```
\begin{array}{lll} \mathtt{return} & \mathtt{str1} \, [\, \mathtt{i} \, ] \, - \, \mathtt{str2} \, [\, \mathtt{i} \, ] \, ; \end{array}
                     }
 8
              }
if (str1[i] == '\0' && str2[i] == '\0') {
    return str1[i] - str2[i];
10
11
               return 1;
12
13
       }
14
       void strcpy(char * str_des, char * str_ori) {
15
               int i;
for (i = 0; str_ori[i] != '\0'; i++) {
    str_des[i] = str_ori[i];
16
17
18
19
              str_des[i] = '\0';
20
21
       }
22
       void strncpy(char * str_des, char * str_ori, unsigned int count) {
23
              for (i = 0; str_ori[i] != '\0' && i <= count; i++) {
    str_des[i] = str_ori[i];</pre>
\begin{array}{c} 24 \\ 25 \end{array}
26
27
              str_des[i] = ' \setminus 0';
^{29}
       }
30
31
       _{int} \ \mathtt{strlen} \left( \begin{smallmatrix} c \, h \, a \, r \, * & \mathtt{str} \end{smallmatrix} \right) \ \left\{
32
              int i;
for (i = 0; str[i] != '\0'; i++);
33
34
              return i;
35
```

1.32. string.h

```
#ifndef STRING_H
#define STRING_H

int strcmp(char* str1, char * str2);
void strcpy(char * str_des, char * str_ori);
int strlen(char* str);

#endif /* STRING_H */
```

1.33. systemcall.asm

```
global __read
global __write
global __cpuspeed
 4
       SECTION .text
 5
6
       __read:
                    \verb"mov" ecx", [esp+8]
 9
              \verb"mov" eax", 3
              \verb"mov" ebx", \quad [\verb"esp+4"]
10
             \begin{array}{ll} \texttt{mov} & \texttt{edx} \;, & \texttt{[esp+12]} \\ \texttt{int} & \texttt{80h} \end{array}
1\,1
12
13
              ret
15
              16
17
             18
19
```

1.34. systemcall.h

```
#ifndef SYSTEMCALL_H
#define SYSTEMCALL_H

void __read(int fd, void* buffer, int count);
void __write(int fd, const void* buffer, int count);
void __cpuspeed(void * ips);

#endif /* SYSTEMCALL_H */
```

user

1.35. commands.c

```
#include "commands.h"
 3
       #include " . . / std/string . h"
       #define NULL 0
 6
       #define COMMAND_MAX_CANT 20
       \begin{array}{ll} {\tt command\_t} & {\tt command\_list} \left[ \, {\tt COMMAND\_MAX\_CANT} \, \right]; \\ {\tt int} & {\tt commands\_added} = 0; \end{array}
 9
10
       command_t * get_command_list() {
    return command_list;
11
12
13
14
       \begin{array}{ll} \verb"int" & \verb"get_commands_added"() & \{\\ \verb"return" & \verb"commands_added"; \end{array}
15
16
17
       }
       19
20
                      command_list [commands_added].name=name;
command_list[commands_added].start=function;
21
22
23
                                      command_list[commands_added].help=helpDescription;
^{24}
                       commands_added++;
^{25}
\frac{26}{27}
       }
       \mathtt{main \ get\_command} \left( \begin{array}{ccc} \mathbf{char} & * & \mathtt{name} \end{array} \right) \left\{
28
               \begin{array}{ll} \text{int } & \textbf{i} \\ \text{for } (\textbf{i} = 0; \textbf{i} < \texttt{commands\_added}; \textbf{i} + +) \\ \end{array}
^{29}
30
                      if (!strcmp(command_list[i].name,name)){
    return command_list[i].start;
31
32
33
34
35
               return NULL;
```

1.36. commands.h

```
#ifndef COMMANDS H
   #define COMMANDS H
    typedef int (*main)(int argc,char * argv[]);
5
6
   main start;
10
       char * help;
11
   };
12
   typedef struct command_struct command_t;
13
14
15
    void add_command(char * name, main function, char* help);
    main get_command(char * name);
17
18
   char * autocomplete(char * name);
19
   #endif //COMMANDS_H
20
```

1.37. shell.c

```
#include "shell.h"
#include "../std/systemcall.h"
#include "../std/stdio.h"
#include "../std/string.h"
 3
       #include "commands.h"
       #define NULL 0
#define COMAND_LINE_MAX 1000
#define EXIT_SYSTEM -15
 8
 9
10
11
       #define HISTORY MAX 20
13
       #define NAME_MAX_LENGTH 50
char name[NAME_MAX_LENGTH] = "unknown";
char * pcname = "itba";
14
1.5
16
17
        char * strnormalise(char * str) {
             r * strnormail: ,
int j, i;
// cambia enters por espacios
for (j = 0; str[j] != '\0'; j++) {
    if (str[j] == '\n' || str[j] == '\t') {
        str[j] = ' ';
}
19
20
21
22
23
^{24}
^{25}
               /// elimina espacios del principio
while (str[0] == ' ') {
    str = str + 1;
26
27
28
29
               30
31
^{32}
                       str[i] = \langle 0 \rangle;
33
               //elimina espacios repetidos en el medio
for (j = 0; str[j] != '\0'; j++) {
    if (str[j] == ' ' && str[j+1] == ' ') {
        strcpy(str + j, str + j + 1);
    }
}
34
35
36
37
38
39
                       }
40
               return str;
41
```

```
42
 43
        44
 ^{45}
 ^{46}
 47
        int execute(char* comand, int argcant, char * argvec[]) { if (comand[0] == '\0') {
 48
 49
 50
                     return 0:
 51
              if (start = get_command(comand);
if (start == NULL) {
    printf("invalid comand: %\n", comand);
 52
 53
 54
                     return -1;
 55
 56
               return start(argcant, argvec);
 57
 58
       }
 59
 60
        \begin{array}{ll} \textbf{int} & \texttt{parseline}\,(\,) & \{ \end{array}
              parseline()
char c;
int i = 0;
char comand_line[COMAND_LINE_MAX];
while ((c = getchar()) != '\n' && i < COMAND_LINE_MAX - 3) {
    comand_line[i] = c;
    :___.</pre>
 61
 62
 63
 64
 65
 66
 67
               68
 69
 70
 71
 72
               comand_line[i] = ' \setminus 0';
              comand_line[i] = '\0';
char* command = strnormalise(comand_line);
int argcant = 0;
char * argvec[50];
int in_quotes = 0;
for (i = 0; command[i] != '\0'; i++) {
    if (command[i] == ' && !in_quotes) {
        command[i] = '\0';
        argvec[argant] = &command[i] + 1];
        argvec[argant] = &command[i] + 1];
 73
 74
 75
 76
 77
 78
 79
 80
                            argvec[argcant] = \&command[i + 1];
 81
                            argcant++;
                     } else if (command[i] == '"') {
 82
                            if (!in_quotes) {
 83
 84
                                   argvec[argcant-1] = \&command[i + 1];
 85
                            \begin{array}{lll} \hbox{\tt command} \hspace{0.1cm} [\hspace{0.1cm} \hbox{\tt i}\hspace{0.1cm} ] \hspace{0.1cm} = \hspace{0.1cm} [\hspace{0.1cm} \backslash \hspace{0.1cm} 0 \hspace{0.1cm} ] \hspace{0.1cm} ; \\ \hbox{\tt in\_quotes} \hspace{0.1cm} = \hspace{0.1cm} ! \hspace{0.1cm} \hbox{\tt in\_quotes} \hspace{0.1cm} ; \end{array}
 86
 87
 88
                     }
 89
 90
               return execute(command, argcant, argvec) == EXIT_SYSTEM;
 91
 92
 93
        int exit_shell(int argc, char* argv[]) {
 94
              clear_shell();
return EXIT_SYSTEM;
 95
 96
        }
 97
 98
        int echo_shell(int argc, char* argv[]) {
 99
              100
101
102
103
              printf("\n");
               return 0;
104
105
        }
106
107
        int getCPUspeed_shell(int argc, char* argv[]) {
              unsigned long ips;
108
109
                 _cpuspeed(&ips);
              110
111
112
               return 0;
113 }
```

```
114
         \begin{array}{ll} int & \texttt{clear\_shell} (int & \texttt{argc} \;,\;\; \texttt{char* argv} \; [] \;) \;\; \{ \\ & \texttt{printf} (\; " \backslash x1B \, [\, 2 \, J" \,) \;; \\ & \texttt{return} \;\; 0; \end{array} 
115
116
117
118
119
120
        int \ isodd\_shell(int \ argc \, , \ char* \ argv \, [] \, ) \ \ \{
              if (argc < 1) {
   printf("Usage: isodd <number>\n");
121
122
123
                     return -1;
124
125
               int number;
               {\tt sscanf} \left( \, {\tt argv}^{'} \left[ \, 0 \, \right] \, , \quad "\, \% l \, " \, , \quad \& \, {\tt number} \, \right) \, ;
126
127
              if (number \% 2 == 0) {
128
129
130
131
                     printf("The number % is ODD", number);
132
               printf("\n");
133
               return 0;
134
       }
135
136
        int help_shell(int argc, char* argv[]) {    printf("\x1B[33mThese are the commands available: \x1B[0m\\n\n");
137
138
139
               command_t *commands = (command_t *)get_command_list();
               \label{eq:commands} \begin{array}{ll} \mbox{int i} = 0 \,; \\ \mbox{while (i < get\_commands\_added()) } \{ \\ \mbox{printf("\x1B[4m% \x1B[0m\t \x \% \n", commands[i].name, commands[} \leftarrow \end{substitute} \end{array}
140
141
142
                            i].help);
143
144
               printf("\nPress\ CTRL+ALT+SUPR\ to\ reboot\ the\ system\n");
145
               return 0;
146
147
        }
148
149
         \verb|int rename_shell(int argc, char* argv[])| \\ \{
               if (argc < 1) {
    printf("Usage: rename <newname>.\n");
150
151
                      return -1;
152
153
154
               strncpy(name, argv[0], NAME_MAX_LENGTH);
155
        }
156
        \begin{array}{lll} \textbf{static void test\_shell\_print\_usage()} \{ & & \\ \textbf{printf("Usage: test} < testcase > . \backslash n"); \\ \textbf{printf("testcases: \backslash n \backslash tprintf \backslash n \backslash tscanf \backslash n");} \end{array}
157
158
159
160
        }
161
        int test_shell(int argc, char* argv[]) {
    if (argc < 1) {
        test_shell_print_usage();
}</pre>
162
163
164
165
                      return -1:
166
167
               int integer;
168
               unsigned int uinteger;
              char* string;
char chr;
169
170
171
               if (!strcmp(argv[0], "printf")) {    printf("\x1B[32mPlease verify the OKval is the same as RETval\\leftarrow n\x1B[1mNOTATION: case[OKval]: RETval\x1B[0m");
172
174
                      printf("\n");
                      printf("string[hola mundo]: %", "hola mundo");
175
                      printf("\n");
printf("char[c]: %c", 'c');
176
177
                      printf("\n");
178
                      printf("integer[-123]: %d", -123);
printf("\n");
179
180
                      printf("unsigned integer[123]: %", 123);
181
                     print("unsigned integer [123]: % printf("\n"); printf("hexa[FFFFFFF]: % ", -1); printf("\n");
182
183
184
              } else if (!strcmp(argv[0], "scanf")) {
185
```

```
186
187
188
                          printf("integer[-123]: %d", integer);
printf("\n");
sscanf("123", "%u", &uinteger);
printf("unsigned integer[123]: %u", uinteger);
printf("\n");
sscanf("hello world scanf", "hello %s scanf", string);
printf("string[world]: %s", string);
printf("\n");
sscanf("c", "%c", &chr);
printf("char[c]: %c", chr);
lse {
189
190
191
192
193
194
195
196
197
198
                     else {
  test_shell_print_usage();
199
200
201
202
                  \begin{array}{l} {\tt printf} \left( \, " \, \backslash \, n \, " \, \right) \, ; \\ {\tt return} \quad 0 \, ; \end{array}
203
204
205
         }
206
207
          void shell_start() {
                 ! shell_start() {
int exit = 0;
add_command("test", test_shell, "test cases for functionality");
add_command("rename", rename_shell, "changes the name of the user 
of this pc");
add_command("echo", echo_shell, "echoes some text, don't forget 
the quotes (\") if you use spaces");
add_command("clear", clear_shell, "clears the screen");
add_command("help", help_shell, "shows help");
add_command("isodd", isodd_shell, "tells if the number is odd or 
not"):
208
209
210
211
212
213
214
                           not");
                  add_command("exit", exit_shell, "exits the system.");
add_command("getCPUspeed", getCPUspeed_shell, "shows actual CPU ↔
215
216
                           speed");
217
                  do {
                          printf("\x1B[33mHi! Whats your name? \x1B[0m"); char c = ' \setminus 0';
218
                          char c = '\
int i = 0;
219
220
                          221
222
223
                           fname[i] = '\0';
if (i == NAME_MAX_LENGTH) {
    while (getchar() != '\n');
224
225
226
227
                           printf("\x1B[2J\x1B[33mWelcome to arnix (ARg uNIX) %!\x1B[0m\←
228
                           n\nYou may type \x1B[1mhelp\x1B[0m for more information\n\\leftrightarrow n", name); while (!exit) {
229
230
                                   printuser();
231
                                   {\tt exit} \; = \; {\tt parseline} \; (\,) \; ; \\
232
233
                           exit = 0;
234
                  } \mathbf{while} (1);
235
```

1.38. shell.h

```
#ifndef SHELL_H
define SHELL_H

void shell_start();

#endif /* SHELL_H */
```