Codigos fuente

Arnix

Modo protegido con GRUB

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1. Codigos fuente

include

1.1. defs.h

```
/*********************
         Defs.h
      #ifndef _defs_
#define _defs_
 6
      #define byte unsigned char
#define word short int
#define dword int
11
12
      /* Flags para derechos de acceso de los segmentos */
#define ACS_PRESENT 0x80 /* segmento
memoria */
13
                                                                          /* segmento presente en ↔
14
      memoria */
#define ACS_CSEG
#define ACS_DSEG
#define ACS_READ
#define ACS_WRITE
#define ACS_IDT
#define ACS_INT_386
#define ACS_INT
                                              0 \ge 18
                                                                          /* segmento de codigo */
                                               0 \times 10
                                                                          /* segmento de datos */
                                                                          /* segmento de lectura */
17
                                              0 \ge 0 2
                                              0 x 0 2
ACS_DSEG
18
                                                                          /* segmento de escritura */
19
                                              OxOE /* Interrupt GATE 32 bits */
( ACS_PRESENT | ACS_INT_386 )
20
^{22}
^{23}
                                               \begin{array}{c|cccc} (ACS\_PRESENT & ACS\_CSEG & ACS\_READ) \\ (ACS\_PRESENT & ACS\_DSEG & ACS\_WRITE) \\ (ACS\_PRESENT & ACS\_DSEG & ACS\_WRITE) \\ \end{array} 
24
      #define ACS_CODE
#define ACS_DATA
#define ACS_STACK
25
26
27
      #pragma pack (1)
                                              /* Alinear las siguiente estructuras a 1 byte \hookleftarrow
29
      /* Descriptor de segmento */
typedef struct {
  word limit ,
30
31
32
33
                 base_1;
^{34}
         byte base_m ,
35
                 access ,
\frac{36}{37}
                  attribs,
                 base_h;
38
      } DESCR_SEG;
39
40
      41
42
43
44
                          selector;
^{45}
         byte
                          cero,
^{46}
47
         word
                          offset_h;
      } DESCR_INT;
48
49
      /* IDTR */
typedef struct {
  word limit;
50
51
52
53
          dword base;
\frac{54}{55}
      } IDTR;
56
```

1.2. kasm.h

```
\frac{4}{5}
   ****************
   #include "defs.h"
   unsigned int
                 _read_msw ();
10
                  _lidt (IDTR *idtr);
11
   v o i d
12
              _mascaraPIC1 (byte mascara); /* Escribe mascara de PIC1 \leftarrow
   void
13
              _mascaraPIC2 (byte mascara); /* Escribe mascara de PIC2 \leftarrow
   void
14
15
              16
   v o i d
17
   v o i d
18
19
              _int_08_hand();
                                /* Timer tick */
   v o i d
21
   void
              _debug (void);
```

1.3. kc.h

```
/********
2
     kc.h
3
    #include "defs.h"
    #ifndef _kc_
#define _kc_
6
7
8
9
    #define WHITE TXT 0x07 // Atributo de video. Letras blancas, fondo ←
10
    11
12
13
    /* Tiempo de espera */void wait(int time);
14
15
16
17
    /* Limpia la pantalla */
18
    void k_clear_screen();
19
    /* Inicializa la entrada del IDT */ void setup_IDT_entry (DESCR_INT *item, byte selector, dword offset, \hookleftarrow
20
21
        byte access,
22
                  byte cero);
23
    \#endif
24
```

1.4. stdarg.h

```
1 /*
2 * stdarg.h
3 *
```

```
* Provides facilities for stepping through a list of function \hookleftarrow
4
           arguments of
      * an unknown number and type.
 5
 6
      st NOTE: Gcc should provide stdarg.h, and I believe their version will\leftrightarrow
                with crtdll. If necessary I think you can replace this with \hookleftarrow
 8
           the GCC
9
               stdarg.h (or is it vararg.h).
10
      * Note that the type used in va_arg is supposed to match the actual \hookleftarrow
      * *after default promotions *. Thus, va_arg (..., short) is not valid.
12
13
      * This file is part of the Mingw32 package.
14
15
16
      * Contributors:
         Created by Colin Peters < colin@bird.fu.is.saga-u.ac.jp>
17
18
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19
20
         This source code is offered for use in the public domain. You may use, modify or distribute it freely.
^{21}
23
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24
25
26
         DISCLAMED. This includes but is not limited to warranties of
         MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE.
27
28
^{29}
        $ Revision: 1.1.1.1 $
     * $Author: brandon6684 $

* $Date: 2001/12/18 22:53:51 $
30
31
32
33
34
        Appropriated for Reactos Crtdll by Ariadne */
35
    #ifndef STDARG_H
#define STDARG H
36
37
38
39
40
      * Don't do any of this stuff for the resource compiler.
41
    #ifndef RC INVOKED
42
43
44
     * I was told that Win NT likes this.
45
^{46}
    #ifndef _VA_LIST_DEFINED
#define _VA_LIST_DEFINED
^{47}
48
49
     #endif
50
    #ifndef _VA_LIST
#define _VA_LIST
typedef char* va_list;
51
52
53
54
     #endif
55
56
57
     * Amount of space required in an argument list (ie. the stack) for an
58
59
        argument of type t.
60
    #define
61
                 _va_argsiz(t)
         (((sizeof(t)) + sizeof(int) - 1) / sizeof(int)) * sizeof(int))
62
63
64
65
66
     * Start variable argument list processing by setting AP to point to \hookleftarrow
           the
67
        argument \ after \ pN\,.
68
    #ifdef
              __GNUC__
69
```

```
st In GNU the stack is not necessarily arranged very neatly in order \leftrightarrow
71
         t o
     * pack shorts and such into a smaller argument list. Fortunately a * neatly arranged version is available through the use of \hookleftarrow
72
         __builtin_next_arg
    #define va_start(ap, pN) \
    ((ap) = ((va_list) __builtin_next_arg(pN)))
#else
74
75
76
77
78
79
      st For a simple minded compiler this should work (it works in GNU too \hookleftarrow
     * vararg lists that don't follow shorts and such).
80
    81
82
83
85
86
87
     * End processing of variable argument list. In this case we do \hookleftarrow
88
          nothing.
89
90
     #define va_end(ap) ((void)0)
91
92
93
     * Increment ap to the next argument in the list while returing a
94
     * pointer to what ap pointed to first, which is of type t.
95
     \ast We cast to void \ast and then to t \ast because this avoids a warning about
97
     * increasing the alignment requirement.
98
99
100
    101
102
103
104
     #endif /* Not RC INVOKED */
105
106
    #endif /* not _STDARG_H_ */
107
```

1.5. varargs.h

```
/* $NetBSD: varargs.h,v 1.11 2005/12/11 12:16:16 christos Exp $ */
 2
3
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            the
            documentation and/or other materials provided with the \hookleftarrow
19
            distribution.
```

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32
          ANY WAY
     * OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY
33
           OF
     * SUCH DAMAGE.
34
35
                          8.2 (Berkeley) 3/22/94
36
        @(#) v ar ar g s . h
37
38
    #ifndef VARARGS H
39
    #define VARARGS H
40
41
    #if !__GNUC_PREREQ__
#define __va_ellipsis
42
43
44
    #else
^{45}
    #define __va_ellipsis
46
    #endif
47
          _GNUC_PREREQ
48
                                 __builtin_va_alist_t
    #define __va_alist_t
50
    #else
51
    #define __va_alist_t
                                long
52
    #endif
53
                           __builtin_va_alist
__va_alist_t __builtin_va_alist; __va_ellipsis
    #define va_alist
#define va_dcl
54
55
56
57
    #endif
58
```

 src

kernel

1.6. kernel.c

```
12
   | kmain()
13
         // Init system.
14
15
        init_descriptor_tables();
16
        init_int80();
17
        init_in_out();
18
        init_keyboard();
19
        init_timer_tick();
20
        init_screen();
21
         // Start Shell
^{23}
        shell_start();
24
```

1.7. lib.asm

```
GLOBAL
              _read_msw ,_lidt
              _int_08_hand
     GLOBAL
              _mascaraPIC1 , _mascaraPIC2 , _Cli , _Sti _debug
 3
     GLOBAL
     GLOBAL
    EXTERN int_08
8
9
    SECTION .text
10
11
12
13
     _Cli:
        cli
                         ; limpia flag de interrupciones
14
15
         ret
16
17
     _Sti:
19
                        ; habilita interrupciones por flag
20
         ret
21
     _mascaraPIC1:
                                  ; Escribe mascara del PIC 1
^{22}
23
         push ebp
mov
^{24}
                        ebp, esp
^{25}
               mοv
                         ax, [ss:ebp+8] ; ax = mascara de 16 bits
\frac{26}{27}
               out 21h, al
              рор
                         ebp
28
              retn
29
30
     _mascaraPIC2:
                                   ; Escribe mascara del PIC 2
        push ebp
mov ebp, esp
31
^{32}
              33
34
35
                         ebp
               pop
36
               retn
37
     _read_msw:
38
39
                                  ; Obtiene la Machine Status Word
               smsw
                         ах
\frac{40}{41}
               retn
42
43
     _{\tt lidt}:
                             ; Carga el IDTR
44
               push
                         ebp
^{45}
               mοv
                         ebp, esp
46
               push
                         ebx
47
                         \mathtt{ebx}\;,\;\; [\,\mathtt{ss}\,\colon\;\, \mathtt{ebp}\;+\;6\,]\quad;\;\; \mathtt{ds}\,\colon\!\mathtt{bx}\;=\;\mathtt{puntero}\;\;\mathtt{a}\;\;\mathtt{IDTR}
               mοv
         \mathtt{rol}\ \mathtt{ebx}\ ,1\ 6
48
^{49}
         lidt [ds: ebx]
                                          ; carga IDTR
50
            рор
                         еbх
51
               pop
                         ebp
52
               retn
53
```

```
54
55
56
57
      ; Debug para el BOCHS, detiene la ejecucion para continuar ; colocar \hookleftarrow
           en el BOCHSDBG: set $eax=0
      _debug:
58
59
                 push
60
                 mοv
                            \mathtt{bp}\ , \quad \mathtt{sp}
61
                 push
                            аx
62
     vuelve: mov
                            \mathtt{ax} \;, \quad 1
63
                 \mathtt{cmp}\ \mathtt{ax}\ ,\ 0
64
           jne vuelve
65
           pop ax
66
67
                retn
```

1.8. lib.c

```
#include "../include/kc.h"
3
 4
                      **************
     *k_clear_screen
 6
    * Borra la pantalla en modo texto color.
7
8
9
10
     void k_clear_screen()
11
         char *vidmem = (char *) 0xb8000;
         unsigned int i=0; 
while (i < (80*25*2))
13
14
15
16
              \verb"vidmem" [i] = \verb"-";"
18
              vidmem[i]=WHITE_TXT;
19
              \mathbf{i} +\! + \, ;
         };
20
21
    }
22
23
    *setup_IDT_entry
* Inicializa un descriptor de la IDT
^{24}
25
26
27
    *Recibe: Puntero a elemento de la IDT
          Selector a cargar en el descriptor de interrupcion
Puntero a rutina de atencion de interrupcion
28
29
30
          Derechos de acceso del segmento
31
32
    ***********************
33
     void setup_IDT_entry (DESCR_INT *item, byte selector, dword offset, \hookleftarrow
34
         byte access,
            byte cero) {
35
36
       item->selector = selector;
37
       \verb|item-> offset_1| = offset & 0xFFFF;
38
       \mathtt{item} \mathop{->} \mathtt{offset\_h} \ = \ \mathtt{offset} \ >> \ 1\,6\,;
39
       item->access = access;
       item->cero = cero;
40
41
```

1.9. loader.asm

```
er ; making entry point visible to linker ; end of kernel land
     global _loader
global eokl
 2
                             ; _main is defined elsewhere
 3
     extern kmain
 6
      setting up the Multiboot header - see GRUB docs for details
     MODULEALIGN equ 1<0
 7
                                                          ; align loaded modules on page \hookleftarrow
           boundaries
     MEMINFO
 8
                equ
                           1<<1
                          1{<<}1 ; provide memory map MODULEALIGN | MEMINFO ; this is the Multiboot 'flag' \hookleftarrow
 9
     FLAGS
                    equ
     field
MAGIC
                           0 x 1 B A D B O O 2
                    equ
10
                                                          ; 'magic number' lets \hookleftarrow
         bootloader find the header \mathtt{CKSUM} equ -(\mathtt{MAGIC} + \mathtt{FLAGS})
     CHECKSUM
                                                         ; checksum required
11
12
13
     section .text
14
     align 4
15
     MultiBootHeader:
16
        dd MAGIC
          dd FLAGS
17
          dd CHECKSUM
18
19
^{20}
            reserve initial kernel stack space
^{21}
          22
23
          _loader:
          mov esp, stack+STACKSIZE; set up the stack
push eax; pass Multiboot magic number
push ebx; pass Multiboot info structure
24
25
26
27
                         ; call kernel proper
; halt machine should kernel return
28
29
          hlt
30
            {\tt dd} \;\; {\tt STACKSIZE} \; + \; {\tt stack}
31
32
         section .bss
33
          align 32
^{34}
          stack:
35
          resb STACKSIZE
                               ; reserve 16\mathtt{k} stack on a quadword boundary
```

driver

1.10. keyboard.c

```
#include "../system/isr.h"
#include "../system/in_out.h"
#include "../system/keyboardlisteners.h"
     #define KEYBOARD 0x60
 5
     #define BUFFER_SIZE 100
 6
     #define LSHIFT_KEY_PRESED_SCAN_CODE 42
#define LSHIFT_KEY_RELESED_SCAN_CODE 170
#define RSHIFT_KEY_PRESED_SCAN_CODE 54
#define RSHIFT_KEY_RELESED_SCAN_CODE 182
1.1
12
     #define BLOQ MAYUS SCAN CODE 58
13
14
     char array[BUFFER_SIZE];
15
16
17
     buffer_t stdin;
18
19
     char * actual_scan_code_table;
20
     ^{21}
22
```

```
23
^{24}
      int shift;
25
      \begin{array}{ll} \textbf{int} & \texttt{bloq\_mayusc} \; ; \\ \end{array}
26
27
      \begin{array}{ll} & \texttt{int} & \texttt{bloq\_mayusc\_unpresed} \ (\ ) \ ; \end{array}
28
      int bloq_mayusc_presed();
29
30
      int bloq_mayusc_presed(){
31
            \verb|bloq_mayusc|=0;
                  \verb"add_key_listener" (-1, BLOQ_MAYUS_SCAN_CODE", bloq_mayusc_unpresed {\leftarrow}
32
                        );
33
            return 0;
34
      }
35
36
      int bloq_mayusc_unpresed(){
37
            bloq_mayusc=1;
                  \verb"add_key_listener" (-1,BLOQ_MAYUS_SCAN_CODE", bloq_mayusc_presed);
38
39
            return 0:
40
      }
41
^{42}
      int shift_presed(){
            \mathtt{shift} ++;
43
44
            \verb"actual_scan_code_table=SHIFT_SCAN_CODE_TABLE";
45
            return 0;
46
      }
^{47}
48
      int shift_relesed(){
^{49}
            shift-
            _{\hbox{\scriptsize if}}\,(\,\hbox{\tt shift}==0)\{
50
51
                  \verb"actual_scan_code_table=SCAN_CODE_TABLE";
52
53
            return 0;
54
     }
55
56
      void IRQ1_handler(registers_t reg){
57
            int tmp;
int i=inb(KEYBOARD);
58
            59
60
61
62
                         char c=actual_scan_code_table[i];
                        \begin{array}{c} if \, (\, b \, lo \, q \, \_mayus \, c \, \, \{\\ if \, (\, c \! > = \! ' \, a \, ' \, \&\& \, c \! < \! = \! ' \, z \, ' \, ) \, \{\\ c \! = \! c \! + \! ' \, A \, ' - \! ' \, a \, ' \, ;\\ \} \, e \, lse \quad if \, (\, c \! > \! = \! ' \, A \, ' \, \&\& \, c \! < \! = \! ' \, Z \, ' \, ) \, \{\\ \end{array}
63
64
65
66
67
                                     c=c+^{\uparrow}a^{\dagger}-^{\dagger}A^{\dagger};
68
69
70
71
                         stdin.array[stdin.end] = c;
                        stdin.end=tmp;
72
                  } else {
73
                                     //TODO: beep
74
                  }
\frac{75}{76}
            }
      }
77
      static void reset(){
78
79
            outb (0 x64,0 xFE);
80
81
82
      {\tt static int cnrl_alt_supr_manager()} \{
            reset();
return 0;
83
84
85
      }
86
87
      void init_keyboard(){
88
           register_interrupt_handler(IRQ1,IRQ1_handler); stdin.start=stdin.end=0;
89
90
            stdin.array=array
92
            stdin.size=BUFFER_SIZE;
```

```
93
              add_{in\_out(0,\&stdin)};
             94
 95
 96
                    init_key_listeners()
                    \verb"add_key_listener" (-1, LSHIFT_KEY_PRESED_SCAN_CODE", shift_presed") \leftarrow \\
                    \verb"add_key_listener" (-1\,, \verb"RSHIFT_KEY_PRESED_SCAN_CODE" , "shift_presed") \hookleftarrow \\
 98
                    add_key_listener(-1,LSHIFT_KEY_RELESED_SCAN_CODE, \leftarrow
99
                    \begin{array}{c} \texttt{shift} \, \texttt{relesed} \, ) \, ; \\ \texttt{add} \, \texttt{key} \, \texttt{listener} \, (-1\,, \texttt{RSHIFT} \, \texttt{KEY} \, \texttt{RELESED} \, \texttt{SCAN} \, \texttt{CODE} \, , \\ \end{array} \, \leftrightarrow \, \\ \\ \end{array}
100
                           shift_relesed);
                    \verb"add_key_listener" (-1, BLOQ_MAYUS_SCAN_CODE", bloq_mayusc_unpresed \hookleftarrow 
101
102
103
                    add_key_listener(3, 83, cnrl_alt_supr_manager);
```

1.11. keyboard.h

```
#ifndef KEYBOARD_H
#define KEYBOARD_H

void init_keyboard();

#endif /* KEYBOARD_H */
```

1.12. screen.c

```
#include "screen.h"
#include "../system/isr.h"
#include "../system/in_out.h"
 3
     #include "timer.h"
 6
     {\tt int16\_t} \ *{\tt video\_memory} \ = \ ({\tt int16\_t} \ *) \ 0\,{\tt xB8000} \ ;
     #define BUFFER_SIZE 1000
 8
     char array_out[BUFFER_SIZE];
buffer_t stdout;
10
1\,1
     #define ESC '\x1B'
#define BELL '\x07
12
13
14
     #define DEFAULT SETTINGS 0x07
15
16
     #define SCREEN_SIZE_X 80
17
18
     #define SCREEN_SIZE_Y 25
     uint8_t screen_state = 0; // 0=normal, 1=scaped, 2=parameters.
19
20
     #define SCREEN MAX PARAM COUNT 16
21
     uint8_t screen_param_count = 0;
int screen_param [SCREEN_MAX_PARAM_COUNT];
22
^{24}
25
     uint8_t screen_cursor_x = 0;
26
     \verb"uint8_t screen_cursor_y = 0;
27
     \label{eq:uint8_t} \verb"uint8_t" screen_settings" = DEFAULT_SETTINGS";
28
     #define VGA HIGH CURSOR BYTE 14
     #define VGA_LOW_CURSOR_BYTE 15
#define VGA_MODE_PORT 0x3D4
30
31
32
     #define VGA_IO_PORT 0x3D5
33
     static void update_cursor() {
34
```

```
\verb|int16_t cursorLocation| = \verb|screen_cursor_y| * \verb|SCREEN_SIZE_X| + \leftarrow
35
          screen_cursor_x;
outb(VGA_MODE_PORT, VGA_HIGH_CURSOR_BYTE);
outb(VGA_IO_PORT, cursorLocation >> 8);
outb(VGA_MODE_PORT, VGA_LOW_CURSOR_BYTE);
36
39
          outb(VGA_IO_PORT , cursorLocation);
40
41
     42
43
44
          if (screen_cursor_y >= SCREEN_SIZE_Y) {
               int i; for (i = 0 * SCREEN_SIZE_X; i < (SCREEN_SIZE_Y - 1) * \leftarrow
^{45}
46
                    SCREEN_SIZE_X; i++) {
video_memory[i] = video_memory[i + SCREEN_SIZE_X];
47
48
               int lastLine = SCREEN_SIZE_Y - 1;
               \begin{array}{lll} \text{for (i = (lastLine) * SCREEN_SIZE_X; i < SCREEN_SIZE_Y *} & \longleftrightarrow \\ & \text{SCREEN\_SIZE\_X; i++) } \end{array}
50
5.1
                    video_memory[i] = blank;
52
53
               screen_cursor_y = (lastLine);
54
          }
55
56
57
     static \ void \ print(char \ c) \ \{
          int16_t *location; location + (screen_cursor_y * SCREEN_SIZE_X + \hookleftarrow
58
59
               screen_cursor_x);
60
          61
62
               if (++screen_cursor_x >= SCREEN_SIZE_X));
screen_cursor_x = 0;
63
64
65
                    screen_cursor_y++;
66
67
            else {
               *location = ( ' ' | (screen\_settings << 8));
68
69
     }
70
71
     static void do_bell() {
   // TODO
73
74
75
     {\tt static\ void\ do\_backspace}\,(\,)\ \{
76
          if (screen_cursor_x) {
78
               screen_cursor_x --
            else if (screen_cursor_y) {
   screen_cursor_x = SCREEN_SIZE_X - 1;
79
80
81
               screen\_cursor\_y--;
82
          print(' \setminus b');
83
84
     }
85
86
     {\tt static \ void \ do\_lineFeed()} \ \{
87
          screen_cursor_x = 0;
88
          screen\_cursor\_y++;
89
90
     92
93
94
     static void do_return() {
    screen_cursor_x = 0;
95
96
97
98
     99
100
          int i; for (i = 0; i < SCREEN_SIZE_X * SCREEN_SIZE_Y; i++) {
101
102
               video_memory[i] = blank;
103
104
```

```
105
          {\tt screen\_cursor\_x} \ = \ {\tt screen\_cursor\_y} \ = \ 0\,;
106
          update_cursor();
107
     }
108
     109
110
111
               screen_clear();
112
113
     }
114
115
     /st Map from ANSI colors to the attributes used by the PC st/
116
     static uint8_t ansi_colors[8] = \{0, 4, 2, 6, 1, 5, 3, 7\};
117
     {\tt static\ void\ do\_scape\_m()}\ \{
118
          119
120
121
122
123
               if (dec == 0)
                    switch (\dot{u})
124
                         case 0:
125
                             {\tt screen\_settings} \ = \ {\tt DEFAULT\_SETTINGS} \ ;
126
                              break;
127
128
                         case 1:
129
                              screen_settings = 0x08;
130
                              break;
131
                         case 4:
132
                              screen settings &= 0xBB:
133
                              break;
134
                         case 5:
135
                              screen\_settings = 0x80;
136
               } else if (dec == 3) { /* foreground */
    //print('3');
137
138
139
                    screen\_settings = (0 xF0 \& screen\_settings) | (0 xOF \& \leftarrow)
               ansi_colors[u]);
} else if (dec == 4) { /* background */
screen_settings = (0x0F & screen_settings) | (ansi_colors[←
140
141 \\
                        u] << 4);
142
               }
          }
143
144
     }
145
     static \ void \ do\_scape(char \ c) \ \{
146
          switch \ (\texttt{screen\_state}) \ \{
147
148
               case 1:
                    if (c == '[') {
149
                         screen_state = 2;
150
                         151
                         int i = 0;
for (; i \le SCREEN_MAX_PARAM_COUNT; i++) {
152
153
154
                              screen_param[i] = 0;
155
                    } else {
156
157
                         screen_state = 0;
                    }
break;
158
159
160
               case 2:
                    if (c >= '0' && c <= '9') {
161
                         \verb|screen_param[screen_param_count - 1]| = 10 * \leftarrow
162
                              screen_param[screen_param_count - 1] + (c - '0');
                    } else if (c == '; ') {
163
                   sc.
} else {
    switch (c) {
        case 'm':
        do_sc
164
                         screen_param_count++;
165
166
167
                                 {\tt do\_scape\_m}\;(\;)\;;
168
169
                                  break;
170
                              c\,a\,s\,e^{-+}\,J^{-+} :
                                  do_scape_J();
171
172
                                  break:
173
                         screen_state = 0;
175
```

```
176
                           break;
177
              }
178
       }
179
       \begin{array}{c} v\:oi\:d & \texttt{screen\_put}\:(\:c\:h\:a\:r\:\:c\:) \quad \{\\ i\:f \quad (\:s\:c\:r\:e\:e\:n\_s\:t\:a\:t\:e\:>\:0\:) \quad \{ \end{array}
180
181
182
                    {\tt do\_scape(c)}\;;
             return;
} else {
    switch (c) {
    case ESC:
183
184
185
186
187
                                screen\_state = 1;
188
                                 return;
                           case '\0':
return;
case BELL:
189
190
191
192
                                 do_bell();
193
                                 return;
194
195
                                 do_backspace();
196
                                break;
197
                           case
198
                                 do_lineFeed();
199
                                 break;
200
                           case '\t'
201
                                 do_tab();
202
                                 break;
203
                           case
204
                                 do_return();
                                 break;
205
206
                           default:
207
                                 print(c);
208
                                 break;
209
210
                    scroll();
211
                    update_cursor();
212
              }
\frac{213}{214}
       }
       void screen_write(char *string) {
    int i = 0;
215
216
217
              while (string[i]) {
218
                    screen_put(string[i++]);
219
220
       }
221
        static void timer_print(registers_t reg) {
222
223
              int i;
              for (i = 0; stdout.start != stdout.end; i++) {
    screen_put(stdout.array[stdout.start]);
224
225
226
                    stdout.start = (stdout.start + 1) % stdout.size;
227
              }
228
       }
229
230
       void \  \, \texttt{init\_screen} \, (\,) \  \, \{ \,
231
              register_tick_subhandler(timer_print);
             stdout.start = stdout.end = 0;
stdout.array = array_out;
stdout.size = BUFFER_SIZE;
add_in_out(1, &stdout);
screen_write("\x1B[2J");
232
233
234
235
236
237
```

1.13. screen.h

```
1  /**
2  * screen.h | Interfaz para manejo de pantalla.
3  */
4  #include "../system/common.h"
```

```
#ifndef SCREEN H
6
    #define SCREEN H
7
8
9
     * Escribe un caracter en pantalla.
     * @param char c: el caracter a escribir.

* Los siguientes ANSI scape Characters fueron implementados:
10
11
12
            Esc [2J
                                 Borra la pantalla y mueve el cursor a (line 0,←
13
           column 0).
                              Cambia el modo de graficos segun los ↔
14
            Esc [#;#;...m
          siguientes atributos:
15
     * Text attributes
16
              All attributes off
Bold on
17
     * 0
18
     * 1
19
              Underscore (on monochrome display adapter only)
20
     * 5
              Blink on
\frac{21}{22}
     * \ \ Foreground \ \ colors
                                Background colors
40 Black
41 Red
23
              Black
     * 30
24
     * 31
              Red
              Green
                                     Green
26
     * 33
              Yellow
                                 43
                                     Yellow
27
     * 34
              Blue
                                 44
                                          Blue
28
     * 35
              Magenta
                                 4.5
                                          Magenta
29
     * 36
              Cvan
                                 46
                                           Cvan
30
     * 37
              White
                                 47
                                          White
31
     * Ej: Esc[34;47m (azul en fondo blanco)
33
    void screen_put(char c);
34
35
36
    #endif
```

1.14. timer.c

```
#include "../system/isr.h"
#include "../system/int80.h"
 3
     #define SUB_FUNC_VEC_SIZE 10
 5
 6
     int80_t sub_handler_vec[SUB_FUNC_VEC_SIZE];
     int count_ticks;
int sub_func_count;
 9
10
11
      unsigned long k;
12
     void register_tick_subhandler(int80_t func) {
   if (sub_func_count < SUB_FUNC_VEC_SIZE - 1) {
      sub_handler_vec[sub_func_count] = func;
}</pre>
13
14
15
16
                 sub_func_count++;
           }
17
     }
18
19
      void IRQO_handler(registers_t regs) {
^{21}
           int i;
^{22}
           if (count_ticks) {
                 if (ticks = 0) {
k = getRDTSC();
23
^{-24}
25
26
                 ticks++;
27
28
           for (i = 0; i < sub_func_count; i++) {
29
                 sub_handler_vec[i](regs);
30
31
    }
```

```
33
34
        count\_ticks = 1;

ticks = -1;
35
        _Sti();
while (ticks < 30);
36
37
        k = getRDTSC() - k;
38
        _Cli();
count_ticks = 0;
39
40
         *((unsigned long*) regs.ebx) = (k / ticks)*18 + k / (ticks * 5);
41
^{42}
    }
^{43}
44
    void init_timer_tick() {
        sub_func_count = 0;
count_ticks = 0;
45
46
        register_interrupt_handler(IRQO, IRQO_handler);
^{47}
        register_functionality(5, cpu_speed);
^{49}
```

1.15. timer.h

```
#include "../system/int80.h"
     #ifndef TIMER_H
#define TIMER_H
 3
 6
     void register_tick_subhandler(int80_t func);
 8
     \begin{array}{c} \mathbf{v} \, \mathbf{oid} & \mathtt{init\_timer\_tick} \, (\,) \, \, ; \end{array}
9
10
1\,1
     void start_ticks();
12
     void stop_ticks();
     13
     #endif /* TIMÉR_H */
14
```

system

1.16. common.h

```
#ifndef COMMON H
     #define COMMON_H
 3
      // Exact-width integer types
 4
     typedef signed char
typedef unsigned char
 5
                                        int8_t;
                                        uint8_t;
 6
      typedef
                   signed short int16_t;
     typedef unsigned short uint16_t;
 9
      typedef
                    signed int
                                        int32_t;
10
     typedef unsigned int
                                        {\tt uint32\_t}\;;
11
     #define NULL ((void*)0)
12
13
     #define PORT_PIC1 0x20
#define PORT_PIC2 0xA0
15
16
     #define SIGNAL_EOI 0x20
17
18
     extern void outw(uint16_t port, uint16_t value);
extern void outb(uint16_t port, uint8_t value);
extern uint8_t inb(uint16_t port);
extern uint16_t inw(uint16_t port);
19
     extern uint32_t getRDTSC();
```

```
24 | #endif // COMMON_H
```

1.17. idt.c

```
#include "common.h'
     #include "idt.h
     #include "isr.h
     static void init_idt();
 5
     static void idt_set_gate(uint8_t, uint32_t, uint16_t, uint8_t);
 6
 8
     \verb|idt_entry_t| | \verb|idt_entries|| [256];
                    idt_ptr;
     idt_ptr_t
11
     // Extern the ISR handler array so we can nullify them on startup.
     extern isr_t interrupt_handlers[];
extern void idt_flush(uint32_t);
12
13
14
15
     void init_descriptor_tables()
16
17
          /* Habilito interrupcion de timer tick*/
18
          _Cli();
          _mascaraPIC1(0xFE);
19
          _mascaraPIC2(0xFF);
20
^{21}
          _Sti();
23
          init_idt();
24
     }
25
26
27
     static void init_idt()
28
     {
29
          idt_ptr.limit = sizeof(idt_entry_t) * 256 -1;
          idt_ptr.base = (uint32_t)&idt_entries;
30
31
          // Remap the irq table.
32
          outb (0 x20, 0x11);
33
          outb (0 xAO , 0x11);
outb (0 xAO , 0x21);
34
35
36
          outb (0 xA1 , 0 x28 )
37
          outb (0x21, 0x04)
38
          outb (0 \times A1, 0 \times 02)
39
          outb(0x21, 0x01)
          outb (0 xA1, 0x01);
40
          outb (0 x21 , 0x0);
outb (0 xA1 , 0x0);
41
^{42}
43
44
          idt_set_gate(0,
                                 (uint32_t)isr0
                                                       0 \times 08 , 0 \times 8E );
                                                       0x08,
45
          idt_set_gate( 1, (uint32_t)isr1
idt_set_gate( 2, (uint32_t)isr2
                                                               0 x 8 E ) :
46
                                                       0x08, 0x8E);
^{47}
          idt_set_gate(
                                 (uint32_t)isr3
                                                       0 x 08 ,
                                                               0 x 8 E )
                                 (uint32_t) isr 4
^{48}
          idt_set_gate(
                                                       0 \times 08,
                                                       0 x 08 ,
49
          idt_set_gate(
                                 (uint32_t)isr5
                                                               0 x 8 E
                            6,
                                                       0 \times 08,
50
          idt_set_gate(
                                 (uint32_t)isr6
                                                               0 x 8E)
                            7,
51
          idt_set_gate(
                                 (\,\mathtt{uint32\_t}\,)\,\mathtt{isr}\,7
                                                       0 \times 08 ,
                                                              0 x 8 E
                                 (uint32_t)isr8
52
          idt_set_gate(8,
                                                       0x08.
                                                               0 x 8 E )
                                (uint32_t)isr9
53
          idt_set_gate(
                                                       0x08,
54
          idt_set_gate(10,
                                 (uint32_t) isr10
                                                       0 x 08 ,
                                                               0 x 8 E
55
          idt_set_gate(11,
                                 (uint32_t) isr11,
                                                       0 \times 08 ,
                                                               0 x 8 E
                                                       0 x 08 ,
56
          idt_set_gate(12,
                                 (uint32_t)isr12
                                                               0 x 8 E
                                                       0 \times 08,
          \verb"idt_set_gate" (13",
57
                                 (uint32_t)isr13
                                                               0 x 8 E )
                                 (uint32_t) isr14
58
                                                       0x08,
                                                               0 x 8 E
          \verb"idt_set_gate" (14",
59
                                 (uint32_t)isr15
                                                       0x08,
                                                               0 x 8 E )
          idt_set_gate(15,
60
          idt_set_gate(16,
                                 (uint32_t)isr16
61
          idt_set_gate(17,
                                 uint32_t) isr17
                                                       0 x 08 ,
                                                               0 x 8 E
                                                       0 \times 08 ,
62
          idt_set_gate(18,
                                 (uint32_t) isr18,
                                                               0 x 8E)
          idt_set_gate(19, (uint32_t)isr19, 0x08, 0x8E); idt_set_gate(20, (uint32_t)isr20, 0x08, 0x8E);
63
64
```

```
65
             \mathtt{idt\_set\_gate} \hspace{0.1cm} (\hspace{0.1cm} \mathtt{uint32\_t} \hspace{0.1cm}) \hspace{0.1cm} \mathtt{isr21} \hspace{0.1cm}, \hspace{0.1cm} 0 \hspace{0.1cm} \mathtt{x08} \hspace{0.1cm}, \hspace{0.1cm} 0 \hspace{0.1cm} \mathtt{x8E} \hspace{0.1cm}) \hspace{0.1cm};
                                                                 0 \times 08,
 66
            idt_set_gate(22, (uint32_t)isr22, 0x08, 0x8E); idt_set_gate(23, (uint32_t)isr23, 0x08, 0x8E);
 67
 68
             idt_set_gate(24,
                                       (uint32_t)isr24,
                                                                 0 x 08 ,
                                                                          0 x 8 E )
 69
             idt_set_gate(25,
                                       (uint32_t) isr25
                                                                 0 x 08 ,
 70
             idt_set_gate(26)
                                       (uint32_t)isr26
                                                                          0 x 8 E
                                                                 0 x 08 ,
 71
             idt_set_gate(27)
                                       (uint32_t)isr27
                                                                          0 x 8E)
 72
             idt_set_gate(28)
                                       (uint32_t) isr28, 0x08, 0x8E
             \mathtt{idt\_set\_gate} \ (29\ ,
 73
                                      (uint32_t)isr29, 0x08, 0x8E)
             idt_set_gate(30, (uint32_t)isr30, 0x08, 0x8E)
 74
 75
            idt_set_gate(31, (uint32_t)isr31, 0x08, 0x8E);
 76
 77
            \verb"idt_set_gate" (32, (uint32_t)irq0, 0x08, 0x8E);
                                                               0 \times 08,
 78
             \verb"idt_set_gate" (33, (uint32_t) irq1,\\
                                                                         0x8E);
 79
             idt_set_gate(34, (uint32_t)irq2,
                                                               0 \times 08,
                                                                         0x8E):
            idt_set_gate(35, (uint32_t)irq3,
idt_set_gate(36, (uint32_t)irq4,
 80
                                                               0 x 08,
                                                                         0x8E);
 81
                                                               0 x 0 8 ,
 82
             idt_set_gate(37,
                                       (uint32_t)irq5,
                                                               0 \times 08,
                                                                         0x8E);
 83
             idt_set_gate(38,
                                       (uint32_t) irq6,
                                                               0 x 0 8 ,
                                                                         0 x 8 E ) ;
                                                               0 \times 08,
                                                                         0x8E);
 84
             idt_set_gate(39)
                                       (uint32_t)irq7,
 85
                                       (uint32_t)irq8,
                                                               0 \times 08,
             \verb"idt_set_gate" (40",
                                                                         0x8E):
                                       (uint32_t)irq9,
 86
             \verb"idt_set_gate" (41",
                                                               0 \times 08,
                                                                         0x8E);
                                        (uint32_t)irq10, 0x08, 0x8É);
 87
             idt_set_gate(42)
 88
             idt_set_gate (43,
                                       (uint32_t)irq11, 0x08, 0x8E)
 89
             idt_set_gate(44)
                                        (uint32_t)irq12
                                                              , 0x08, 0x8E)
90
             \verb"idt_set_gate" (45",
                                       (uint32_t) irq13, 0x08, 0x8E);
91
             idt_set_gate(46)
                                       (uint32_t)irq14, 0x08, 0x8E);
             idt_set_gate(47, (uint32_t)irq15, 0x08, 0x8E);
 92
 93
 94
 95
            \mathtt{idt\_set\_gate} \left( \, 0 \, \mathtt{X80} \, , \quad \left( \, \mathtt{uint32\_t} \, \right) \, \mathtt{isr80h} \, \, , \quad 0 \, \mathtt{x08} \, , \quad 0 \, \mathtt{x8E} \, \right) \, ;
96
97
            idt_flush((uint32_t)&idt_ptr);
98
99
      }
100
101
       static\ void\ idt\_set\_gate(uint8\_t\ num\ ,\ uint32\_t\ base\ ,\ uint16\_t\ sel\ ,\ \hookleftarrow
             {\tt uint8\_t~flags}\,)
102
             idt_entries[num].base_lo = base & 0xFFFF;
103
             \verb|idt_entries[num||.base_hi| = (base|>> 16) & 0xFFFF;
104
105
106
            \begin{array}{lll} \verb|idt_entries[num]|.sel & = & \verb|sel| \\ \verb|idt_entries[num]|.always0 & = & 0; \end{array}
107
108
             idt_entries[num].flags = flags;
109
110
```

1.18. idt.h

```
#include "common.h"
      void init_descriptor_tables();
 4
 5
      // interrupt gate descriptor
 6
      {\color{red} \textbf{struct}} \quad {\color{red} \textbf{idt\_entry\_struct}} \quad \{
           uint16_t base_lo;
uint16_t sel;
 9
           uint8_t always0;
10
           uint8_t flags;
11
           uint16_t base_hi;
     \label{eq:continuous} \begin{tabular}{ll} $\_\_$ attribute\_\_ ((packed)); \end{tabular}
12
13
      typedef struct idt_entry_struct idt_entry_t;
14
15
16
      // array of interrupt handlers descriptor (for lidt).
17
      struct idt_ptr_struct {
           uint16_t limit;
uint32_t base;
18
19
```

```
\label{eq:continuous} \begin{tabular}{ll} $\_\_$ attribute\_\_ ((packed)); \end{tabular}
21
    ty\,p\,e\,d\,ef\ st\,r\,u\,c\,t\ i\,d\,t\,\_p\,t\,r\,\_s\,t\,r\,u\,c\,t\ i\,d\,t\,\_p\,t\,r\,\_t\;;
22
^{24}
    #define IDT_SIZE 256
25
     // interrupciones default del procesador.
26
27
     extern void isr0();
     extern void isr1()
28
29
     extern void isr2
     extern void isr3
31
     extern void isr4
32
     extern void
                   isr5
\frac{33}{34}
     extern void
                   isr6
     extern void
                   isr7
35
     extern void
                   isr8(
     extern
             void
                   isr9
37
     extern
             v o i d
38
     extern void
                   isr11(
39
     extern
             void
                   isr12
40
     extern void
                   isr13 (
41
     extern void
                   isr14
     extern void
                   isr15 (
43
     extern
             v o i d
44
     extern
             void
                   isr17
^{45}
     extern void isr18 (
    extern void extern void
46
                   isr19
47
                   isr20
48
     extern void
                   isr21(
     extern void
                   isr22
50
     extern
             v o i d
                   isr23
51
     extern void
                   isr24
52
     extern void
                   isr25
53
     extern void
                   isr26
     extern void
                   isr27
55
     extern
             void
^{56}
     extern
             v o i d
                   isr29
57
     extern void
                   isr30(
58
     extern void
                   isr31(
59
     extern void
                   irq0()
60
     extern void
                   irq1()
61
     extern void
                   irq2
62
     extern
             v o i d
                   irq3
63
     extern
             v o i d
64
     extern void
                   irq5(
65
     extern void
                   irq6(
66
     extern void
                   ira7(
67
     extern void
                   irq8 (
68
     extern void
                   irq9 (
69
     extern void
                   irq10()
70
     extern void
                   irq11()
71
     extern void irq12()
72
     extern void
                   irq13()
73
     extern void
                   irq14();
74
     extern void
                   irq15();
75
    extern void isr80h();
```

1.19. in out.c

```
#include "int80.h"

#include "in_out.h"

buffer_t * in_out_vector [10];

void READ_INTERRUPT_handler(registers_t regs) {
   int i;
   buffer_t * buffein_out_vector [regs.ebx];
   for (i=0;i<regs.edx && buff->start!=buff->end;i++) {
```

```
*((\,{\tt char}\,*)\,(\,{\tt regs.ecx+i}\,)\,) = {\tt buff}\,-> {\tt array}\,[\,{\tt buff}\,-> {\tt start}\,]\,;\\ {\tt buff}\,-> {\tt start}\,=(\,{\tt buff}\,-> {\tt start}\,+1)\,\% {\tt uff}\,-> {\tt size}\,;
10
11
12
13
                    if (i<regs.edx) {
14
                            *((char*)(regs.ecx+i))='\setminus 0';
15
16
17
          void WRITE_INTERRUPT_handler(registers_t regs){
18
19
                   i\,n\,t\quad \textbf{i}\ ;
20
                    int tmp;
^{21}
                   buffer_t * buff=in_out_vector[regs.ebx];
                    {\tt tmp} \!=\! (\, {\tt buff} \mathop{->} \! {\tt end} \mathop{+} \! 1\,) \,\, \% {\tt uff} \mathop{->} \! {\tt size}
22
                    \textcolor{red}{\textbf{for}\,(\,i\!=\!0;} i\!<\!\texttt{regs}\,.\,\texttt{edx}\,\,\&\&\,\,\,\texttt{tmp}\,!\!=\!\texttt{buff}\,-\!>\!\texttt{start}\,;\,i++,\\ \texttt{tmp}\,=\!(\,\texttt{buff}\,-\!>\!\texttt{end}\,+1)\,\,\%\texttt{uff}\,\longleftrightarrow\,\,(\,i\!=\!0;} i\!<\!\texttt{regs}\,.\,\,\&\&\,\,\,\,\texttt{tmp}\,!\!=\!\texttt{buff}\,-\!>\!\texttt{start}\,;\,i++,\\ \texttt{tmp}\,=\!(\,\texttt{buff}\,-\!>\!\texttt{end}\,+1)\,\,\%
23
                              ->size) {
24
                              buff \rightarrow array [buff \rightarrow end] = *((char*)(regs.ecx+i));
25
                              buff->end=tmp;
26
\frac{27}{28}
         }
         void add_in_out(int n, buffer_t * buff){
   in_out_vector[n]=buff;
29
30
31
          }
^{32}
33
34
          \verb"init_in_out"()" \{
                   register_functionality(3,READ_INTERRUPT_handler);
register_functionality(4,WRITE_INTERRUPT_handler);
35
36
```

1.20. in out.h

```
#ifndef IN_H
#define IN_H
3 4
     struct buffer_struct
{
 5
 6
 7
 8
          char * array;
 9
          int start;
int end;
10
11
     };
12
13
     {\tt typedef\ struct\ buffer\_struct\ buffer\_t;}
     #endif // IN_OUT_H
1.5
```

1.21. int80.c

```
#include "isr.h"
#include "int80.h"

# define SUB_FUNC_VEC_SIZE 10

int80_t sub_funcs_vec[SUB_FUNC_VEC_SIZE];

void register_functionality(uint8_t n, int80_t func) {
   if (n<SUB_FUNC_VEC_SIZE){
      sub_funcs_vec[n] = func;
}</pre>
```

```
14
            }
15
      }
16
17
      void int80_handler(registers_t regs){
            if(regs.eax < SUB_FUNC_VEC_SIZE)
19
                   sub_funcs_vec[regs.eax](regs);
20
21
      }
22
23
      void nofunc(registers_t regs){
^{24}
^{25}
26
27
28
      void init_int80(){
29
            \begin{array}{ll} & \texttt{int} & \texttt{i} ; \\ & \texttt{for} & (\texttt{i} = \texttt{0}; \texttt{i} < \texttt{SUB\_FUNC\_VEC\_SIZE} ; \texttt{i} + +) \{ \end{array}
30
31
                    sub_funcs_vec[i]=nofunc;
^{32}
             \tt \acute{r}egister\_interrupt\_handler\,(\,0\,X80\,\,,\,int80\_handler\,)\,\,;
33
34
```

1.22. int80.h

```
#include "isr.h"

#ifndef INT80_H

#define INT80_H

typedef void (*int80_t)(registers_t);
void register_functionality(uint8_t n, int80_t func);
void init_int80();

#endif /* INT80_H */
```

1.23. isr.c

```
#include "common.h"
     #include "isr.h
     #include "idt.h"
     \verb|isr_t| interrupt_handlers[IDT_SIZE];
 5
 6
     void register_interrupt_handler(uint8_t n, isr_t handler) {
   interrupt_handlers[n] = handler;
 7
 9
10
11
     void isr_handler(registers_t regs) {
          if (regs.int_no==-128){//cableo orrendo, pero por alguna razon me \hookleftarrow lo pone negativo
12
13
                \verb"regs.int_no*=-1";
14
15
           if (interrupt_handlers[regs.int_no] != NULL) {
16
                isr_t handler = interrupt_handlers[regs.int_no];
17
                handler (regs);
18
19
     }
20
     void irq_handler(registers_t regs) {
    if (regs.int_no >= IRQ8) {
        outb(PORT_PIC2, SIGNAL_EOI);
^{21}
^{22}
23
24
25
          outb (PORT_PIC1 , SIGNAL_EOI) ;
```

```
26 | isr_handler(regs);
27 |}
```

1.24. isr.h

```
#include "common.h"
 3
     #ifndef ISR_H
     #define ISR_H
 5
     #define IRQ0 32
#define IRQ1 33
 6
     #define IRQ2 34
     #define IRQ3 35
#define IRQ4 36
10
     #define IRQ5 37
#define IRQ6 38
11
12
13
     #define IRQ7 39
     #define IRQ8 40
14
15
     #define IRQ9 41
     #define IRQ10 42
#define IRQ11 43
#define IRQ12 44
#define IRQ13 45
16
17
18
19
20
     #define IRQ14 46
^{21}
     #define IRQ15 47
\frac{22}{23}
     typedef struct registers
24
          uint32_t ds;
25
          uint32_t edi, esi, ebp, esp, ebx, edx, ecx, eax; // pusha pushs.
uint32_t int_no, err_code;
^{26}
27
28
          uint32_t eip, cs, eflags, useresp, ss; // processor automatic ←
                pushs.
29
     } registers_t;
30
     typedef void (*isr_t)(registers_t);
void register_interrupt_handler(uint8_t n, isr_t handler);
31
33
34
     #endif //ISR H
```

1.25. keyboardlisteners.c

```
#ifndef KEYBOARDLISTENER H
     #define KEYBOARDLISTENER H
     #define MAX_SCAN_CODE 300
    #define CTRL_KEY_PRESED_SCAN_CODE 29
#define CTRL_KEY_RELESED_SCAN_CODE 157
 6
 7
    #define ALT_KEY_PRESED_SCAN_CODE 56
#define ALT_KEY_RELESED_SCAN_CODE 184
10
1\,1
12
     typedef \ int \ (*key\_listener)();\\
13
     int activate(int scan_code);
14
     void add_key_listener(int mode, int scan_code, key_listener listener);
16
     void init_key_listeners();
18
     #endif //KEYBOARDLISTENER_H
```

1.26. keyboardlisteners.h

```
#ifndef KEYBOARDLISTENER H
    #define KEYBOARDLISTENER H
    #define MAX_SCAN_CODE 300
5
    #define CTRL_KEY_PRESED_SCAN_CODE 29
#define CTRL_KEY_RELESED_SCAN_CODE 157
6
    #define ALT_KEY_PRESED_SCAN_CODE 56
    #define ALT_KEY_RELESED_SCAN_CODE 184
11
    typedef int (*key_listener)();
12
13
    int activate(int scan_code);
14
    void add_key_listener(int mode, int scan_code, key_listener listener);
void init_key_listeners();
15
16
17
    #endif //KEYBOARDLISTENER_H
18
```

asm

1.27. idt.asm

```
[\ \tt GLOBAL\ idt\_flush] \quad ; \quad \tt Allows\ the\ C\ code\ to\ call\ idt\_flush\,(\,)\ .
 3
         mov eax, [\mathtt{esp}+4] ; Get the pointer to the IDT, passed as a \hookleftarrow
          parameter.
lidt [eax]
                                ; Load the IDT pointer.
 5
 6
          ret
     {\tt macro\ ISR\_NOERRCODE\ 1}
       {\tt global \ isr \%1}
 9
10
       isr \%1:
                                               ; Disable interrupts firstly.; Push a dummy error code.; Push the interrupt number.
11
         cli
          {\tt push \ byte} \ 0
12
          push byte %1
13
14
          jmp isr_common_stub
                                               ; Go to our common handler code.
15
     %endmacro
16
     ; This macro creates a stub for an ISR which passes it's own
17
18
       error code
19
     macro ISR_ERRCODE 1
20
      global isr %1
21
       isr %1:
^{22}
        cli
                                               ; Disable interrupts.
          push byte \%1
23
                                                ; Push the interrupt number
24
          jmp isr_common_stub
25
     %endmacro
26
     ; This macro creates a stub for an IRQ — the first parameter is ; the IRQ number , the second is the ISR number it is remapped to .
27
28
     \%macro IRQ 2
29
30
       global irq\%1
31
       irq %1:
cli
32
33
          push byte 0
          push byte \%2
^{34}
35
          jmp irq_common_stub
     %endmacro
36
37
     ISR_NOERRCODE O
38
     ISR_NOERRCODE
     ISR_NOERRCODE 2
```

```
ISR_NOERRCODE 3
      ISR_NOERRCODE
ISR_NOERRCODE
42
43
 44
      ISR_NOERRCODE
      ISR_NOERRCODE
46
      ISR_ERRCODE
      ISR_NOERRCODE 9
47
      ISR_ERRCODE
                        1.0
48
      ISR_ERR CODE
49
                        11
      ISR_ERRCODE
50
                        12
51
      ISR_ERRCODE
 52
      ISR_ERRCODE
53
      {\tt ISR\_NOERRCODE} \quad 15
      ISR_NOERRCODE 16
54
      ISR_NOERRCODE
55
                        17
      ISR_NOERRCODE
ISR_NOERRCODE
56
58
      ISR_NOERRCODE
                        20
59
      ISR_NOERRCODE 21
      ISR_NOERRCODE
60
                        2.2
      ISR_NOERRCODE
ISR_NOERRCODE
61
                        23
                        24
62
      ISR_NOERRCODE
 63
 64
      ISR_NOERRCODE
65
      ISR_NOERRCODE 27
66
      ISR_NOERRCODE 28
      ISR_NOERRCODE 29
ISR_NOERRCODE 30
67
68
      ISR_NOERRCODE 31
 69
 70
 71
      IRO
 72
      IRO
              1,
                     33
73 \\ 74
      IRO
              2,
                      34
              3.
      IRO
                      35
 75
      IRQ
                      36
 76
      IRQ
 77
      IRQ
              6,
                      38
78
79
      IRQ
                      39
      T R.O
                      40
 80
              9 ,
      IRQ
                      41
 81
            10,
      IRQ
                      42
 82
      IRQ
                      43
 83
      IRQ
            12,
                      44
84
      IRQ
            13,
                      45
 85
      IRO
            14,
                      46
 86
      IRQ
            15,
                     47
 87
 88
        global isr80h
 89
        isr80h:
90
         cli
                                                ; Disable interrupts firstly.
           {\tt push \ byte} \ 0
                                                ; Push a dummy error code.
91
           push byte 80\,\mathrm{h}
                                                ; Push the interrupt number.
; Go to our common handler code.
92
93
           jmp isr_common_stub
94
95
96
      ; In isr.c
97
      extern isr_handler
98
     ; ISR stub. It saves the processor state, sets ; up for kernel mode segments, calls the C-level fault handler,
99
100
101
       and finally restores the stack frame.
102
      isr_common_stub:
103
           pusha
                                            ; Pushes edi, esi, ebp, esp, ebx, edx, ecx, eax
104
          mov ax, ds
push eax
105
                                            ; Lower 16-bits of eax = ds.
                                            ; save the data segment descriptor
106
107
108
           mov ax, 0x10; load the kernel data segment descriptor
109
           \verb"mov" ds", \verb"ax"
110
           \verb"mov" es", \verb"ax"
111
           \verb"mov" fs , \verb"ax"
112
           \verb"mov gs", \verb"ax"
113
           call isr_handler
```

```
115
          pop ebx
mov ds, bx
116
                             ; reload the original data segment descriptor
117
118
           mov es, bx
119
           mov fs, bx
120
           mov gs, bx
121
           popa
add esp, 8
                             ; Pops edi,esi,ebp,...; Cleans up the pushed error code and pushed ISR \hookleftarrow
122
123
               number
124
           sti
125
                              ; pops 5 things at once: CS, EIP, EFLAGS, SS, and \hookleftarrow
           iret
               ESP
126
127
      ; In isr.c
      extern irq_handler
128
129
      ; IRQ stub. It saves the processor state, sets ; up for kernel mode segments, calls the C-level fault handler,
130
131
      , and finally restores the stack frame. irq_common_stub:
132
133
134
          pusha
                                           ; Pushes edi, esi, ebp, esp, ebx, edx, ecx, eax
135
           mov ax, ds
push eax
136
                                           ; Lower 16-{\tt bits} of eax ={\tt ds}.
137
                                           ; save the data segment descriptor
138
           mov ax, 0 \times 10 ; load the kernel data segment descriptor
139
           mov ds, ax
140
           mov es, ax
141
142
           mov fs, ax
143
           mov gs, ax
144
145
           call irq_handler
146
           pop ebx
mov ds, bx
147
                             ; reload the original data segment descriptor
148
149
           mov es, bx
150
           \verb"mov" fs , bx"
151
           mov gs, bx
152
                             ; Pops edi,esi,ebp...; Cleans up the pushed error code and pushed ISR \hookleftarrow
153
           154
               number
           sti
155
                              ; pops 5 things at once: CS, EIP, EFLAGS, SS, and \hookleftarrow
156
           iret
                ESP
```

1.28. common.asm

```
global outb
    global outw
3
    global inb
    global inw
 5
    global getRDTSC
6
    \verb"getRDTSC":
7
        rdtsc
8
        ret
10
11
    outb:
        12
13
14
15
        ret
16
17
18
        \verb"mov" dx", [esp+4]
        mov ax, [esp+8]
out dx, ax
19
20
```

 std

1.29. getchar.c

```
#include "stdio.h"
      #define STREAM_SIZE 500
      typedef int (*flusher)(char * streampointer);
      char stream[STREAM_SIZE];
 8
9
      char * streamout=stream;
10
      11
12
                  -1)\{
13
                  return 1;
14
15
16
            return 0;
17
      }
18
      \begin{array}{c} {\tt char} & {\tt getchar} \; (\;) \, \{ \\ {\tt char} & {\tt c=*streamout} \; ; \end{array}
19
\frac{20}{21}
            i\,f\,(\,c{=}{=}\,{}^{\scriptscriptstyle |}\,\backslash\,0\,\,{}^{\scriptscriptstyle |}\,)\,\{
22
                   streamout=stream;
23
                   char * streamin=stream;
^{24}
                   int i,j;
^{25}
                   for (i=0; i < STREAM_SIZE; i++){
\frac{26}{27}
                         stream[i] = | \setminus 0 |;
                  while (!intro_flush(streamin)){
    if (*streamin!='\0')
28
29
                              streamin++;
                         31
^{32}
33
34
35
                         else if (*streamin==' \b') {
                                \begin{array}{c} \text{if (streamin} > \text{stream}) \left\{\\ \text{printf ("\b");}\\ \text{*streamin='\0';} \end{array} \right. 
36
37
38
39
                                      streamin --;
40
                               \begin{tabular}{ll} $"*streamin='\setminus 0'; \\ & $else$ & if (*streamin=='\setminus t') \{ \end{tabular}
41
^{42}
43
                                                  *streamin=^{'}\setminus 0 ';
44
45
46
                  c=*streamout;
47
48
            streamout++;
49
            return c;
50
51
```

1.30. printf.c

```
#include "stdio.h"
       static void prints(char * string);
       static char * numberBaseNtoString(unsigned int number, int base, char \leftarrow
 5
              * out);
 6
       void putchar (char c) {
    __write(1, &c, 1);
 7
 8
 9
10
       void printf(char * formatString, ...) {
11
             int integer;
unsigned int unsigenedInteger;
char * string;
char out [40];
12
13
14
15
16
              char c;
17
              va_list args;
18
19
^{20}
              va_start(args, formatString);
^{21}
              \begin{array}{c} \text{while} \ \ (* \texttt{formatString} \ != \ \ \ \ \ \ ) \ \ \{ \\ \text{if} \ \ (* \texttt{formatString} \ == \ \ \ \ \%) \ \ \{ \end{array}
^{22}
23
24
25
                            formatString++;
26
27
                             switch (*formatString) {
28
                                          \mathtt{c} \; = \; \mathtt{va\_arg} \, (\, \mathtt{args} \; , \; \; \underline{\mathsf{i}} \, \underline{\mathsf{n}} \, \underline{\mathsf{t}} \, ) \; ;
29
30
                                           putchar(c);
31
                                           break:
32
                                    case
33
                                           \mathtt{string} \; = \; \mathtt{va\_arg} \, (\, \mathtt{args} \; , \; \; \mathtt{char} \; \; \ast \,) \; ;
^{34}
                                           prints(string);
35
                                           break;
^{36}
                                    c\,a\,s\,e^{-\,\dagger}\,d^{\,\dagger} :
                                           integer = va_arg(args, int);
if (integer < 0) {
   integer = -integer;</pre>
37
38
39
40
                                                  putchar ( '- ' );
41
                                           \tt prints (numberBaseNtoString(integer \ , \ 10 \ , \ out));
42
43
                                    break; case 'u':
44
                                           unsigenedInteger = va_arg(args, unsigned int);
^{45}
^{46}
                                           prints (numberBaseNtoString (unsigenedInteger, 10,
                                                  out));
                                           break;
47
48
                                    case 'o':
                                           \begin{array}{ll} {\tt integer} = {\tt va\_arg(args, unsigned\ int);} \\ {\tt prints(numberBaseNtoString(integer, 8, out));} \\ {\tt break;} \end{array}
49
50
51
52
53
                                           {\tt unsigenedInteger} \ = \ {\tt va\_arg} \, (\, {\tt args} \; , \; \; {\tt unsigned} \; \; int \; ) \; ;
                                           {\tt prints} \, (\, {\tt numberBaseNtoString} \, (\, {\tt unsigenedInteger} \,\, , \,\, \, 16 \,, \,\, \, \hookleftarrow \,\,
54
                                                  out));
55
                                           break;
56
                                    case
57
                                           putchar ( ' %');
58
59
                     } else {
60
                             putchar (*formatString);
61
62
63
                     formatString++;
64
65
              va_end(args);
66
```

```
68
 69
 70
 71
                       string++;
 72
 73
        }
\frac{74}{75}
         static char * numberBaseNtoString(unsigned int number, int base, char \leftarrow
                * out) {
 76
                int digits[40];
int position = 0;
char * numbers = "0123456789ABCDEF";
int index = 0;
 77
 78
 79
 80
 81
                82
 83
 84
 85
 86
                                      \mathtt{number} = 0;
                               \label{eq:continuous} \left. \begin{array}{ll} \text{else } \{ & \text{digits} \left[ \, \text{position} \, \right] \, = \, \text{number} \, \, \% \, \, \text{base} \, ; \\ & \text{ } \end{array} \right.
 87
 88
 89
 90
 91
                               position++;
                       }
 92
 93
                       \begin{array}{lll} \mbox{for (index} = 0; \mbox{ position} > 0; \mbox{ position} --, \mbox{ index} ++) \ \{ \mbox{ out[index]} = \mbox{ numbers[digits[position} -1] \ \% \mbox{ base]}; \end{array}
 94
 95
 96
                        \verb"out[index] = \verb"\0";
 97
                } else {
   out [0] = '0';
   out [1] = '\0';
 98
99
100
101
102
103
                {\tt return} \  \  \, {\tt out} \ ;
104
```

1.31. scanf.c

```
#include "../../src/std/string.h"
#include "stdio.h"
3
      5
      }
 6
 7
 8
      int \  \  \, sscanf\left(\, ch\, ar \  \, *stream \,\, , \  \  \, ch\, ar \  \, *format \,\, , \  \, \ldots \, \right) \  \, \left\{
 9
            va_list ap;
            va_start(ap, format);
10
            int i = 0;
int j = 0;
int converted;
1\,1
12
13
14
            \begin{array}{lll} i\,n\,t & *\,i\,n\,t\,e\,g\,e\,r \ , & i\,T\,m\,p \ , & i\,T\,m\,p\,2 \ ; \end{array}
15
            char* string;
16
            char *chr;
17
18
            unsigned int *uinteger;
19
            20
21
^{22}
                        i++;
23
                         switch (format [i++]) {
^{24}
                              case
                                    chr = va_arg(ap, char*);
*chr = stream[j++];
break;
25
26
```

```
case 'd':
28
29
                                   {\tt integer} \; = \; {\tt va\_arg} \, (\, {\tt ap} \; , \; \; {\tt int} \; \; * \, ) \; ; \\
                                  iTmp = 0;

iTmp2 = 1;
30
31
                                   if (stream[j] == '-') { iTmp2 = -1; }
32
33
^{34}
                                        j++;
35
                                   36
37
38
39
                                   *integer = iTmp*iTmp2;
40
41
                             case
                                  uinteger = va_arg(ap, unsigned int *);
iTmp = 0;
while (isNumber(stream[j])) {
42
43
44
^{45}
                                       iTmp = iTmp * 10 + (stream [j] - '0');
46
                                        j++;
47
48
                                   *uinteger = iTmp;
49
                                  break;
50
                             case
                                  string = va_arg(ap, char *);
iTmp = 0;
while (stream[j] != ' ') {
    string[iTmp++] = stream[j++];
51
52
53
54
55
56
                                   string[iTmp] = ' \setminus 0';
57
                                   break;
                            default:
// WRONG %X
58
59
                                   return converted;
60
61
62
63
                           (format[i] == stream[j]) {
64
                            i++;
                          j++;
else {
//WRONG FORMAT STRING
65
66
67
68
                             return converted;
69
70
                 }
71
           }
72
```

1.32. stdio.h

```
#include "../../include/varargs.h"

#include "../../include/stdarg.h"

#ifndef STDIO_H

#define STDIO_H

char getchar();

void putchar(char c);
void printf( char * formatString, ...);
int sscanf(char *formatString, char *format, ...);

#endif //STDIO_H
#endif //STDIO_H
```

1.33. string.c

```
int strcmp(char* str1, char* str2) {
 3
           int i;
           for (i = 0; str1[i] != '\0' && str1[i] != '\0'; i++) {
    if (str1[i] != str2[i]) {
        return str1[i] - str2[i];
 6
7
 8
           fif (str1[i] == '\0' && str2[i] == '\0') {
   return str1[i] - str2[i];
9
10
1\,1
12
           return 1;
     }
13
14
      void strcpy(char * str_des, char * str_ori) {
15
16
           int i;
for (i = 0; str_ori[i] != '\0'; i++) {
17
18
                 str_des[i] = str_ori[i];
19
           \mathtt{str\_des}\,[\,\mathtt{i}\,] \;=\; \,{}^{\scriptscriptstyle{|}}\!\setminus\! 0\,\,{}^{\scriptscriptstyle{|}}\,;
20
21
     }
22
      void strncpy(char * str_des, char * str_ori, unsigned int count) {
           int i; for (i = 0; str_ori[i] != '\0' && i <= count; i++) {
^{24}
25
26
                 str_des[i] = str_ori[i];
27
28
           str_des[i] = ' \setminus 0';
29
     }
30
31
     int strlen(char*str) \ \{\\
           int i;
for (i = 0; str[i] != '\0'; i++);
32
33
34
           return i;
35
```

1.34. string.h

```
#ifndef STRING_H
#define STRING_H

int strcmp(char* str1, char * str2);
void strcpy(char * str_des, char * str_ori);
int strlen(char* str);

#endif /* STRING_H */
```

1.35. systemcall.asm

```
global __read
     global __write
global __cpuspeed
 5
      {\tt SECTION} \quad . \; {\tt text}
 6
 7
      __read:
 8
                 \verb"mov" ecx", [esp+8]
            mov eax,3
10
            \verb"mov" ebx", \quad [\verb"esp+4"]
1\,1
            mov edx, [esp+12]
12
            int 80h
13
            ret
14
```

```
1.5
    __write:
16
             \verb"mov" ecx", [esp+8]
        17
18
19
20
         int 80h
21
         ret
22
23
    __cpuspeed:
24
          \verb"mov" ebx", [esp+4]
          mov eax,5
^{26}
          i\,n\,t-80\,h
          ret
```

1.36. systemcall.h

```
#ifndef SYSTEMCALL_H
define SYSTEMCALL_H

void __read(int fd, void* buffer, int count);
void __write(int fd, const void* buffer, int count);
void __write(int fd, const void* buffer, int count);
void __cpuspeed(void * ips);

#endif /* SYSTEMCALL_H */
```

user

1.37. commands.c

```
#include "commands.h"
       #include "../std/string.h"
 3
      #define NULL 0
#define COMMAND_MAX_CANT 20
 5
 8
       command_t command_list[COMMAND_MAX_CANT];
 9
       \verb"int" commands_added=0;
10
       command_t * get_command_list() {
    return command_list;
11
12
13
14
15
       int get_commands_added() {
              return commands_added;
16
      }
17
18
19
       void add_command(char * name, main function, char* helpDescription){
^{20}
              \begin{array}{ll} \textbf{if} \; (\; \texttt{commands\_added} < \texttt{COMMAND\_MAX\_CANT} \;) \; \{ \end{array}
                     command_list[commands_added].name=name;
command_list[commands_added].start=function;
\frac{21}{22}
23
                                   command_list[commands_added].help=helpDescription;
^{24}
                     {\tt commands\_added++};
^{25}
              }
26
      }
27
       \mathtt{main \ get\_command} \left( \begin{array}{cc} c \ h \ ar \end{array} \right. \ast \ \mathtt{name} \left. \right) \left\{ \right.
28
29
              \begin{array}{ll} \textbf{int} & \textbf{i} ; \\ \textbf{for} & (\textbf{i} = 0; \textbf{i} < \texttt{commands\_added}; \textbf{i} + +) \{ \end{array}
30
                    if (!strcmp(command_list[i].name,name)){
   return command_list[i].start;
31
33
              }
34
```

```
35 | return NULL;
36 |}
```

1.38. commands.h

```
#ifndef COMMANDS_H
#define COMMANDS_H
3
    typedef int (*main)(int argc, char * argv[]);
 4
5
6
 7
    struct command_struct {
 8
        char * name;
9
         main start;
10
         char * help;
11
    };
12
13
    typedef struct command_struct command_t;
14
15
    void add_command(char * name, main function, char* help);
     \mathtt{main get\_command(char * name)}; \\
16
17
    char * autocomplete(char * name);
18
19
    #endif //COMMANDS_H
```

1.39. shell.c

```
#include "shell.h"
#include "../std/systemcall.h"
#include "../std/stdio.h"
#include "../std/string.h"
 3
       #include "commands.h"
      #define NULL 0
#define COMAND_LINE_MAX 1000
#define EXIT_SYSTEM -15
 8
10
11
       #define HISTORY MAX 20
13
      #define NAME_MAX_LENGTH 50
char name[NAME_MAX_LENGTH] = "unknown";
char * pcname = "itba";
14
15
16
17
       char * strnormalise(char * str) {
19
             int j, i;
              // cambia enters por espacios

for (j = 0; str[j] != '\0'; j++) {

    if (str[j] == '\n' || str[j] == '\t') {

        str[j] = ' ';
20
21
22
23
                     }
^{25}
              // elimina espacios del principio
while (str[0] == ' ') {
    str = str + 1;
^{26}
27
28
29
30
                /elimina espacios del final
31
              for (i = strlen(str) - 1; i > 0 && str[i] == ' '; i--) {
                     str[i] = '\0';
^{32}
33
              //elimina espacios repetidos en el medio for (j = 0; str[j] != '\0'; j++) {
34
35
```

```
36
   37
   38
                                                                                              j--;
    39
    40
    41
                                                 return str;
    42
    43
                          44
    45
    ^{46}
    ^{47}
                          int execute(char* comand, int argcant, char* argvec[]) { if (comand[0] == '\0') {
    ^{48}
    49
   50
                                                                       return 0;
   51
                                               main start = get_command(comand);
if (start == NULL) {
   printf("invalid comand: %\n", comand);
    52
   53
   54
   55
                                                                         return -1:
   56
                                                 return start(argcant, argvec);
   57
   58
                         }
    59
    60
                           int parseline() {
                                               char c;
int i = 0;
char comand_line[COMAND_LINE_MAX];
while ((c = getchar()) != '\n' && i < COMAND_LINE_MAX - 3) {</pre>
    61
    62
    63
   64
    65
                                                                       comand_line[i] = c;
    66
   67
                                                  \begin{array}{ll} \text{if } & \text{(i} >= \texttt{COMAND\_LINE\_MAX} - 3) \\ & \text{while } & \text{(getchar()} & != & \lceil \backslash \text{n} \rceil ) \\ & \text{printf(} & \lceil \backslash \text{n} & \rceil ) ; \end{array} 
    68
    69
    70
    71
    72
                                                 comand_line[i] = ' \setminus 0';
                                                char* command = strnormalise(comand_line);
int argcant = 0;
char * argvec[50];
    73
    74
    75
                                                for (i = 0; command[i] != '\0'; i++) {
    if (command[i] == ' && !in_quotes) {
        command[i] = '\0';
        command[i] = '\0';
    76
    78
   79
                                                                                               \verb"argvec[argcant] = \&command[i + 1];
    80
                                                                                              argcant++;
se if (command[i] == '"') {
if (!in_quotes) {
    81
    82
    83
    84
                                                                                                                     argvec[argcant-1] = \&command[i + 1];
    85
                                                                                              \begin{array}{l} \begin{subarray}{l} \tt J \\ \tt command [i] = \begin{subarray}{l} \begin{subarray}{
    86
    87
                                                                       }
    88
    89
    90
                                                 return execute(command, argcant, argvec) == EXIT_SYSTEM;
    91
    92
   93
                          clear_shell();
return EXIT_SYSTEM;
   94
   95
   96
                          }
   97
                           int \  \, echo\_shell (\, int \  \, argc \, , \  \, char* \, \, argv \, [\,] \,) \  \, \{ \,
   98
                                                \begin{array}{lll} & & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & \\ & & \\ & \\ & & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ &
  99
100
101
102
103
                                                printf("\n");
                                                 return 0;
104
                         }
105
106
                          int getCPUspeed_shell(int argc, char* argv[]) {
107
                                                unsigned long ips;
108
109
                                               __cpuspeed(&ips);
```

```
// printf ("Su procesador esta ejecutando %1 instrucciones por \leftrightarrow segudo \n",ips); printf ("The CPU speed is: %1.%d MHz\n", (ips) / (1024 * 1024), \leftrightarrow ((10 * ips) / (1024 * 1024)) % 10);
110
111
112
             return 0;
113
114
       115
116
117
118
       }
119
120
       int \ isodd\_shell(int \ argc \, , \ char* \ argv \, [] \, ) \ \ \{
             if (argc < 1) {
    printf("Usage: isodd <number>\n");
121
122
123
                   return -1;
124
125
              int number;
              \tt sscanf(argv[0], "\%", \&number); \\
126
127
               printf("The number % is NOT ODD, its EVEN.", number); else {
             if (number \% 2 == 0) {
128
129
130
                  printf("The number %1 is ODD", number);
131
132
             printf("\n");
133
             return 0;
134
135
       }
136
       137
138
139
             \begin{array}{ll} \text{int } \mathbf{i} = \mathbf{0}; \\ \text{while } (\mathbf{i} < \texttt{get\_commands\_added()}) \ \{ \\ \text{printf("} \times 1B[4m\% \times 1B[0m \times t \times t \% \times n", \text{ commands[i].name}, \text{ commands}] \leftarrow \\ \end{array}
140
141
142
                         i | help);
143
144\,4
             printf("\nPress\ CTRL+ALT+SUPR\ to\ reboot\ the\ system\n");
145
             return 0;
146
147
       }
148
149
       int rename_shell(int argc, char* argv[]) {
             if (argc < 1) {
    printf("Usage: rename <newname>.\n");
150
151
                    return -1:
152
153
154
             strncpy(name, argv[0], NAME_MAX_LENGTH);
155
       }
156
       static void test_shell_print_usage() {
    printf("Usage: rename < testcase > .\n");
    printf("testcases:\n\tprintf\n\tscanf\n");
157
158
159
160
       }
161
162
       int \ test\_shell (int \ argc \, , \ char* \ argv []) \ \{
             if (argc < 1) {
    test_shell_print_usage();</pre>
163
164
165
                   return -1;
166
167
              int integer;
             unsigned int uinteger;
168
169
             char* string;
170
             char chr;
171
172
173
             if (!strcmp(argv[0], "printf")) {    printf("\x1B[32mPlease verify the OKval is the same as RETval\\leftarrow n\x1B[1mNOTATION: case[OKval]: RETval\x1B[0m");
175
                   printf("\n");
printf("string[hola mundo]: %s", "hola mundo");
printf("\n");
printf("char[c]: %c", 'c');
176
177
178
179
```

```
180
                    \mathtt{printf}\left(\,{}^{\shortmid\prime}\backslash n\,{}^{\prime\prime}\,\right)\,;
                    printf("integer[-123]: %1", -123);
181
                    printf("\n");
182
183
                    printf("unsigned integer[123]: %u", 123);
184
                    printf("hexa[FFFFFFFF]: %x", -1);
185
                printf("hexa[rfffffff]: %x", -1);
printf("\n");
else if (!strcmp(argv[0], "scanf")) {
printf("\x1B[32mPlease verify the OKval is the same as RETval\←
n\x1B[1mNOTATION: case[OKval]: RETval\x1B[0m");
186
187
188
                    n\x1B|\text{ImNOTATION: case | OKVal|: RETVAl\x1B|}
printf("\n");
sscanf("-123", "%d", &integer);
printf("integer[-123]: %d", integer);
printf("\n");
sscanf("123", "%u", &uinteger);
printf("unsigned integer[123]: %u", uinteger);
printf("\n");
189
190
191
192
193
194
195
                    print( \n );
sscanf("hello world scanf", "hello % scanf", string);
printf("string[world]: %s", string);
printf("\n");
sscanf("c", "%c", &chr);
printf("char[c]: %c", chr);
196
197
198
199
200
201
              } else {
202
                    test_shell_print_usage();
203
                    return -1;
204
             printf("\n");
return 0;
205
206
207
       }
208
        void shell_start() {
209
             int exit = 0;
add_command("test", test_shell, "test cases for functionality");
add_command("rename", rename_shell, "changes the name of the user ←
210
211
212
             213
214
215
216
                    not");
              add_command("exit", exit_shell, "exits the system.");
add_command("getCPUspeed", getCPUspeed_shell, "shows actual CPU ↔
218
219
              do {
                    220
221
                    int i = 0;
                    while ((c = getchar()) != '\n' && i < NAME_MAX_LENGTH) { name [i++] = c;
223
224
225
                    226
227
229
230
                    printf("\x1B[3\x1B[3\x3mWelcome to arnix (ARg uNIX) \%!\x1B[0\xm] \leftarrow \
                    n \nYou may type \x1B[1 mhelp\x1B[0m for more information\n\↔ n", name);
while (!exit) {
231
                          printuser();
232
233
                           exit = parseline();
234
235
                    exit = 0;
236
             } \mathbf{while} (1);
237
```

1.40. shell.h

```
#ifndef SHELL_H
#define SHELL_H

void shell_start();

#endif /* SHELL_H */
```