

Quantum-Circuit Optimization

Native Basis Gate Set



IBM

Native Basis Gate Set



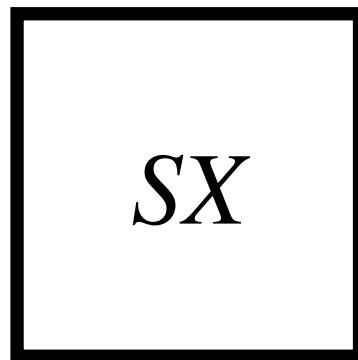
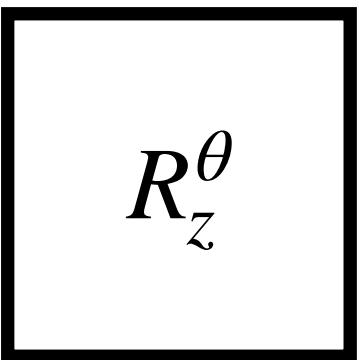
IBM

$$R_z^\theta$$

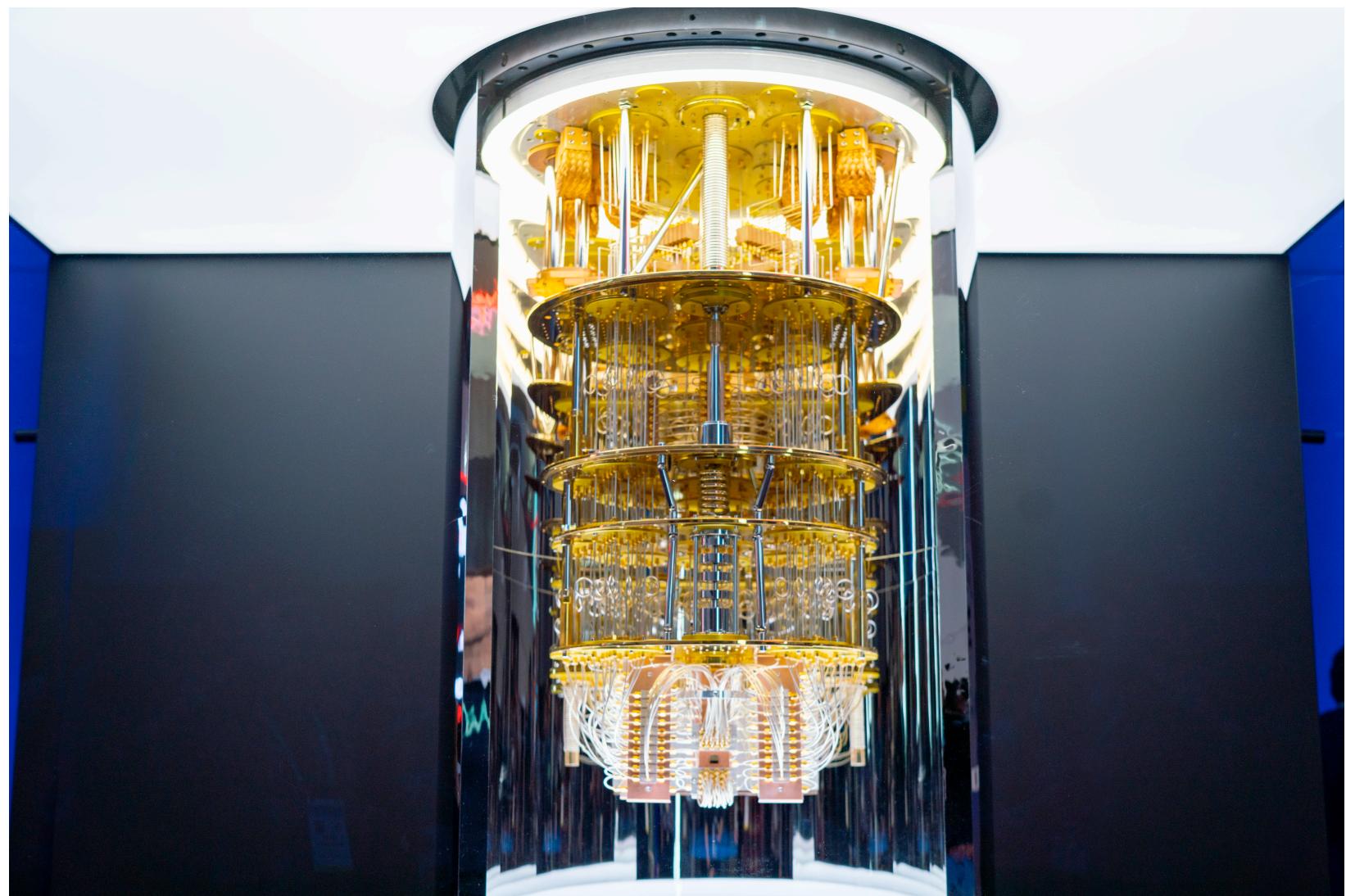
Native Basis Gate Set



IBM



Native Basis Gate Set



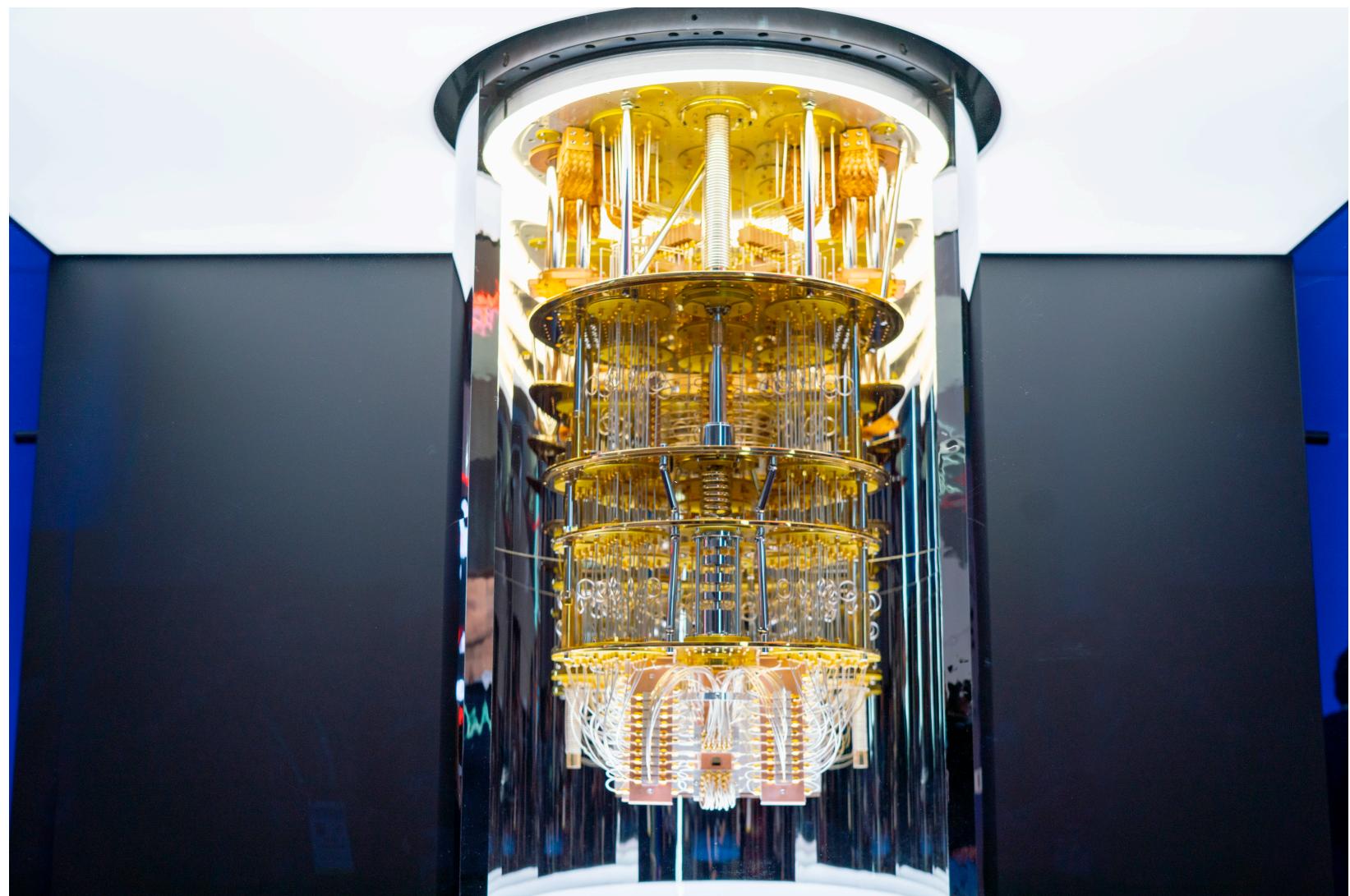
IBM

R_z^θ

SX

X

Native Basis Gate Set



IBM

R_z^θ

SX

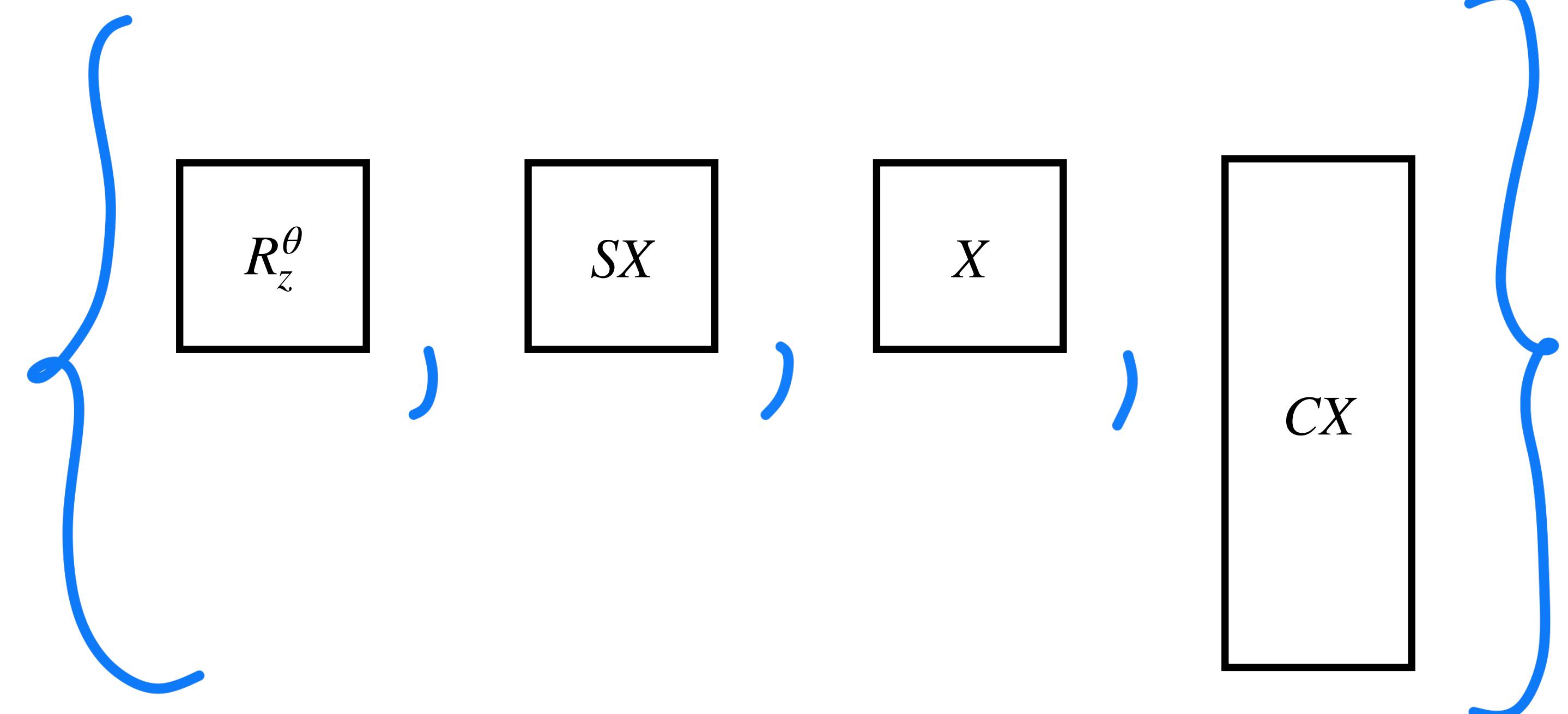
X

CX

Native Basis Gate Set



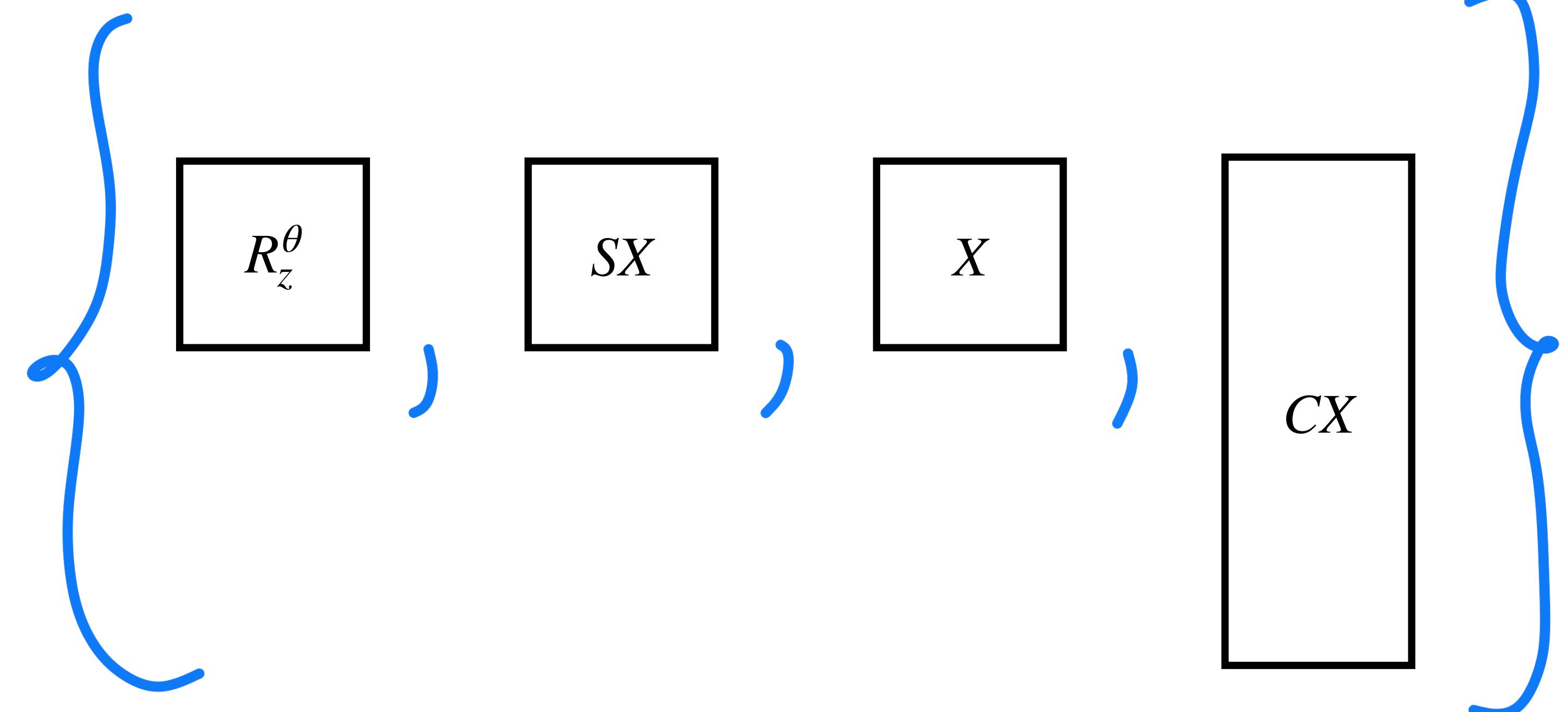
IBM



Native Basis Gate Set



IBM

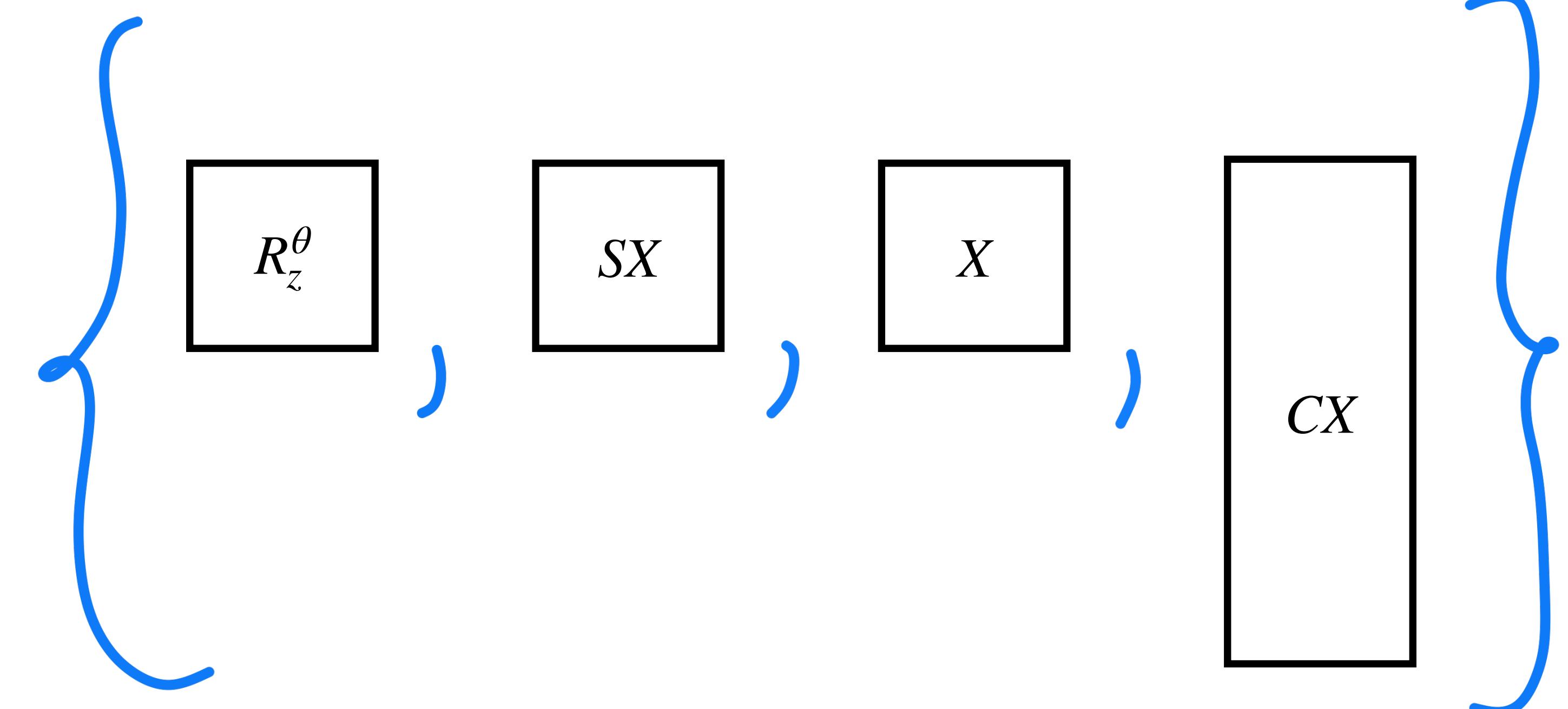


"universal"

Native Basis Gate Set



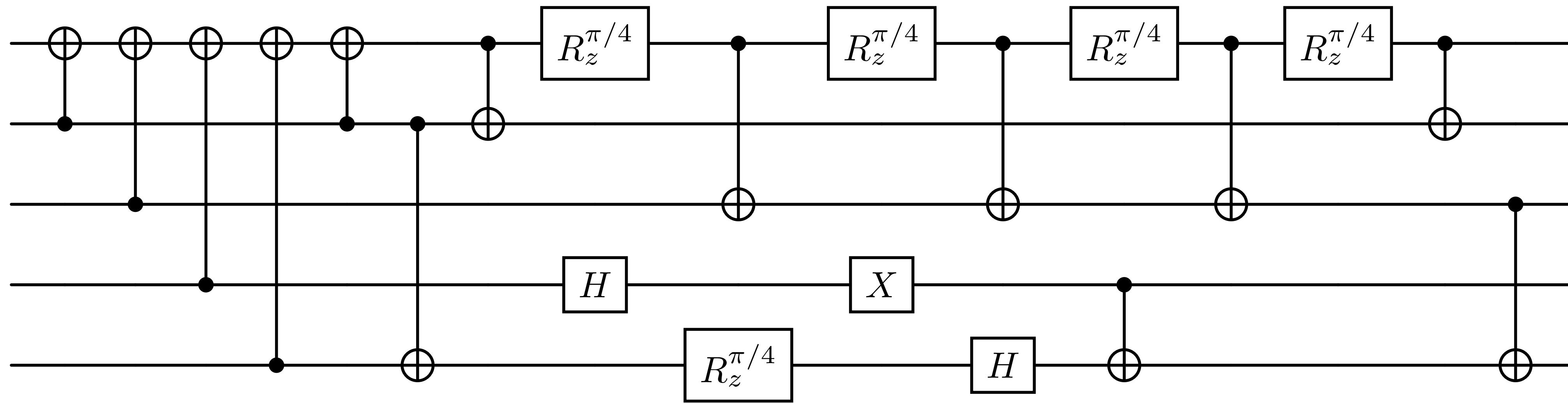
IBM



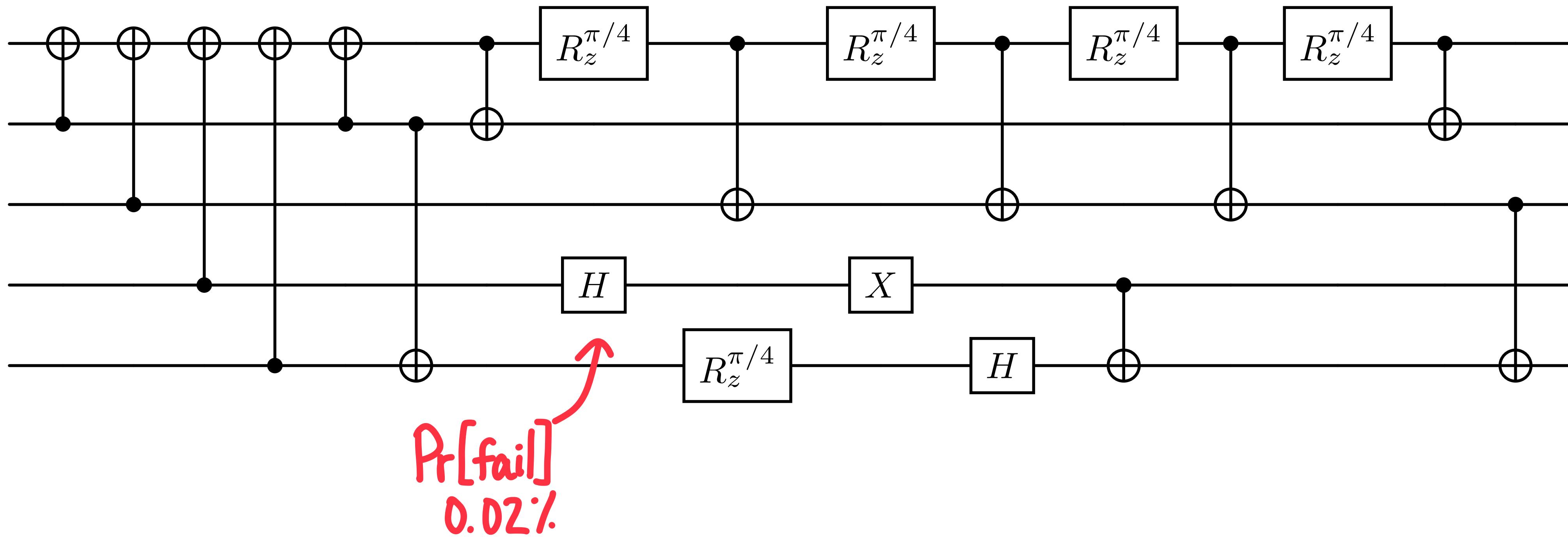
"universal"

i.e. approximate any computation to arbitrary precision

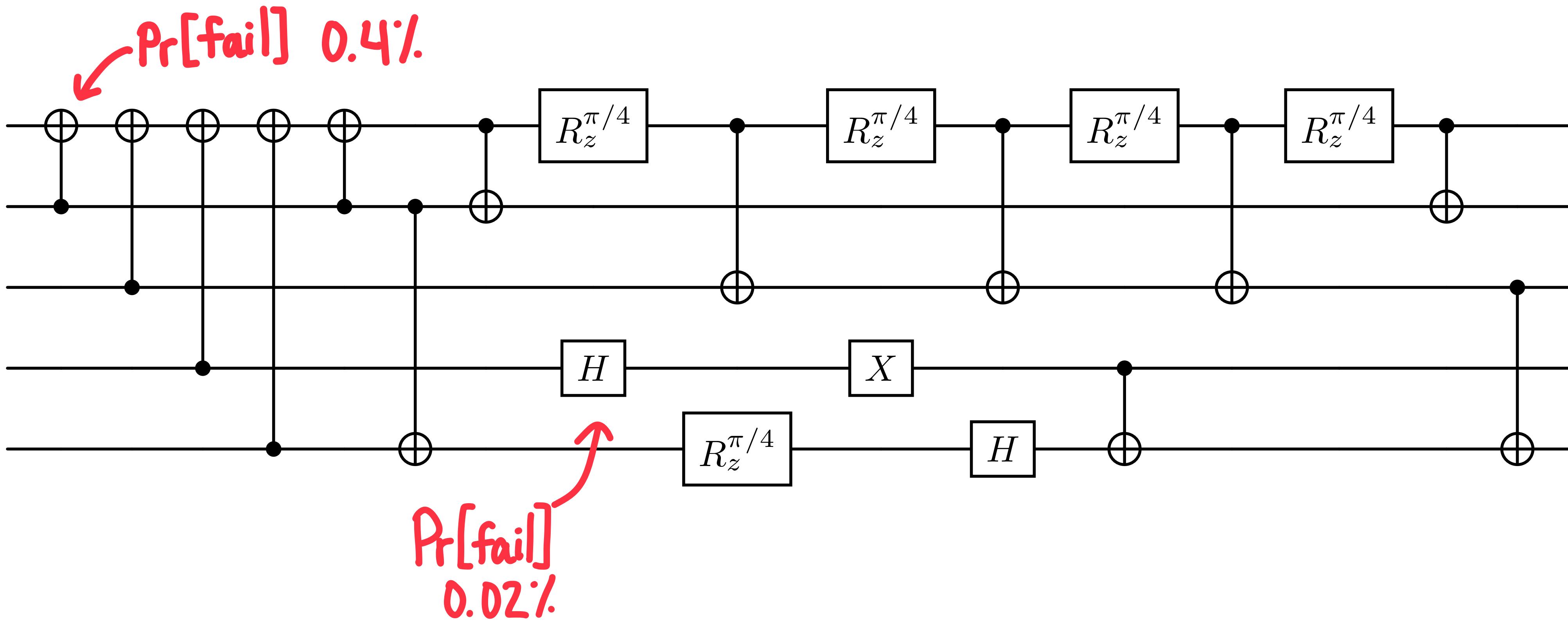
Optimization Objectives



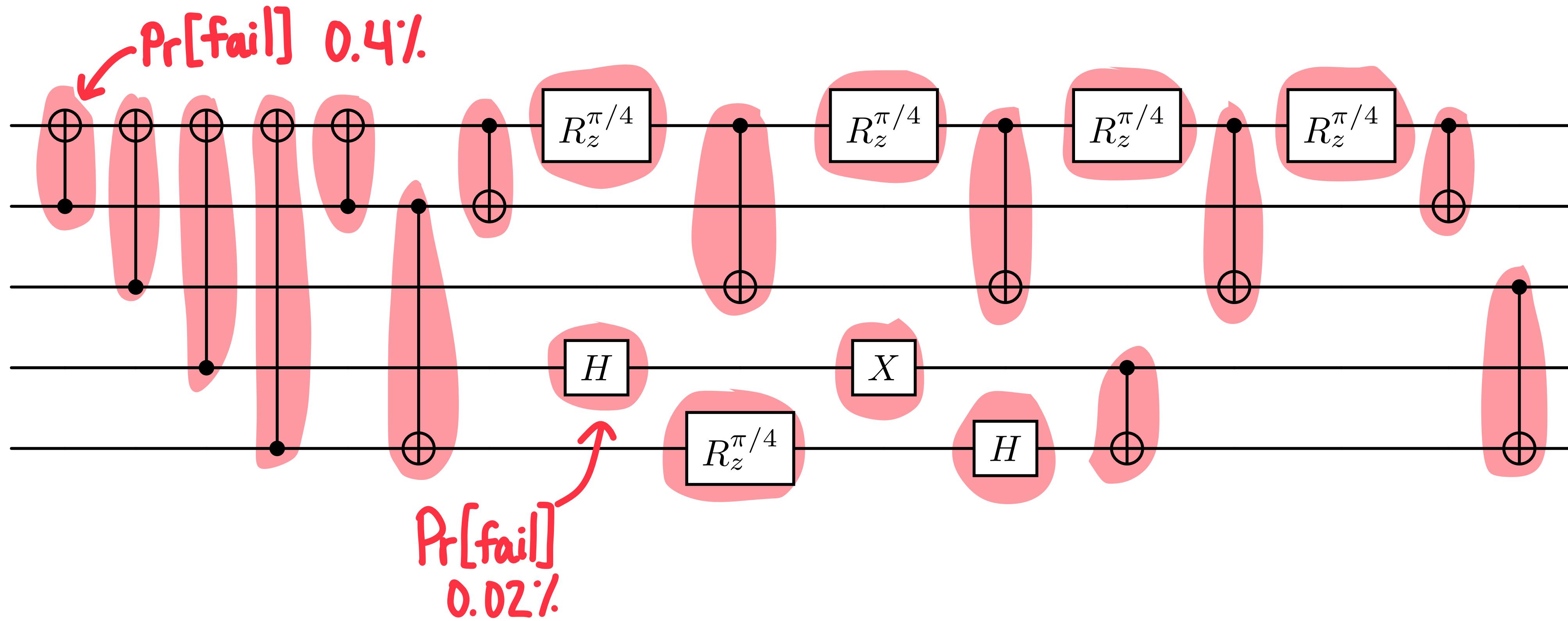
Optimization Objectives



Optimization Objectives



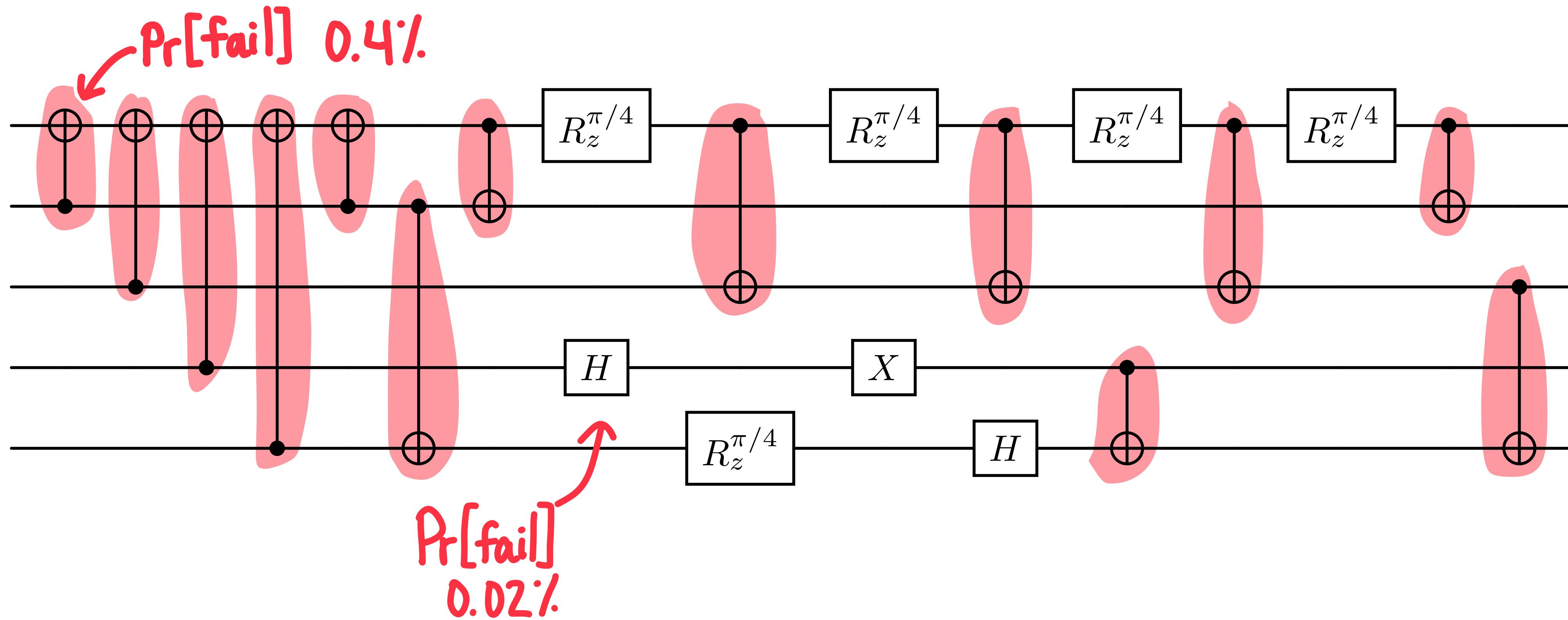
Optimization Objectives



NISQ

- total gate count

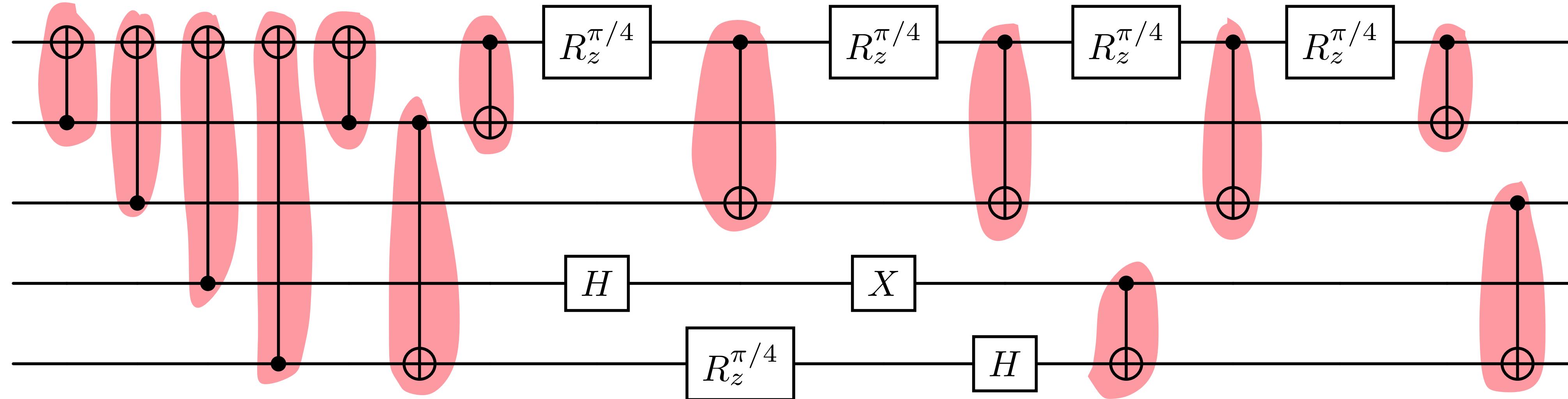
Optimization Objectives



NISQ

- total gate count
- 2q gate count

Optimization Objectives



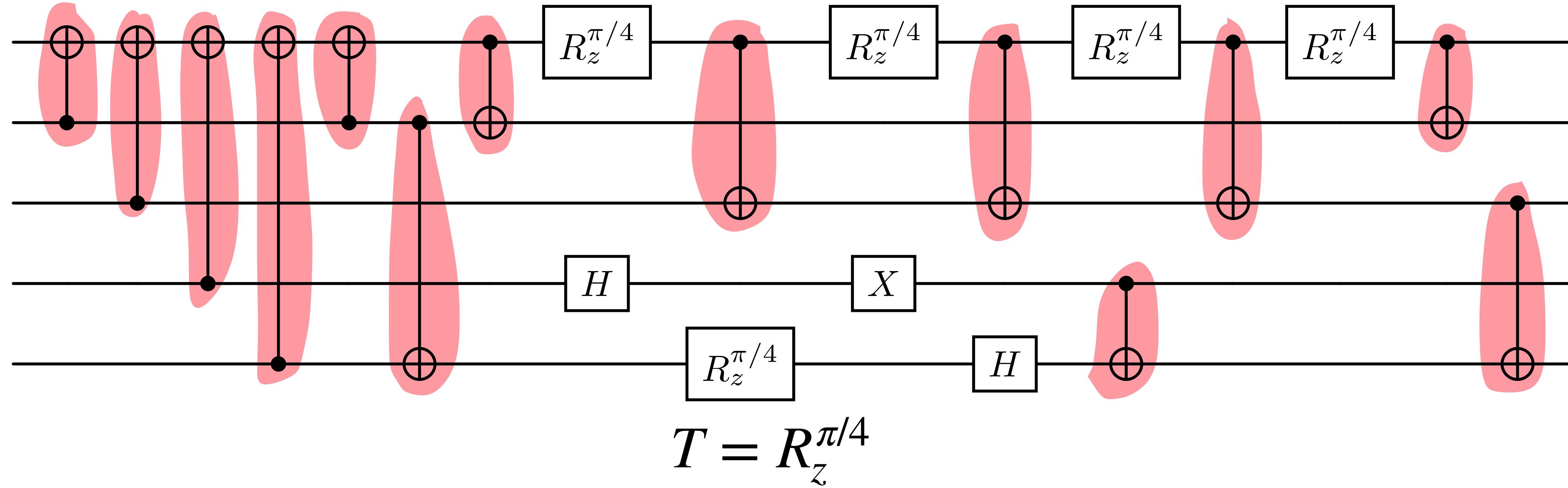
NISQ

- total gate count
- 2q gate count

FTQC

- T count and 2q gate count

Optimization Objectives



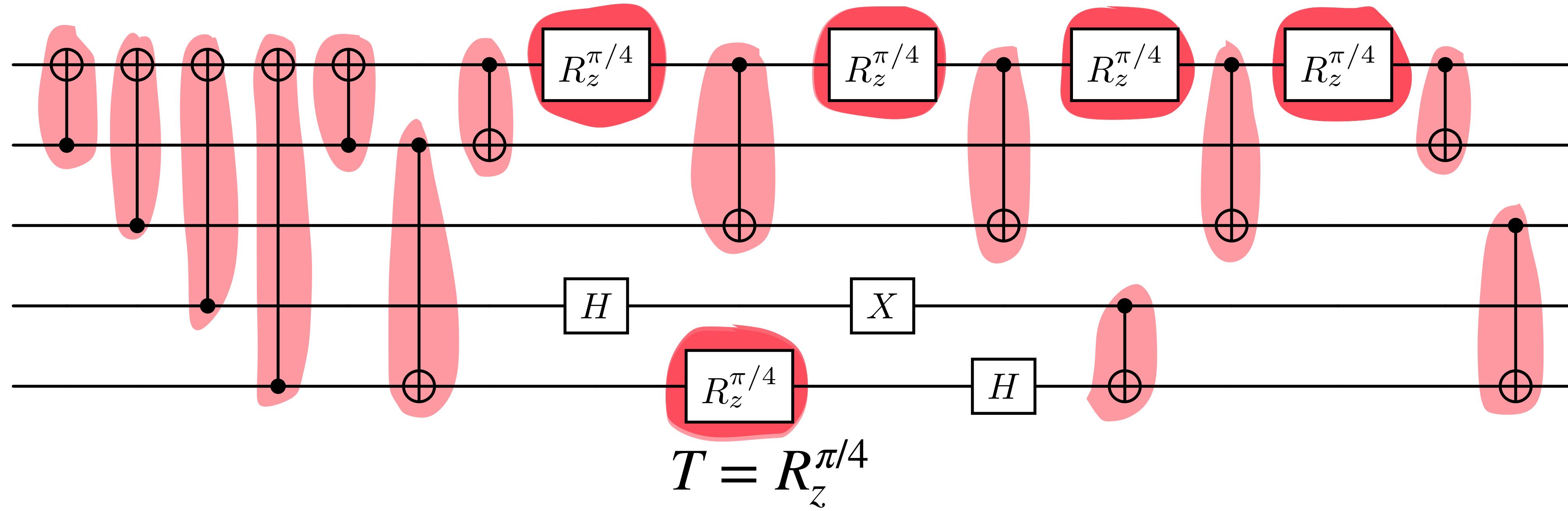
NISQ

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Optimization Objectives



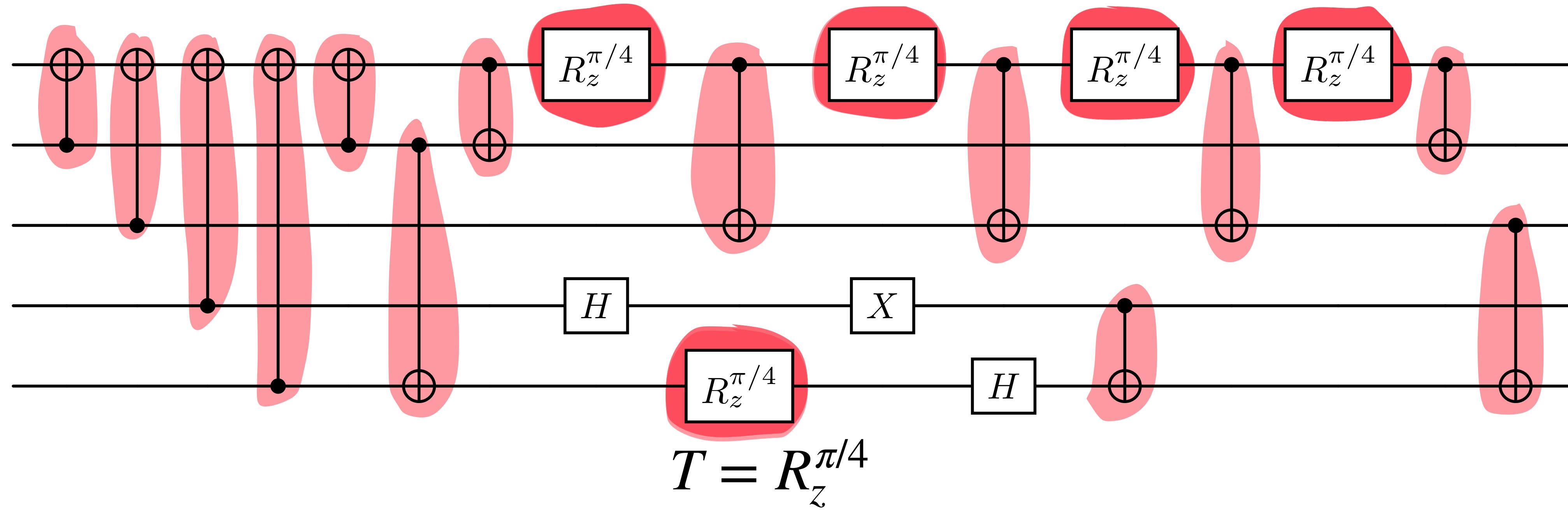
NISQ

- total gate count
- 2q gate count

FTQC

- T count and 2q gate count

Optimization Objectives



NISQ

- total gate count
- 2q gate count

and many more...

FTQC

- T count and 2q gate count

High-level problem

Given a circuit and optimization objective, output an equivalent circuit that minimizes the optimization objective.

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Given a circuit and optimization objective, output an equivalent circuit that minimizes the optimization objective.

Optimising quantum circuits is generally hard

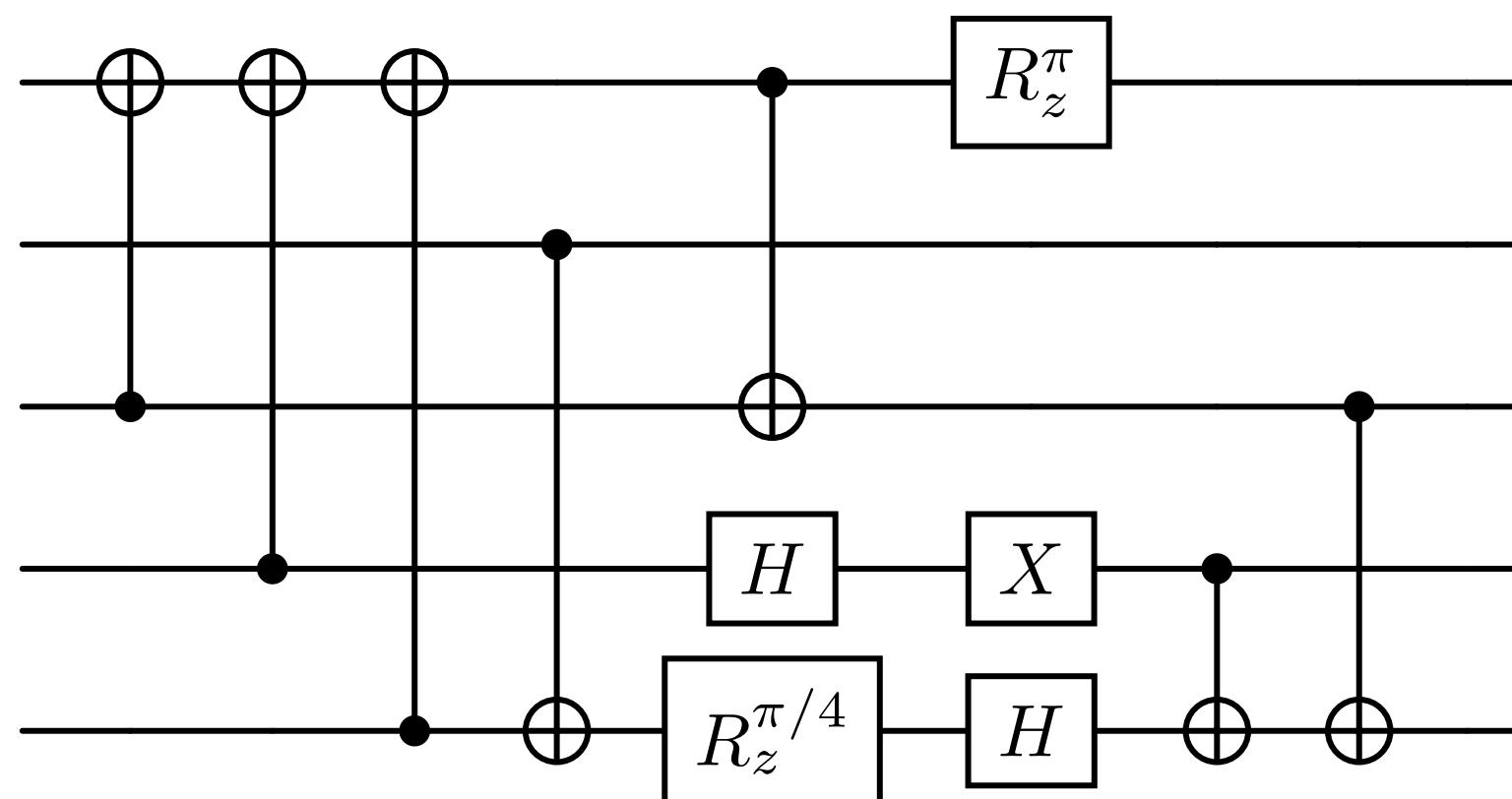
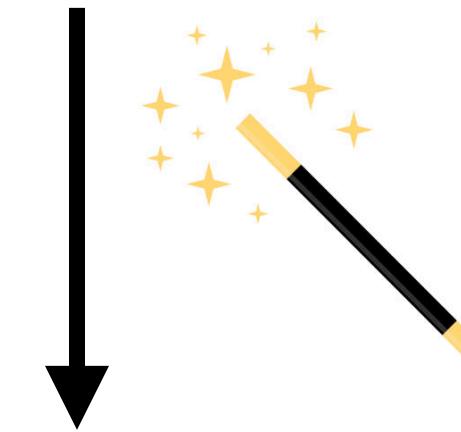
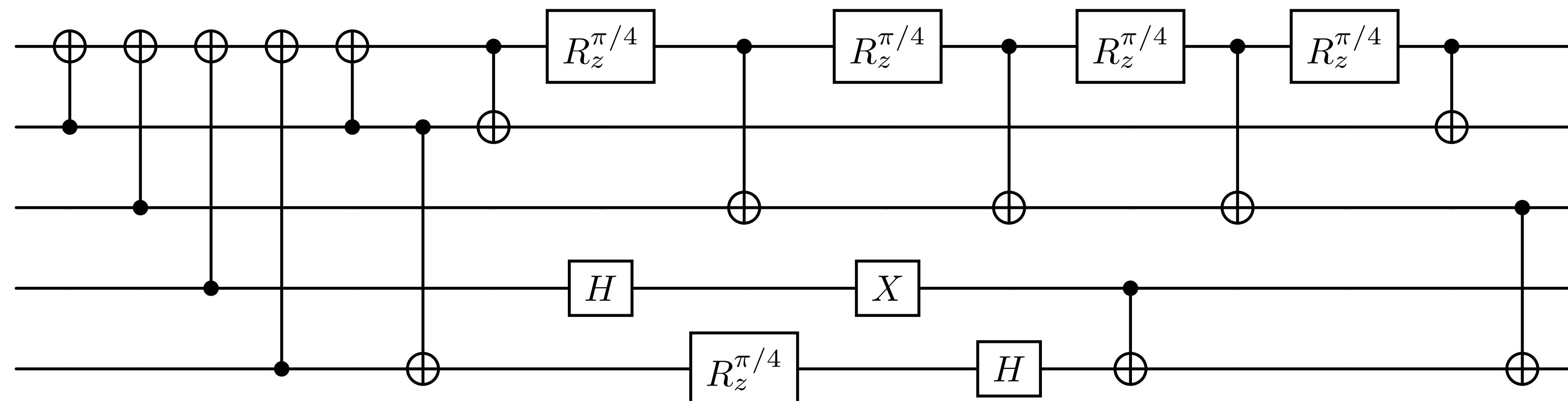
John van de Wetering¹ and Matthew Amy²

¹University of Amsterdam

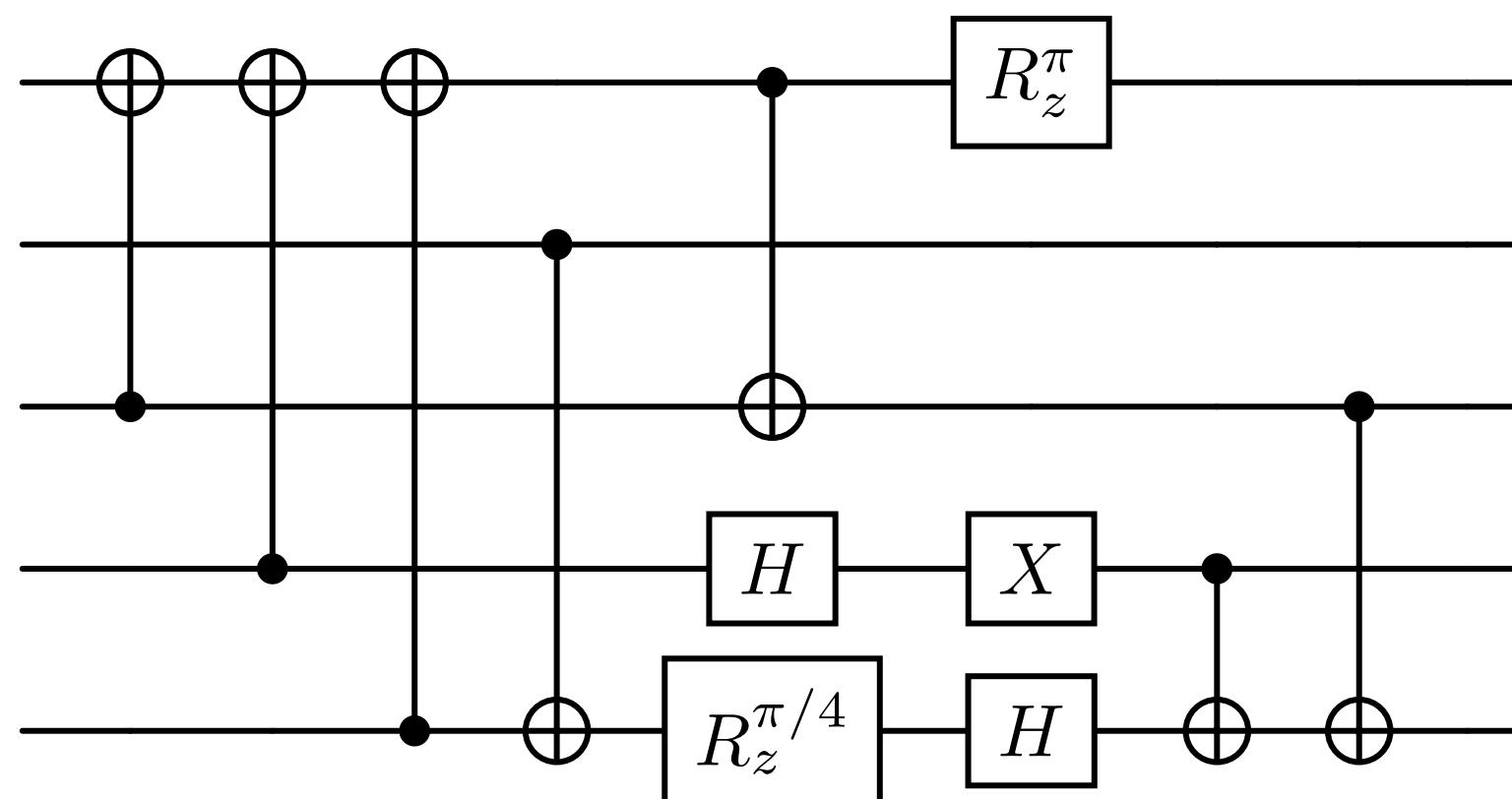
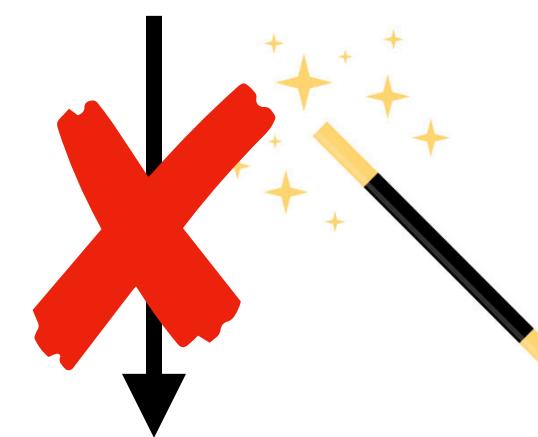
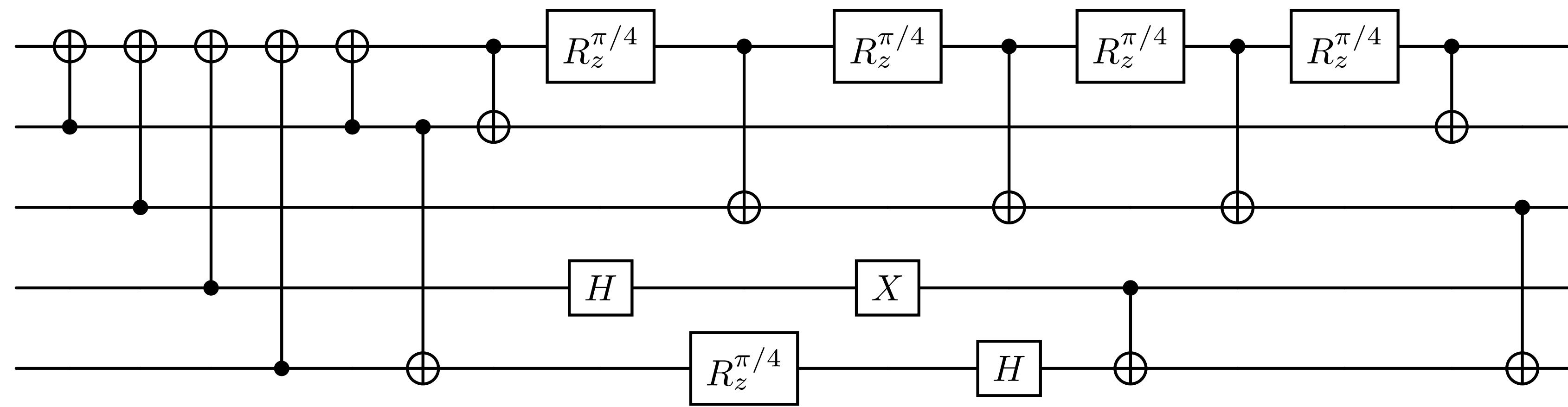
²Simon Fraser University

July 28th 2024

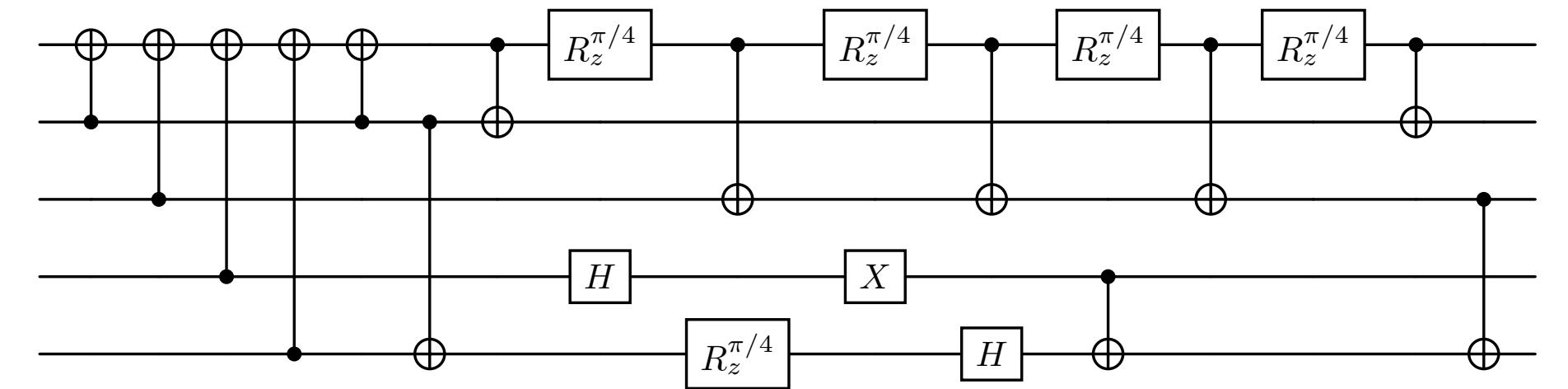
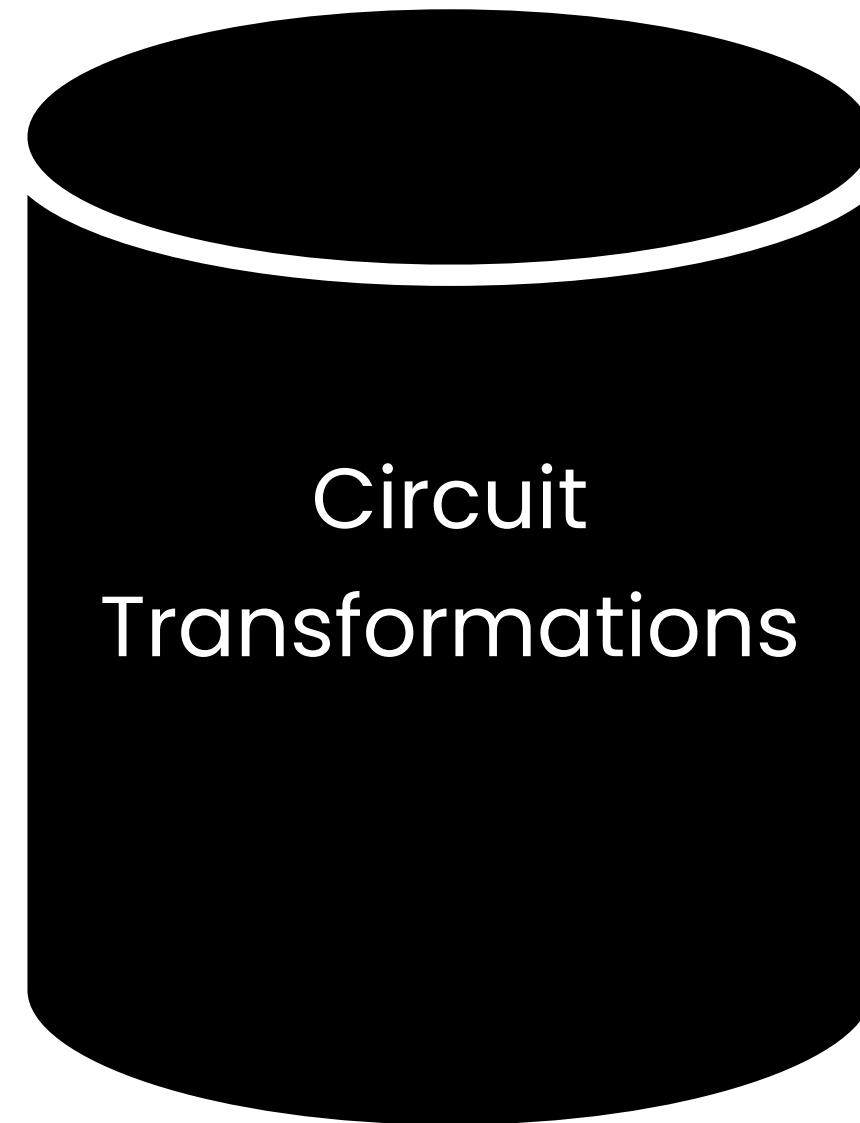
General Approach



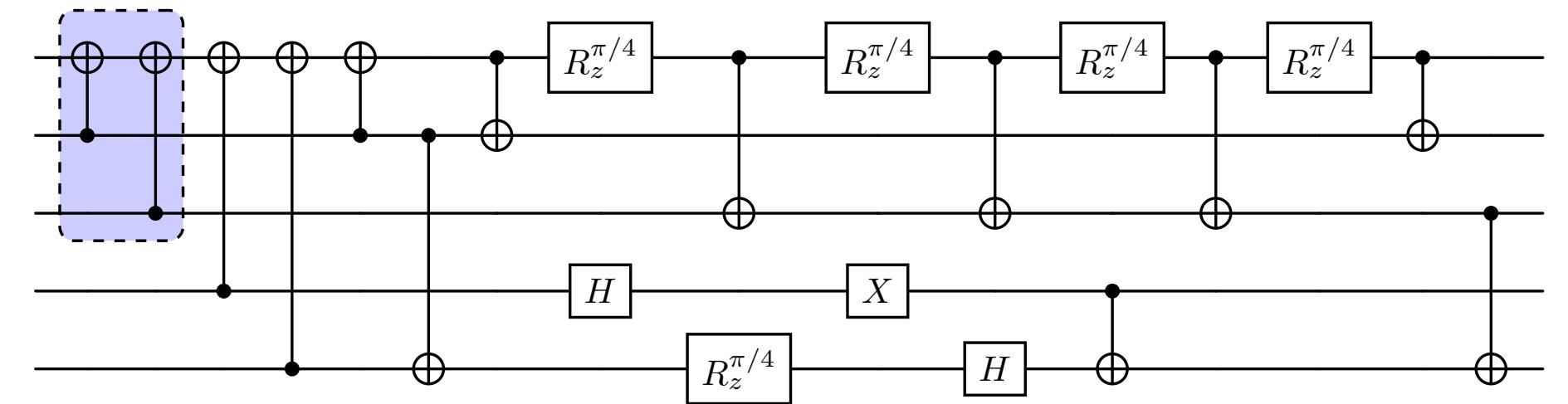
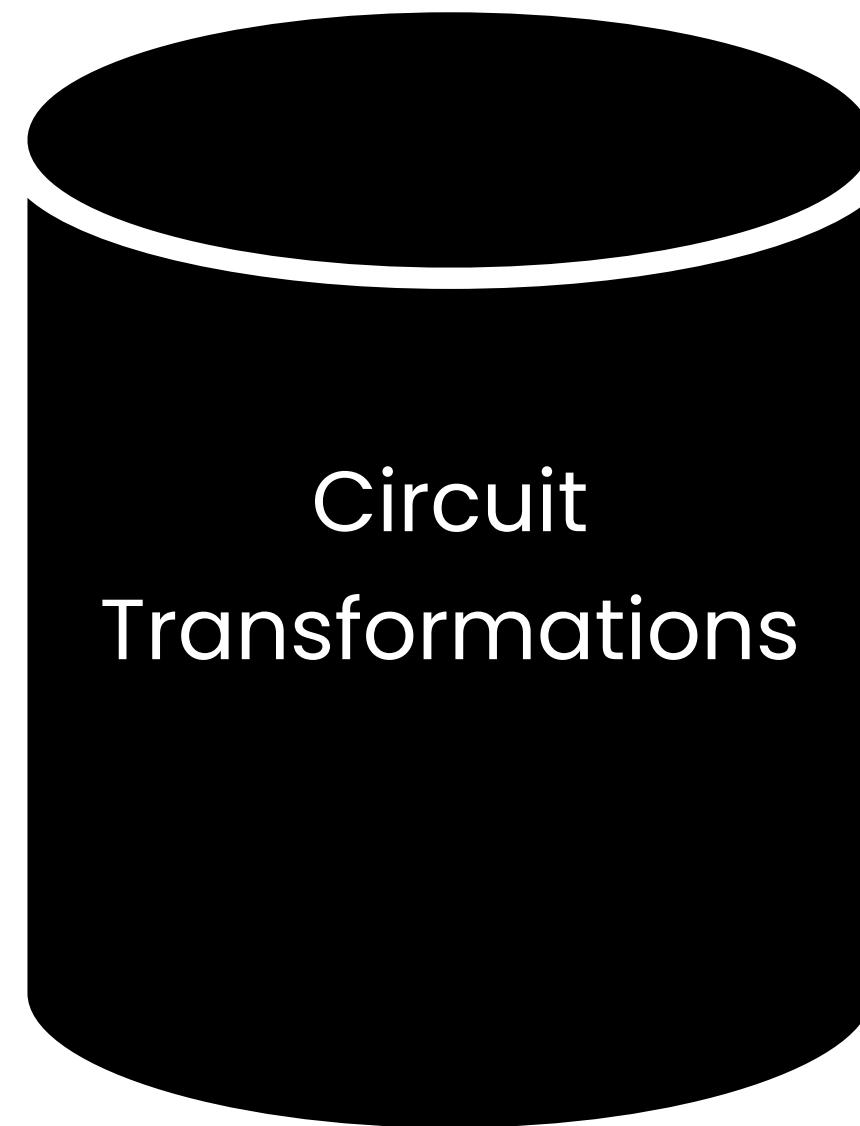
General Approach



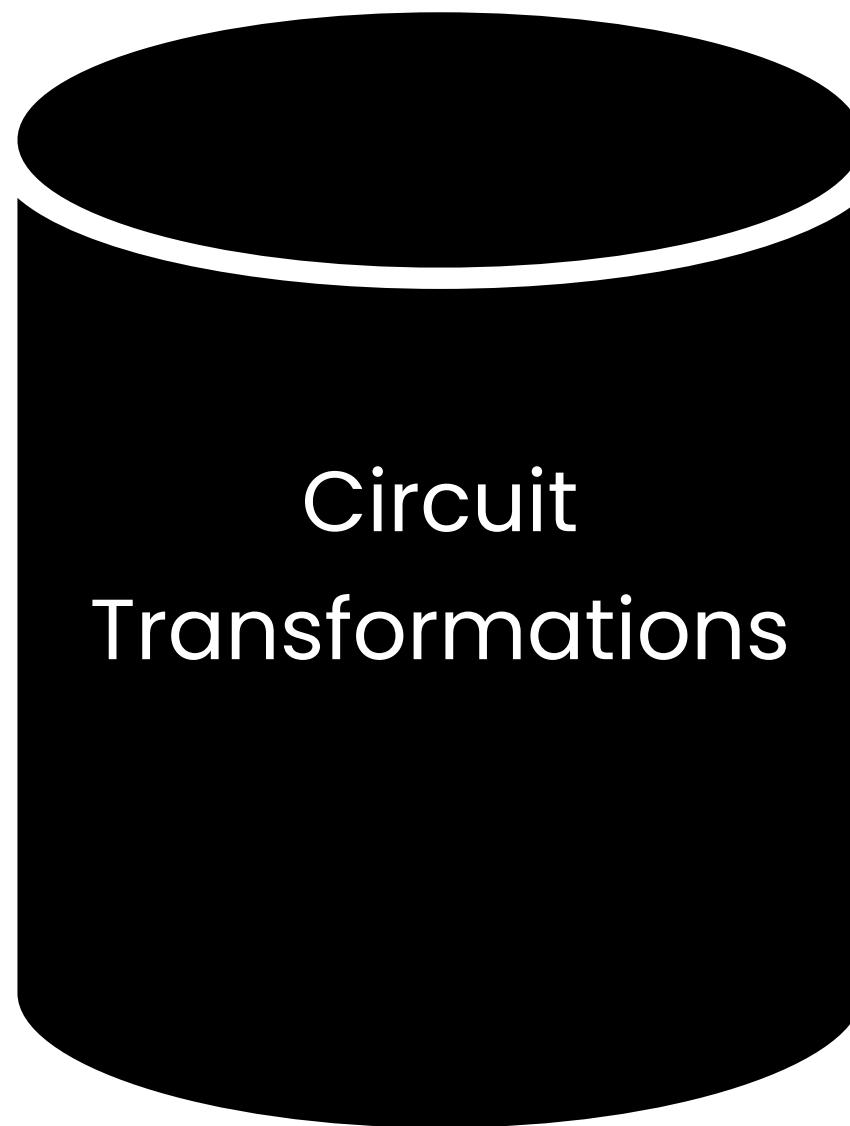
General Approach



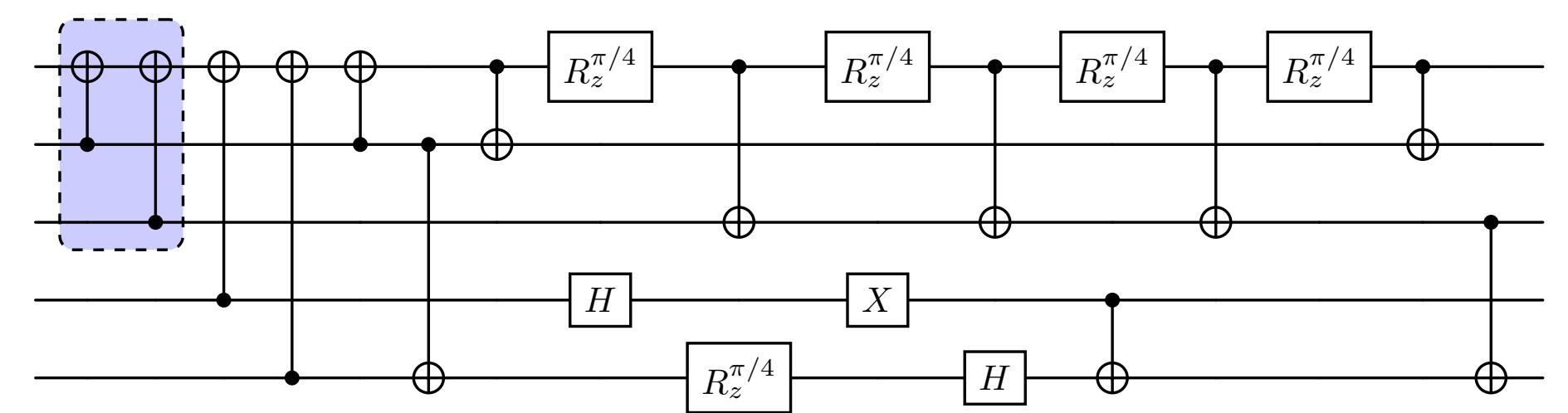
General Approach



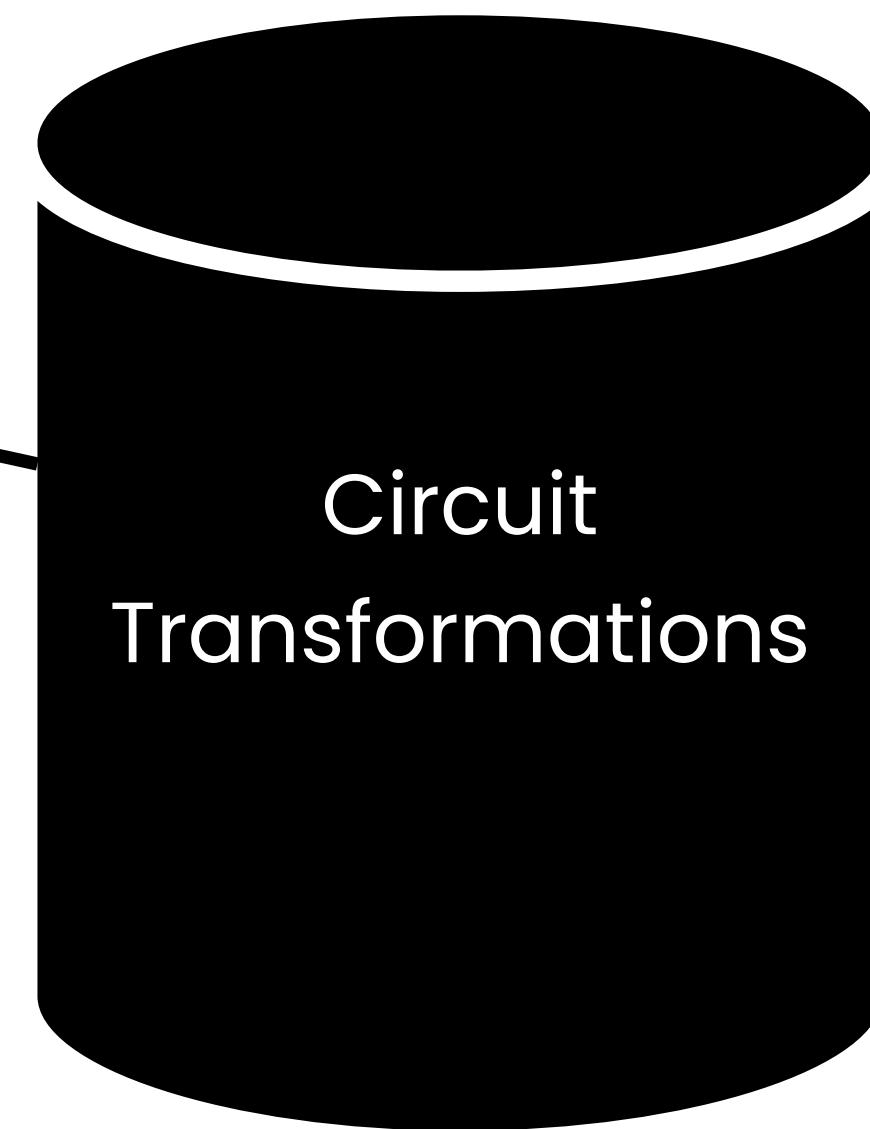
General Approach



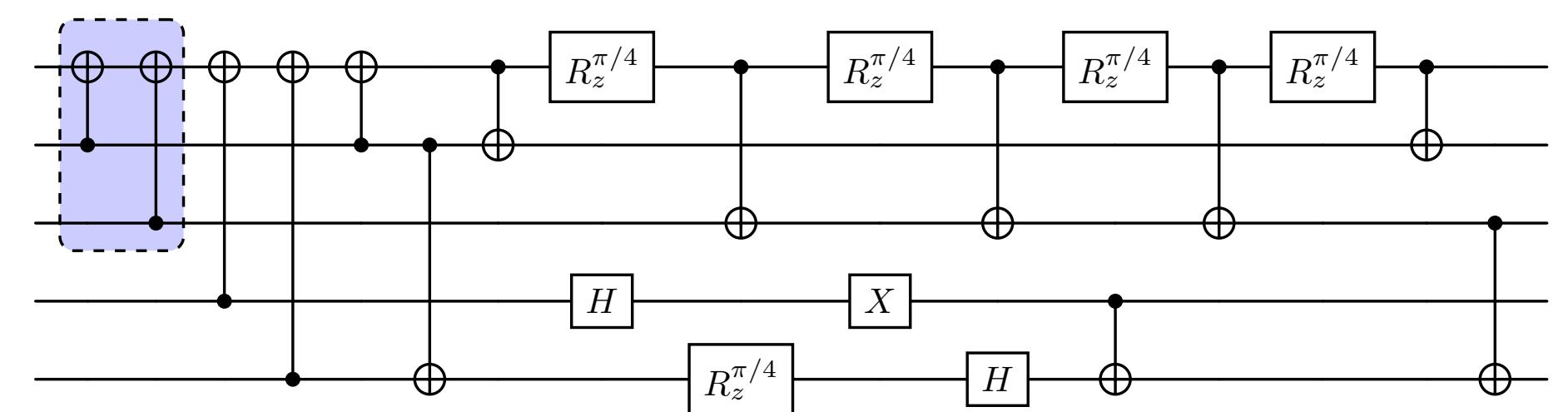
"subcircuit"



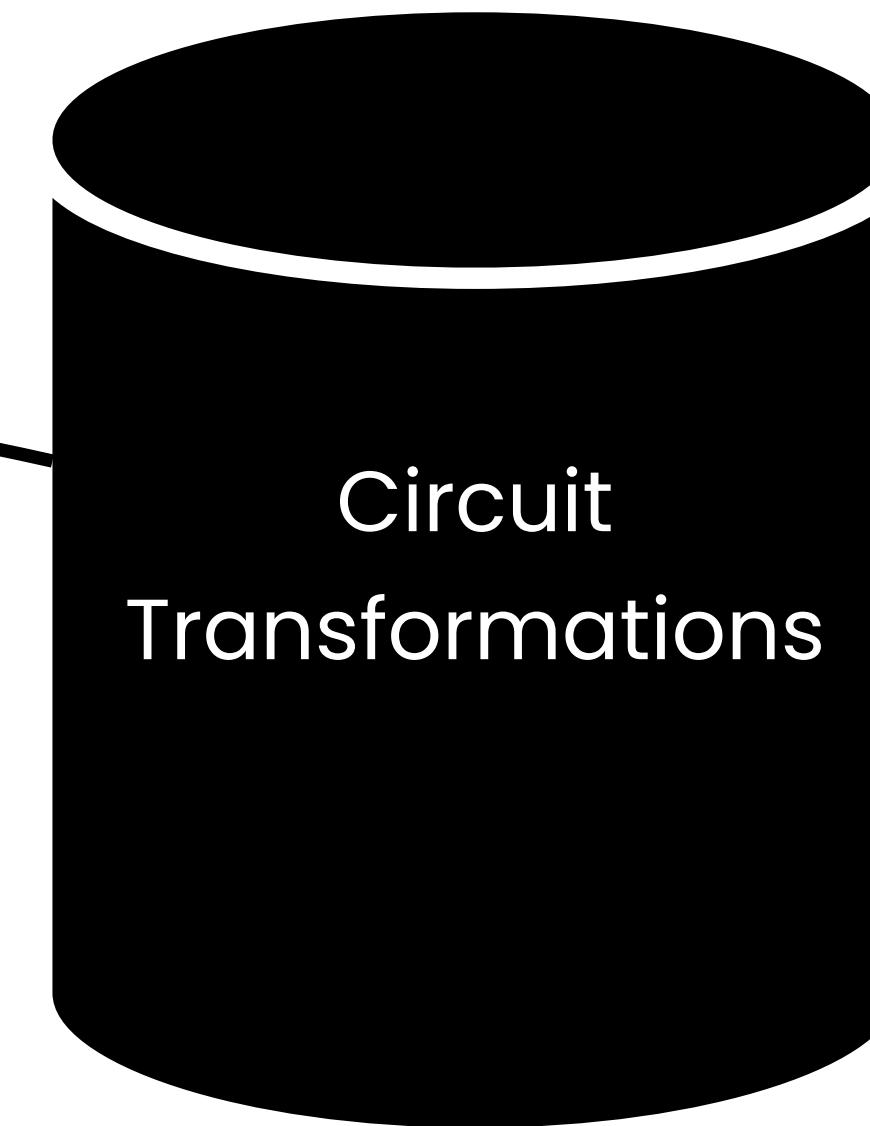
General Approach



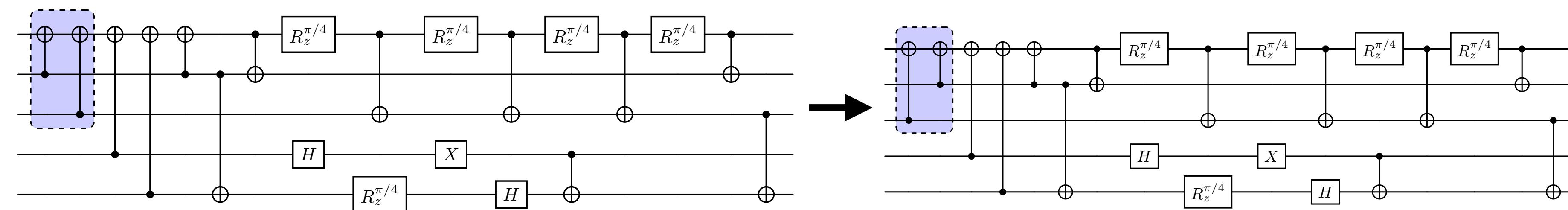
"subcircuit"



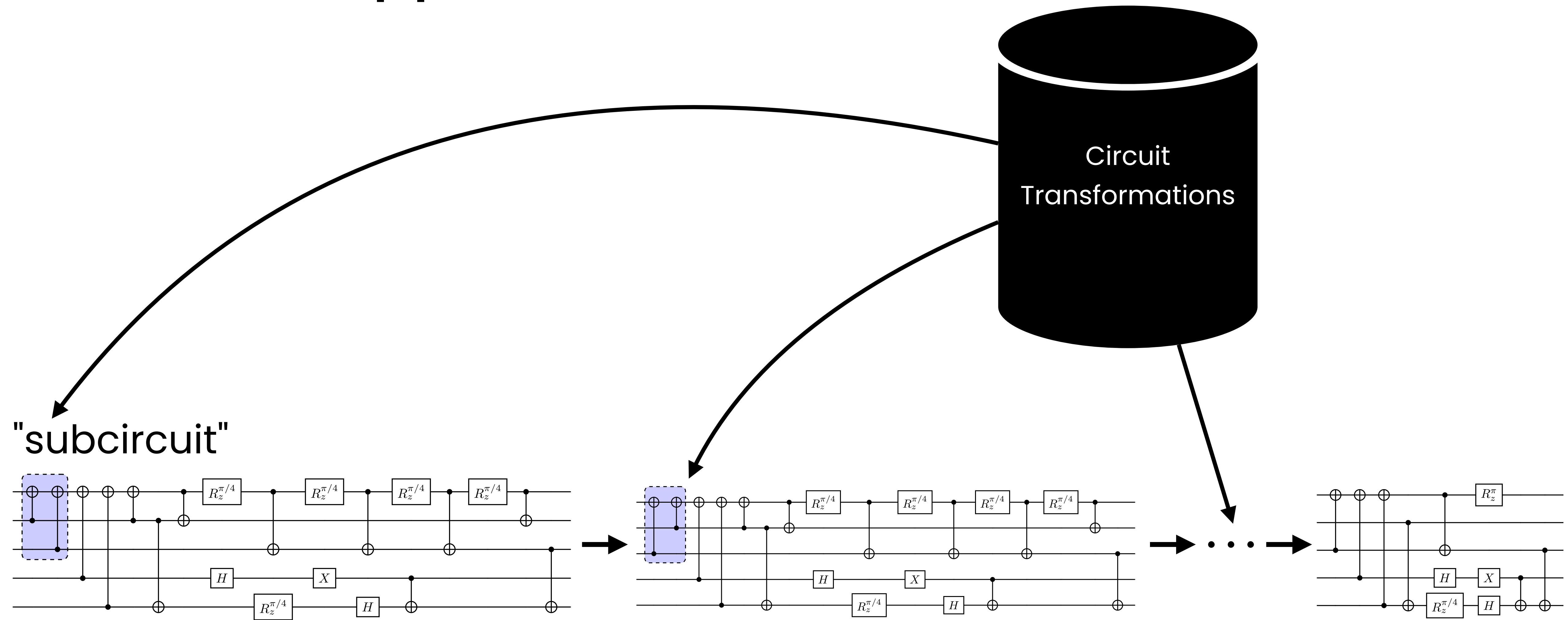
General Approach



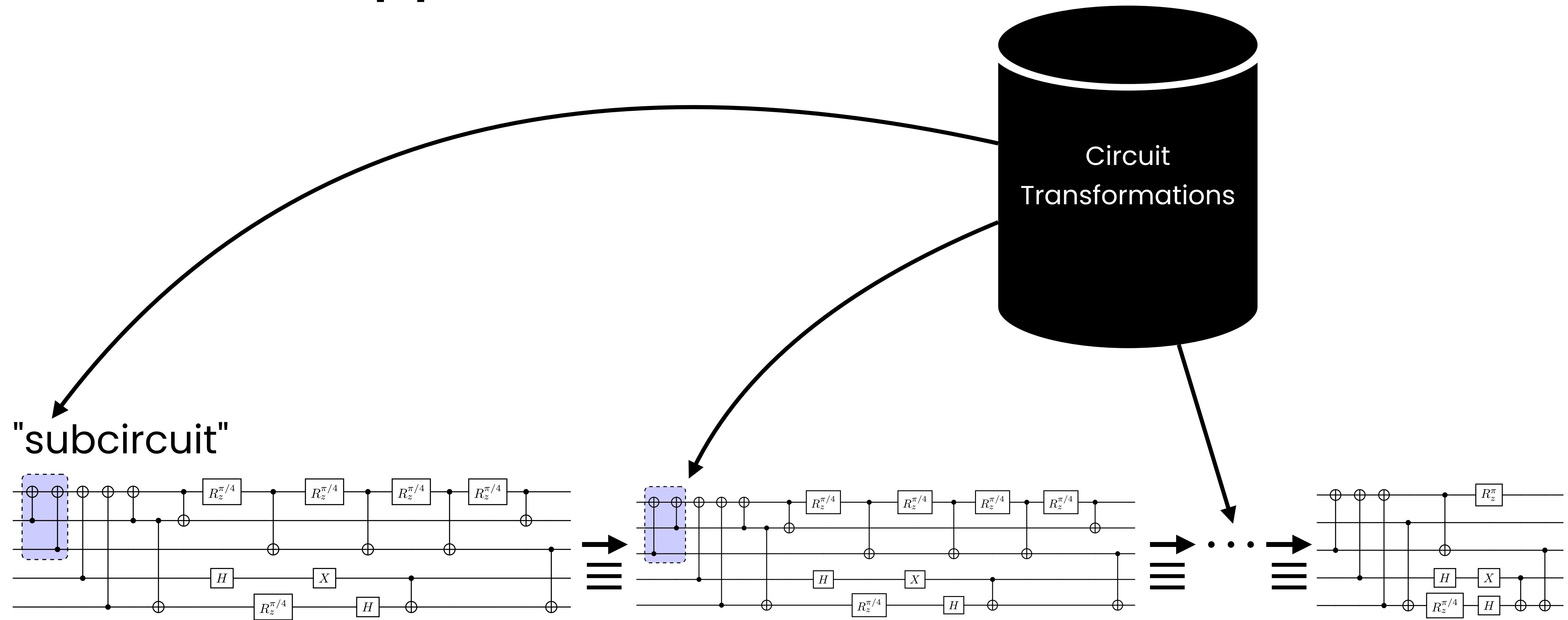
"subcircuit"



General Approach



General Approach



Circuit Equivalence

Circuits are unitary matrices: $U^\dagger U = UU^\dagger = I$

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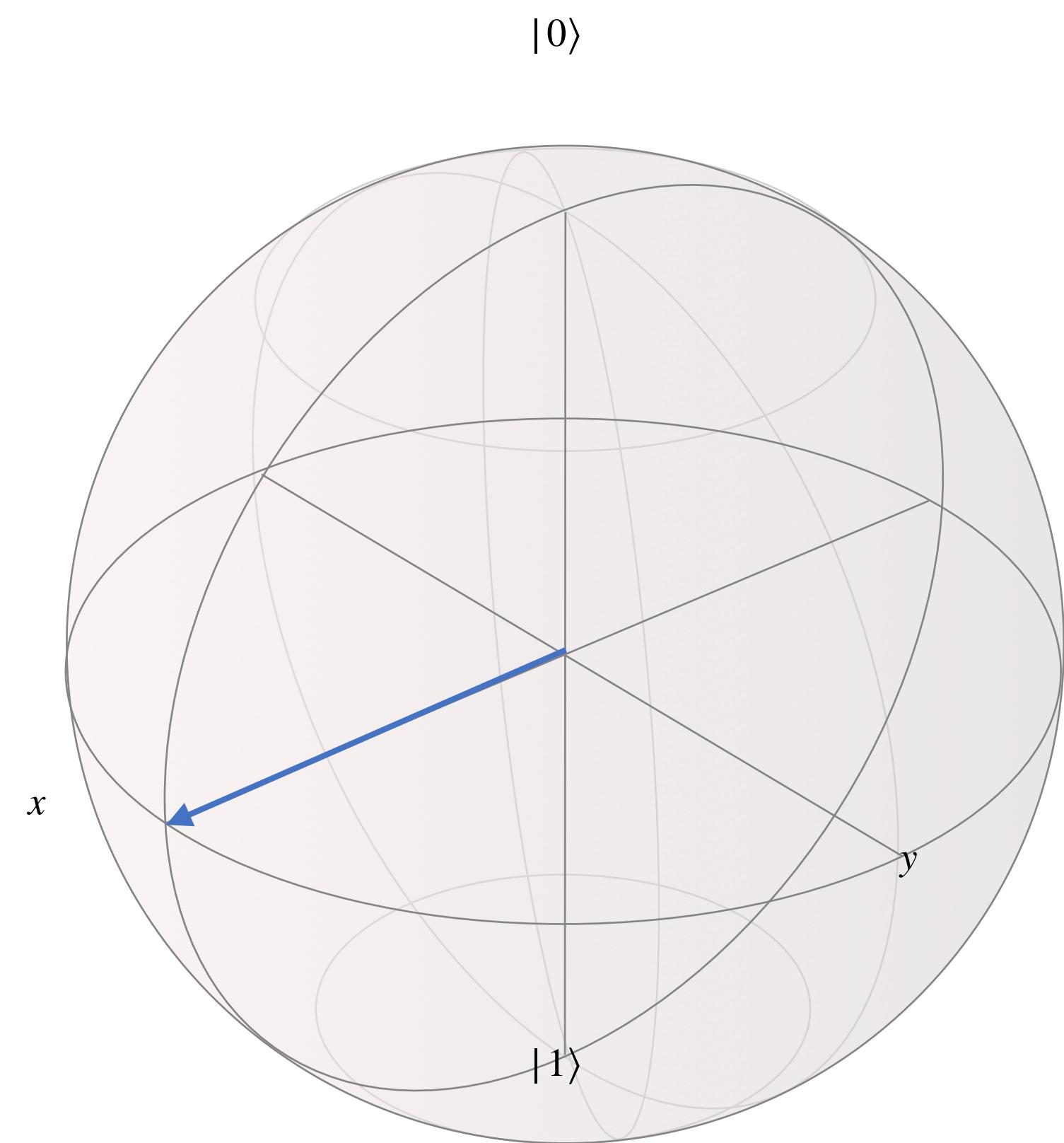
$$C_1 \equiv C_2 \iff U_{C_1} = U_{C_2}$$

"global phase"

Circuit Equivalence

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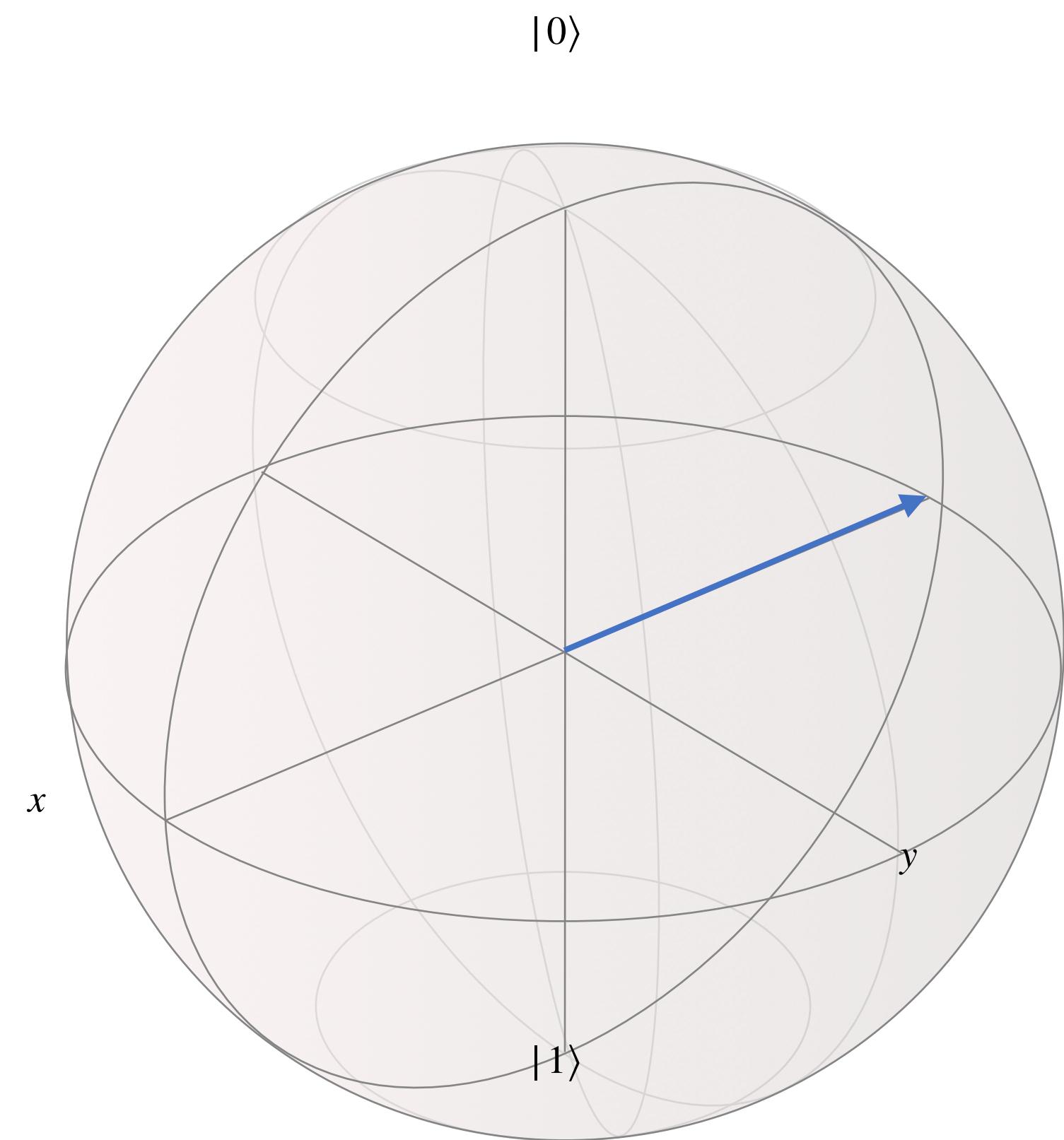
"global phase"



Circuit Equivalence

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"global phase"

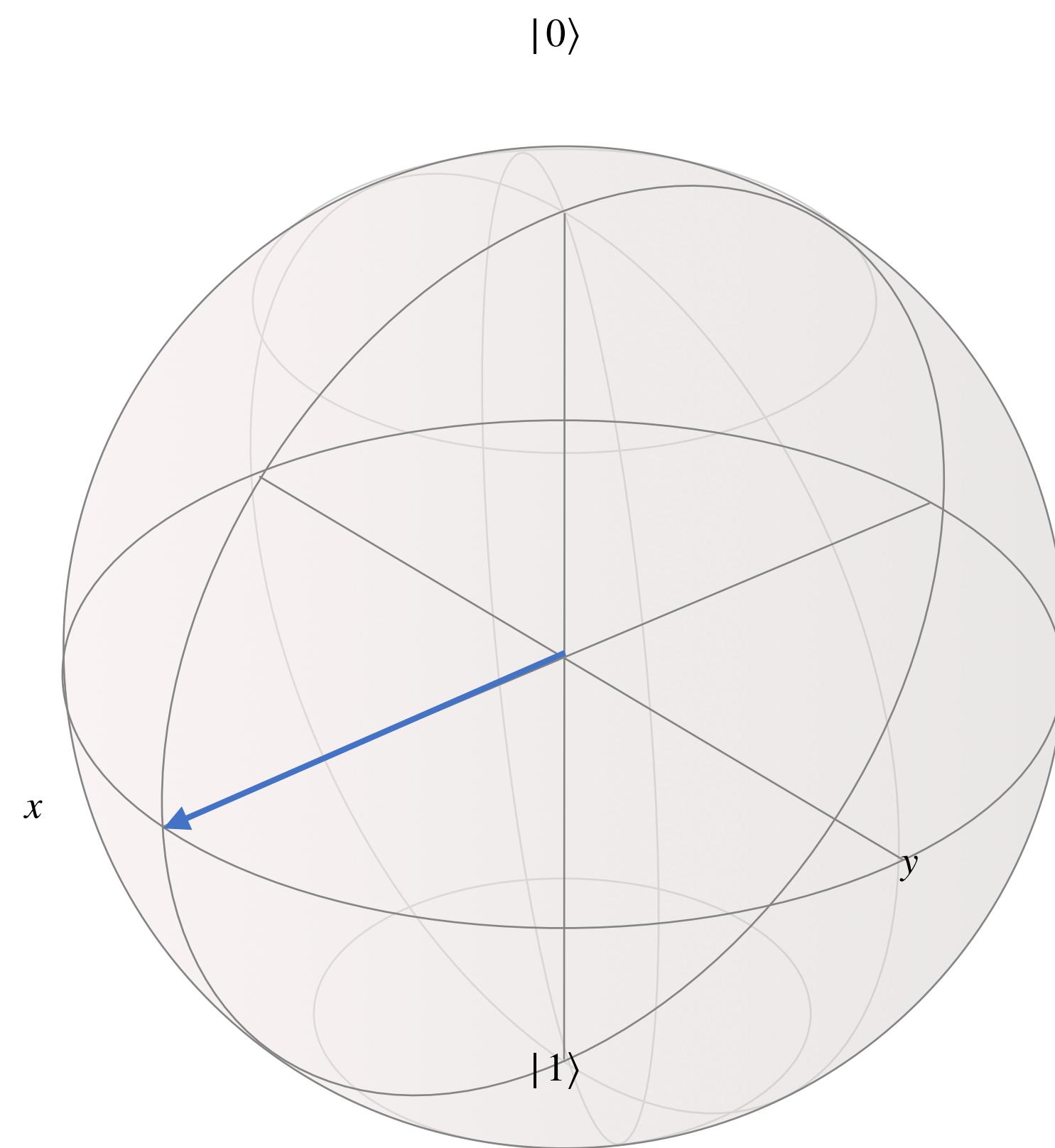


Circuit Equivalence

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"global phase"



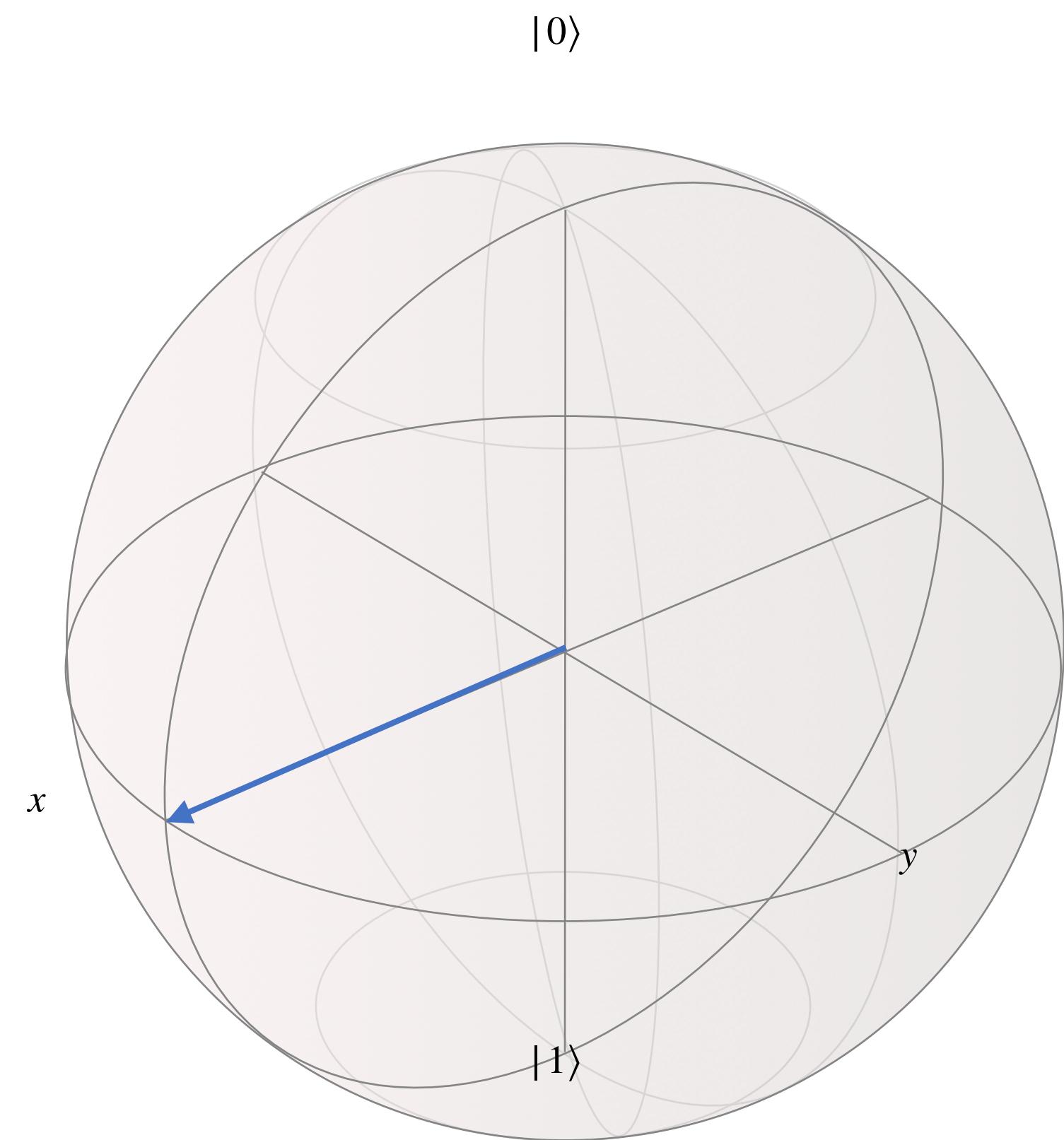
Circuit Equivalence

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"global phase"

$$C_1 \equiv C_2 \iff \exists \varphi . U_{C_1} = e^{i\varphi} U_{C_2}$$



Circuit Equivalence

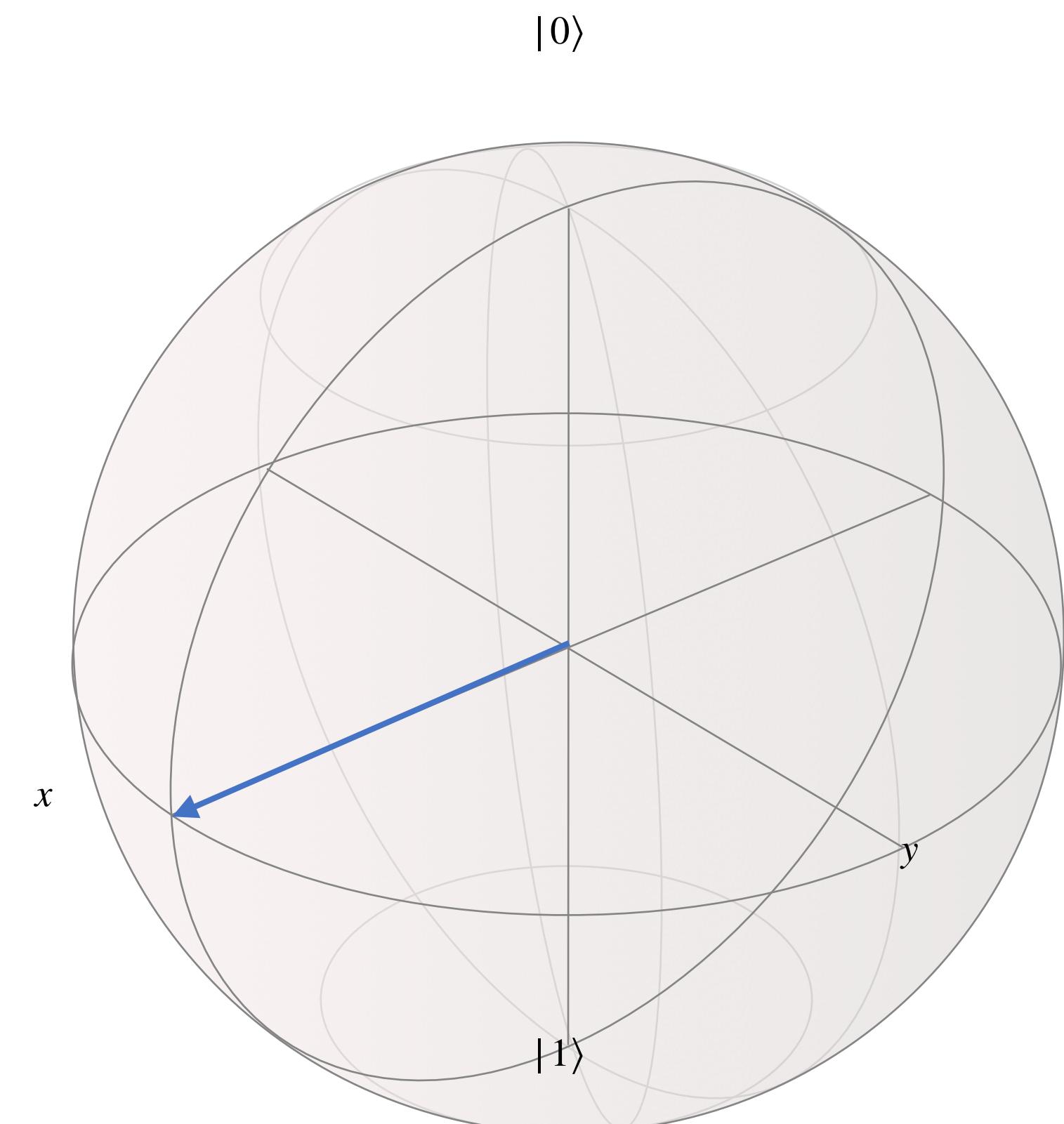
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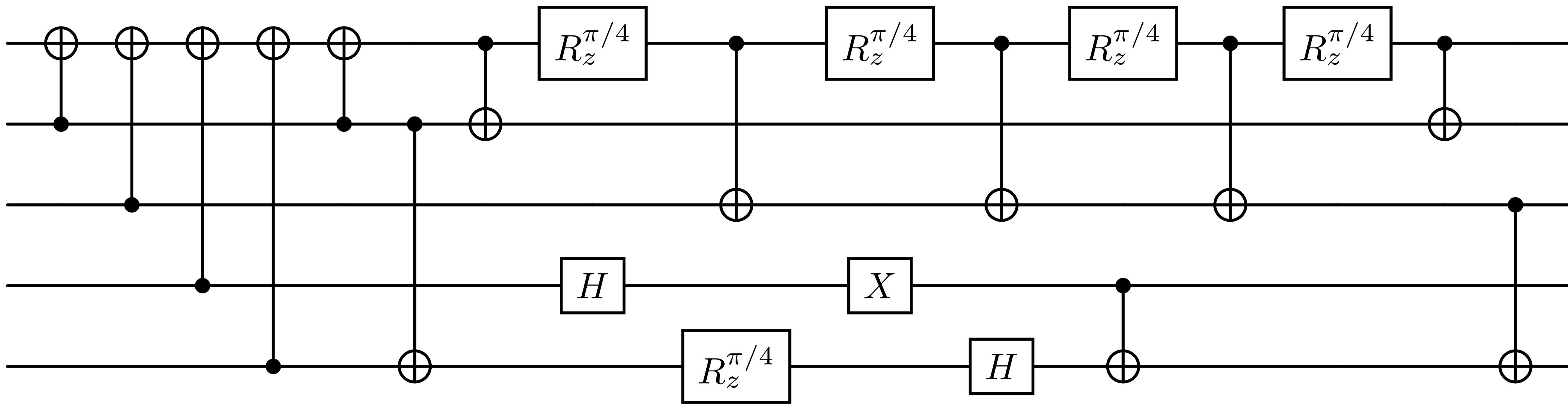
"global phase"

$$C_1 \equiv C_2 \iff \exists \varphi . U_{C_1} = e^{i\varphi} U_{C_2}$$

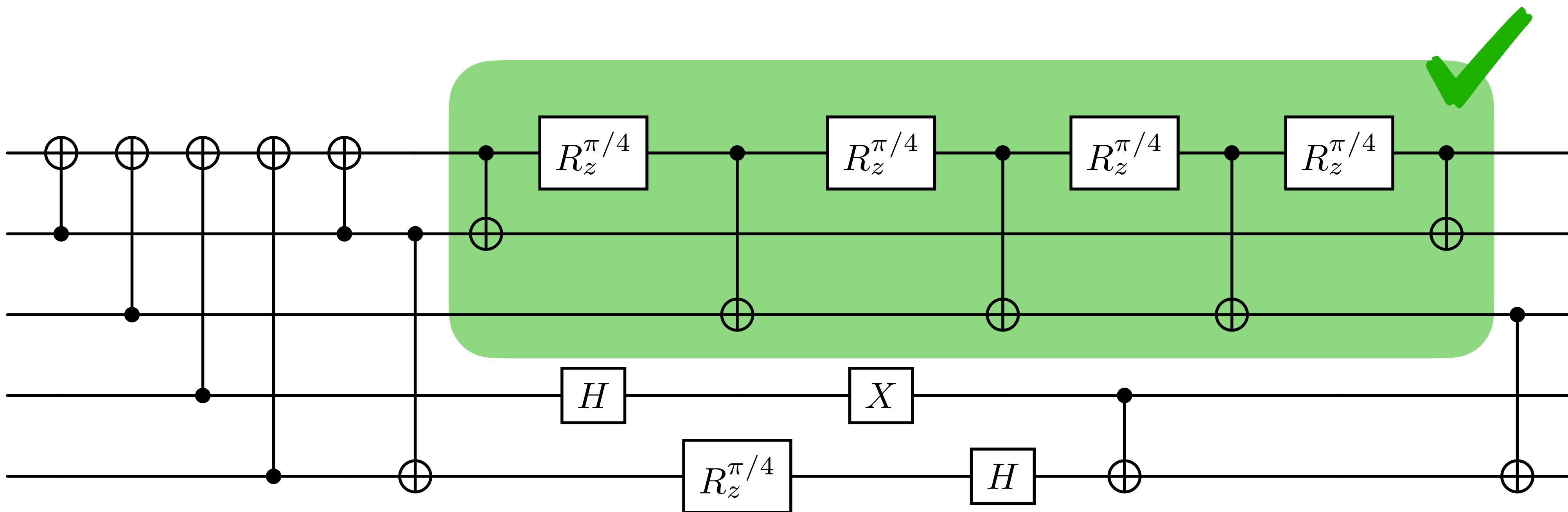
Hard for even a quantum computer to verify
(QMA-Complete)



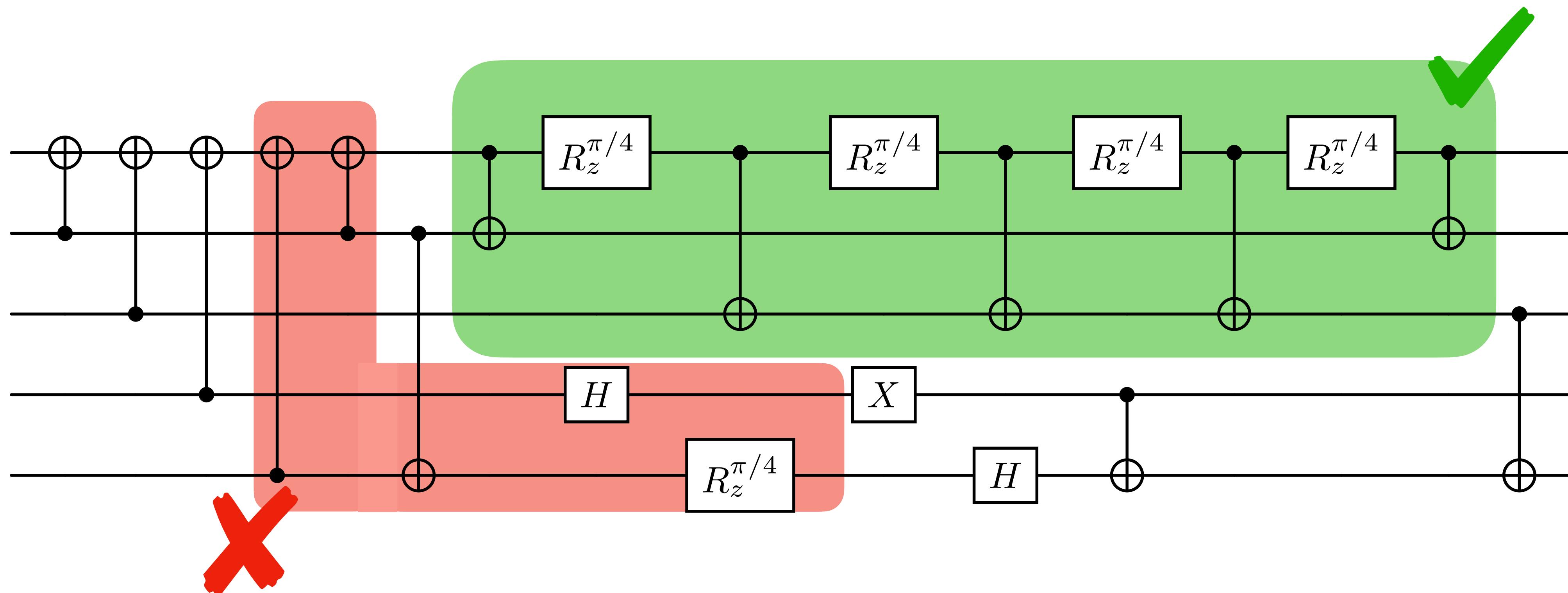
Subcircuit



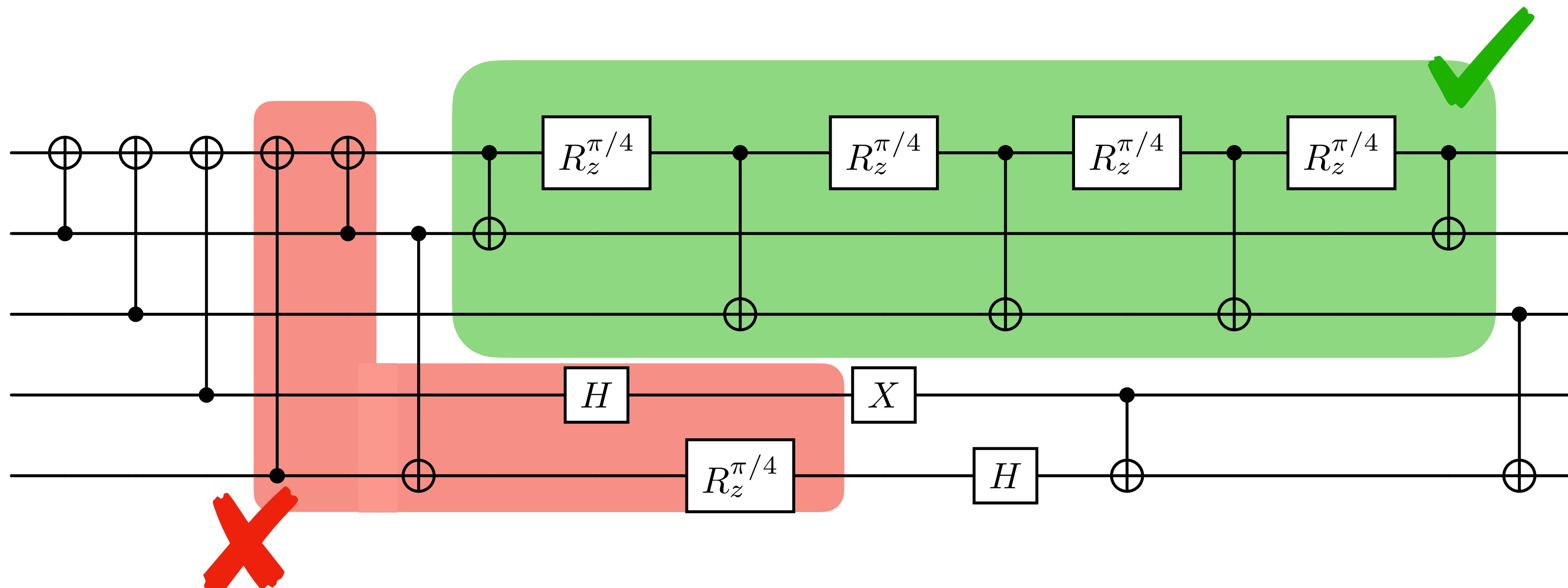
Subcircuit



Subcircuit

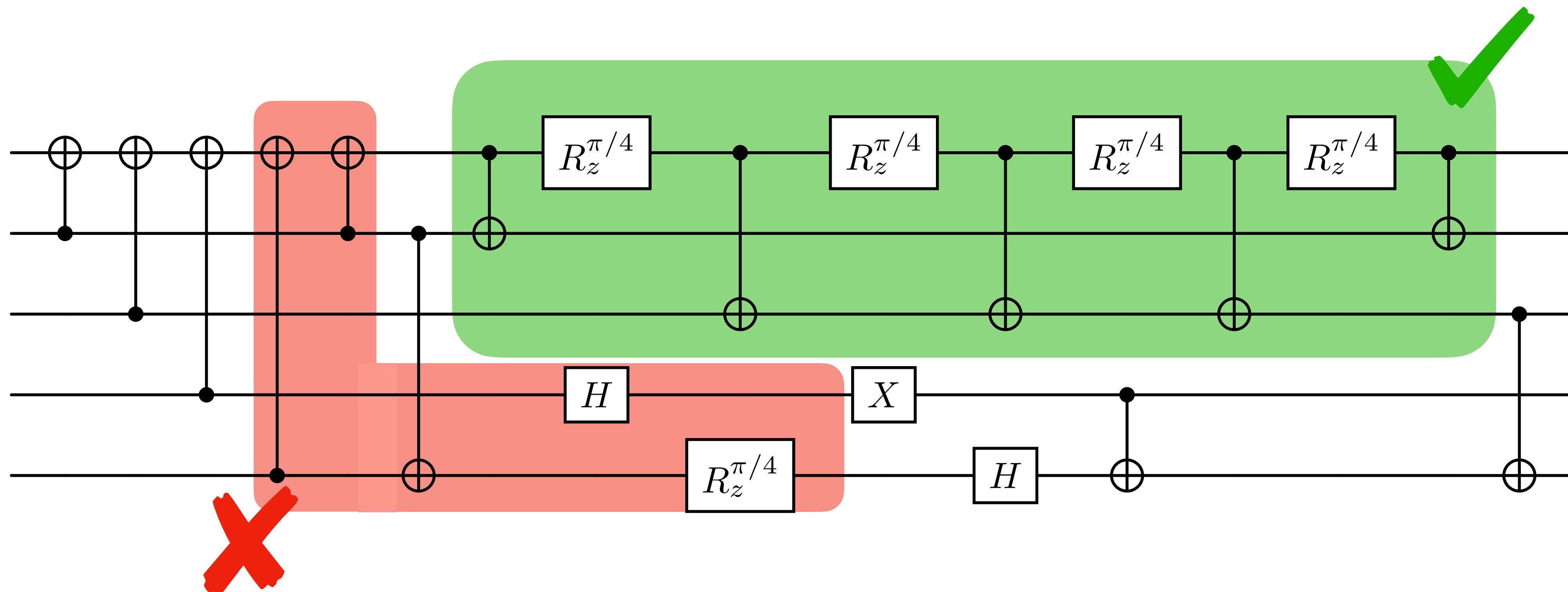


Subcircuit



"convex subgraph"

Subcircuit



"convex subgraph"

i.e. any path between two vertices in the DAG also in subgraph

Rewrite Rules

Rewrite Rules

"peephole optimization"

Rewrite Rules

"peephole optimization"

Program 1

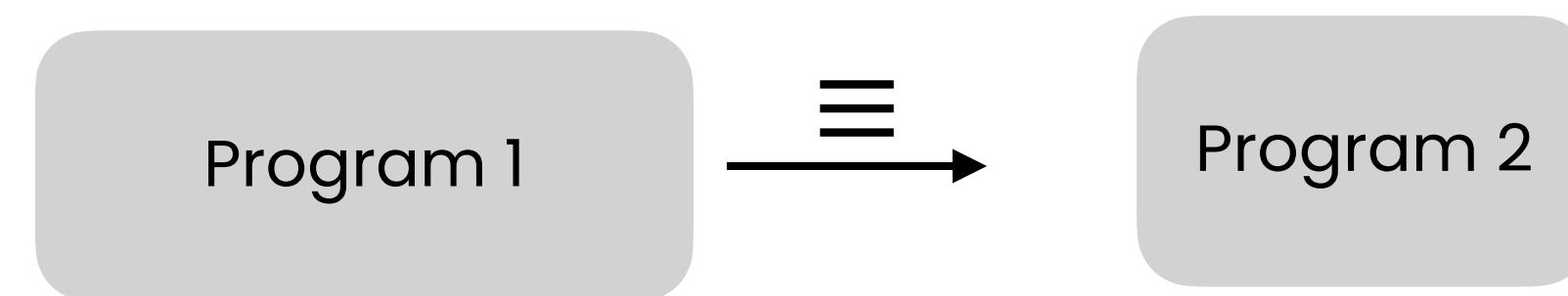
Rewrite Rules

"peephole optimization"



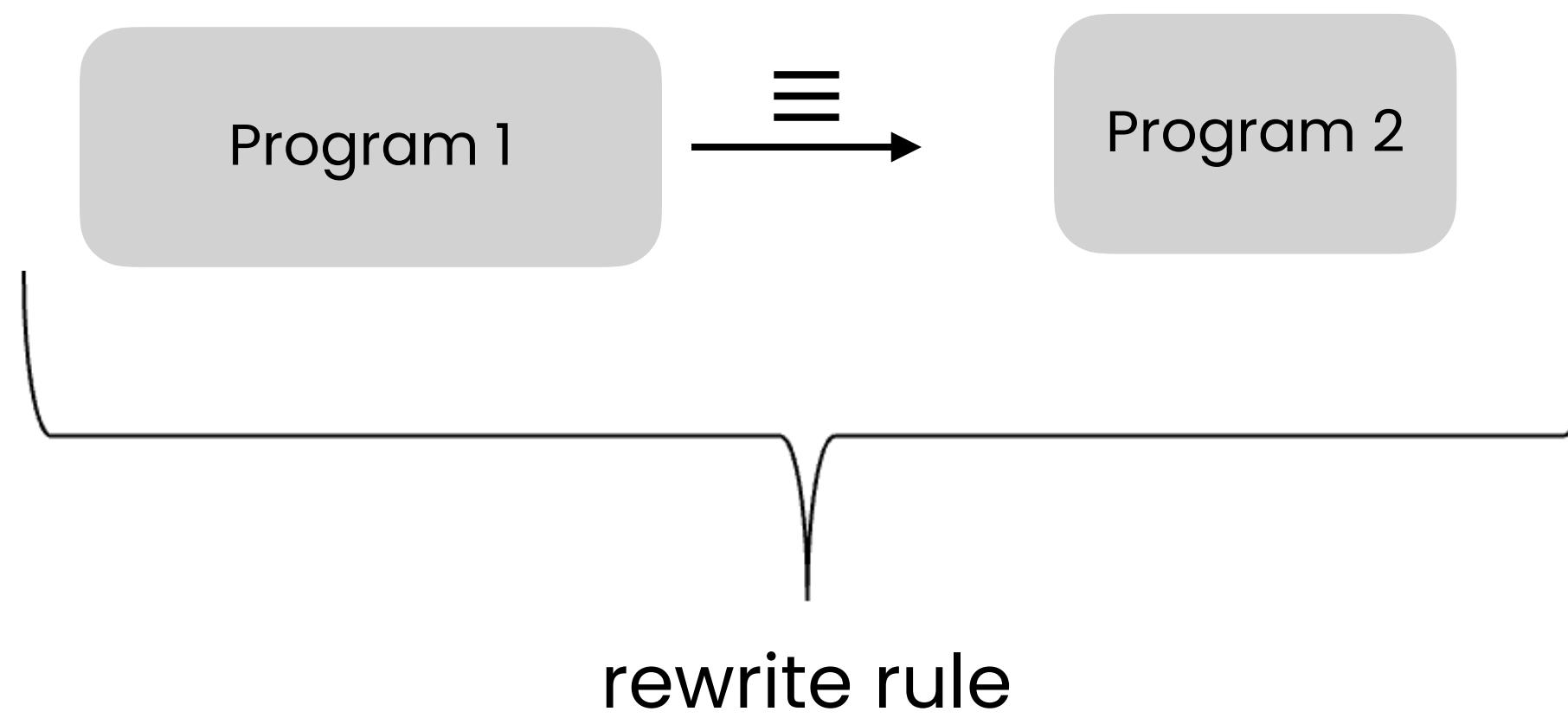
Rewrite Rules

"peephole optimization"



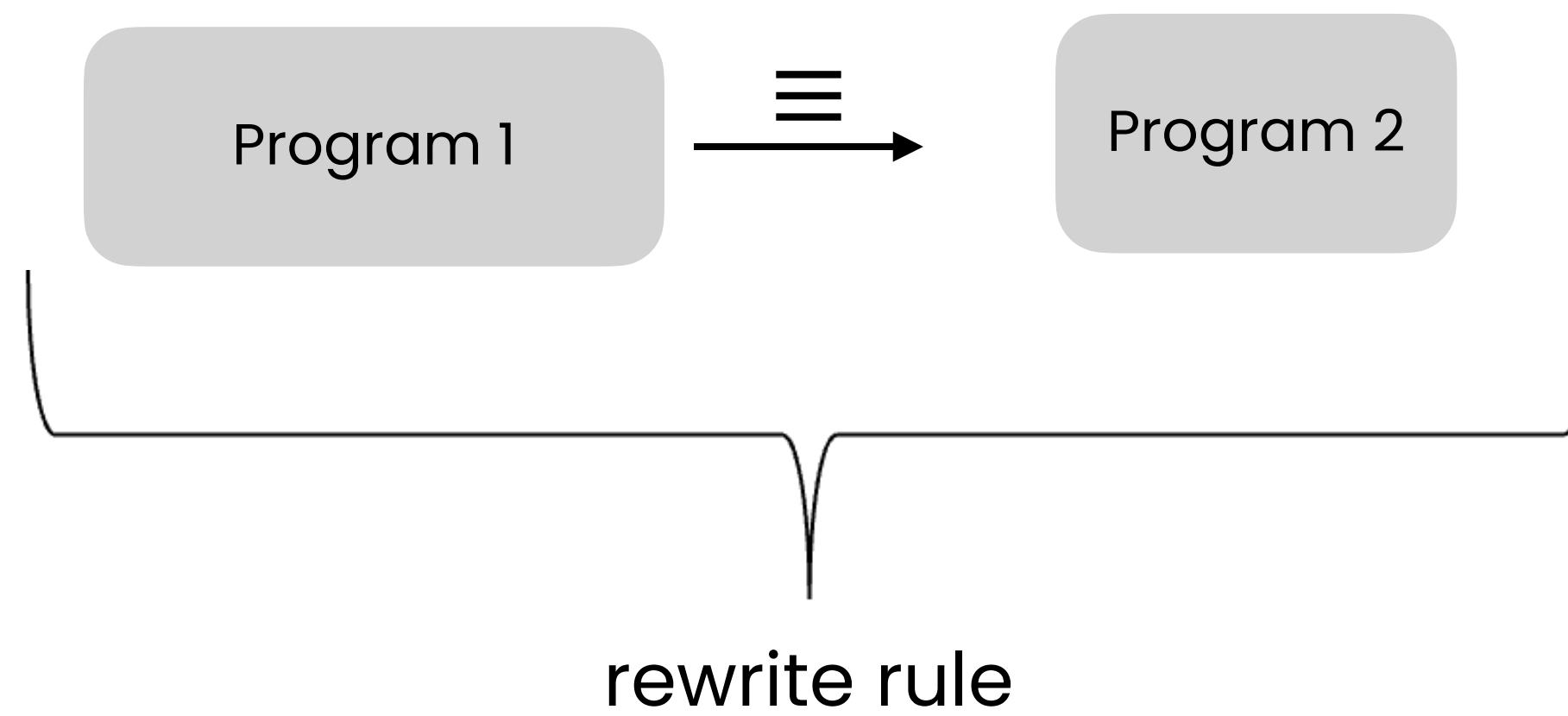
Rewrite Rules

"peephole optimization"



Rewrite Rules

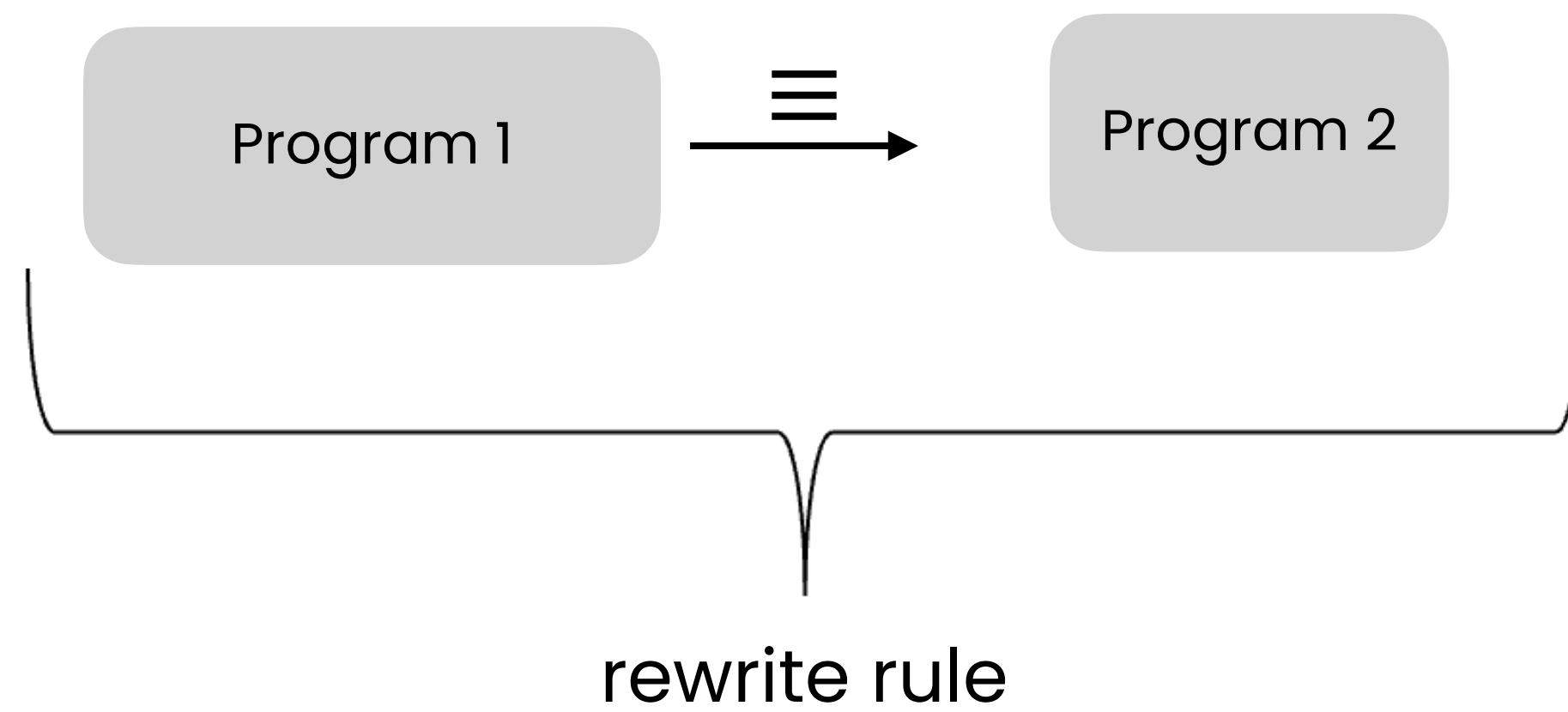
"peephole optimization"



- Optimization: LHS and RHS same gate set

Rewrite Rules

"peephole optimization"



- Optimization: LHS and RHS same gate set
- Decomposition: LHS higher level than RHS

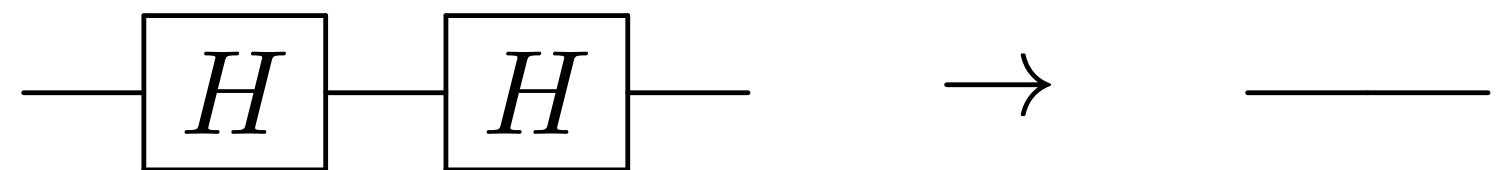
Simple Rewrite Rules

Simple Rewrite Rules

Cancel

Simple Rewrite Rules

Cancel



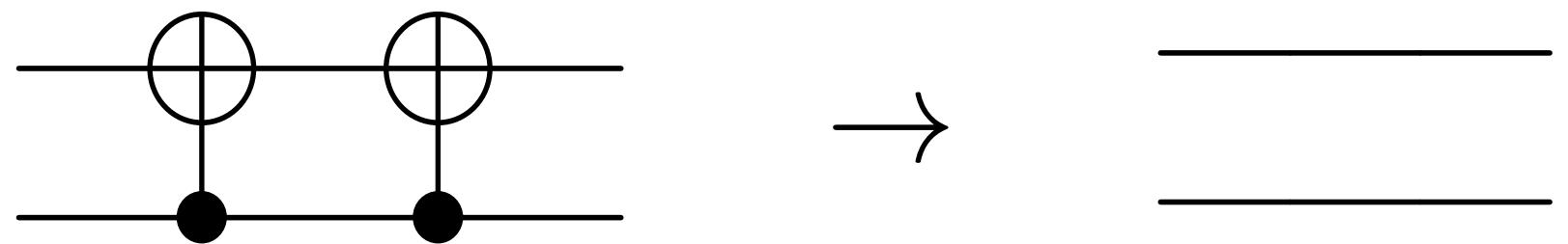
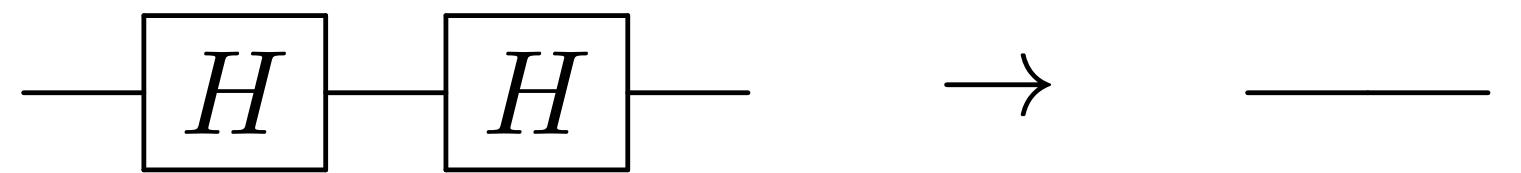
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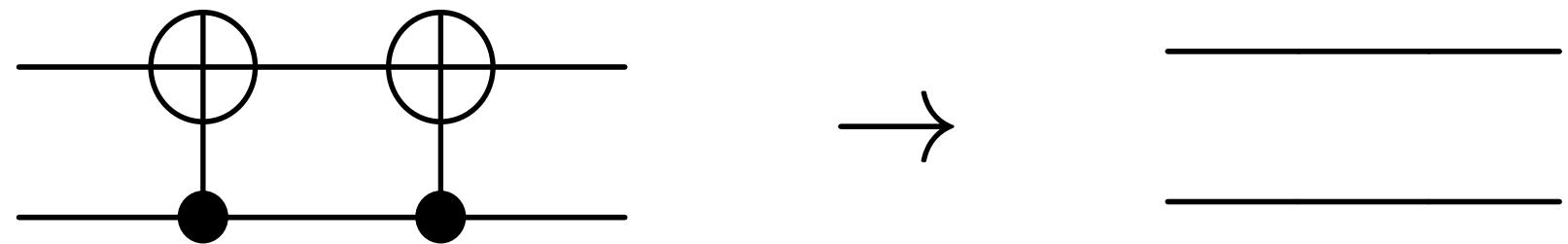
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Simple Rewrite Rules

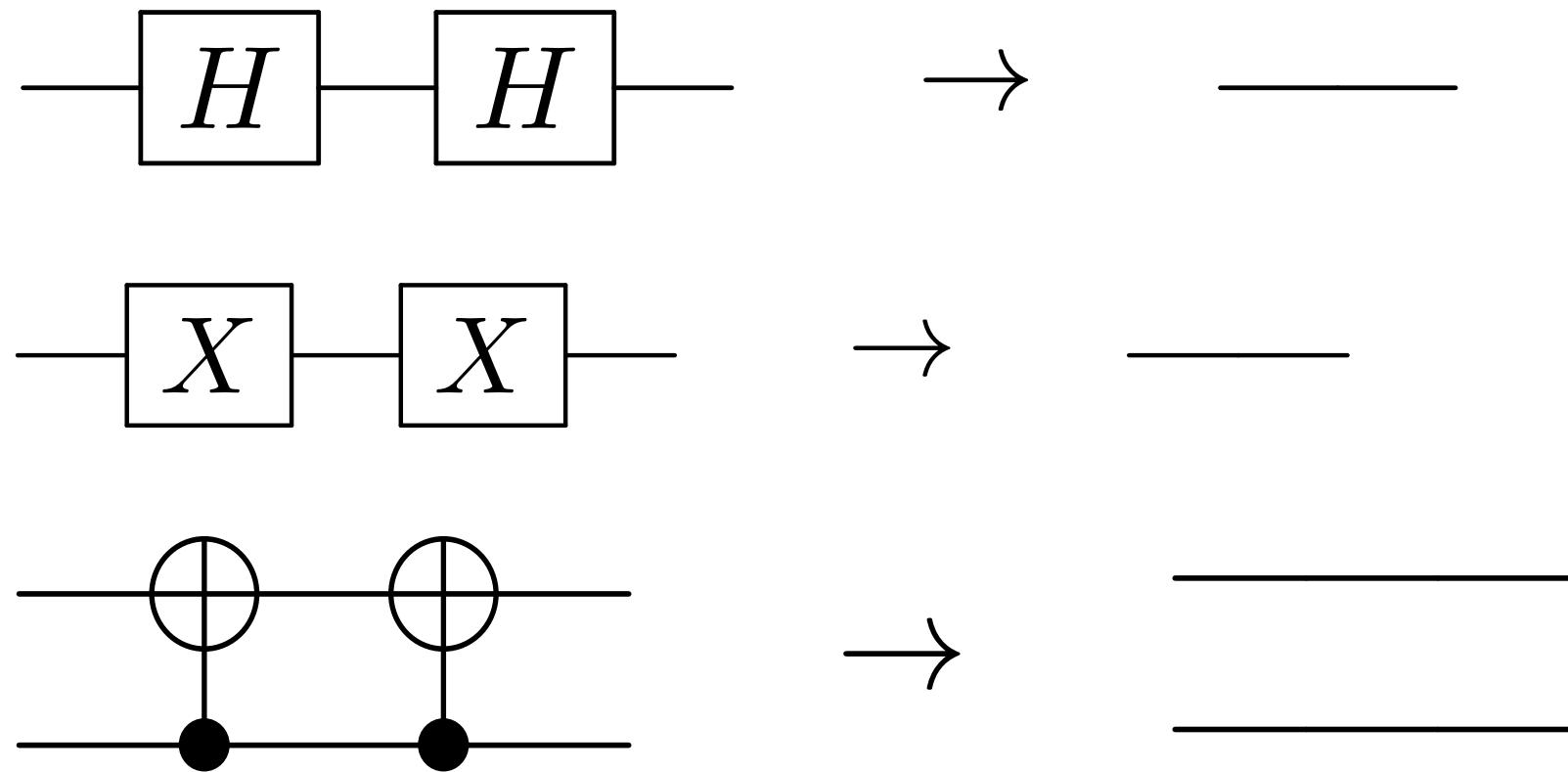
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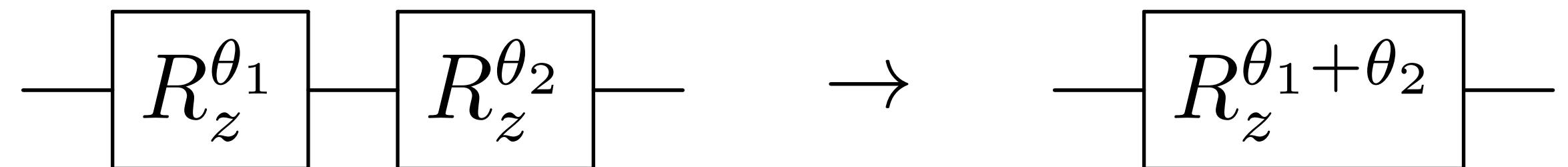
Merge

Simple Rewrite Rules

Cancel

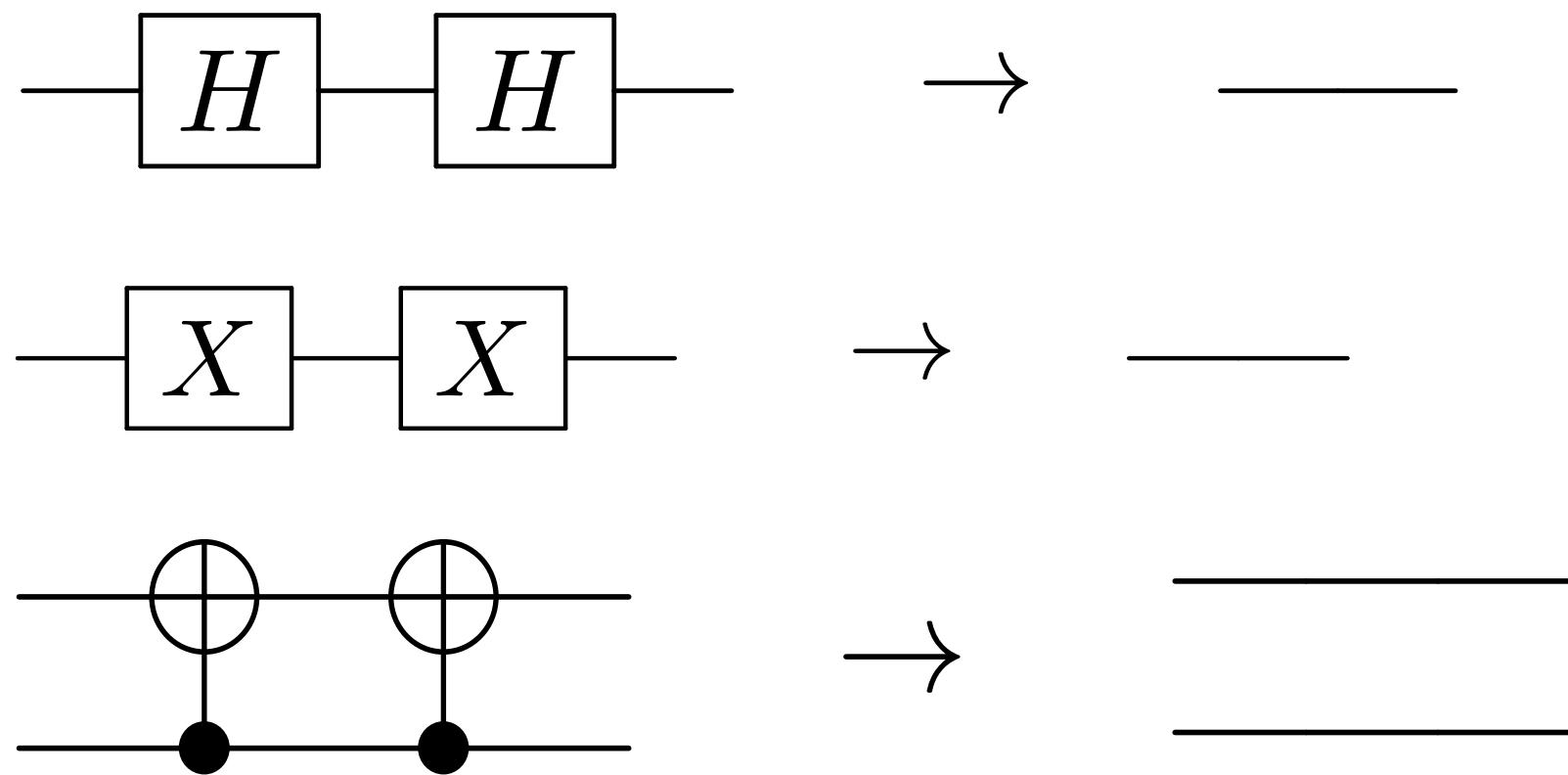


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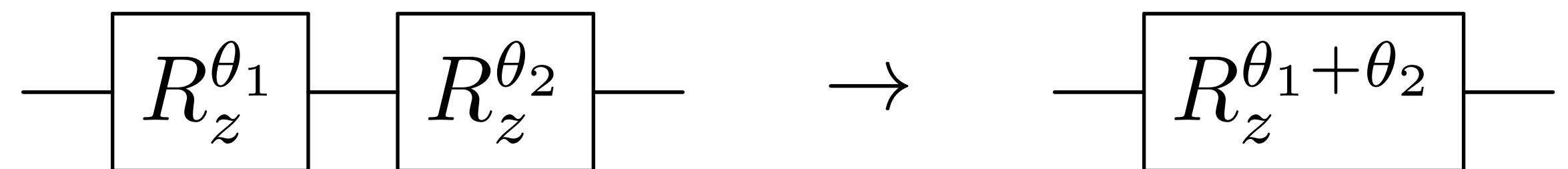


Simple Rewrite Rules

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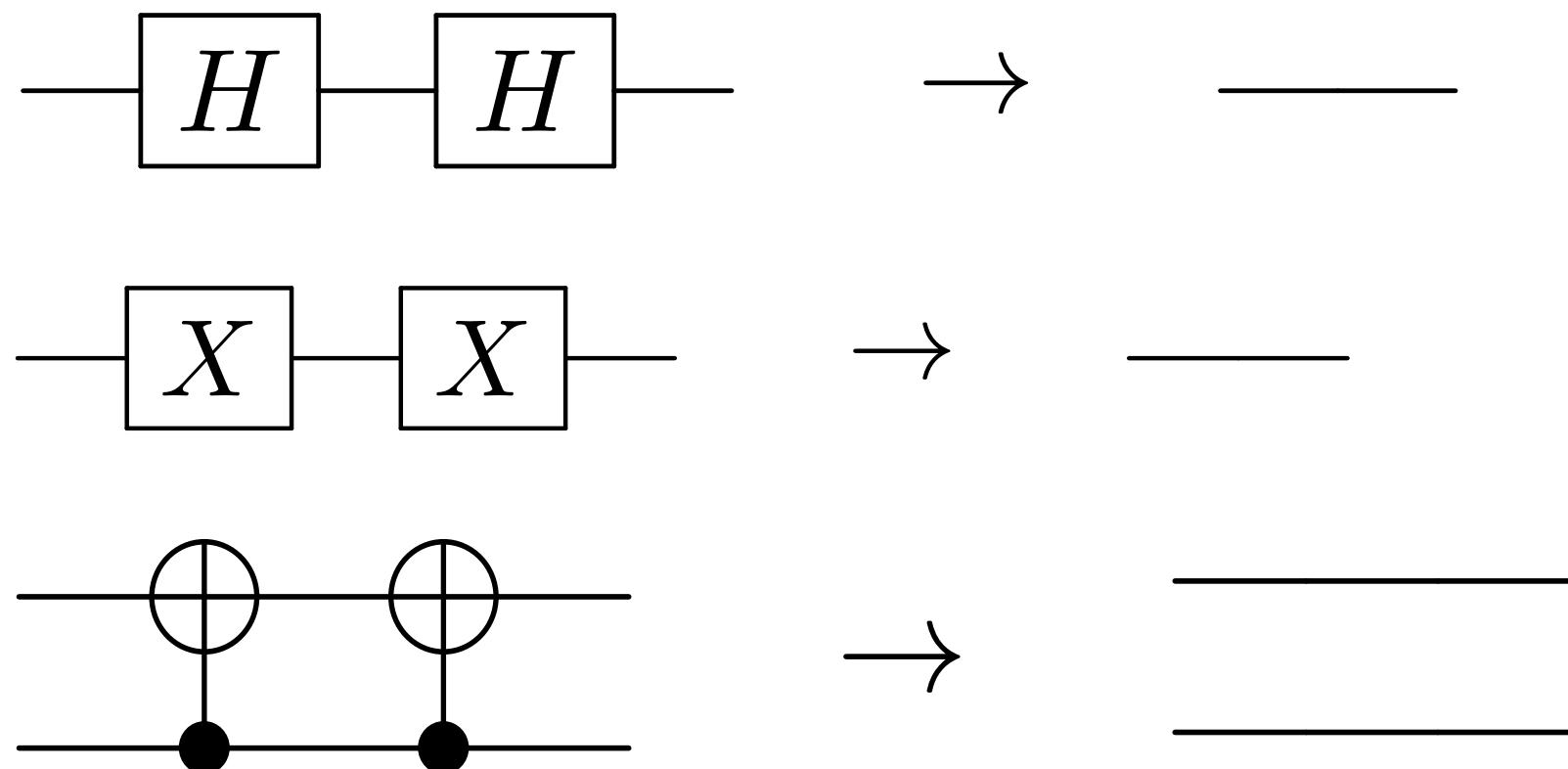
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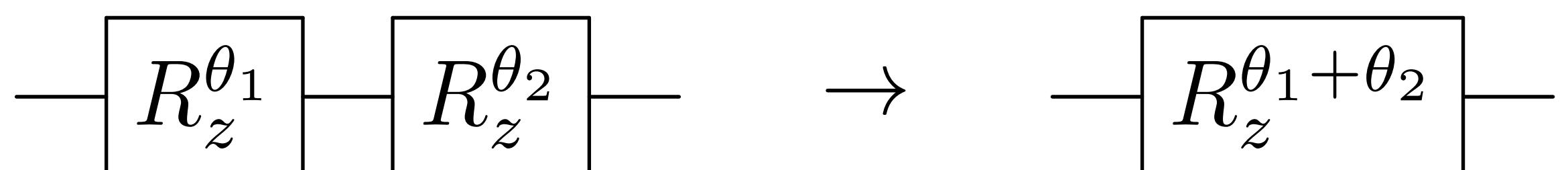
Commute

Simple Rewrite Rules

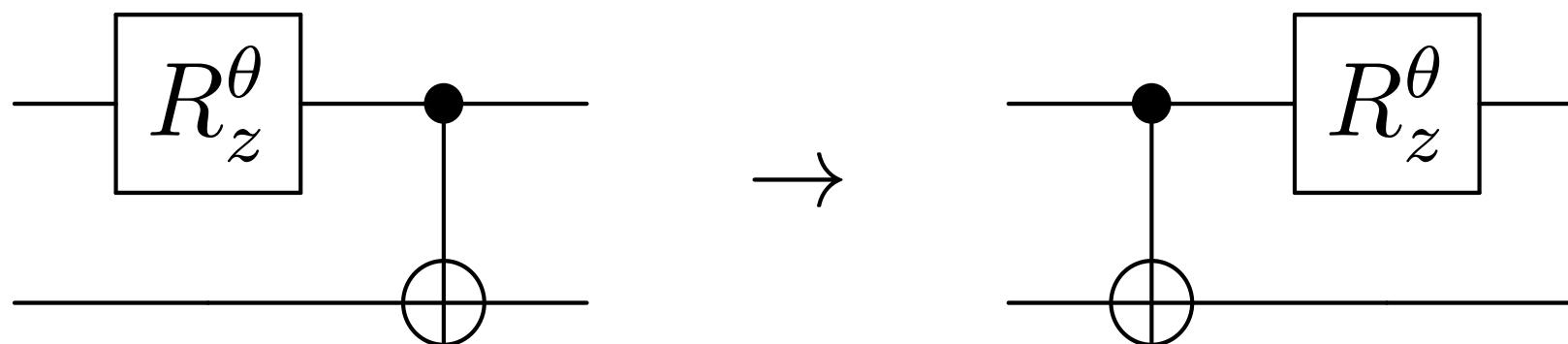
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Merge

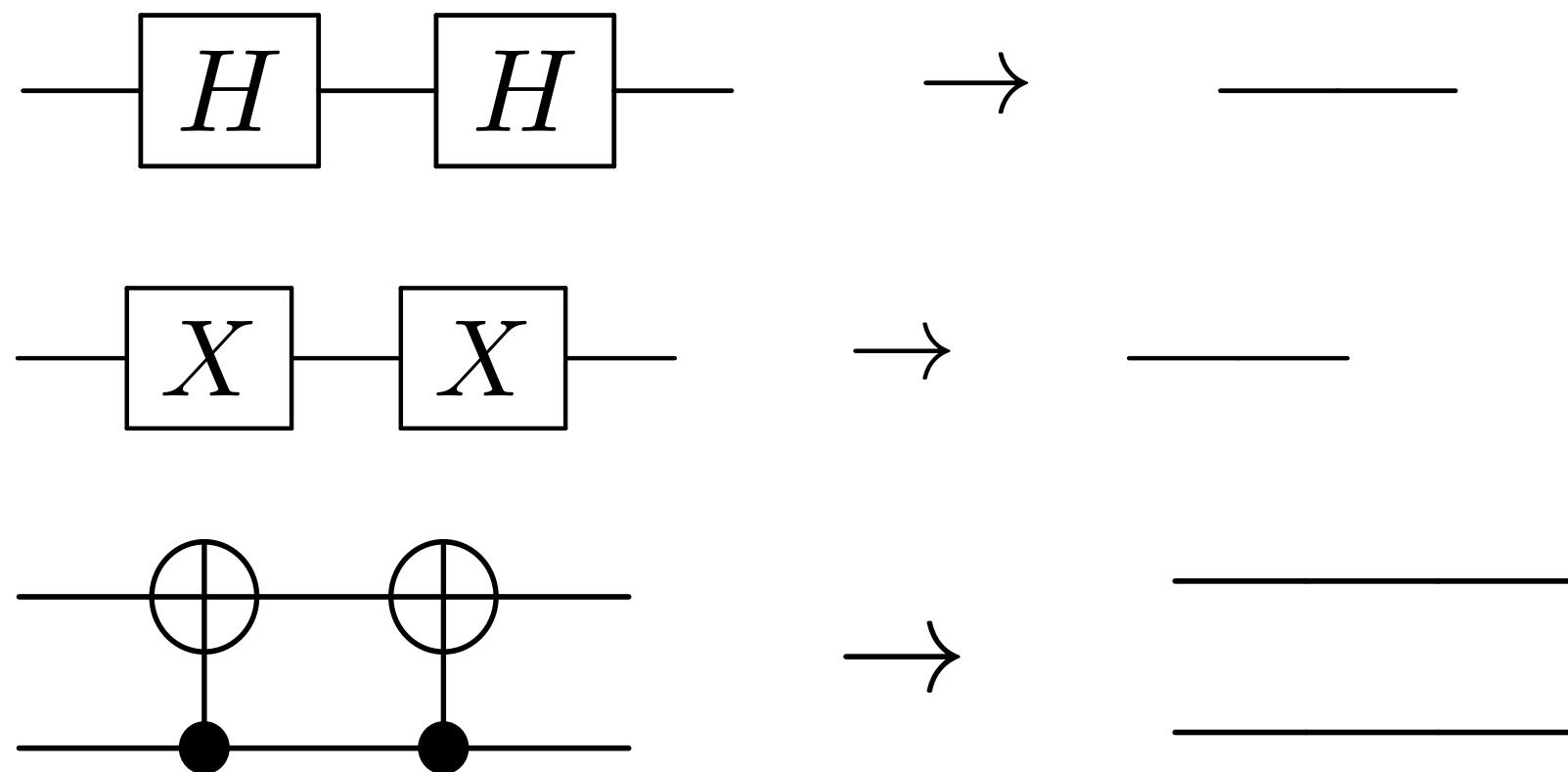


Commute

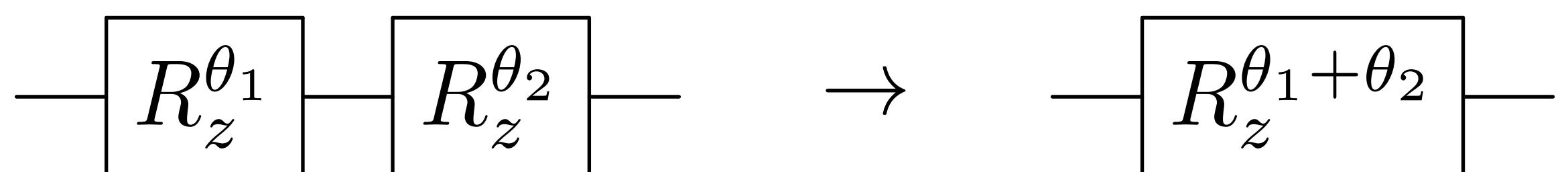


Simple Rewrite Rules

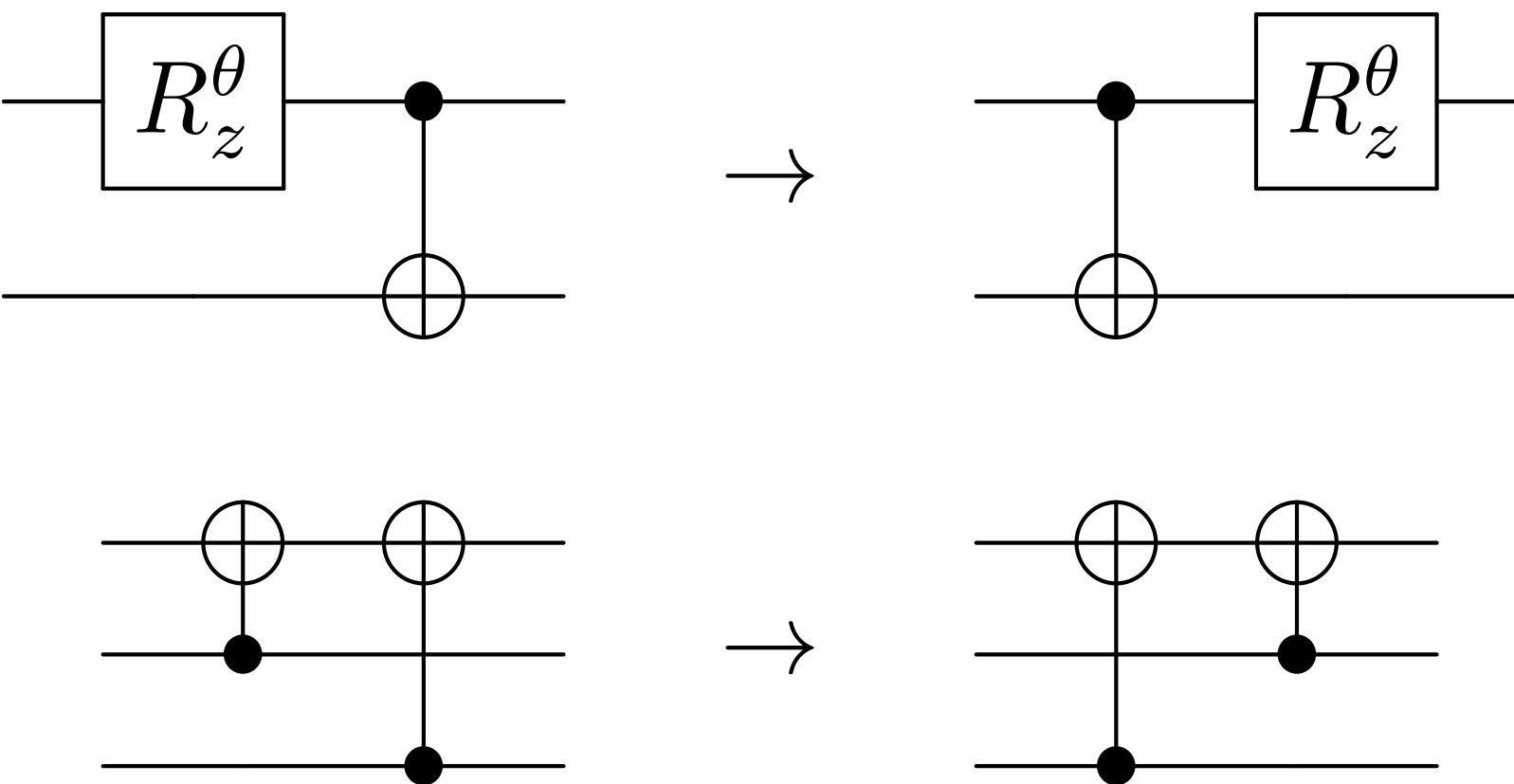
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Merge

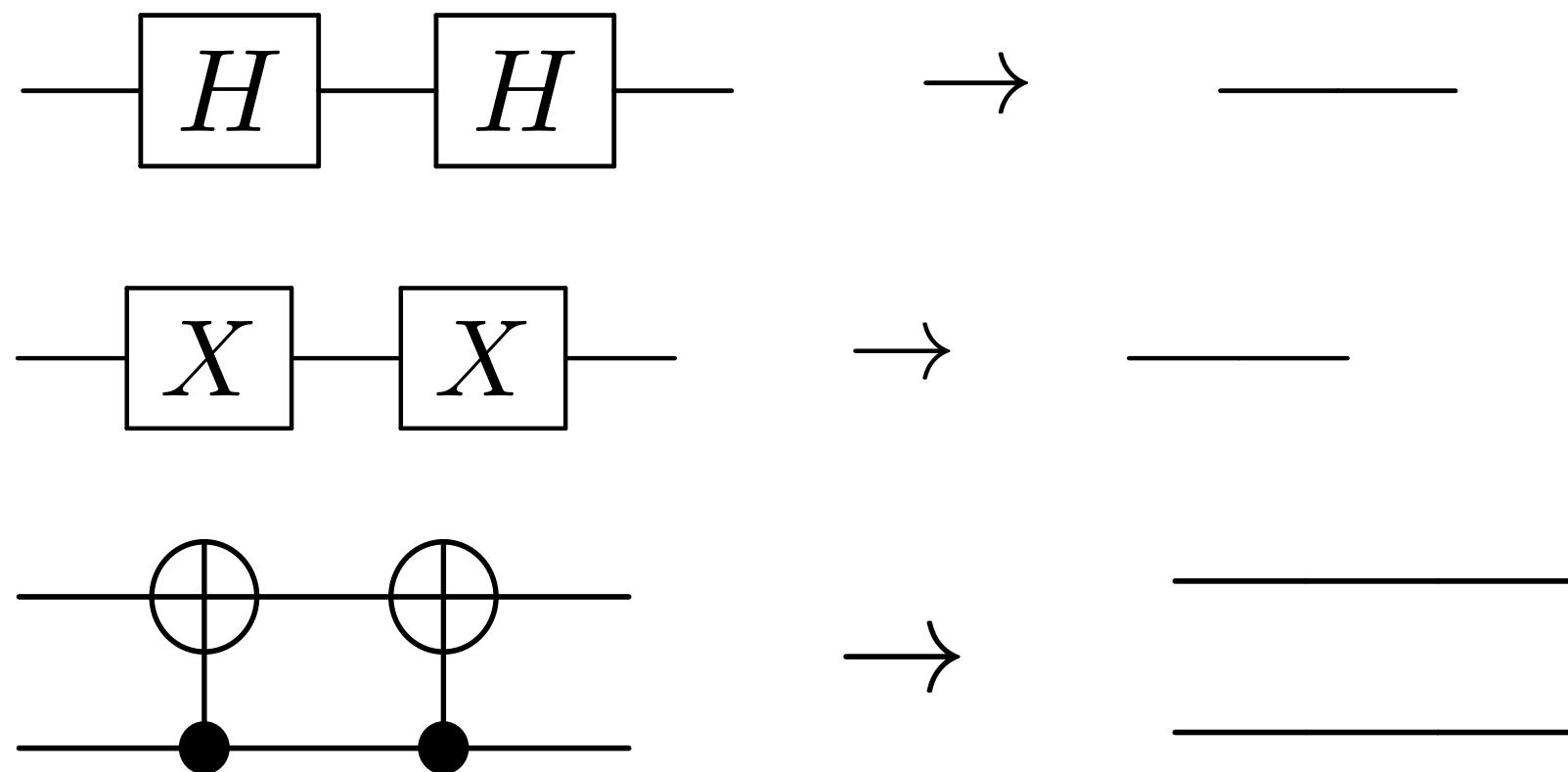


Commute

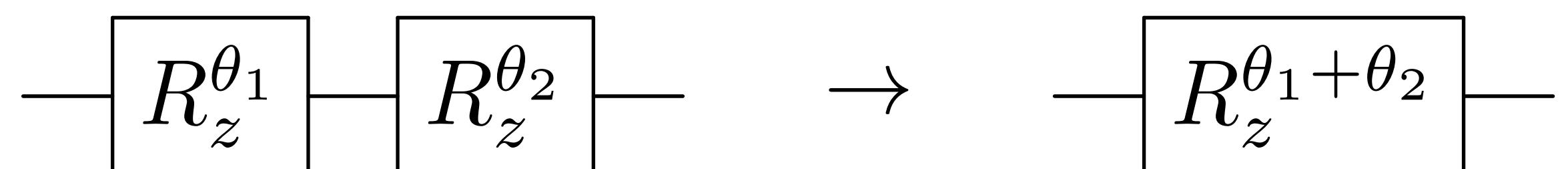


Simple Rewrite Rules

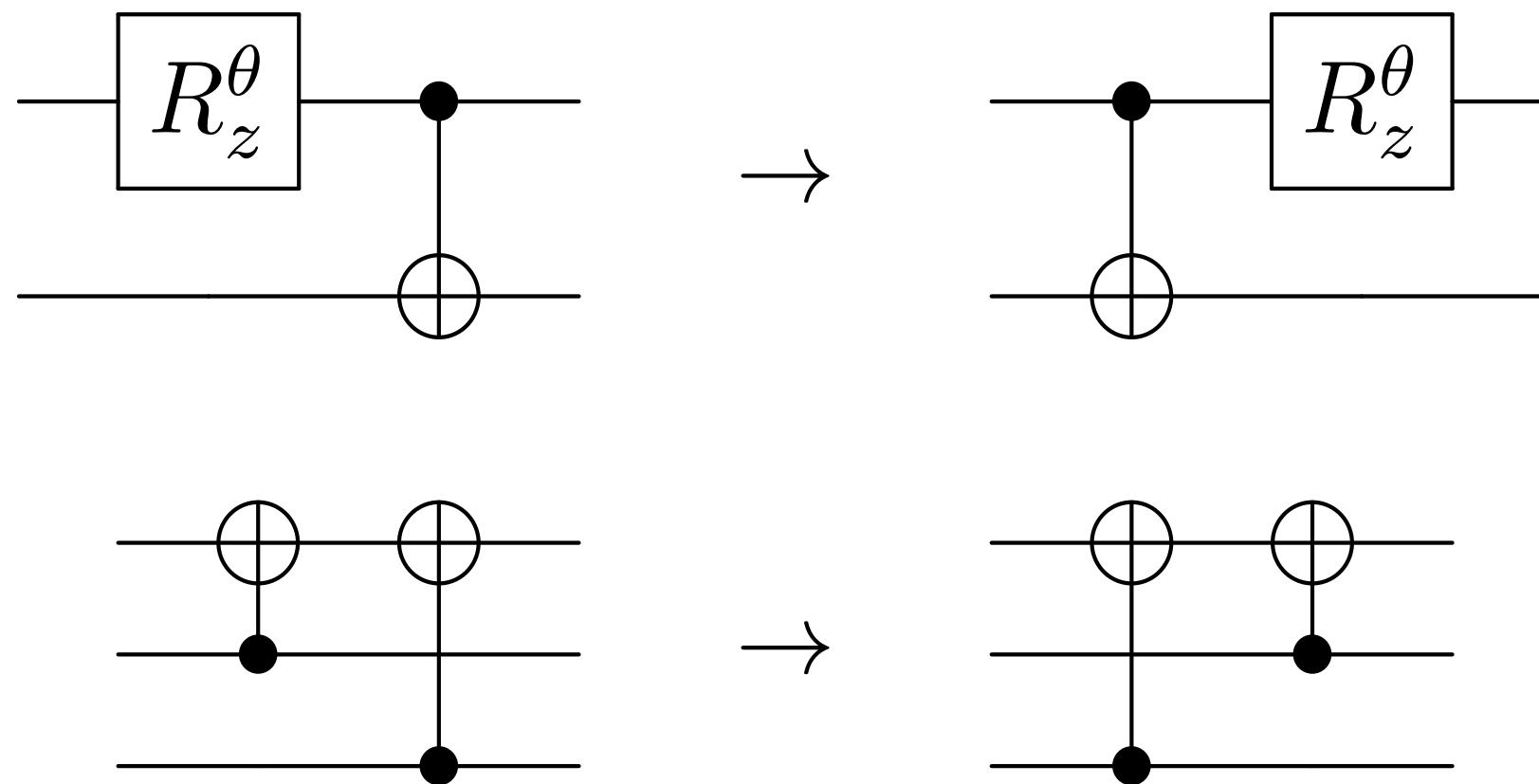
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Merge



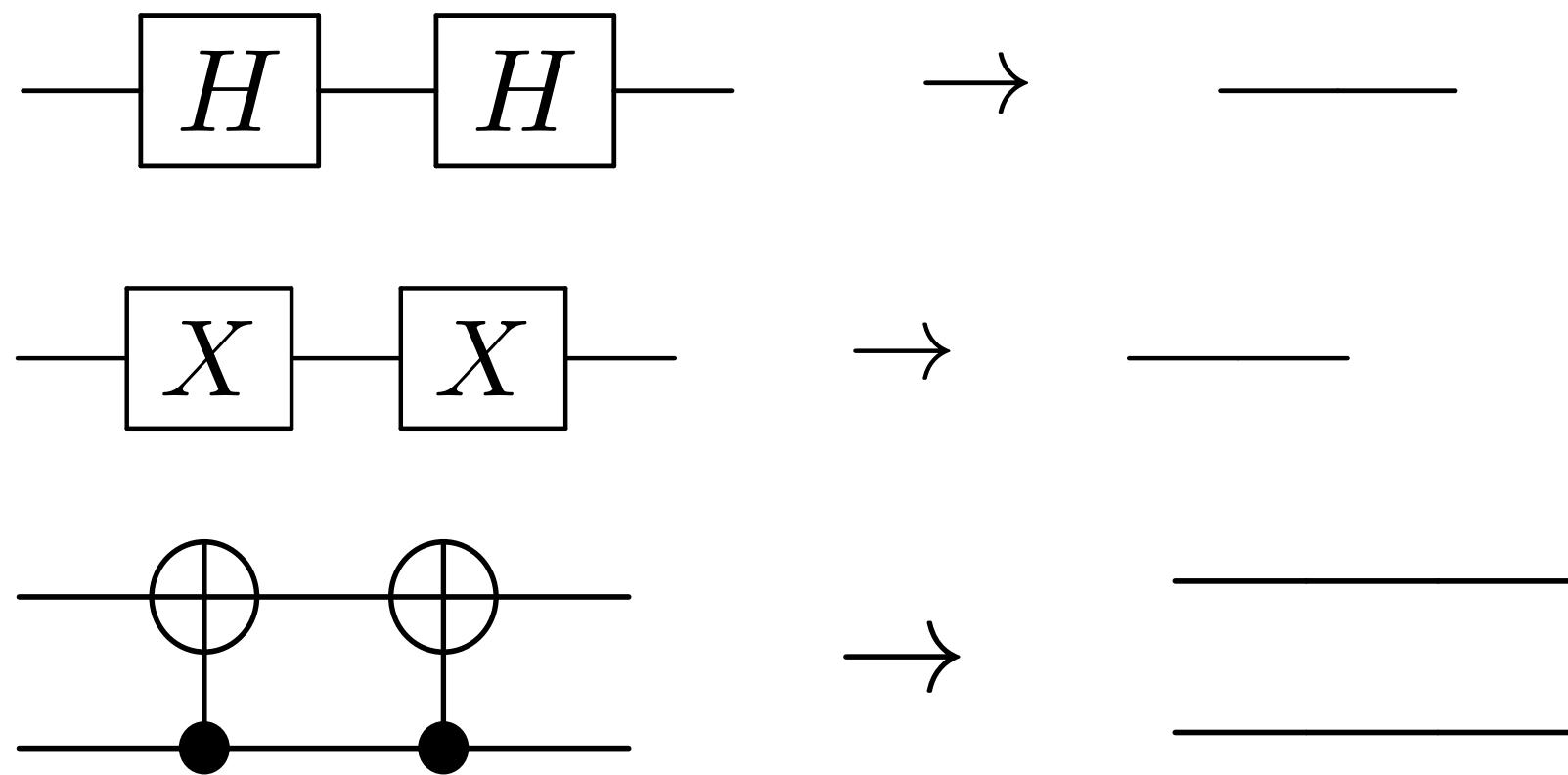
Commute



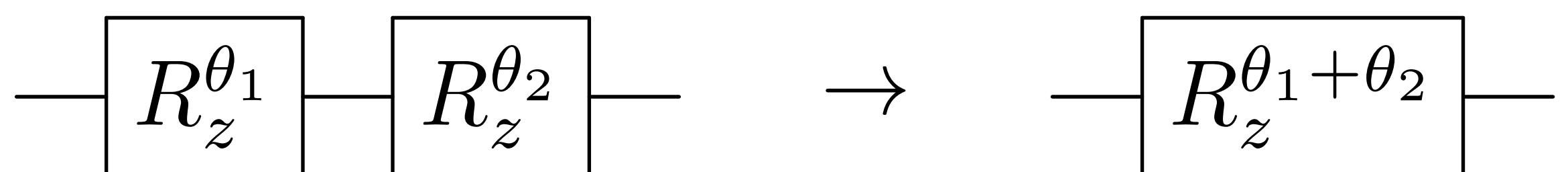
Global Phase

Simple Rewrite Rules

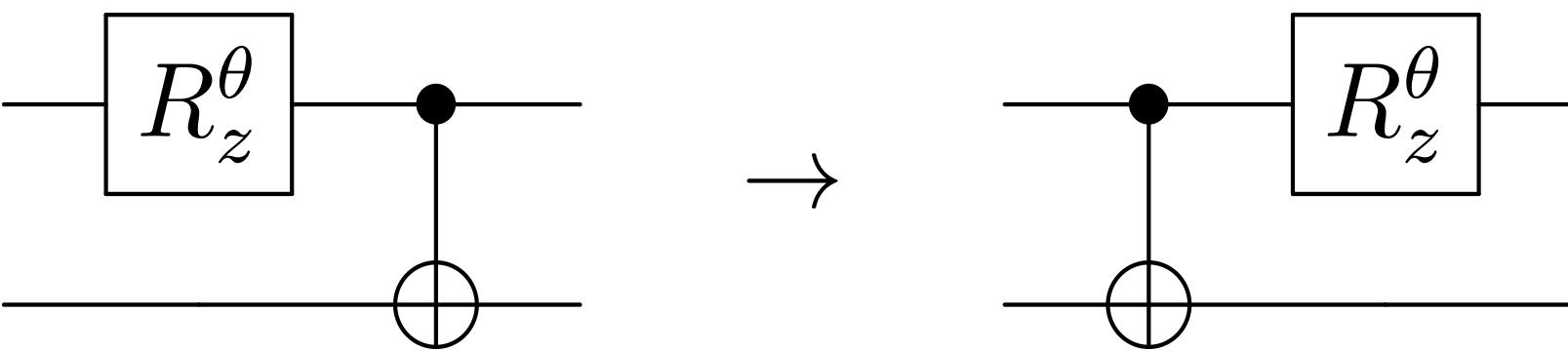
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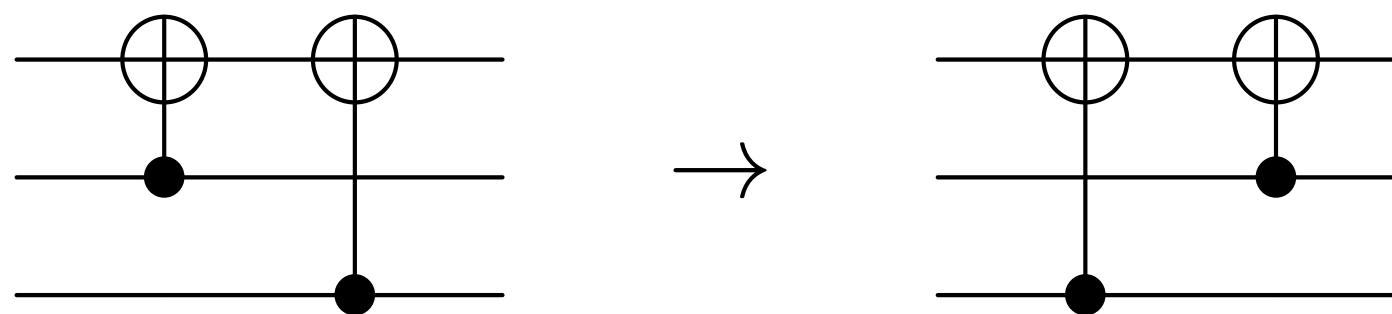
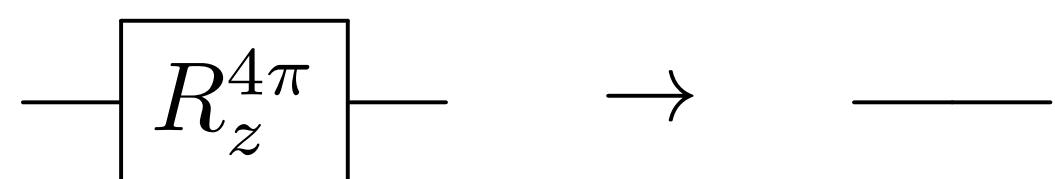
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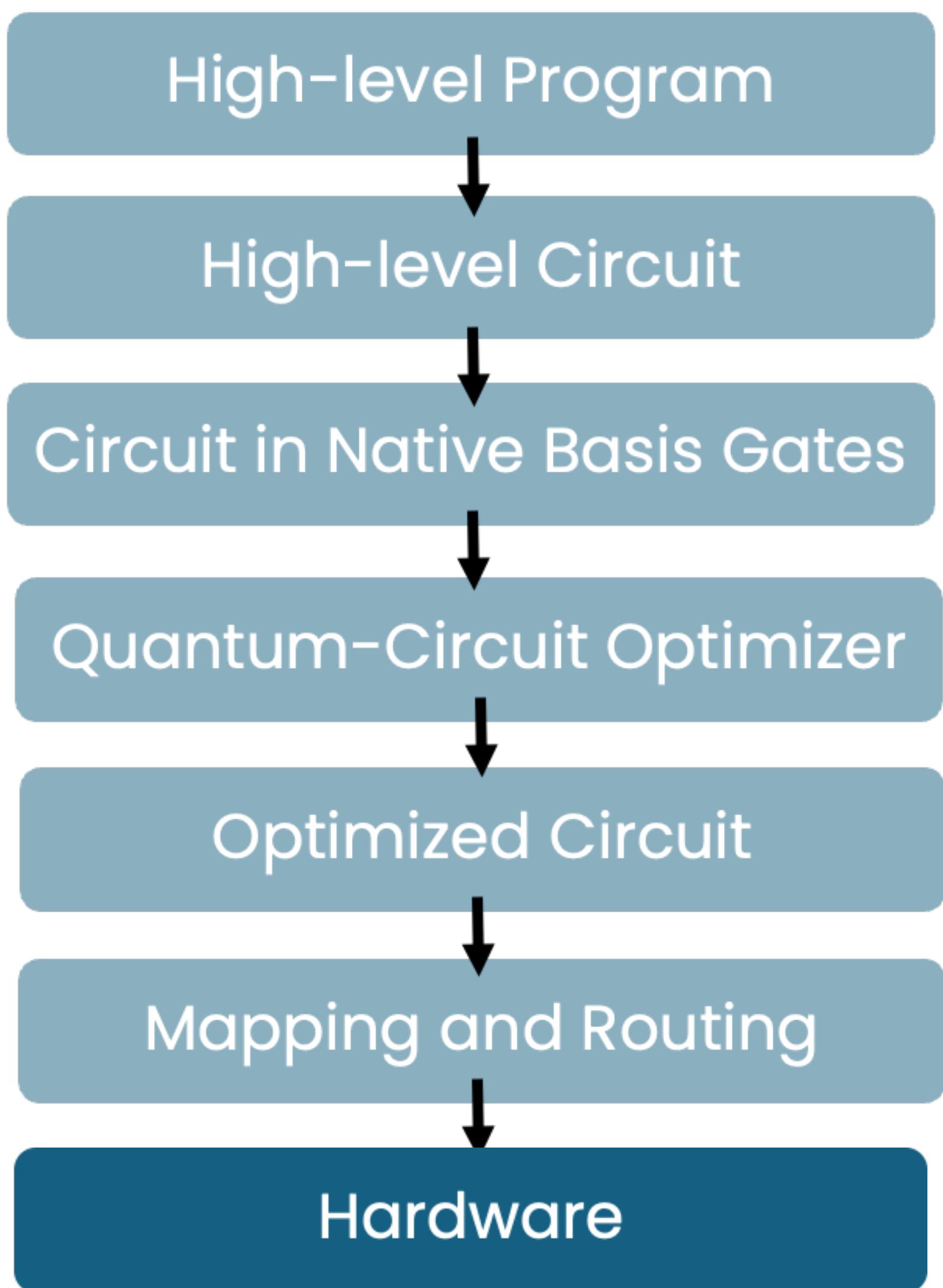
Commute



Global Phase



Diverse Hardware



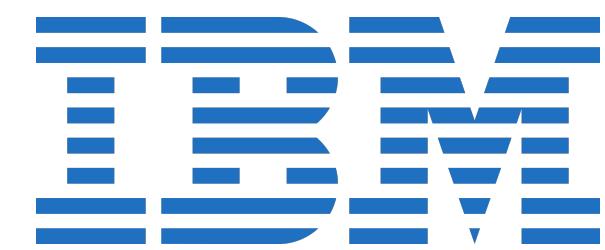
Diverse Hardware

Hardware

Superconducting



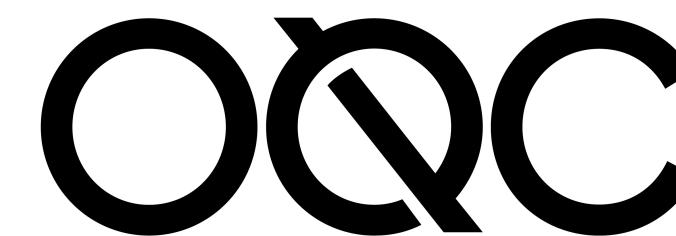
Google
Quantum AI



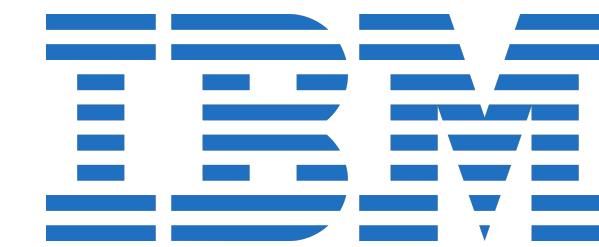
Diverse Hardware

Hardware

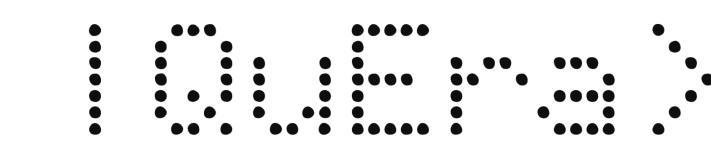
Superconducting



Google
Quantum AI



Neutral Atom



Diverse Hardware

Ion Trap



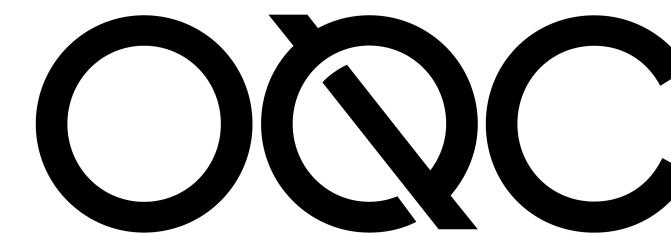
IONQ



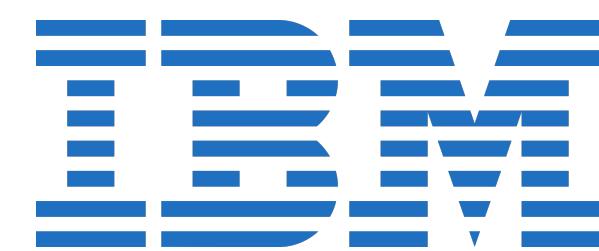
QUANTINUUM

Hardware

Superconducting



Google
Quantum AI



Neutral Atom



COMPUTING INC.

Diverse Hardware

Ion Trap



The image shows the Microsoft Majorana 1 quantum processor chip, a red and gold printed circuit board with various electronic components and a central square chip labeled "Majorana 1". Above the image are the logos for IONQ (orange hexagon) and Neutral Atom (black stylized letter Q). To the right of the image is a news article snippet.

News • February 19, 2025 • 7 min read

Microsoft unveils Majorana 1, the world's first quantum processor powered by topological qubits

by [Chetan Nayak](#), Technical Fellow and Corporate Vice President of Quantum Hardware

rigetti

OQC

Neutral Atom

IQuEra

COMPUTING INC.



**Google
Quantum AI**

IBM

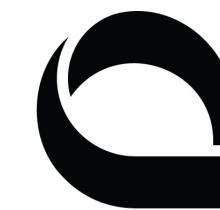
aws

Diverse Hardware

Ion Trap

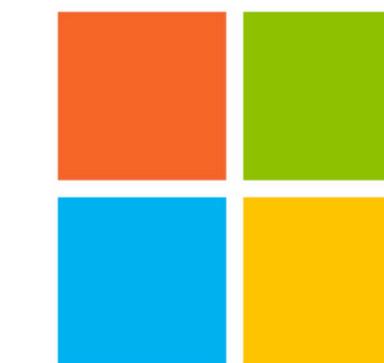


IONQ



QUANTINUUM

Topological



Hardware

Superconducting

rigetti

OQC



Google
Quantum AI

IBM

aws

Neutral Atom

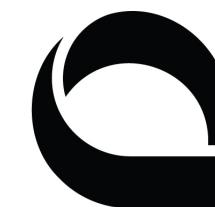
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Diverse Hardware

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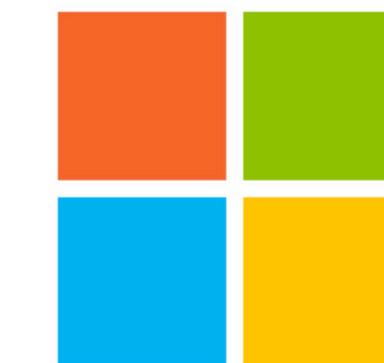


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QUANTINUUM

Topological

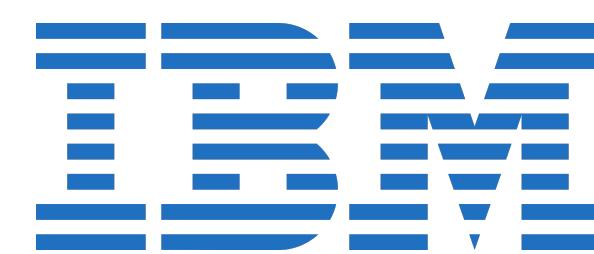


Hardware

Superconducting



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U1, U2, U3, CX



Neutral Atom



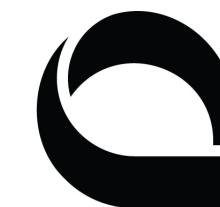
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Diverse Hardware

Ion Trap



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QUANTINUUM

Topological



Hardware

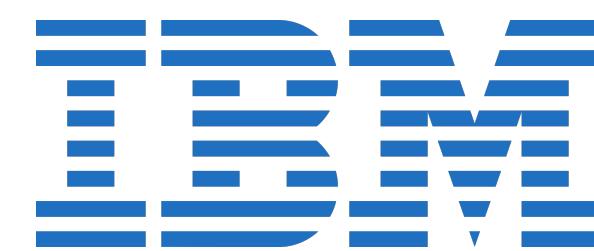
Superconducting

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~~U1, U2, U3, CX~~ → Rz, SX, X, CX

Neutral Atom

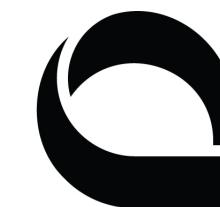
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Diverse Hardware

Ion Trap

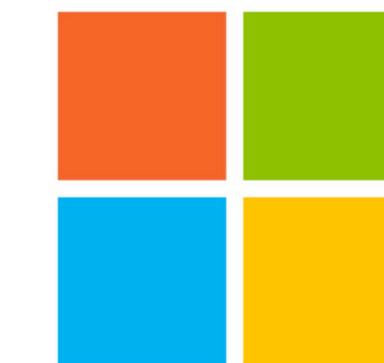


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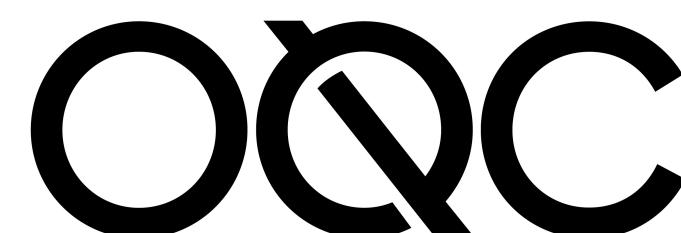
QUANTINUUM

Topological

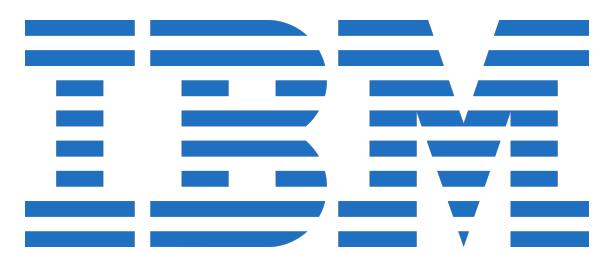


Hardware

Superconducting



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U1, U2, U3, CX

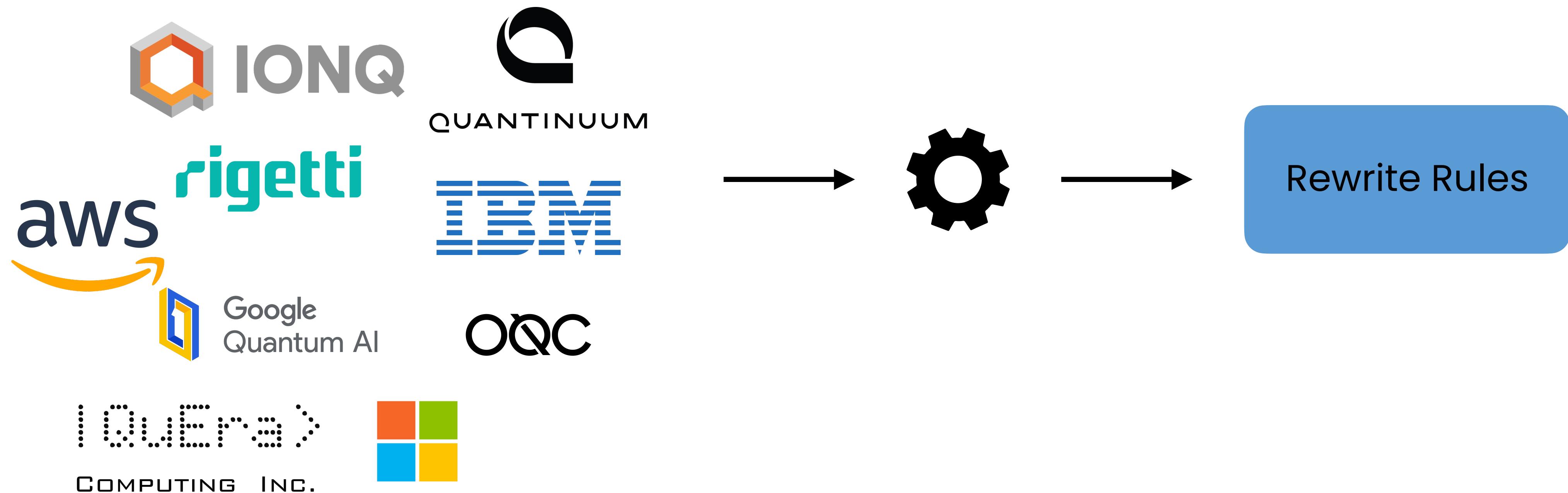
\rightarrow Rz, SX, X, CX

\rightarrow Rz, Rx, SX, X, CZ, Rzz



Synthesis!

Arbitrary gate set



Naïve Synthesis

```
rules = []
```

Naïve Synthesis

```
rules = []
circuits = enumerate(max_qubits, max_size)
```

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```
rules = []
circuits = enumerate(max_qubits, max_size)
for c1 in circuits:
    for c2 in circuits:
```

Naïve Synthesis

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rules = []
circuits = enumerate(max_qubits, max_size)
for c1 in circuits:
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        if verify(c1, c2):
            rules.append(c1 → c2)
```

Naïve Synthesis

```
rules = []
circuits = enumerate(max_qubits, max_size)
for c1 in circuits:
    for c2 in circuits:
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            rules.append(c1 → c2)
```

Naïve Synthesis

```
rules = []
circuitsbig = enumerate(max_qubits, max_size)
for c1 in circuits:
    for c2 in circuits:
        if verify(c1, c2):
            rules.append(c1 → c2)
expensive
```

Naïve Synthesis

```
rules = []
circuits = enumerate(max_qubits, max_size)
for c1 in circuits:
    for c2 in circuits:
        if verify(c1, c2):
            rules.append(c1 → c2)
```

expensive

big

uh oh...

QUESO

Synthesizing Quantum-Circuit Optimizers

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LAUREN PICK, University of Wisconsin-Madison, USA

SWAMIT TANNU, University of Wisconsin-Madison, USA

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QUESO

(Symbolic)
Circuit

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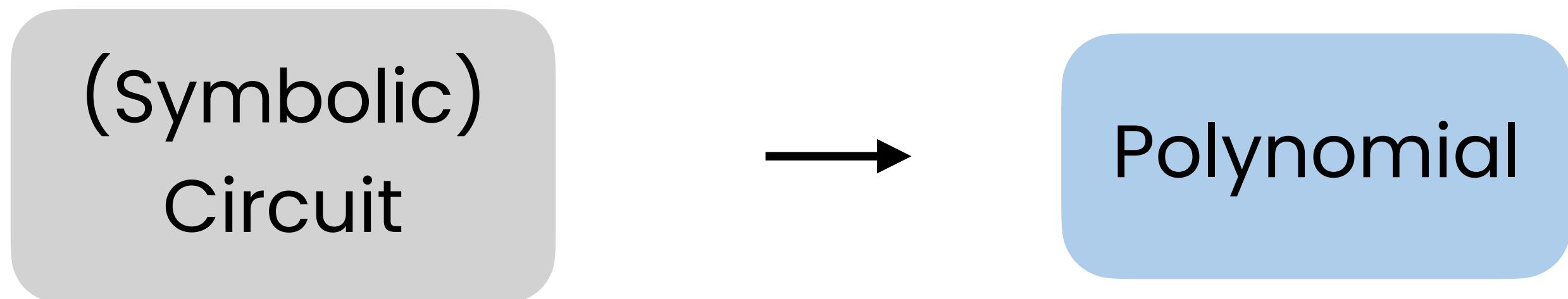
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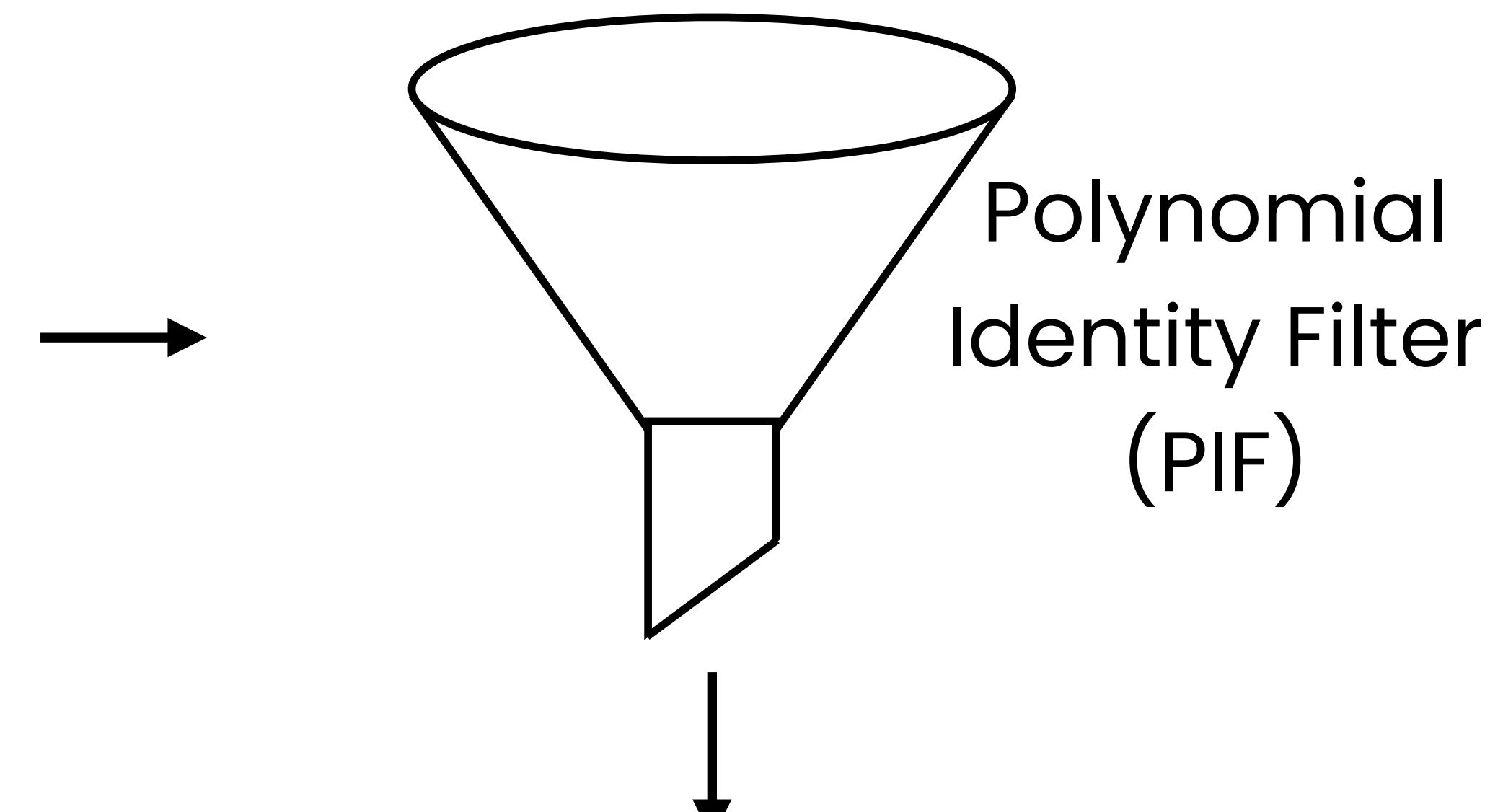
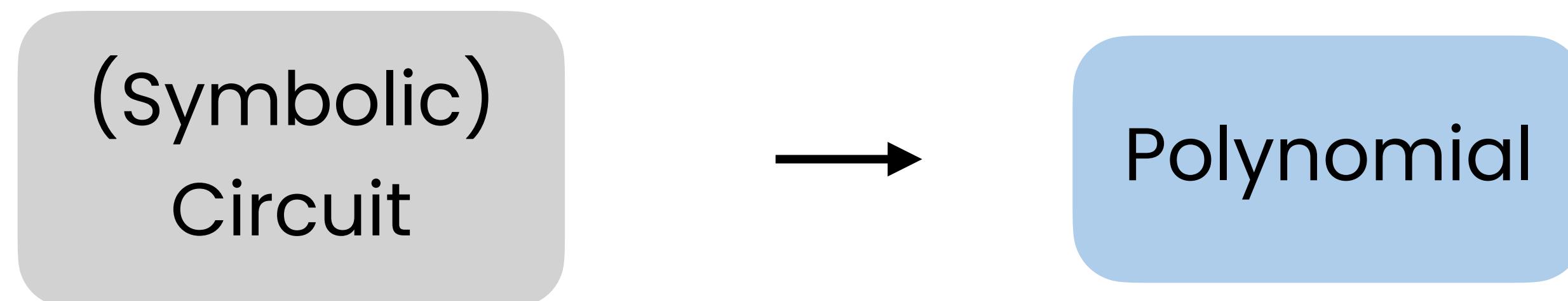
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Synthesizing Quantum-Circuit Optimizers

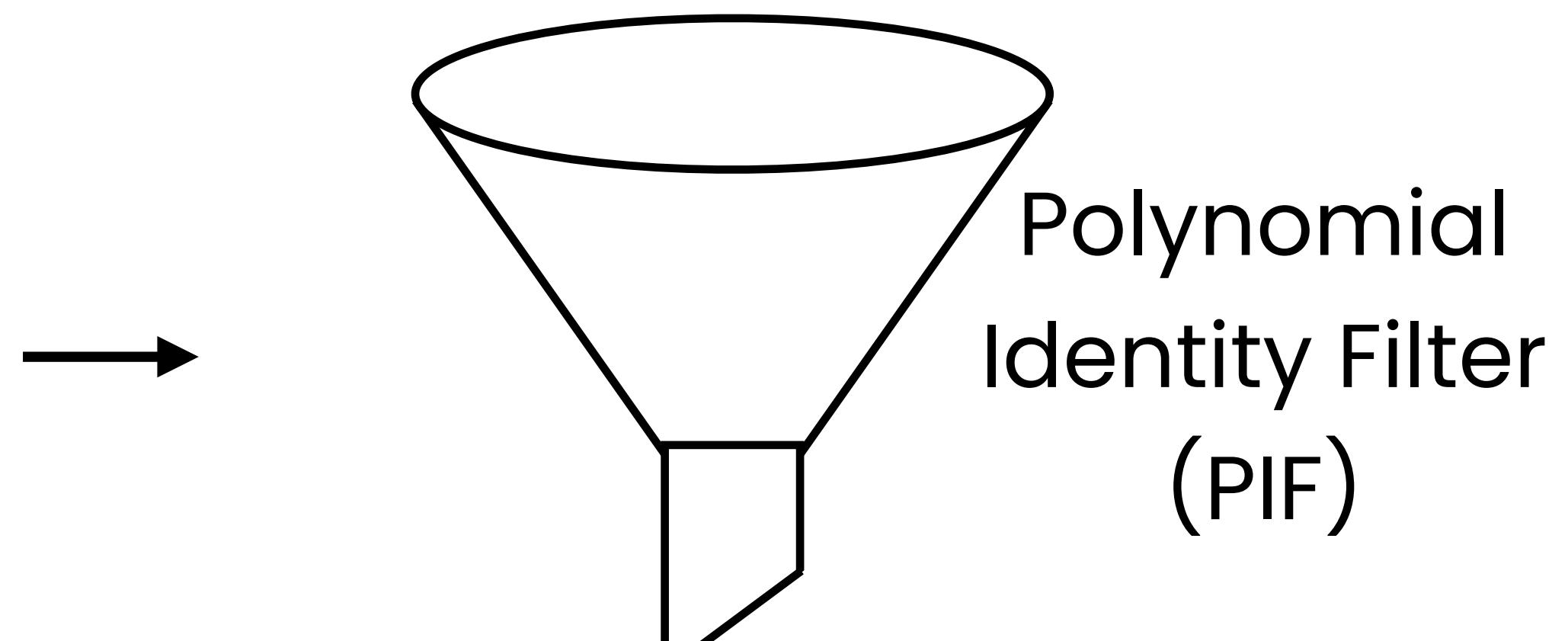
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Circuit Equivalence
Classes

QUESO

(Symbolic)
Circuit



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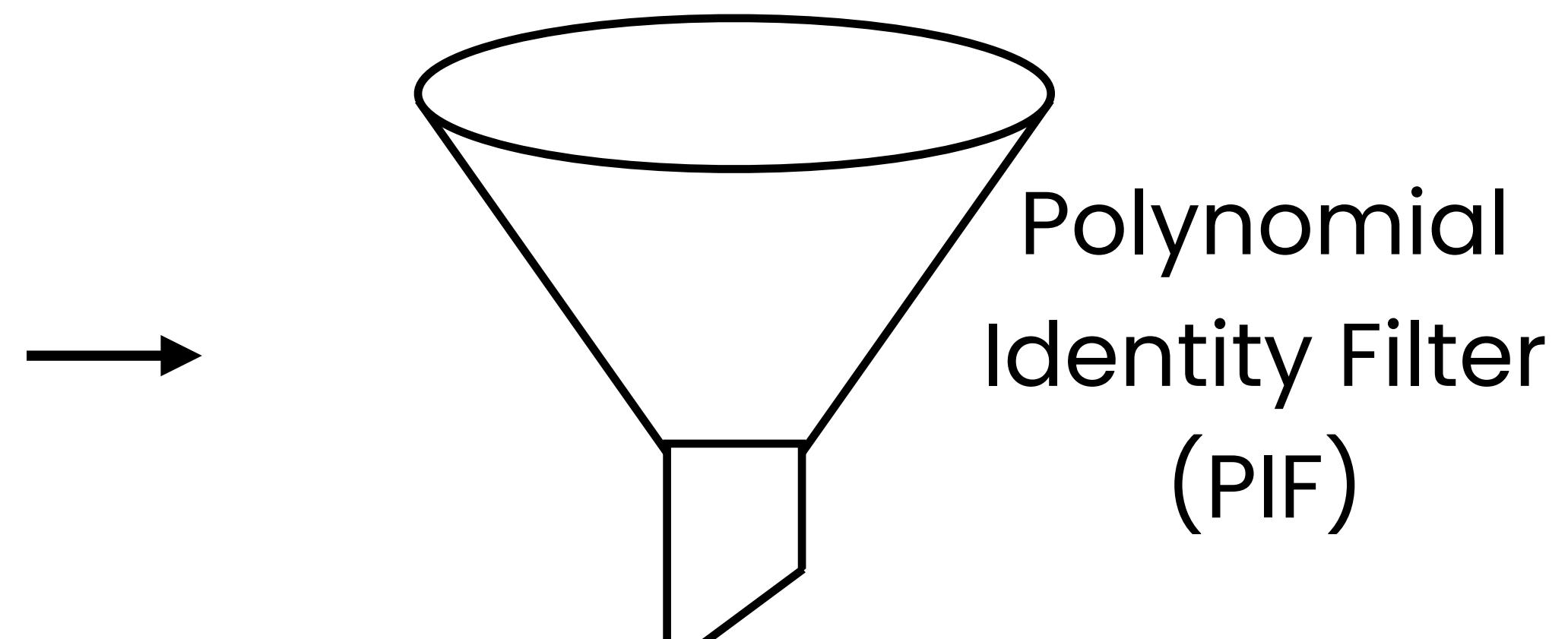
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Polynomial
Circuit Equivalence
Classes

QUESO

Feynman's path integral formulation
for quantum mechanics

(Symbolic)
Circuit



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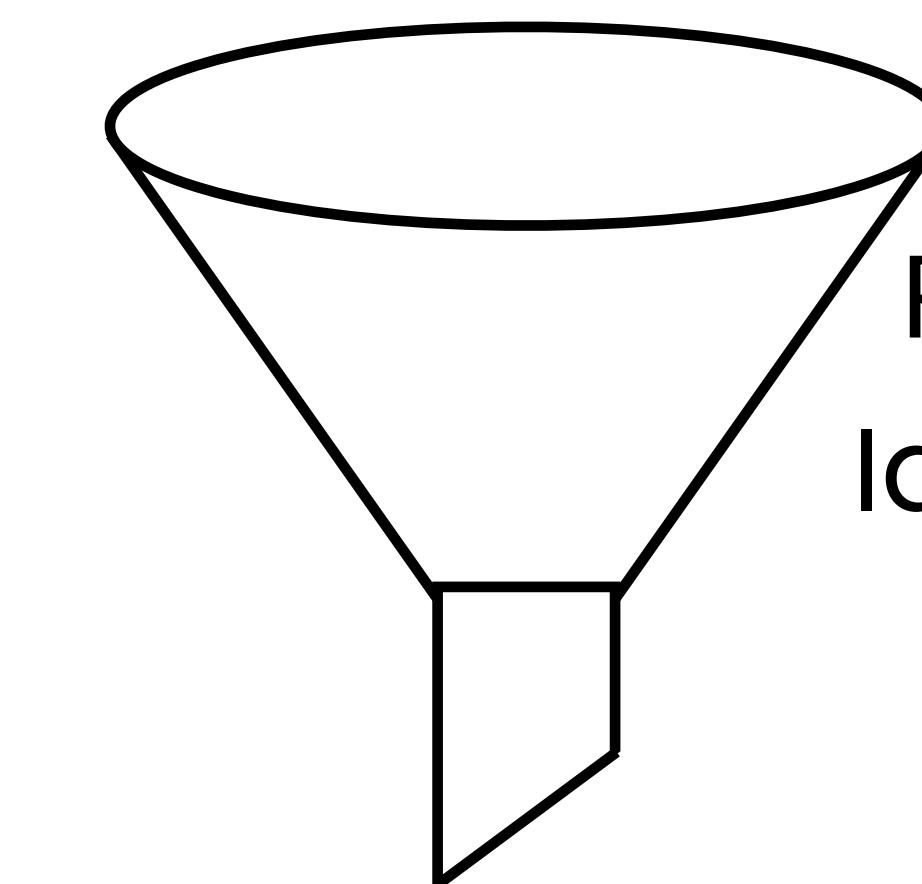
Polynomial
Circuit Equivalence
Classes

QUESO

Schwartz-Zippel Lemma for
polynomial identity testing (PIT)

Feynman's path integral formulation
for quantum mechanics

(symbolic)
Circuit



Polynomial
Identity Filter
(PIF)

Polynomial
Circuit Equivalence
Classes

Synthesizing Quantum-Circuit Optimizers

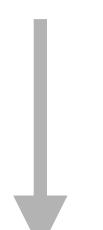
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QUESO

Feynman's path integral formulation
for quantum mechanics

(Symbolic)
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Synthesizing Quantum-Circuit Optimizers

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Path-sum Semantics

Algebraically represent semantics of quantum gates

$$R_z(\theta) : |x\rangle \rightarrow e^{i(2x-1)\theta} |x\rangle$$

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$$CNOT : |x_1 x_2\rangle \rightarrow |x_1(x_1 \oplus x_2)\rangle$$

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More generally: $|\mathbf{x}\rangle \rightarrow \phi(\mathbf{x}, \rho) |f(\mathbf{x})\rangle$

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Algebraically represent semantics of quantum gates

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$$CNOT : |x_1 x_2\rangle \rightarrow |x_1(x_1 \oplus x_2)\rangle$$

More generally: $|x\rangle \rightarrow \phi(x, \rho) |f(x)\rangle$

amplitude state

Simple Circuit as Polynomial

Circuit

$R_z(\theta) \quad q_1 ;$

Polynomial

$e^{i(2(0)-1)\theta} |0\rangle$

Simple Circuit as Polynomial

Circuit

$R_z(\theta) \quad q_1 ;$

Polynomial

$e^{-i\theta} |0\rangle$

Simple Circuit as Polynomial

Circuit

$R_z(\theta) \quad q_1 ;$

Polynomial

$e^{-i\theta}|0\rangle + e^{i\theta}|1\rangle$

Simple Circuit as Polynomial

Circuit

$R_z(\theta) \quad q_1 ;$

Polynomial

$$e^{-i\theta} |0\rangle + e^{i\theta} |1\rangle$$

QUESO

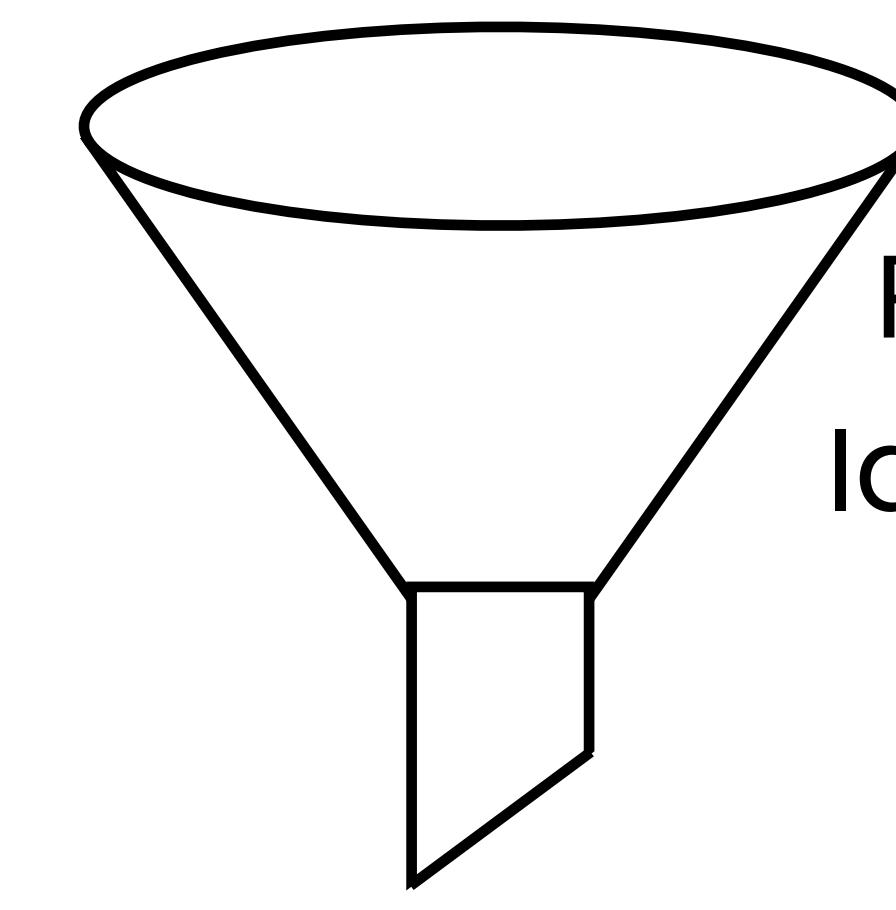
Schwartz-Zippel Lemma for
polynomial identity testing (PIT)

Feynman's path integral formulation
for quantum mechanics

(Symbolic)
Circuit



Polynomial



Polynomial
Identity Filter
(PIF)

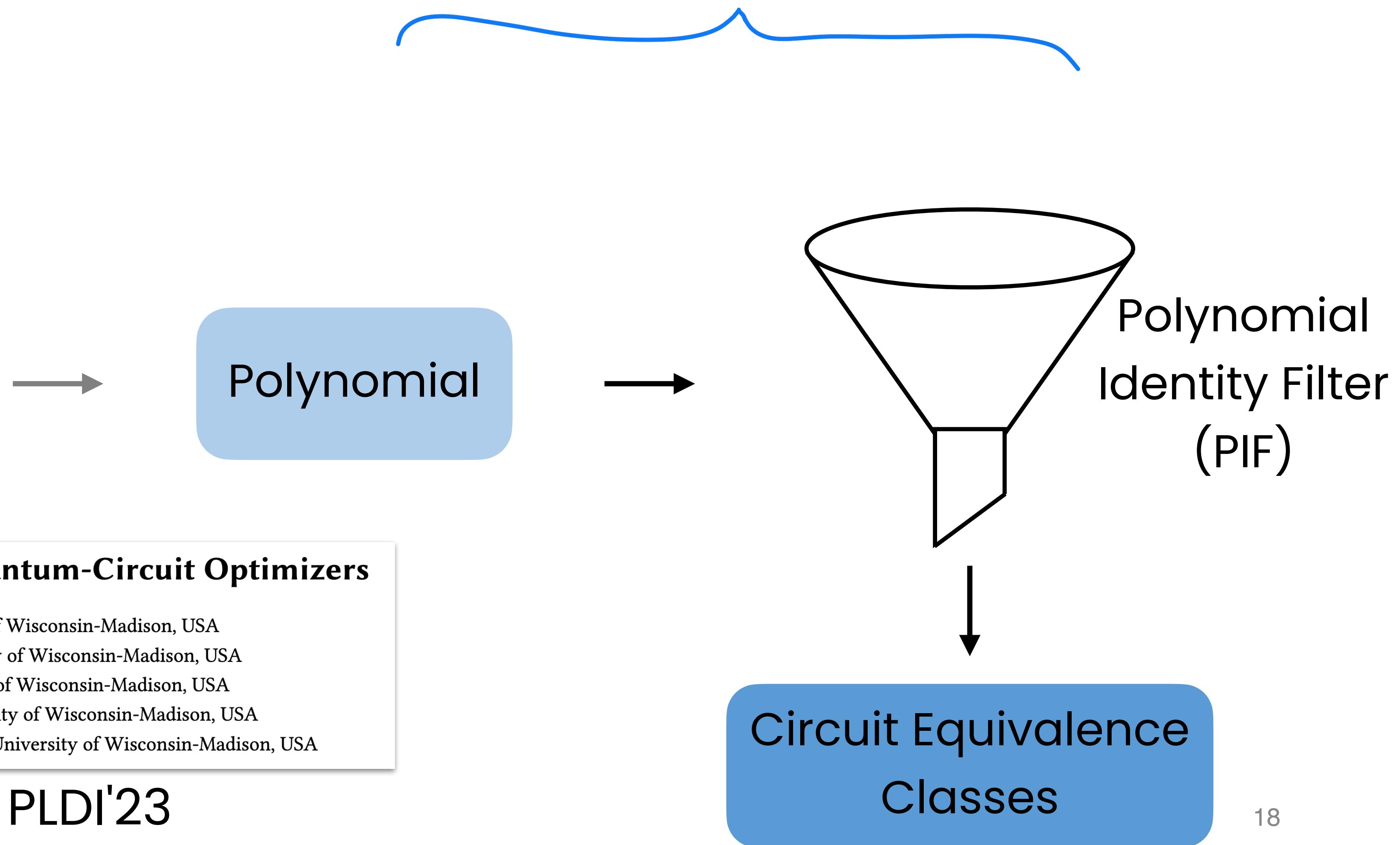
Circuit Equivalence
Classes

Synthesizing Quantum-Circuit Optimizers

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QUESO

Schwartz-Zippel Lemma for
polynomial identity testing (PIT)



Polynomial Identity Testing (PIT)

$$\mathbf{x} \in \mathbb{C}^3 \quad p_1(\mathbf{x}) = x_1 x_2$$

Polynomial Identity Testing (PIT)

$$\mathbf{x} \in \mathbb{C}^3 \quad \begin{aligned} p_1(\mathbf{x}) &= x_1x_2 \\ p_2(\mathbf{x}) &= x_1x_2 + x_3x_2 \end{aligned}$$

Polynomial Identity Testing (PIT)

$$\mathbf{x} \in \mathbb{C}^3 \quad p_1(\mathbf{x}) = x_1x_2$$
$$p_2(\mathbf{x}) = x_1x_2 + x_3x_2$$

How can we check $p_1 \equiv p_2$?

Schwartz-Zippel Lemma

$$p_1(\mathbf{x}) = x_1x_2$$

$$p_2(\mathbf{x}) = x_1x_2 + x_3x_2$$

Schwartz-Zippel Lemma

$$p_1(\mathbf{x}) = x_1x_2 \quad \text{finite } R \subset \mathbb{C}$$

$$p_2(\mathbf{x}) = x_1x_2 + x_3x_2$$

Schwartz-Zippel Lemma

$$p_1(\mathbf{x}) = x_1 x_2$$

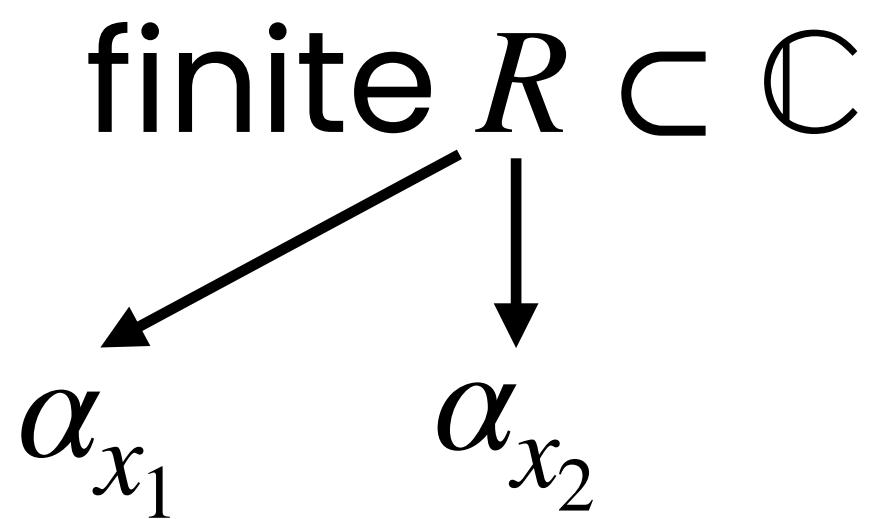
$$p_2(\mathbf{x}) = x_1 x_2 + x_3 x_2$$

$$\alpha_{x_1} \xrightarrow{\text{finite } R \subset \mathbb{C}}$$

Schwartz-Zippel Lemma

$$p_1(\mathbf{x}) = x_1 x_2$$

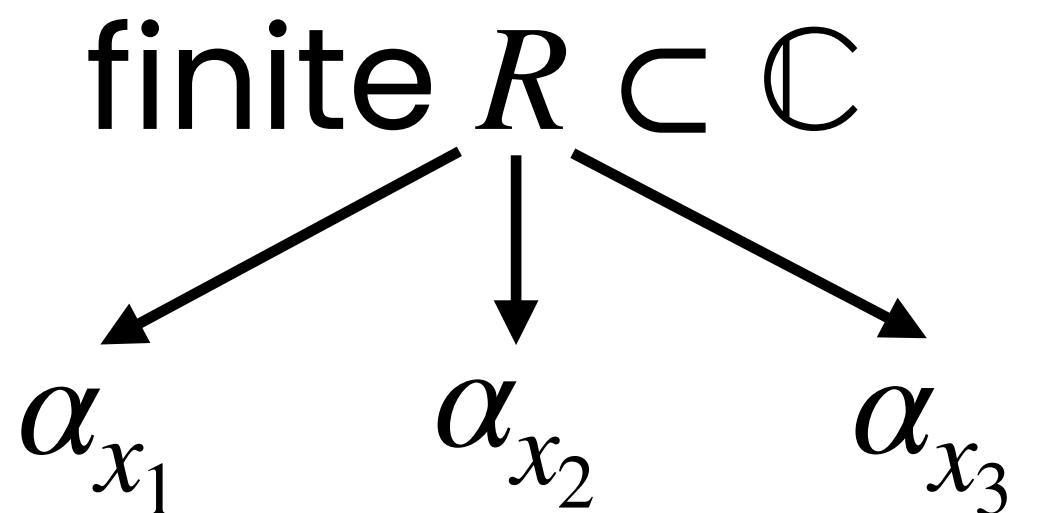
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Schwartz-Zippel Lemma

$$p_1(\mathbf{x}) = x_1 x_2$$

$$p_2(\mathbf{x}) = x_1 x_2 + x_3 x_2$$



Schwartz-Zippel Lemma

$$p_1(\alpha) = \alpha_{x_1} \alpha_{x_2}$$

$$p_2(\alpha) = \alpha_{x_1} \alpha_{x_2} + \alpha_{x_3} \alpha_{x_2}$$

Schwartz-Zippel Lemma

$$p_1(\alpha) = \alpha_{x_1} \alpha_{x_2}$$

$$p_2(\alpha) = \alpha_{x_1} \alpha_{x_2} + \alpha_{x_3} \alpha_{x_2}$$

Check $p_1(\alpha) = p_2(\alpha)$

Schwartz-Zippel Lemma

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$$p_2(\alpha) = \alpha_{x_1} \alpha_{x_2} + \alpha_{x_3} \alpha_{x_2}$$

Check $p_1(\alpha) = p_2(\alpha)$

If $p_1 \neq p_2$, then the probability that $p_1(\alpha) = p_2(\alpha)$ is at most $\frac{d}{|R|}$ (max total degree)

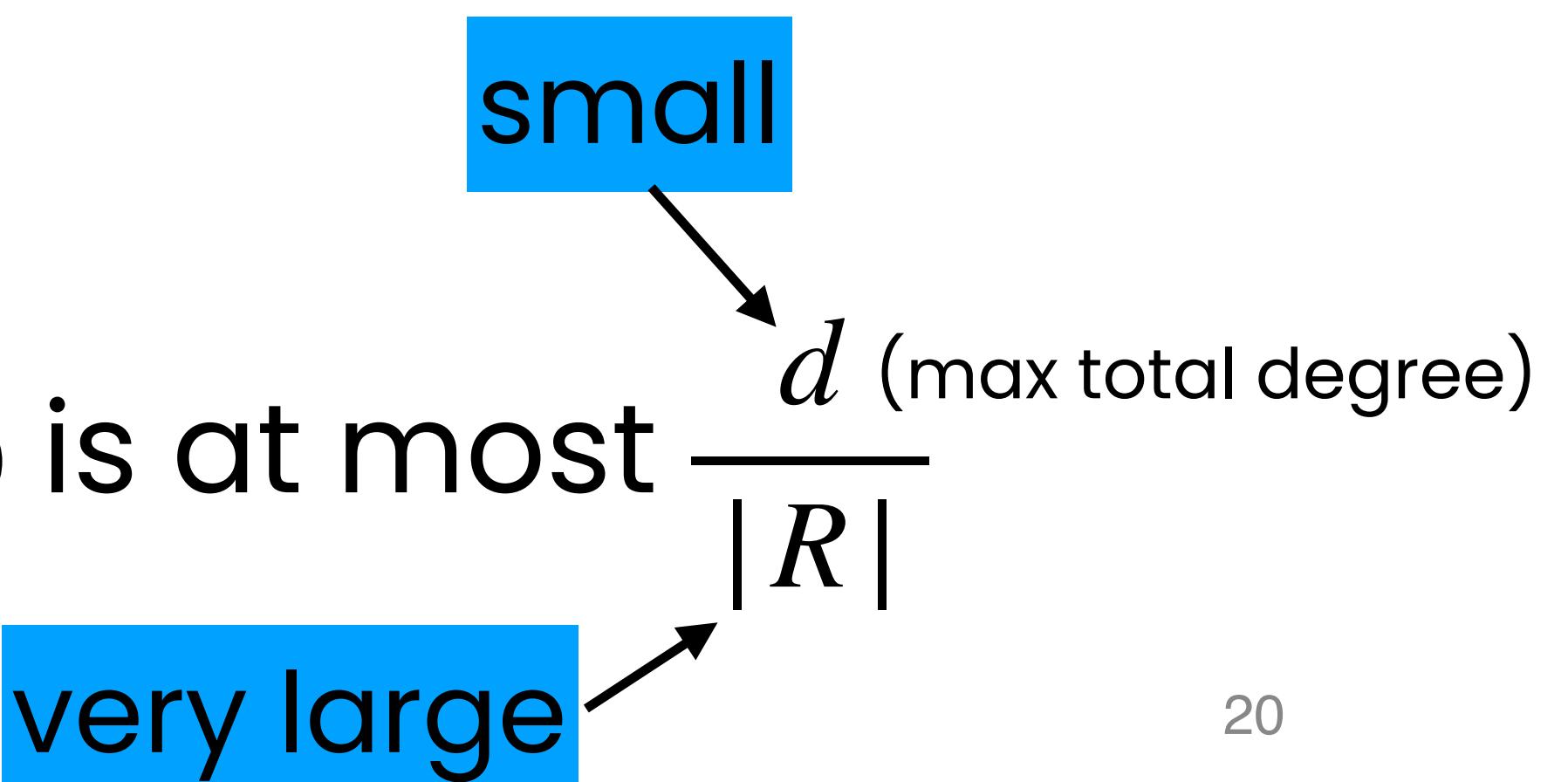
Schwartz-Zippel Lemma

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If $p_1 \neq p_2$, then the probability that $p_1(\alpha) = p_2(\alpha)$ is at most $\frac{d}{|R|}$

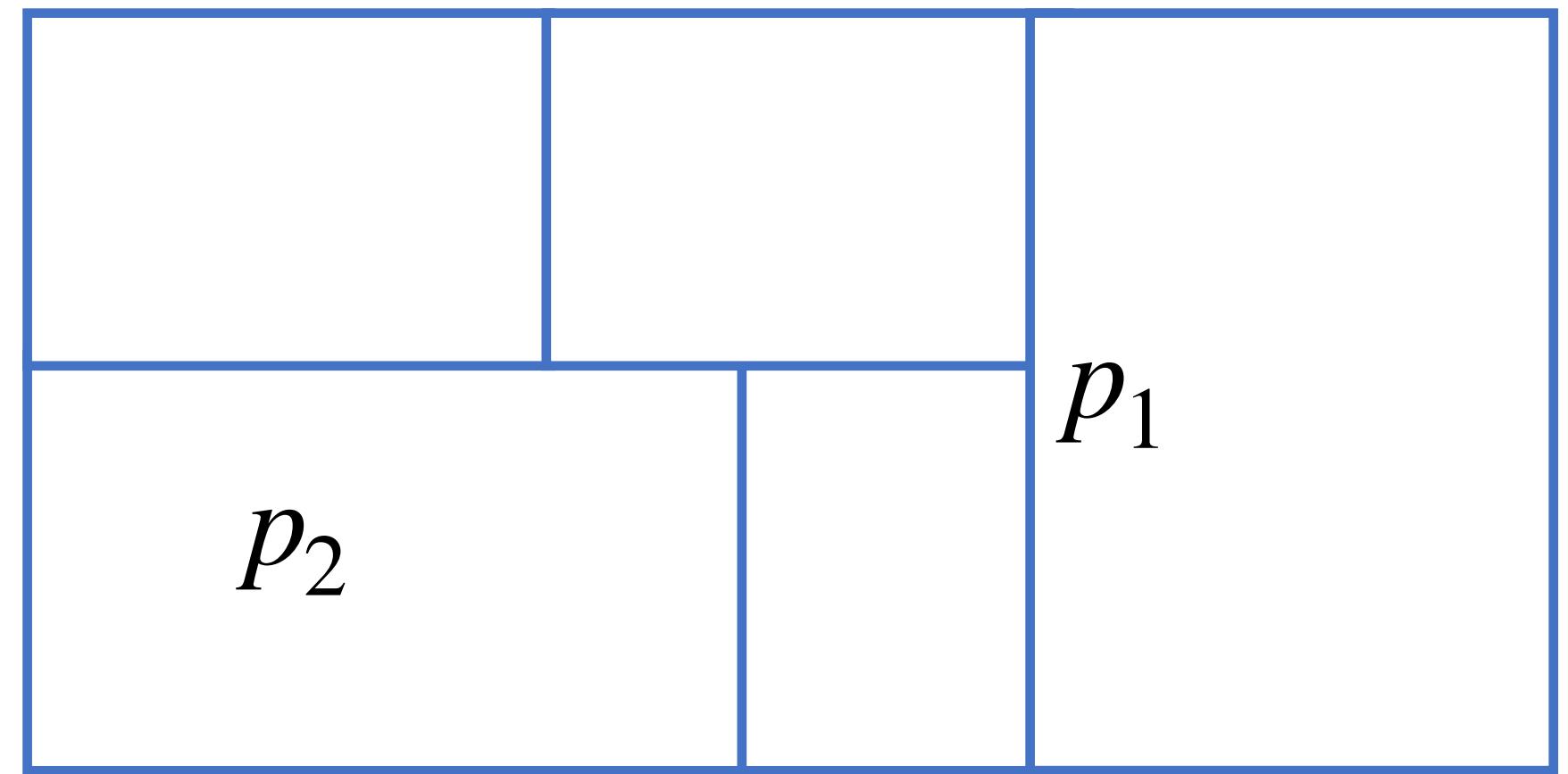


The diagram consists of two blue rectangular boxes. The top box contains the word "small" and has a black arrow pointing from it towards the fraction $\frac{d}{|R|}$. The bottom box contains the words "very large" and has a black arrow pointing from it towards the same fraction.

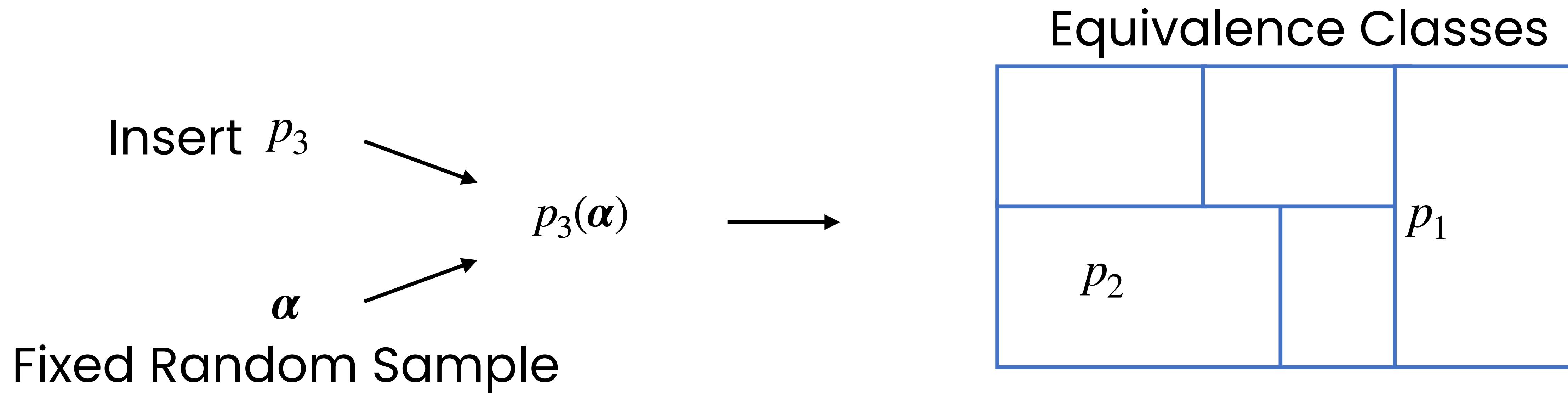
Polynomial Identity Filter (PIF)

Insert p_3
 α
Fixed Random Sample

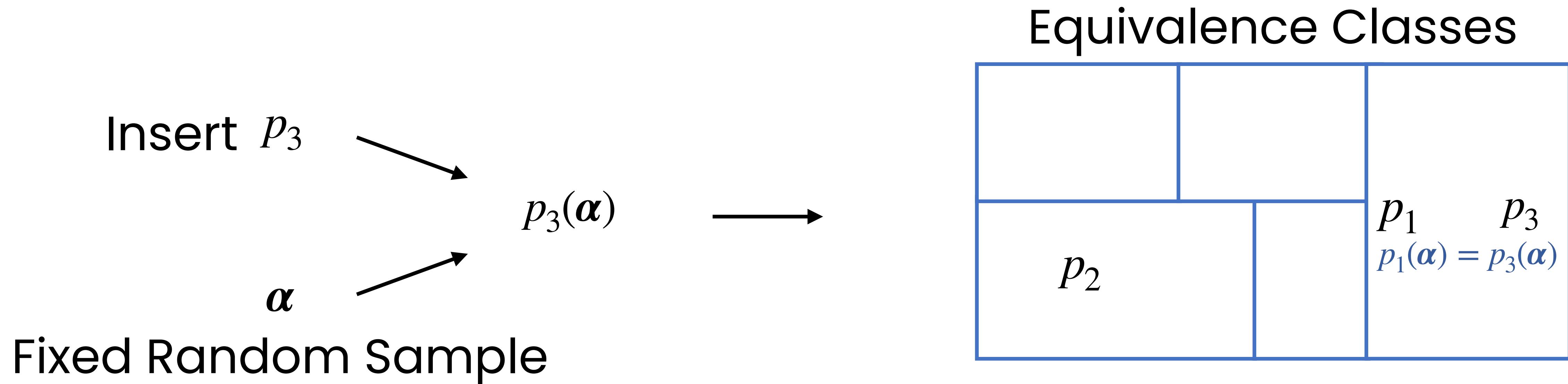
Equivalence Classes



Polynomial Identity Filter (PIF)



Polynomial Identity Filter (PIF)



QUESO's Improvements

```
rules = []
circuits = enumerate(max_qubits, max_size)
for c1 in circuits:
    for c2 in circuits:
        if verify(c1, c2):
            rules.append(c1 → c2)
```

expensive

big

uh oh...

QUESO's Improvements

```
rules = []
circuitsbig = enumerate(max_qubits, max_size)
for c1 in circuits:
    for c2 in circuits:
        if verify(c1, c2):
            insert into PIF
            rules.append(c1 → c2)
expensive
```

uh oh...

QUESO's Improvements

```
rules = []
circuits = enumerate(max_qubits, max_size)
for c1 in circuits:
    for c2 in circuits:
        if verify(c1, c2):
            insert into PIF
            rules.append(c1 → c2)
expensive
```

big

uh oh...

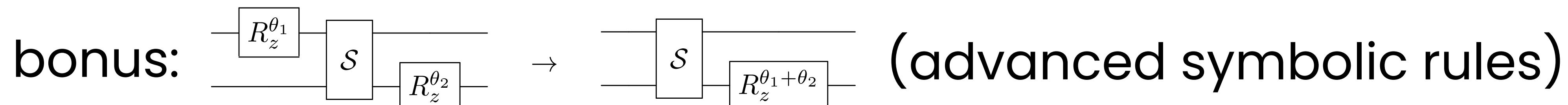
expensive

Efficient equivalence check between circuit pairs!

QUESO's Improvements

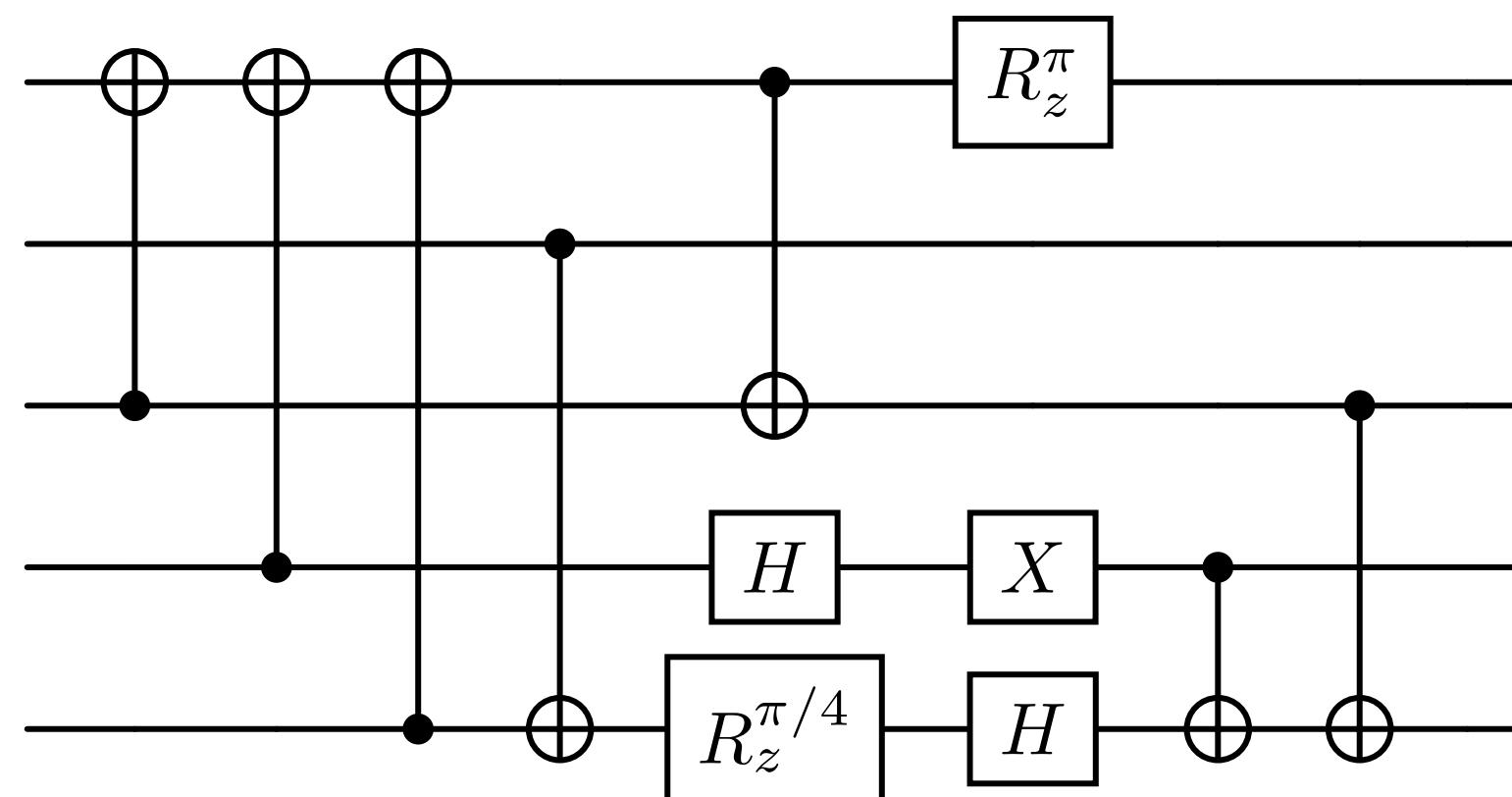
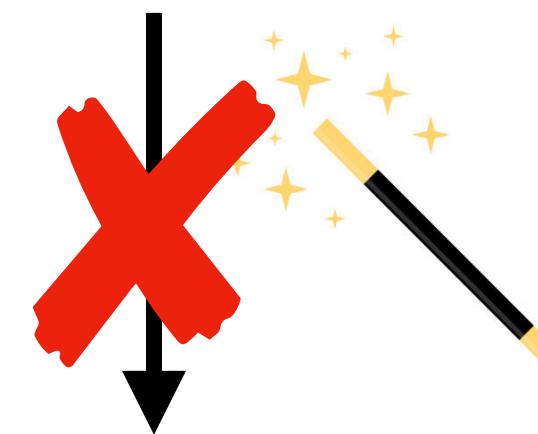
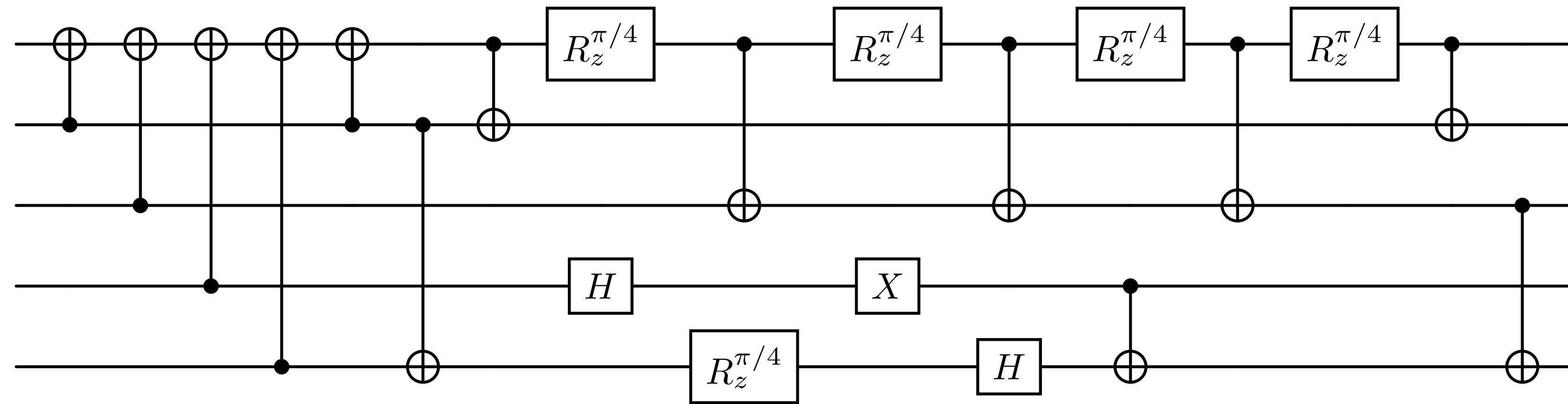
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Efficient equivalence check between circuit pairs!

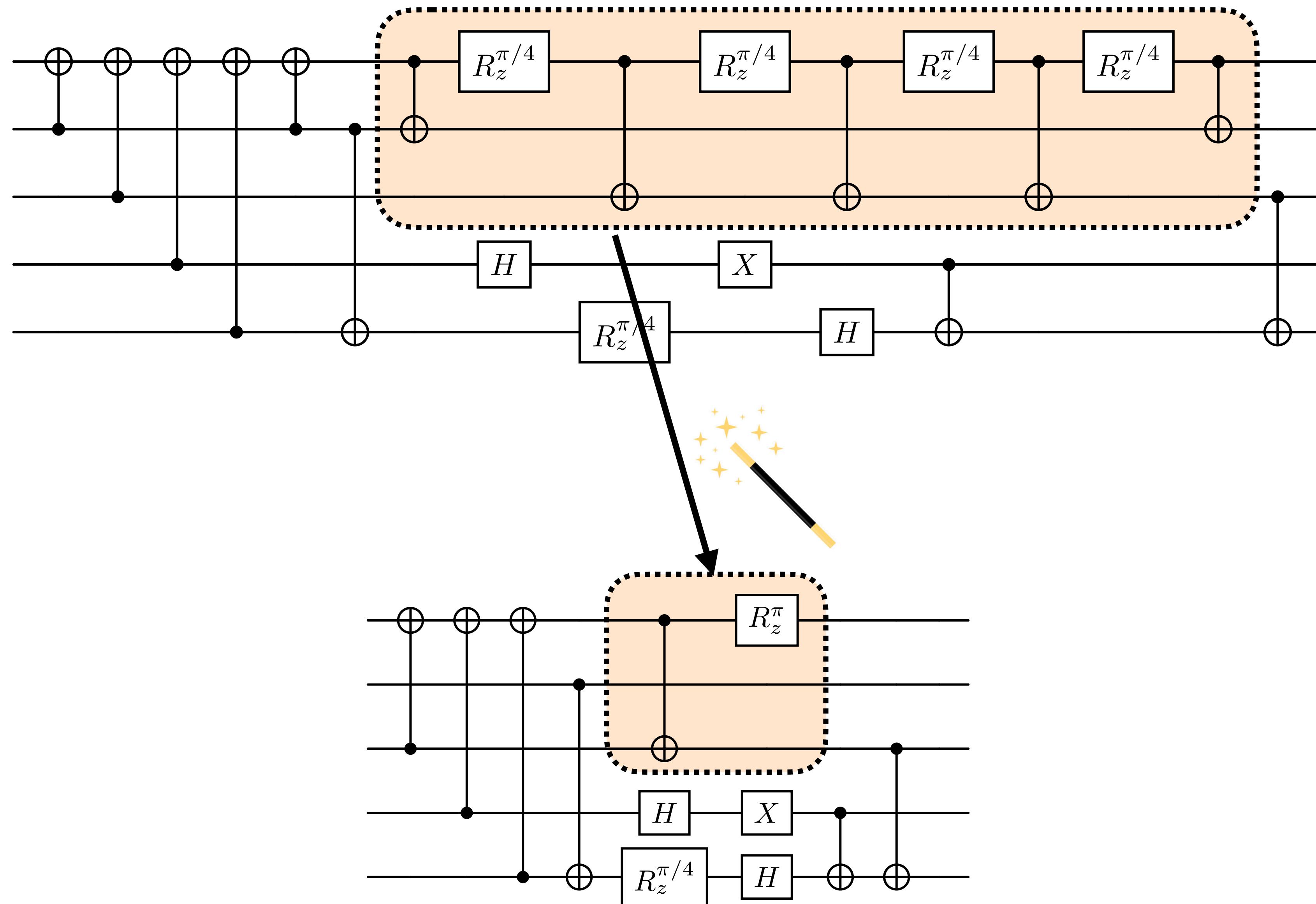


Circuit Resynthesis

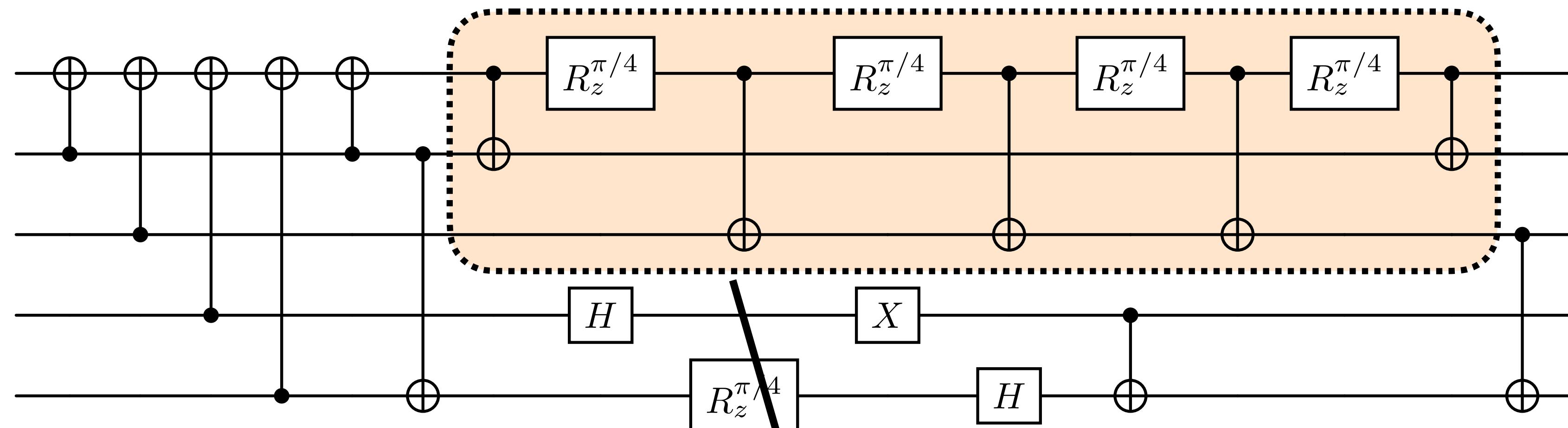
Circuit Resynthesis



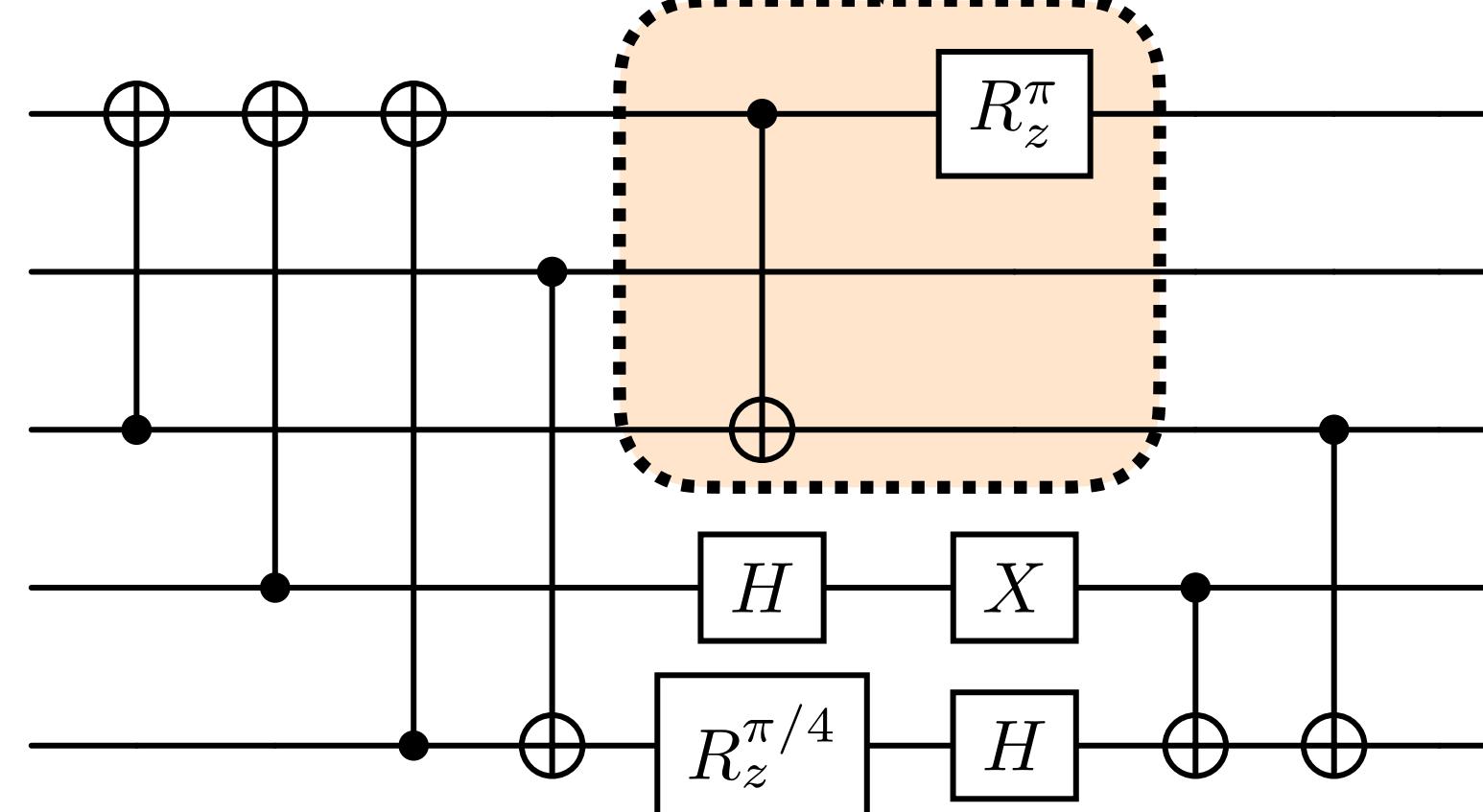
Circuit Resynthesis



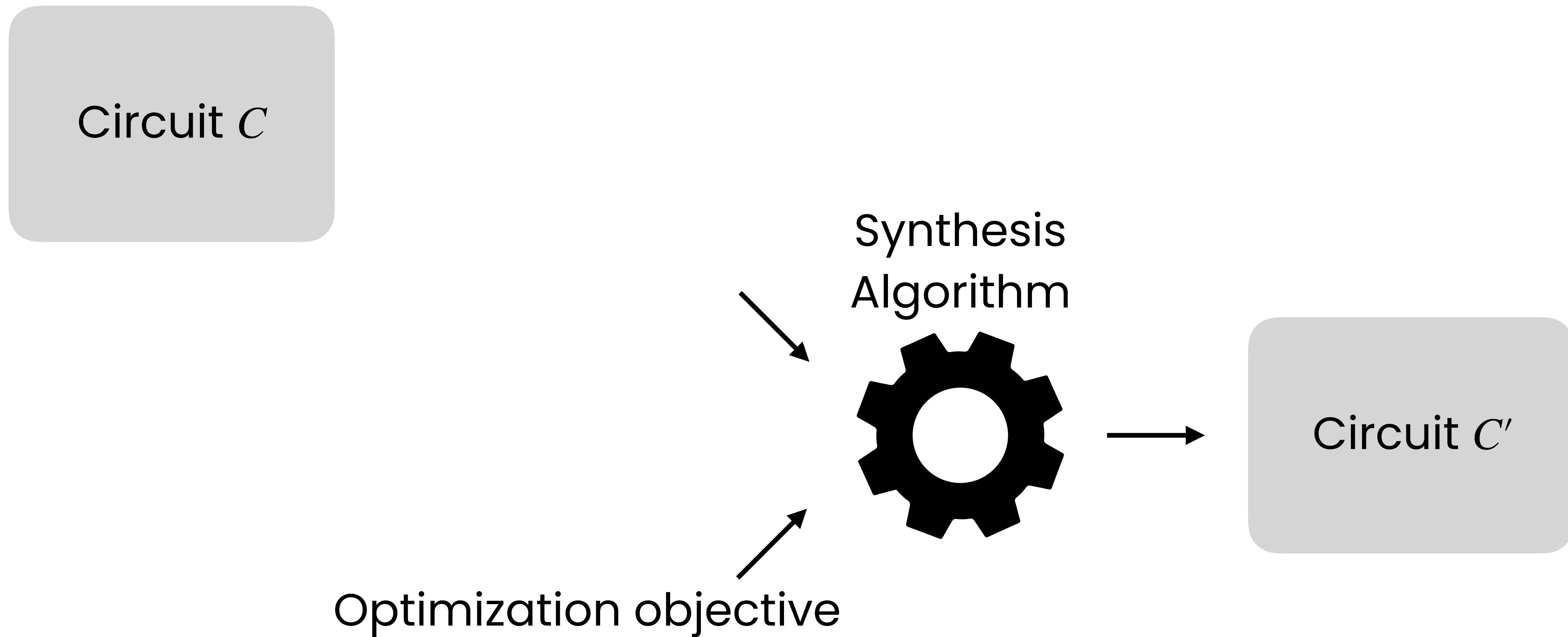
Circuit Resynthesis



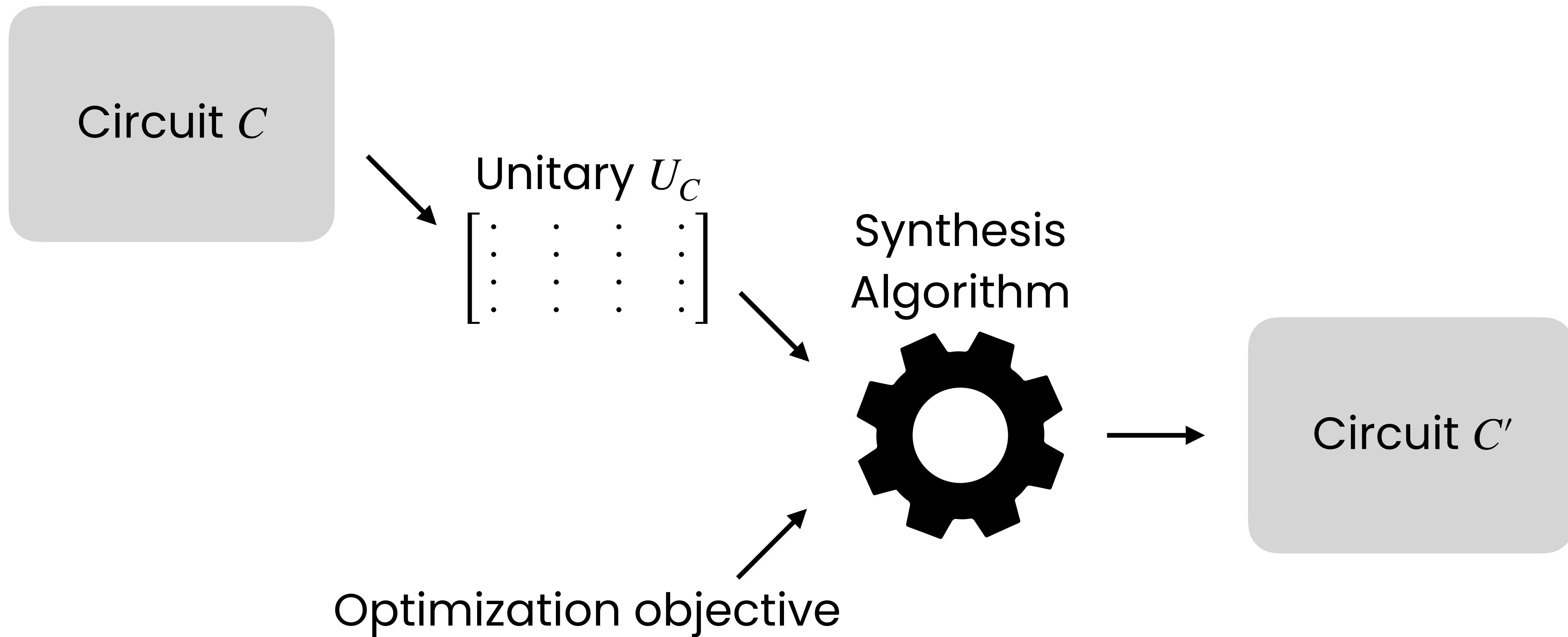
Unitary Synthesis



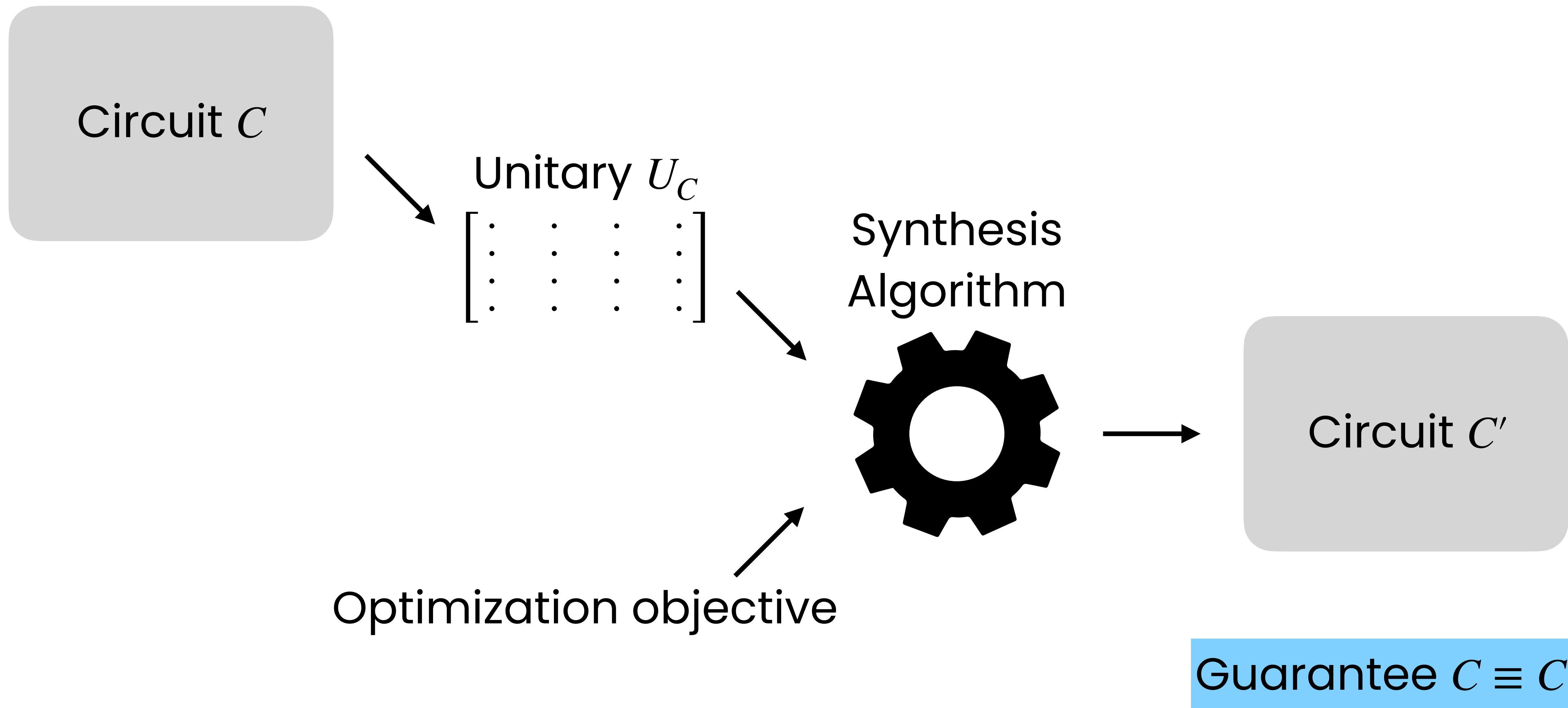
Unitary Synthesis



Unitary Synthesis

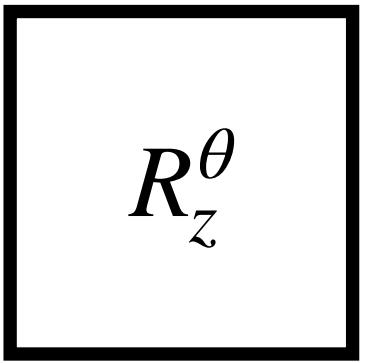


Unitary Synthesis

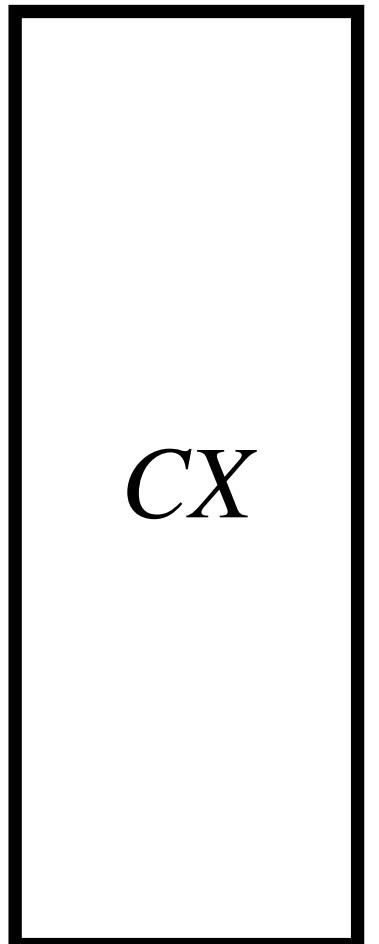
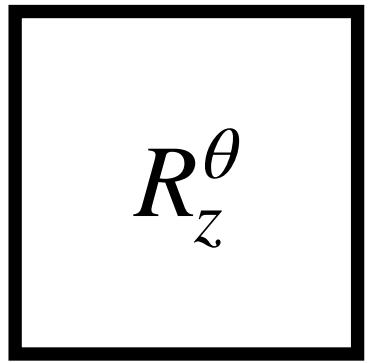


Circuit to Unitary

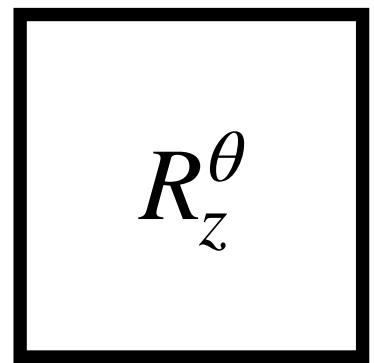
Circuit to Unitary



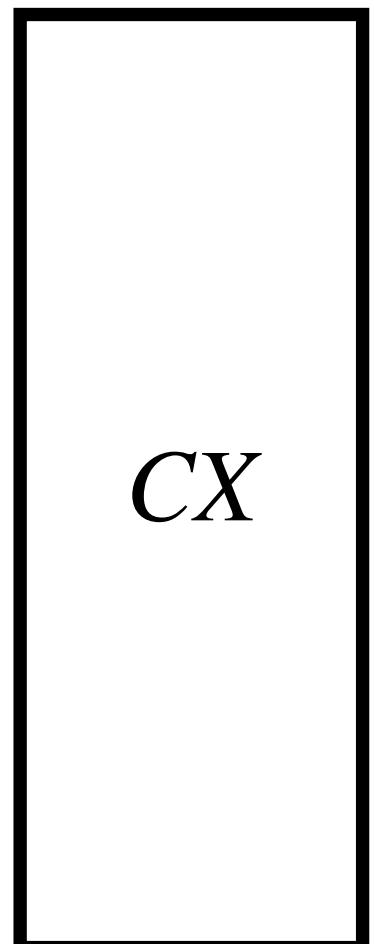
Circuit to Unitary



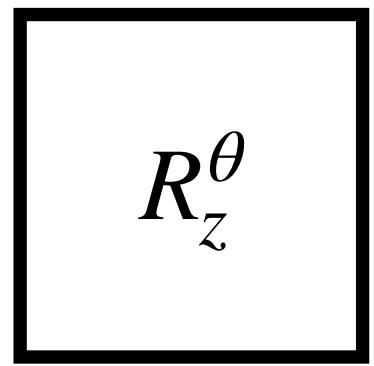
Circuit to Unitary

 R_z^θ

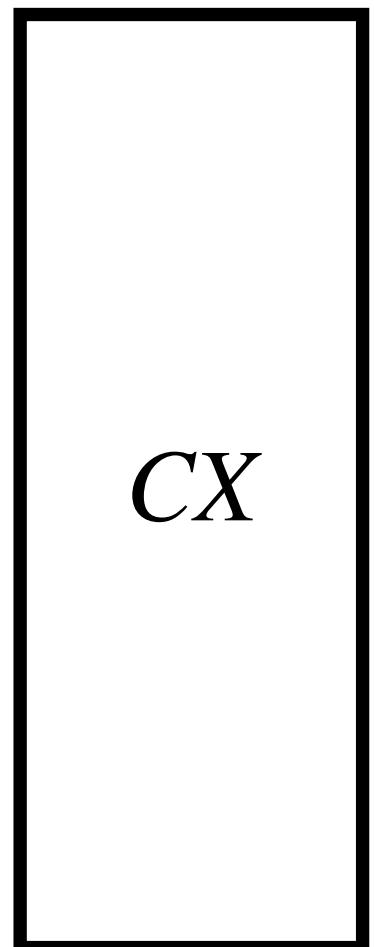
$$U_{R_z^\theta} := \begin{bmatrix} e^{-i\frac{\theta}{2}} & 0 \\ 0 & e^{i\frac{\theta}{2}} \end{bmatrix}$$

 CX

Circuit to Unitary

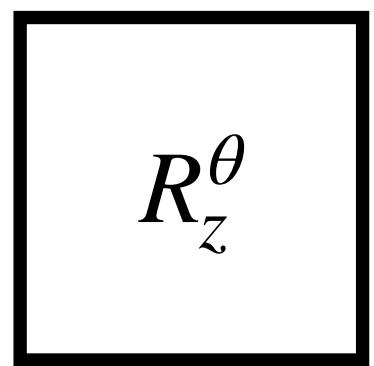


$$U_{R_z^\theta} := \begin{bmatrix} e^{-i\frac{\theta}{2}} & 0 \\ 0 & e^{i\frac{\theta}{2}} \end{bmatrix}$$

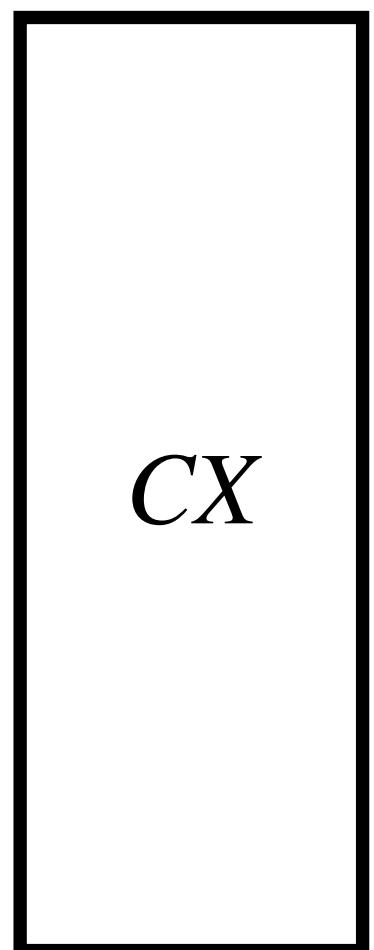


$$U_{CX} := \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$

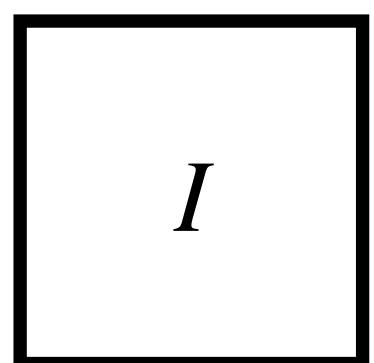
Circuit to Unitary



$$U_{R_z^\theta} := \begin{bmatrix} e^{-i\frac{\theta}{2}} & 0 \\ 0 & e^{i\frac{\theta}{2}} \end{bmatrix}$$

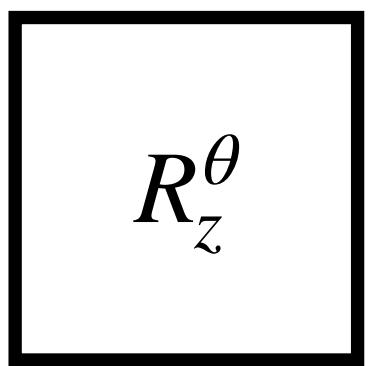


$$U_{CX} := \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$

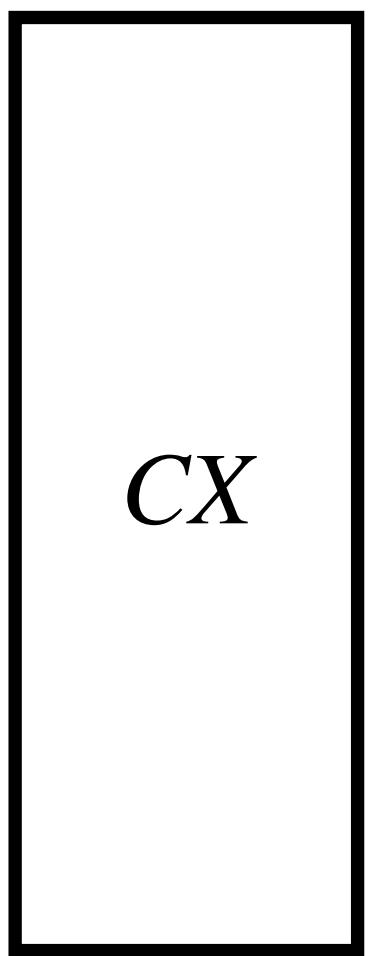


$$U_I := \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$

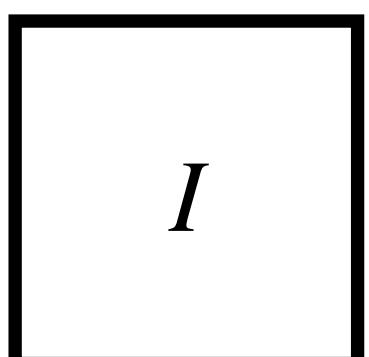
Circuit to Unitary



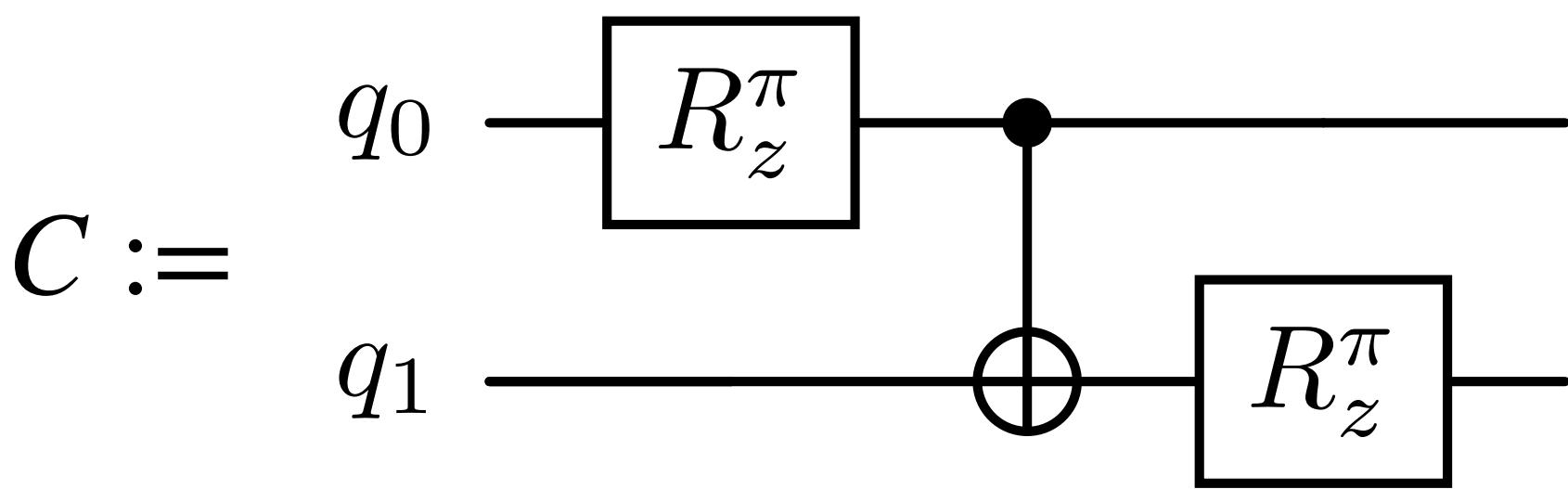
$$U_{R_z^\theta} := \begin{bmatrix} e^{-i\frac{\theta}{2}} & 0 \\ 0 & e^{i\frac{\theta}{2}} \end{bmatrix}$$



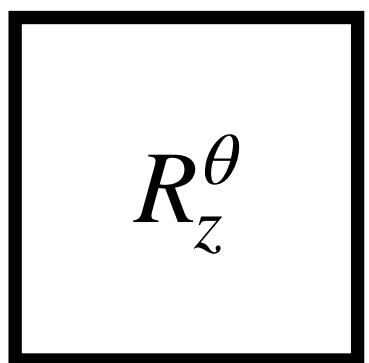
$$U_{CX} := \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$



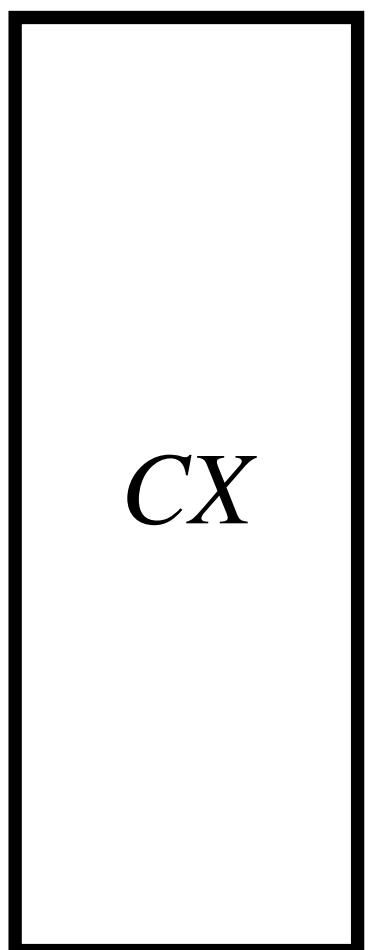
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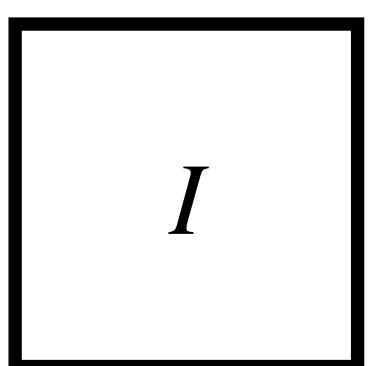
Circuit to Unitary



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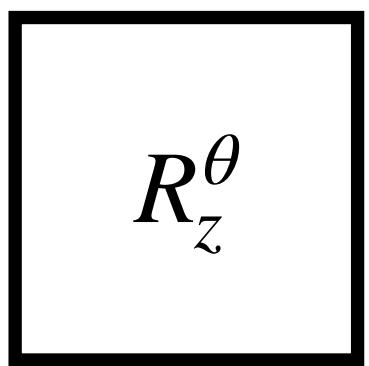
$$U_{CX} := \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$



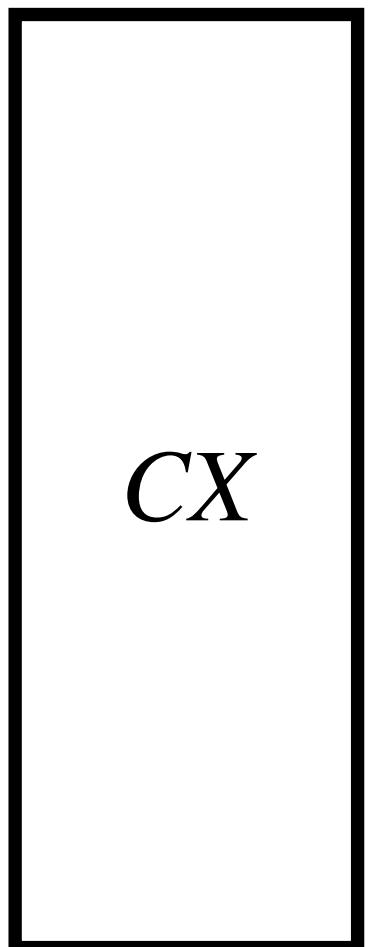
$$U_I := \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$

$$C := \begin{array}{c} q_0 \xrightarrow{R_z^\pi} \bullet \\ \text{---} \quad | \\ q_1 \xrightarrow{\oplus} R_z^\pi \end{array}$$
$$U_C = U_{R_z^\pi}$$

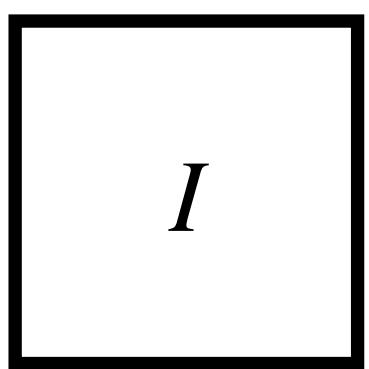
Circuit to Unitary



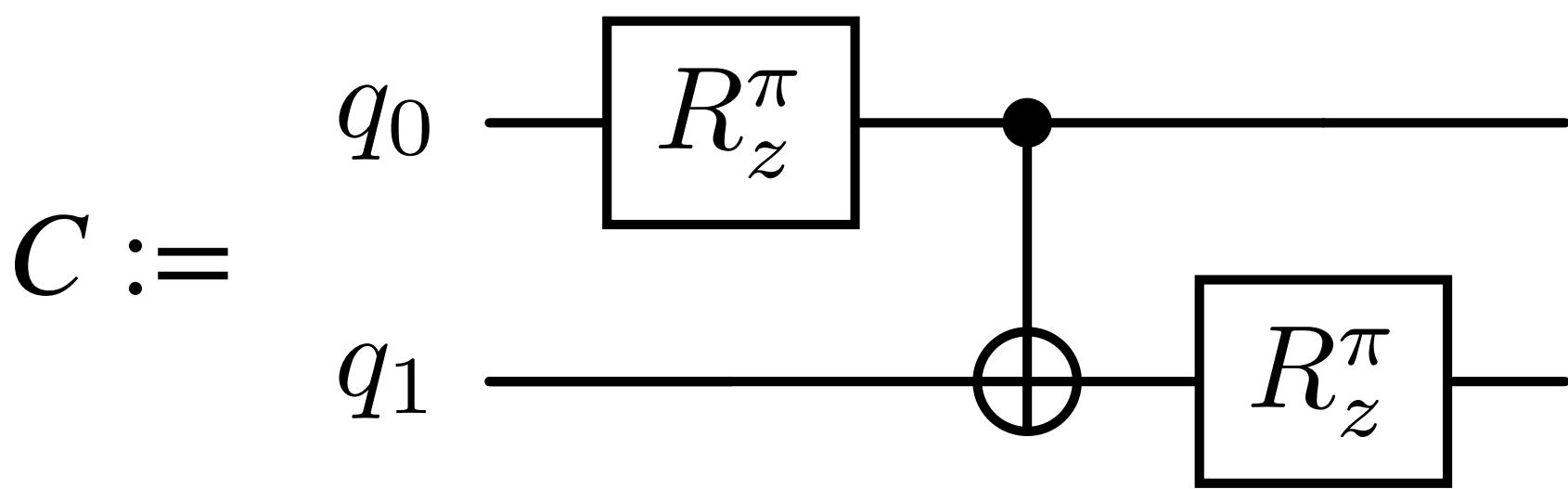
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$$U_{CX} := \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$



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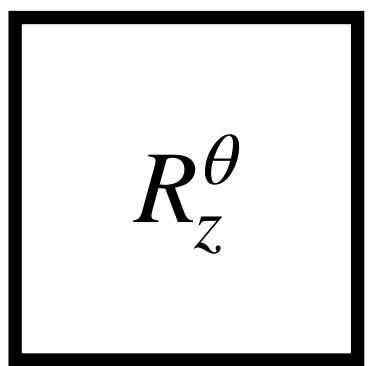


$$U_C =$$

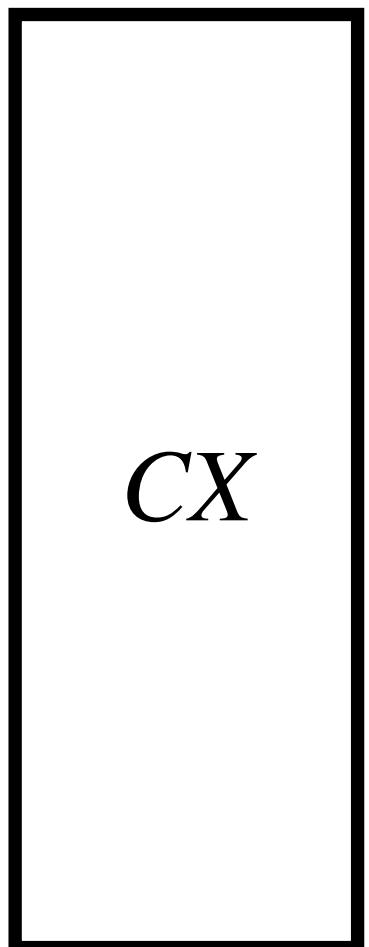
$$2^2 \times 2^2$$

$$U_{R_z^\pi}$$

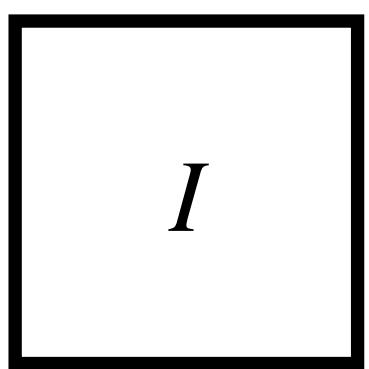
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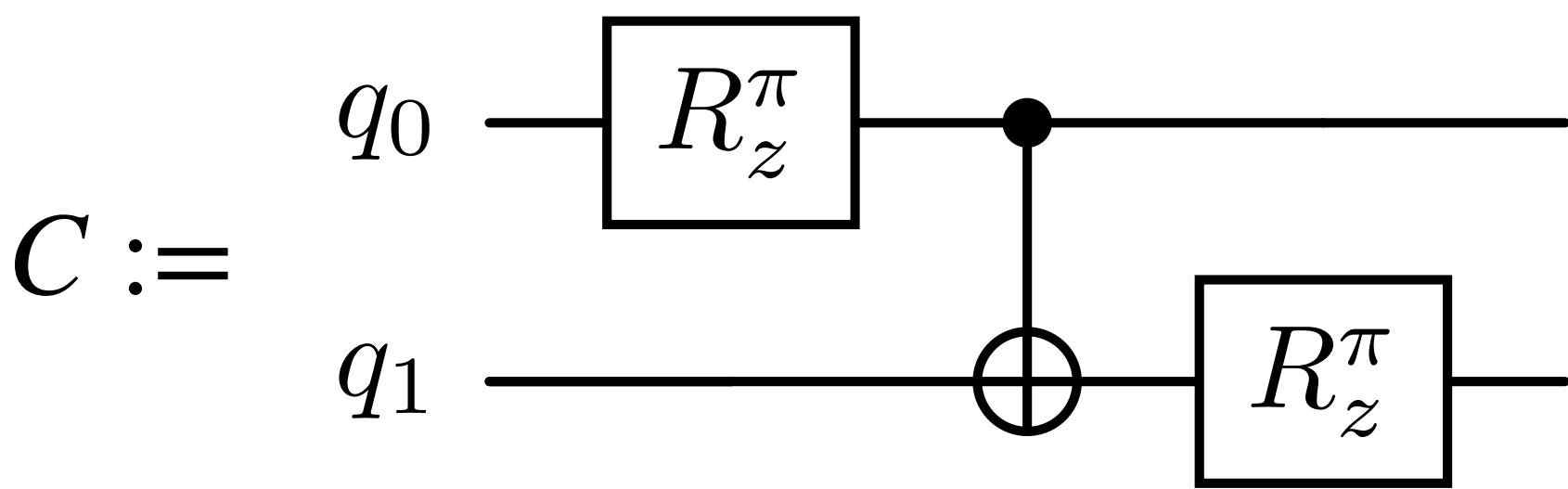
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$$U_I := \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$



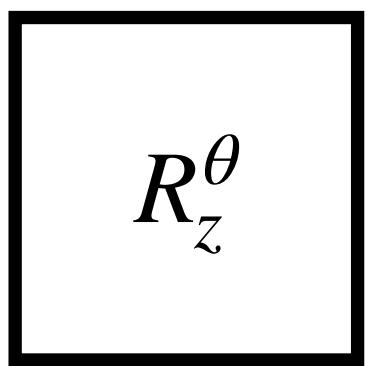
$$U_C =$$

$$2^2 \times 2^2$$

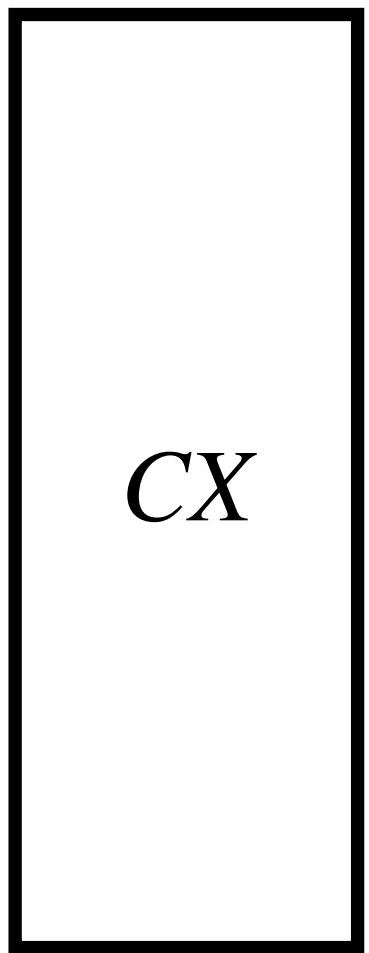
$$U_{R_z^\pi}$$

$$2 \times 2$$

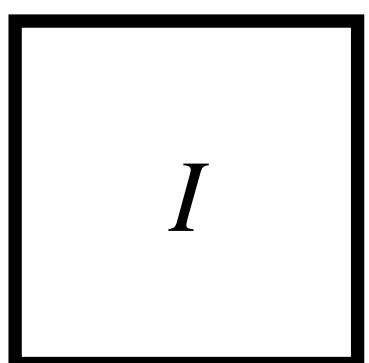
Circuit to Unitary



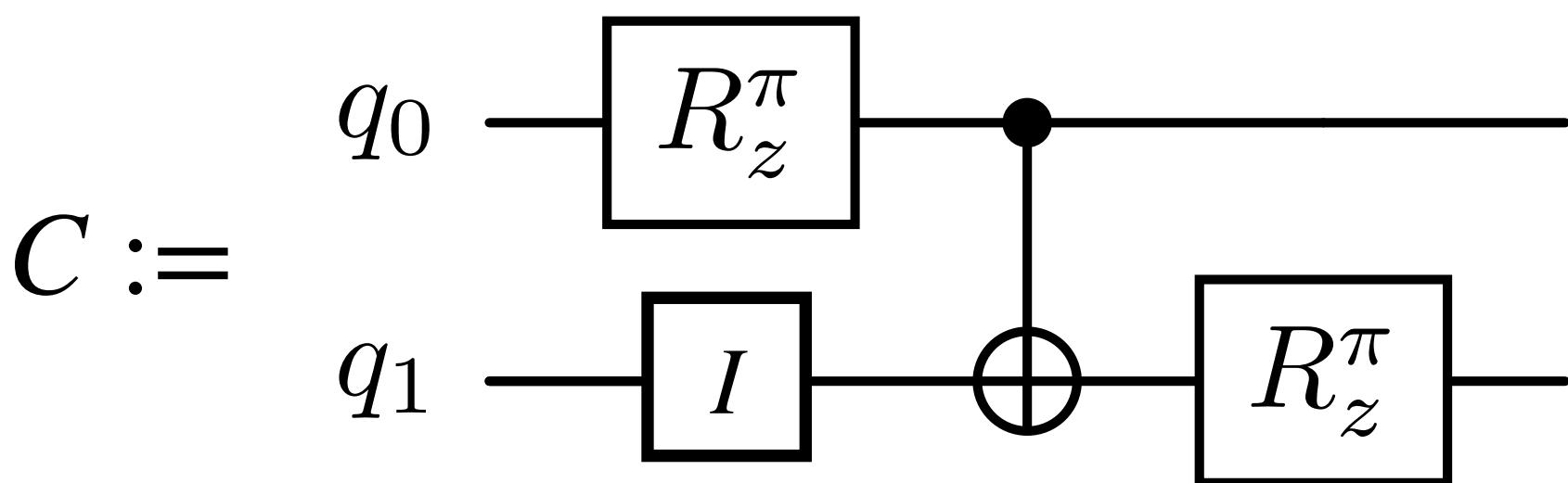
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$$U_I := \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$



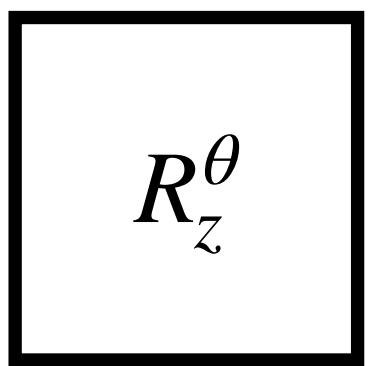
$$U_C =$$

$2^2 \times 2^2$

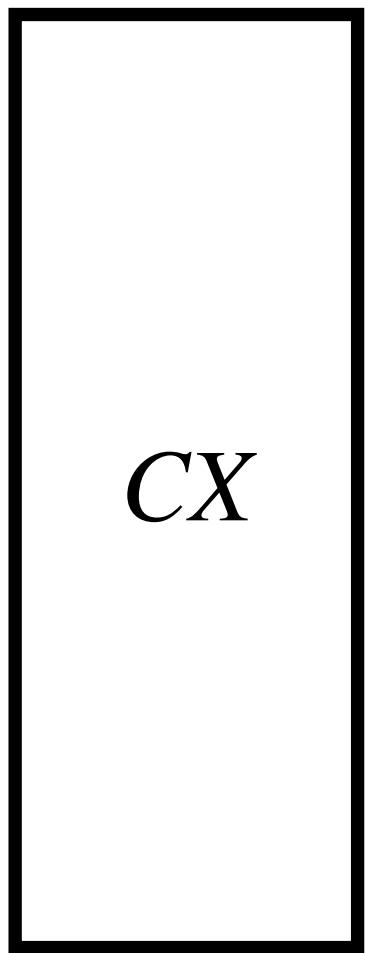
$$U_{R_z^\pi}$$

2×2

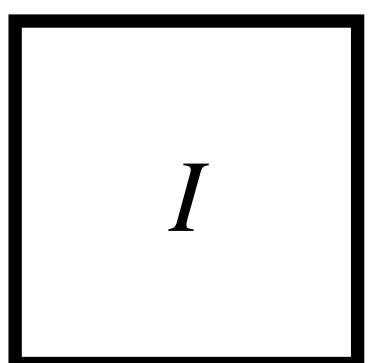
Circuit to Unitary



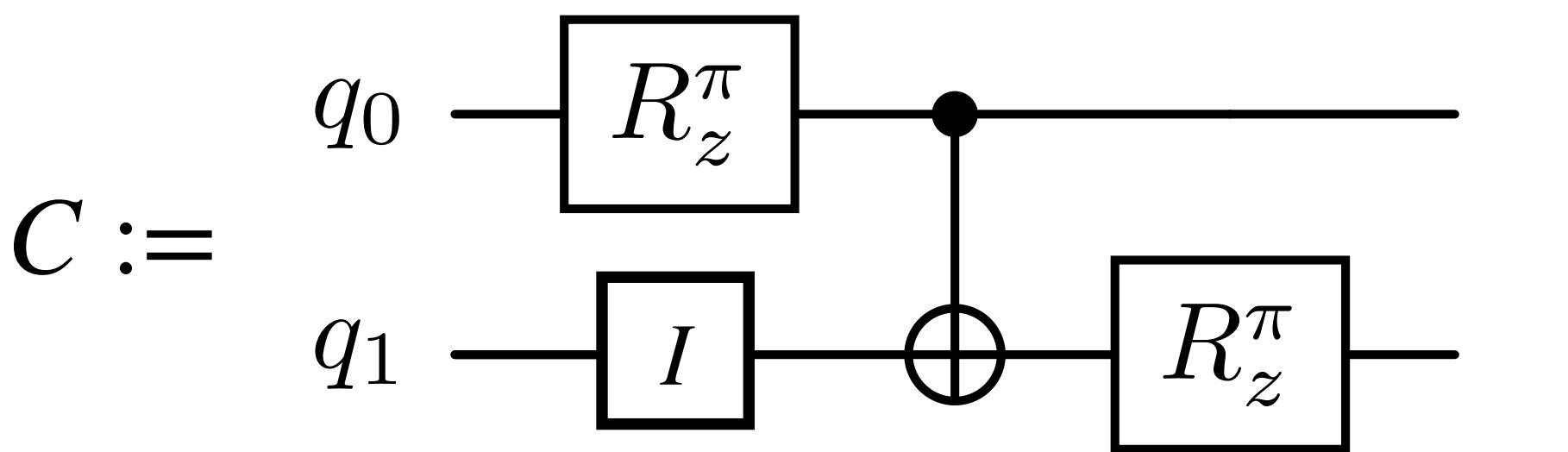
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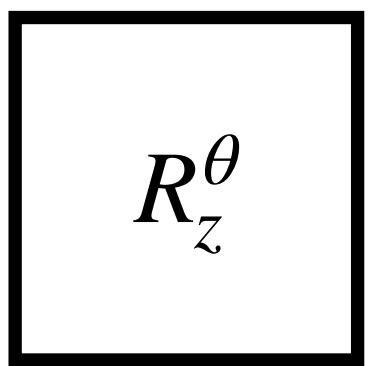


$$U_I := \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$

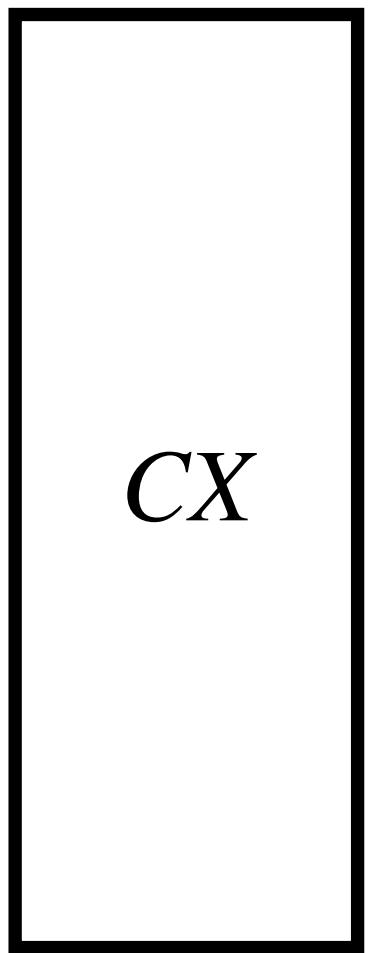


$$U_C = \frac{(U_{R_z^\pi} \otimes U_I)}{2 \times 2}$$

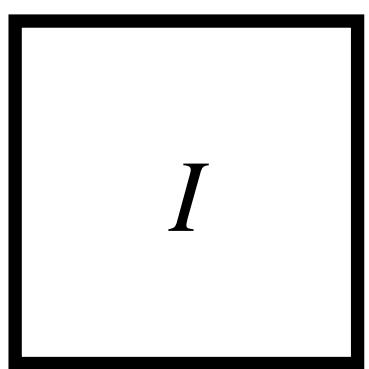
Circuit to Unitary



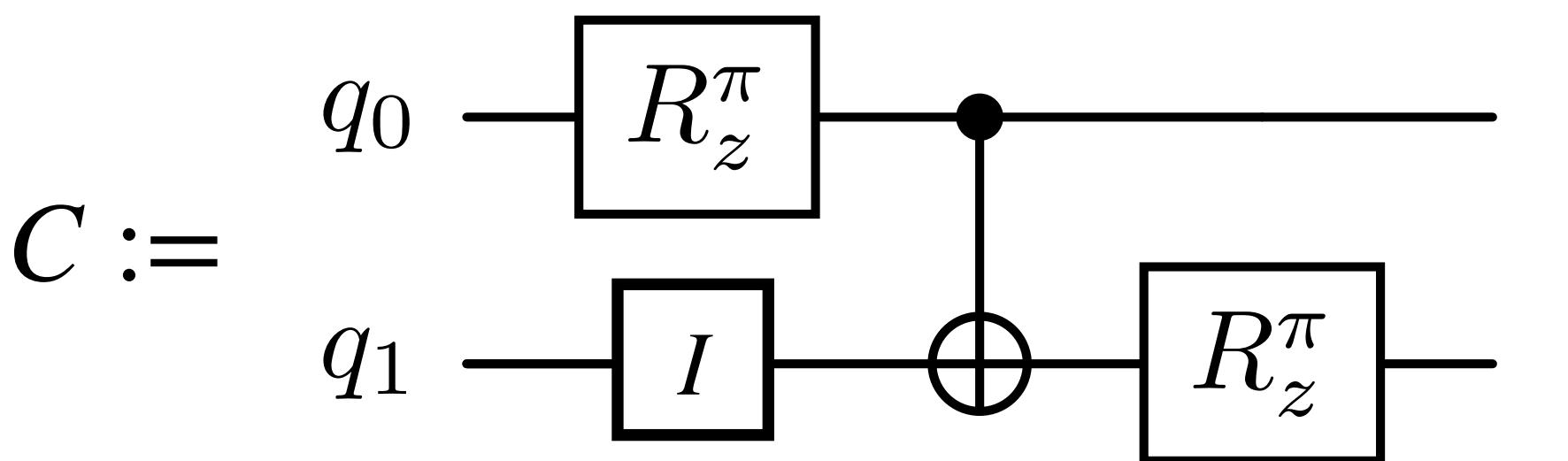
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$$U_{CX} := \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$



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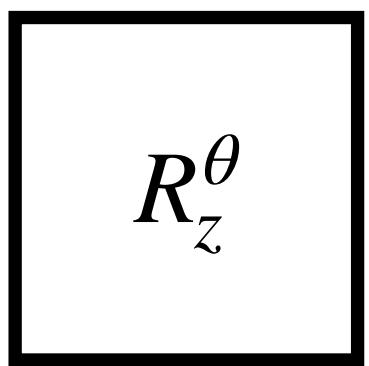


$$U_C =$$

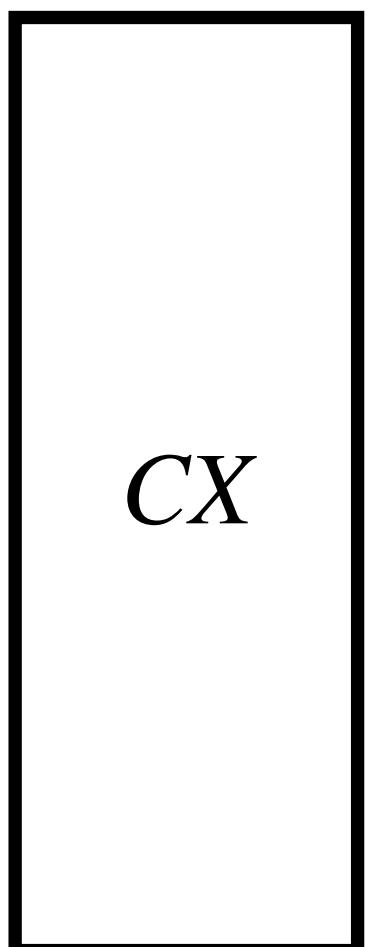
$2^2 \times 2^2$

$$\frac{(U_{R_z^\pi} \otimes U_I)}{2 \times 2 \quad 2 \times 2}$$

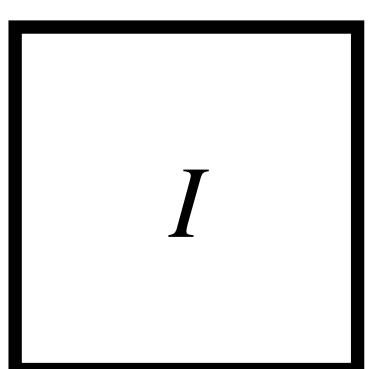
Circuit to Unitary



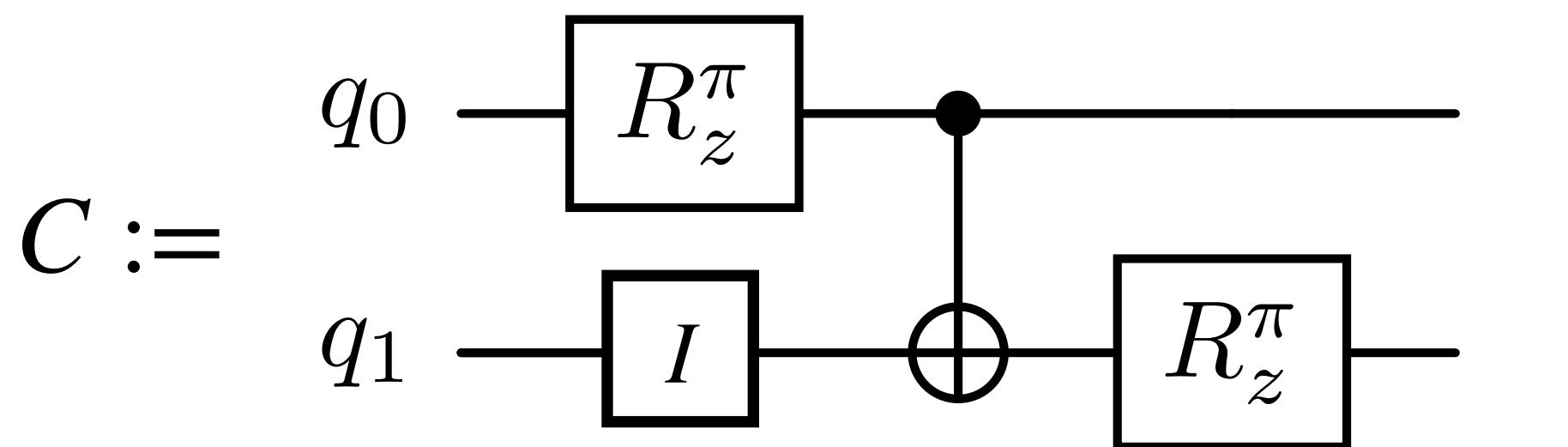
$$U_{R_z^\theta} := \begin{bmatrix} e^{-i\frac{\theta}{2}} & 0 \\ 0 & e^{i\frac{\theta}{2}} \end{bmatrix}$$



$$U_{CX} := \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$



$$U_I := \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$



$$U_C =$$

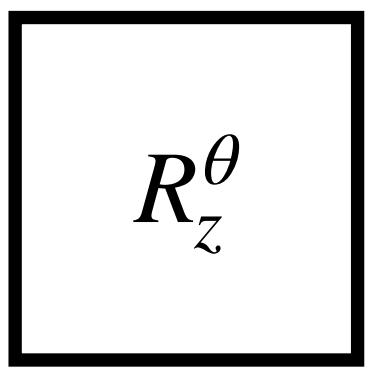
$2^2 \times 2^2$

$$\frac{(U_{R_z^\pi} \otimes U_I)}{2 \times 2 \quad 2 \times 2}$$

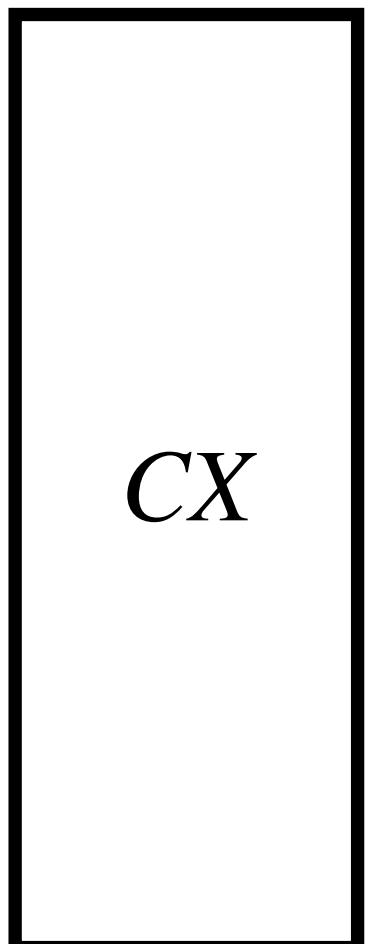
$\frac{2^2 \times 2^2}{2^2 \times 2^2}$

$$(U_{R_z^\pi} \otimes U_I) = \begin{bmatrix} e^{-i\frac{\pi}{2}} \cdot U_I & 0 \cdot U_I \\ 0 \cdot U_I & e^{i\frac{\pi}{2}} \cdot U_I \end{bmatrix}$$

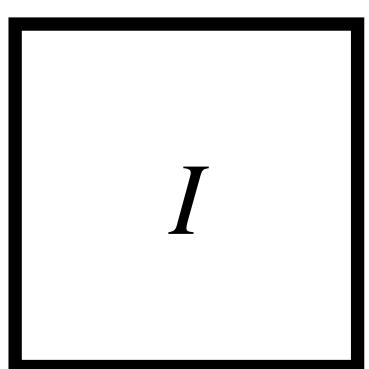
Circuit to Unitary



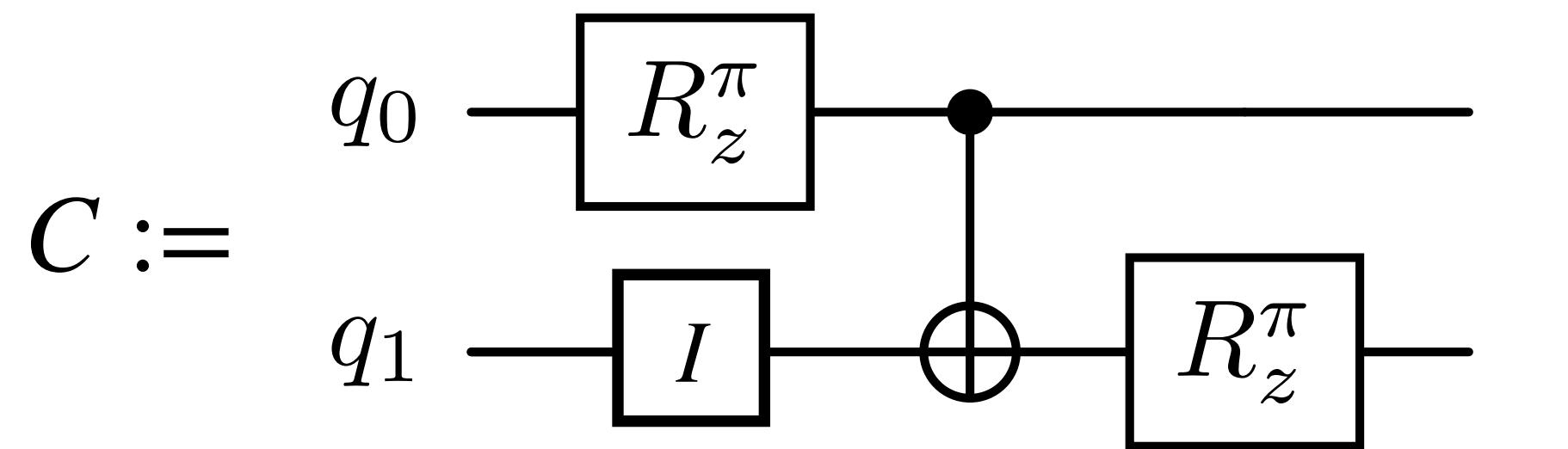
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$$U_{CX} := \begin{bmatrix} 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 \\ 0 & 0 & 1 & 0 \end{bmatrix}$$



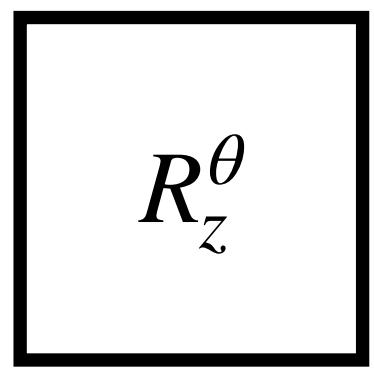
$$U_I := \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$



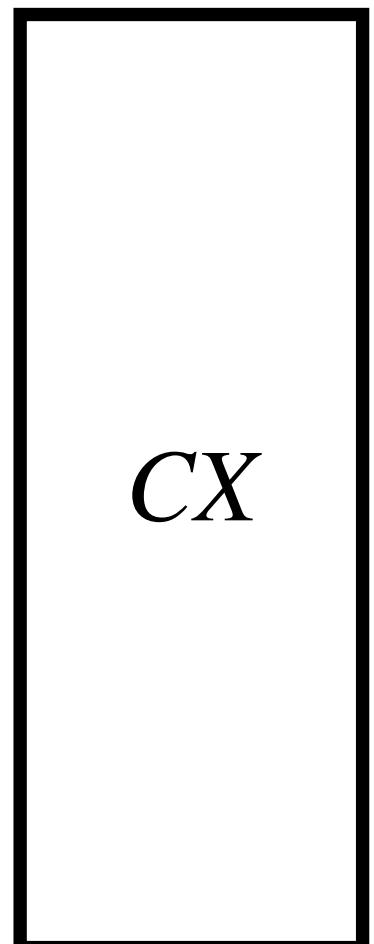
$$U_C = \frac{(U_{R_z^\pi} \otimes U_I)}{2^2 \times 2^2}$$

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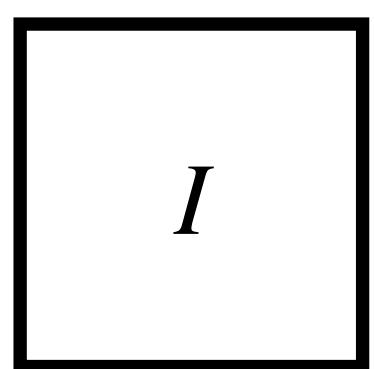
Circuit to Unitary



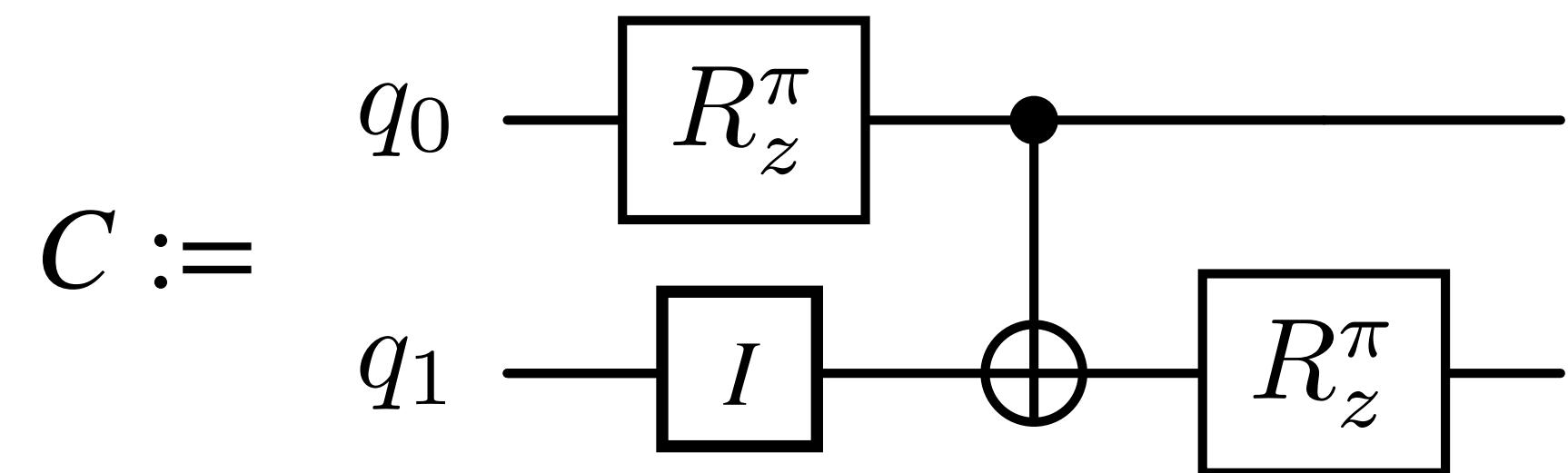
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$$U_I := \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$



$$U_C =$$

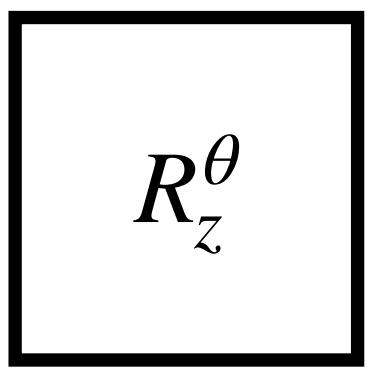
$$2^2 \times 2^2$$

$$U_{CX} \cdot (U_{R_z^\pi} \otimes U_I)$$

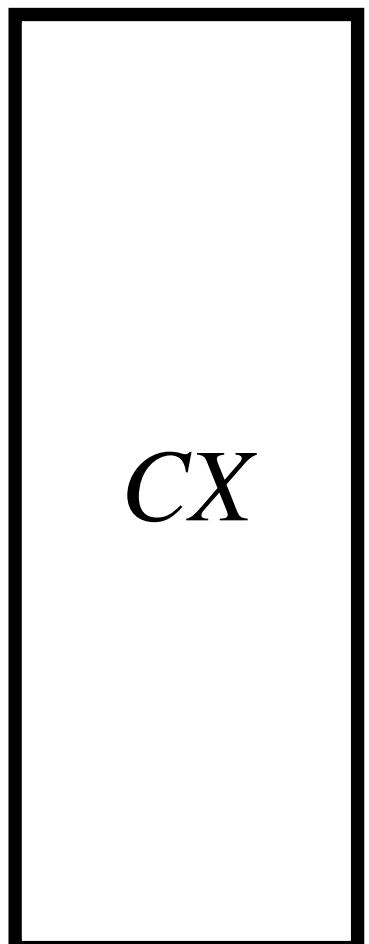
$$\frac{2 \times 2 \quad 2 \times 2}{2^2 \times 2^2}$$

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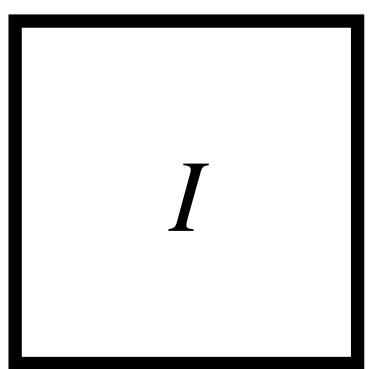
Circuit to Unitary



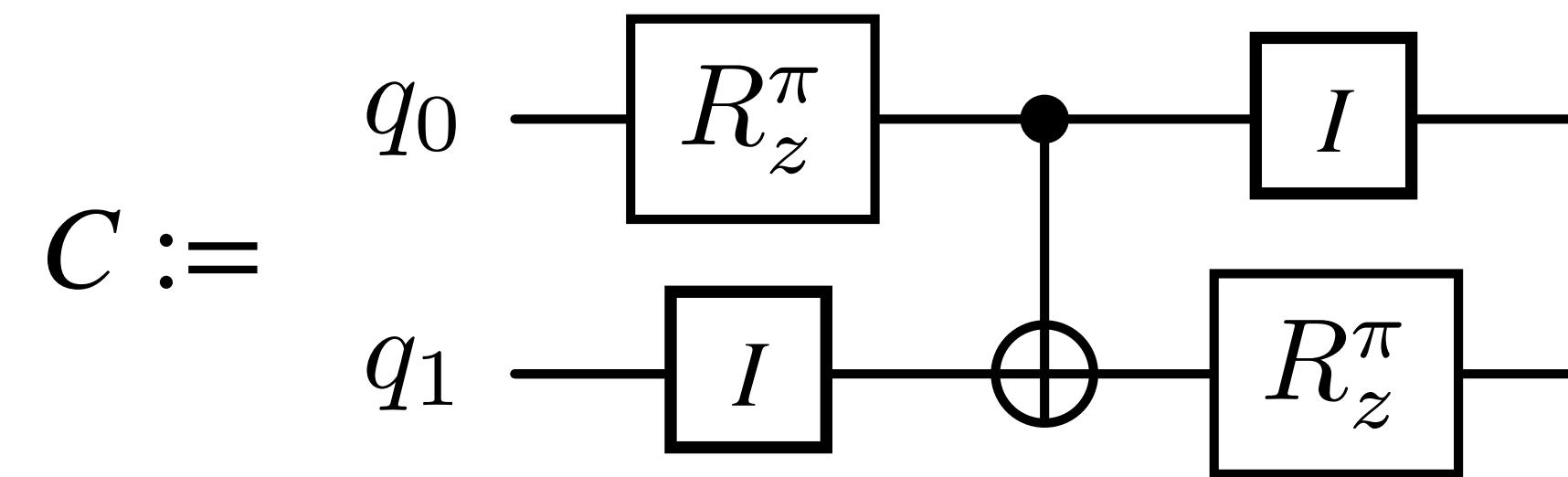
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$$U_I := \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$$



$$U_C = (U_I \otimes U_{R_z^\pi}) \cdot U_{CX} \cdot (U_{R_z^\pi} \otimes U_I)$$

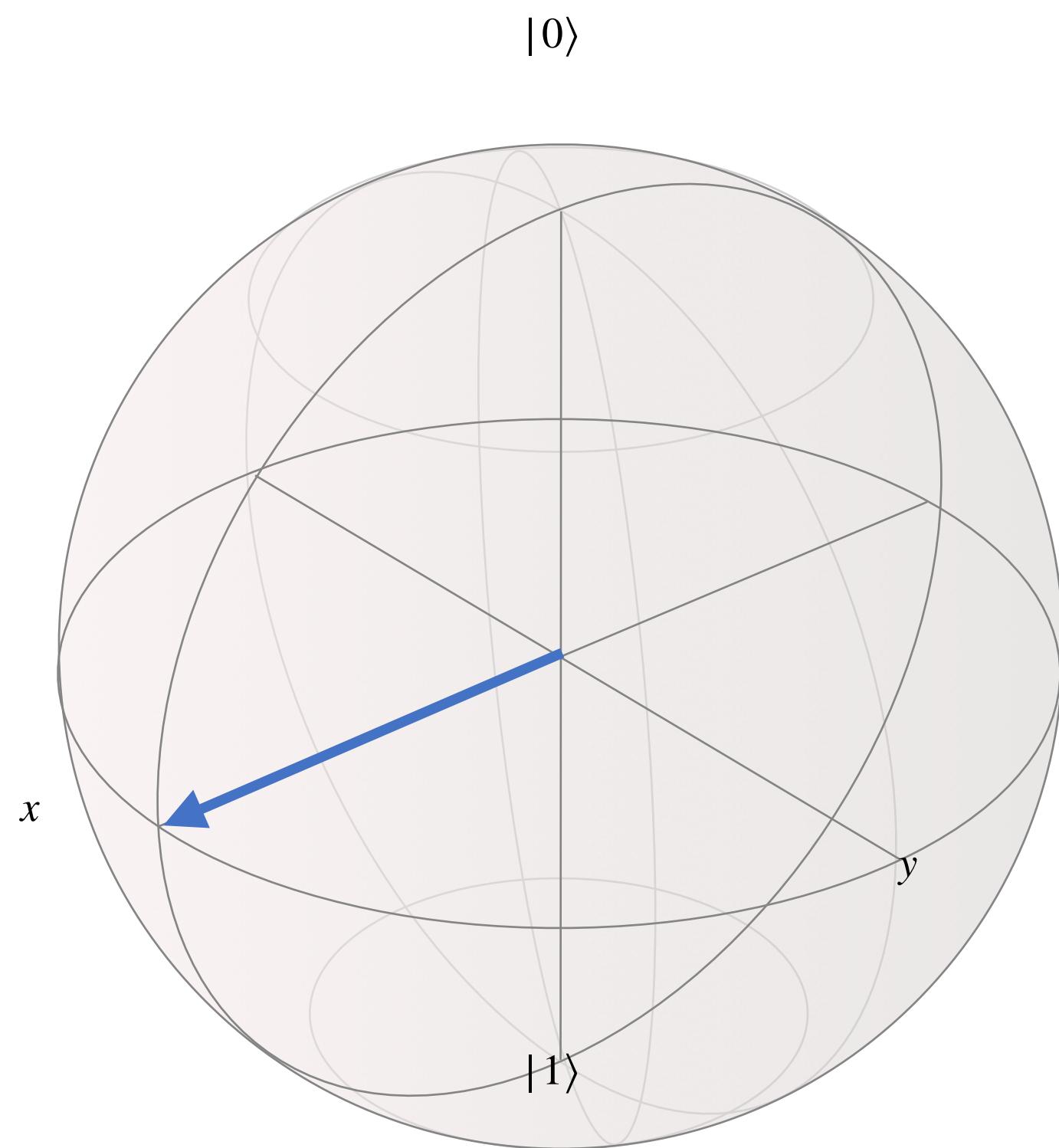
$$\frac{2^2 \times 2^2}{2^2 \times 2^2}$$

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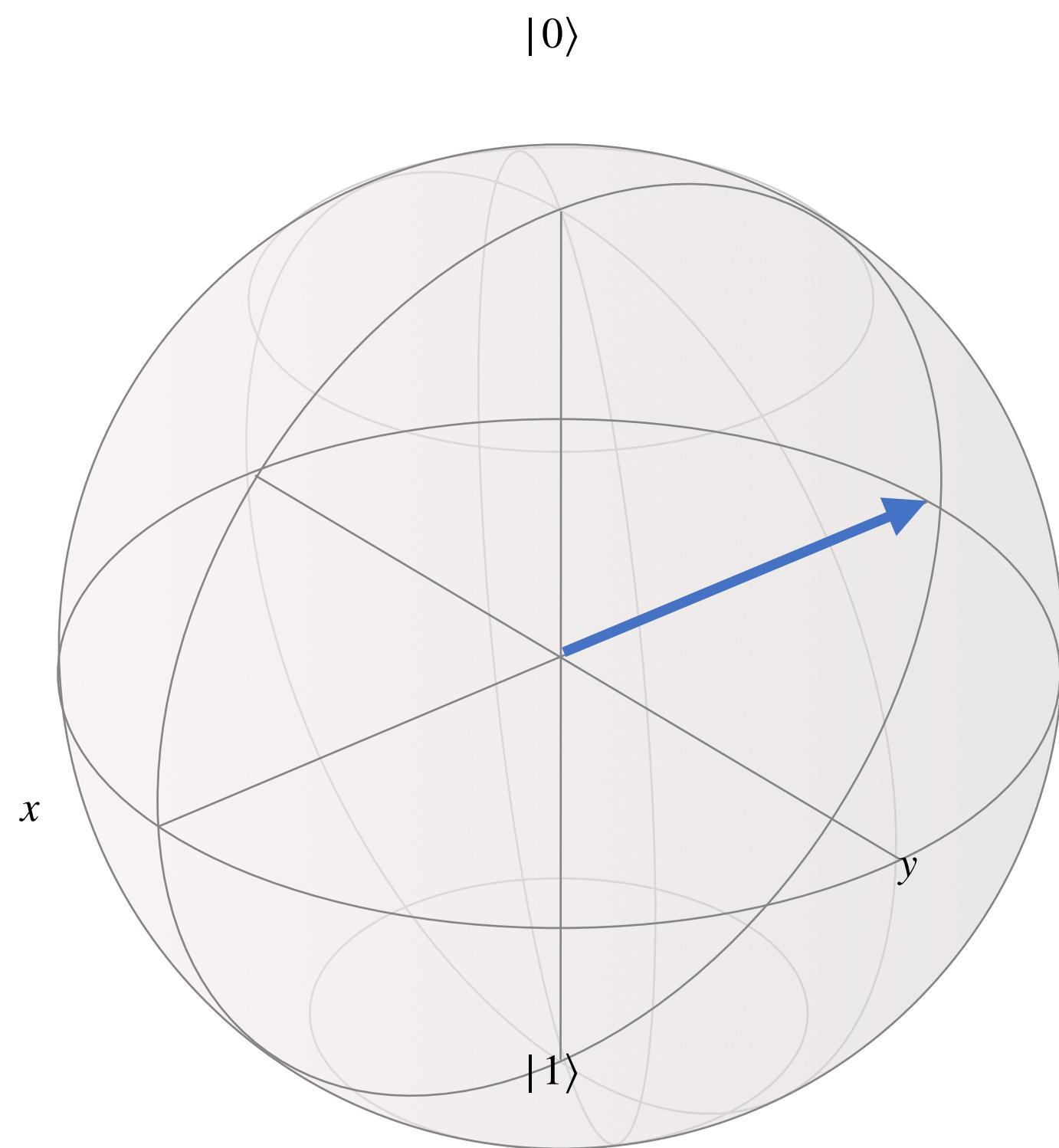
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$$\frac{2 \times 2}{2 \times 2} \quad \frac{2 \times 2}{2 \times 2}$$

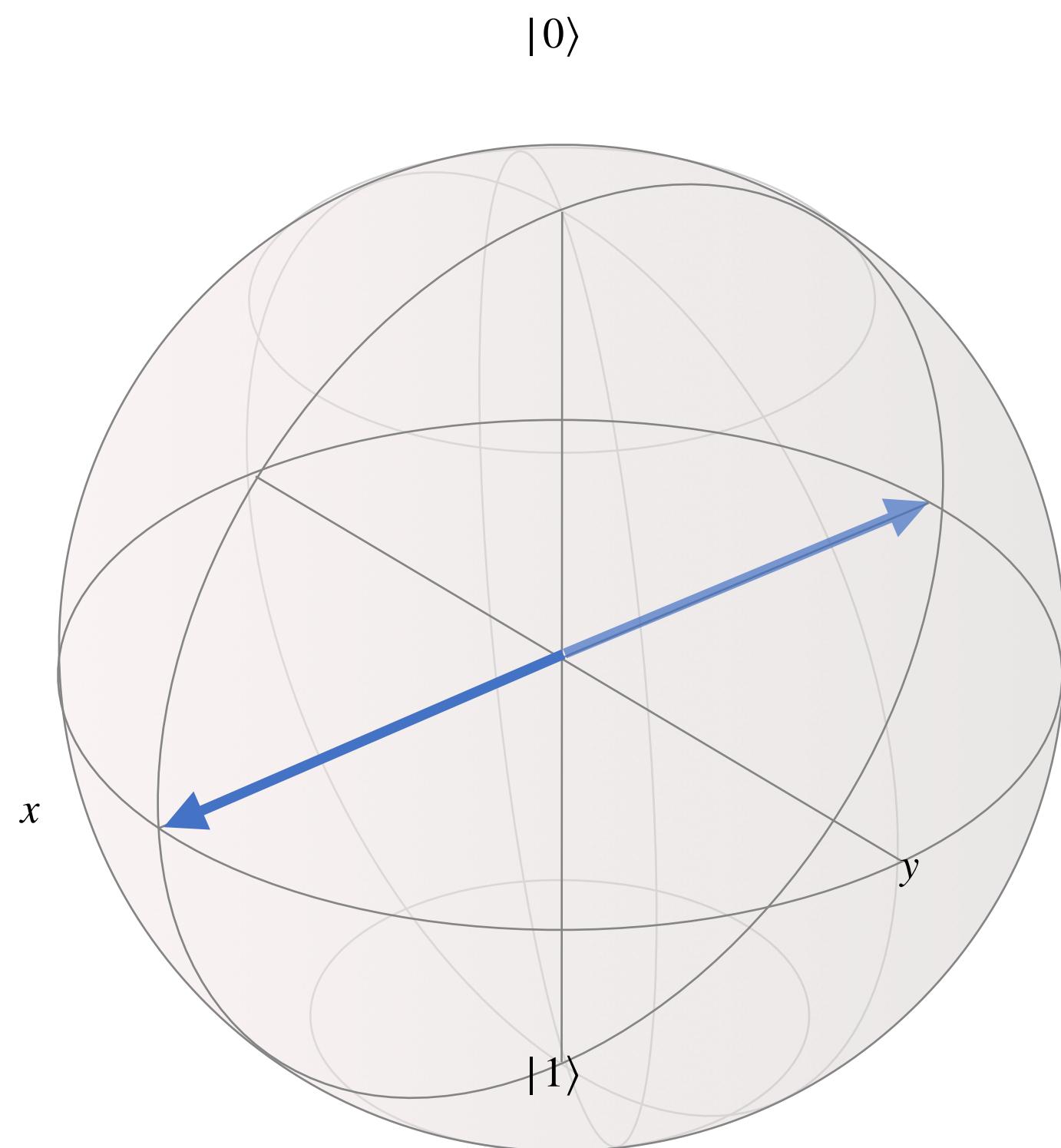
Approximate Circuit Equivalence



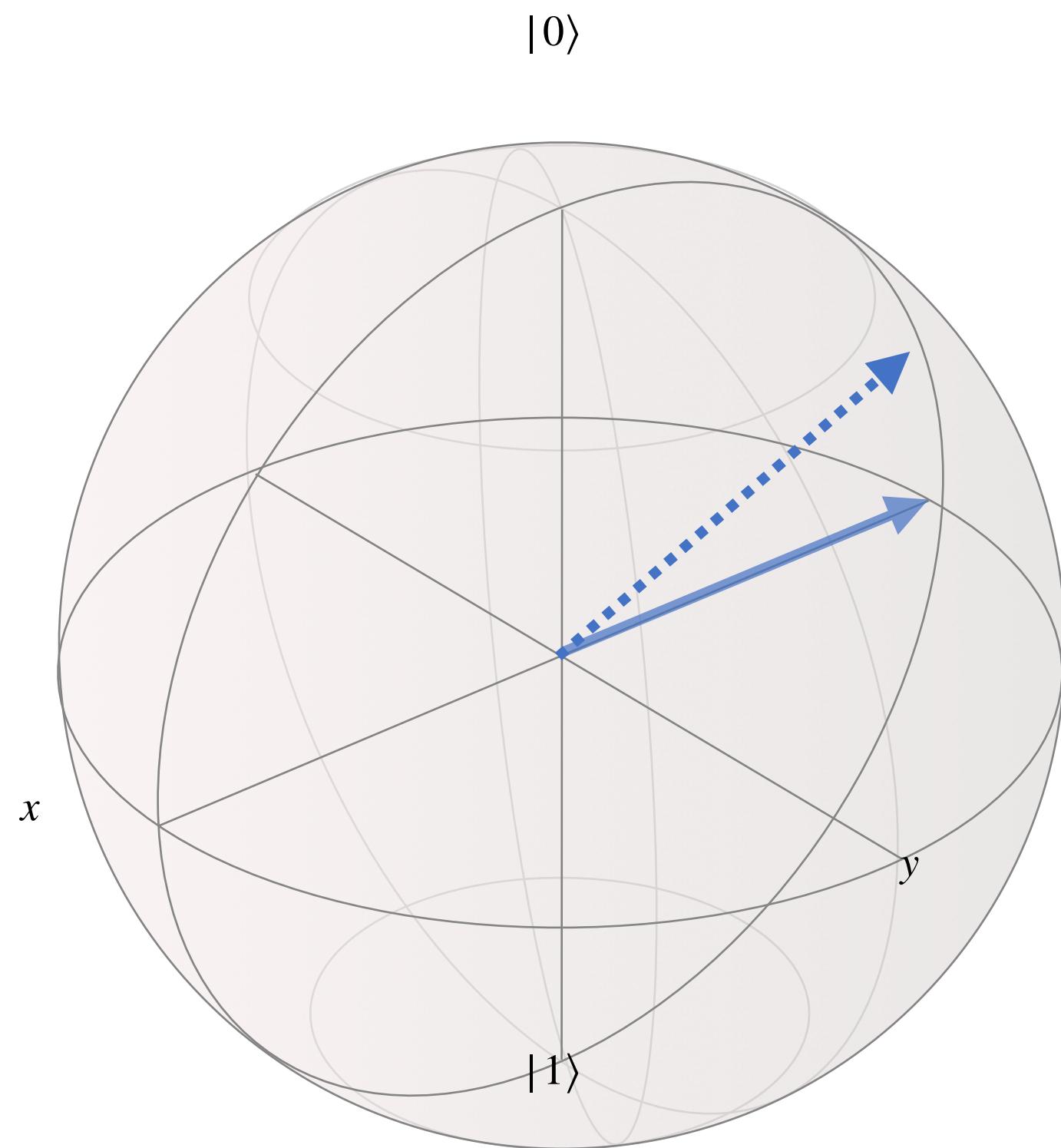
Approximate Circuit Equivalence



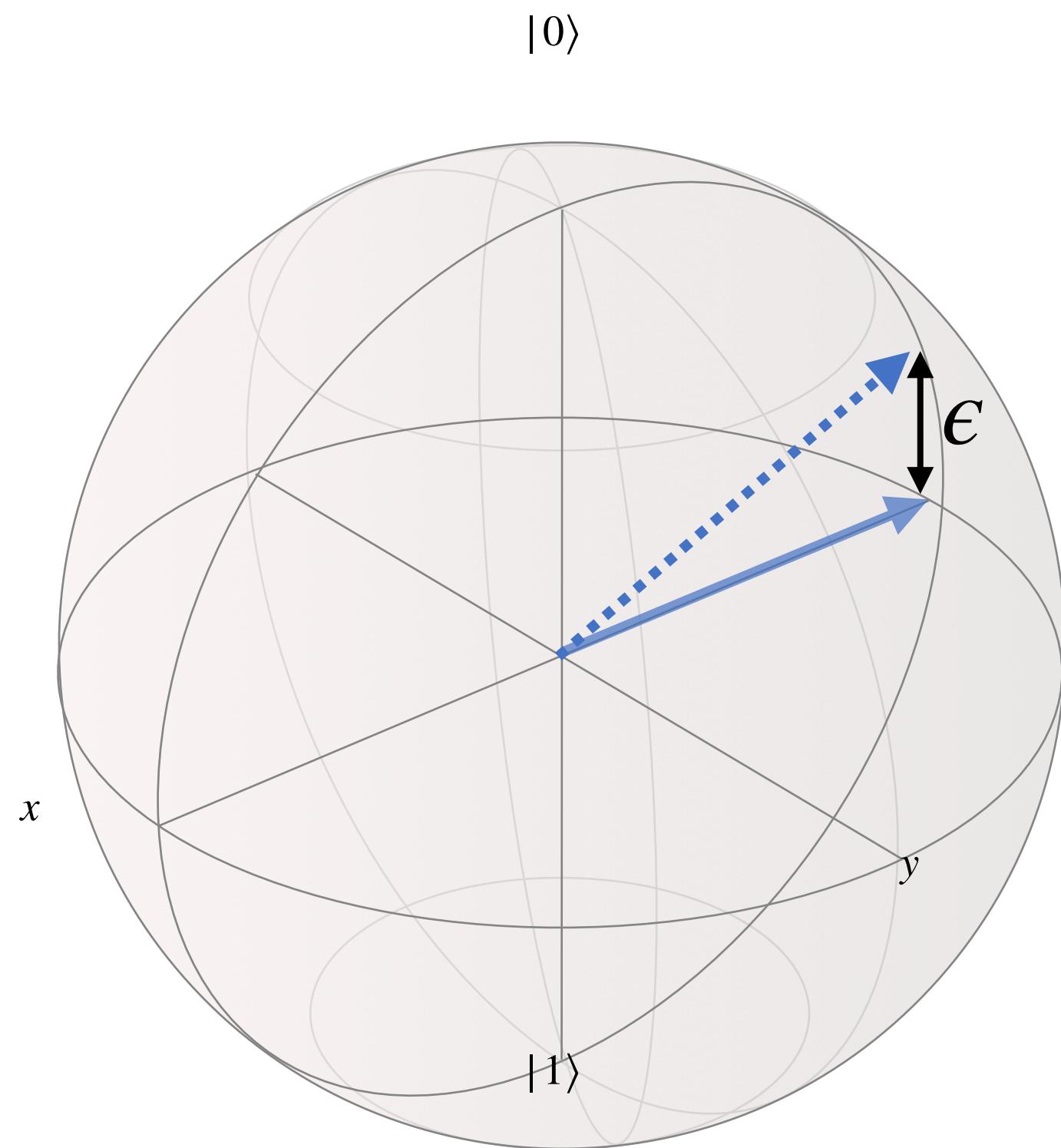
Approximate Circuit Equivalence



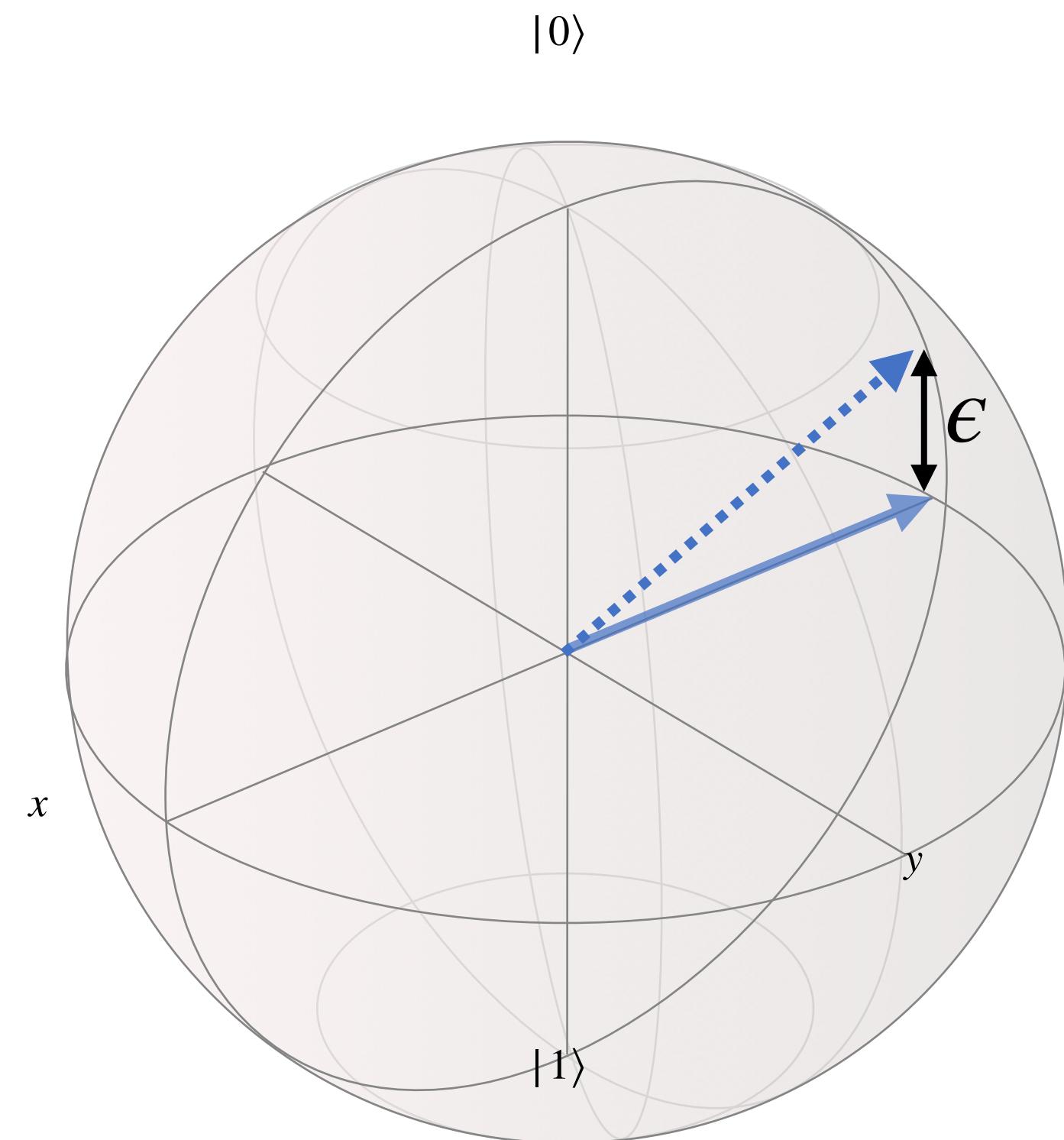
Approximate Circuit Equivalence



Approximate Circuit Equivalence



Approximate Circuit Equivalence



$$\Delta(U_C, U_{C'}) \leq \epsilon \iff C \equiv_{\epsilon} C'$$

Hilbert-Schmidt Distance

"easy" to compute

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$$\Delta_{HS}(U, U') := \sqrt{1 - \frac{|Tr(U^\dagger U')|^2}{2^{2n}}}$$

Hilbert–Schmidt Distance

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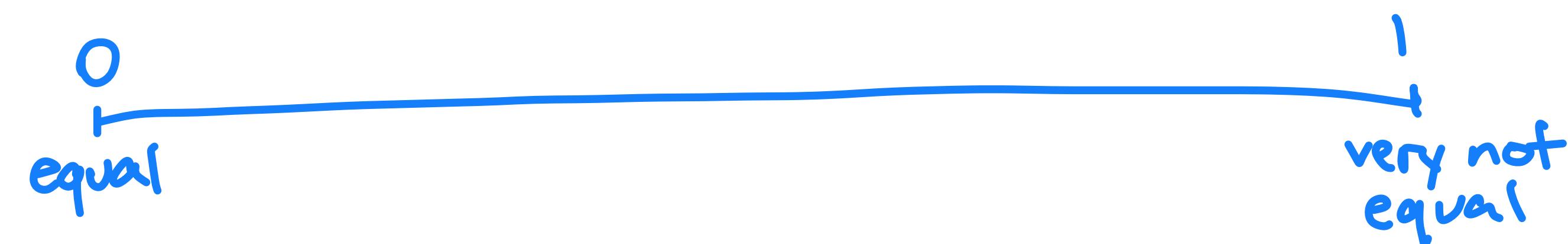
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↑
qubits

Hilbert-Schmidt Distance

"easy" to compute

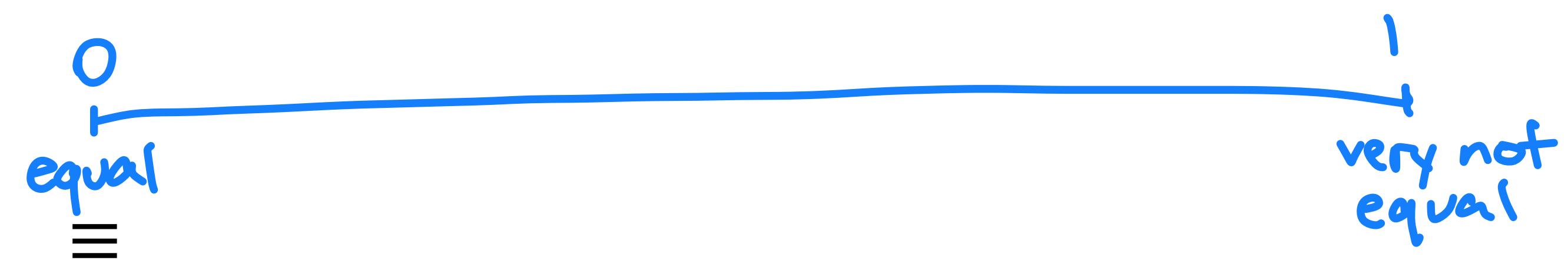
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Hilbert-Schmidt Distance

"easy" to compute

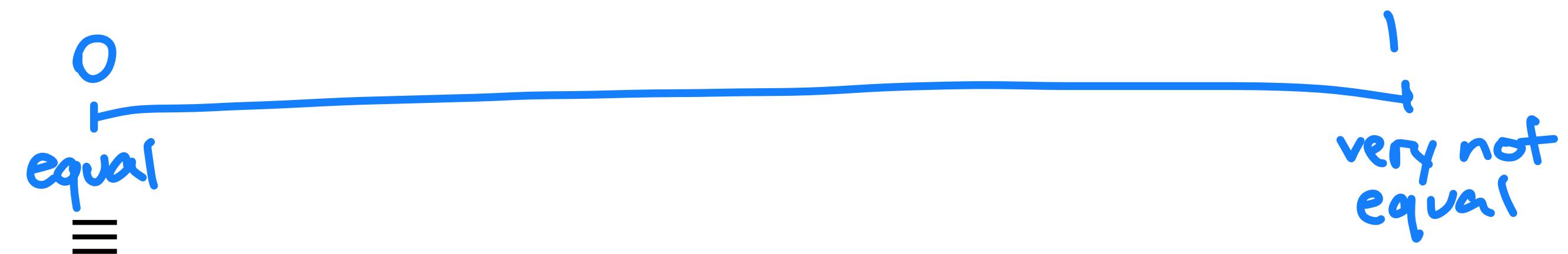
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Hilbert–Schmidt Distance

"easy" to compute

$$\Delta_{HS}(U, U') := \sqrt{1 - \frac{|Tr(U^\dagger U')|^2}{2^{2n}}}$$



Upper bound on total variation distance

Why Approximate Circuits?

Ideal

Circuit C

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Ideal

Circuit C

+ No noise

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Circuit C

$$+ \text{ No noise} = U_C$$

Why Approximate Circuits?

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Circuit C

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Reality

Circuit C

$$+ \text{ Noise} = U_{C_{noisy}}$$

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Circuit
 $C' \equiv_{\epsilon} C$

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 $C' \equiv_{\epsilon} C$

k fewer gates than C

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 $C' \equiv_\epsilon C$

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The hope: $U_{C'_{noisy}}$ closer to U_C than $U_{C_{noisy}}$

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 $C' \equiv_\epsilon C$

k fewer gates than C

The hope: $U_{C'_{noisy}}$ closer to U_C than $U_{C_{noisy}}$

Noise from k gates $> \epsilon$

Why Approximate Circuits?

FTQC **requires**
approximations for
arbitrary angle rotations

Ideal

Circuit C

$$+ \text{ No noise} = U_C$$

Reality

Circuit C

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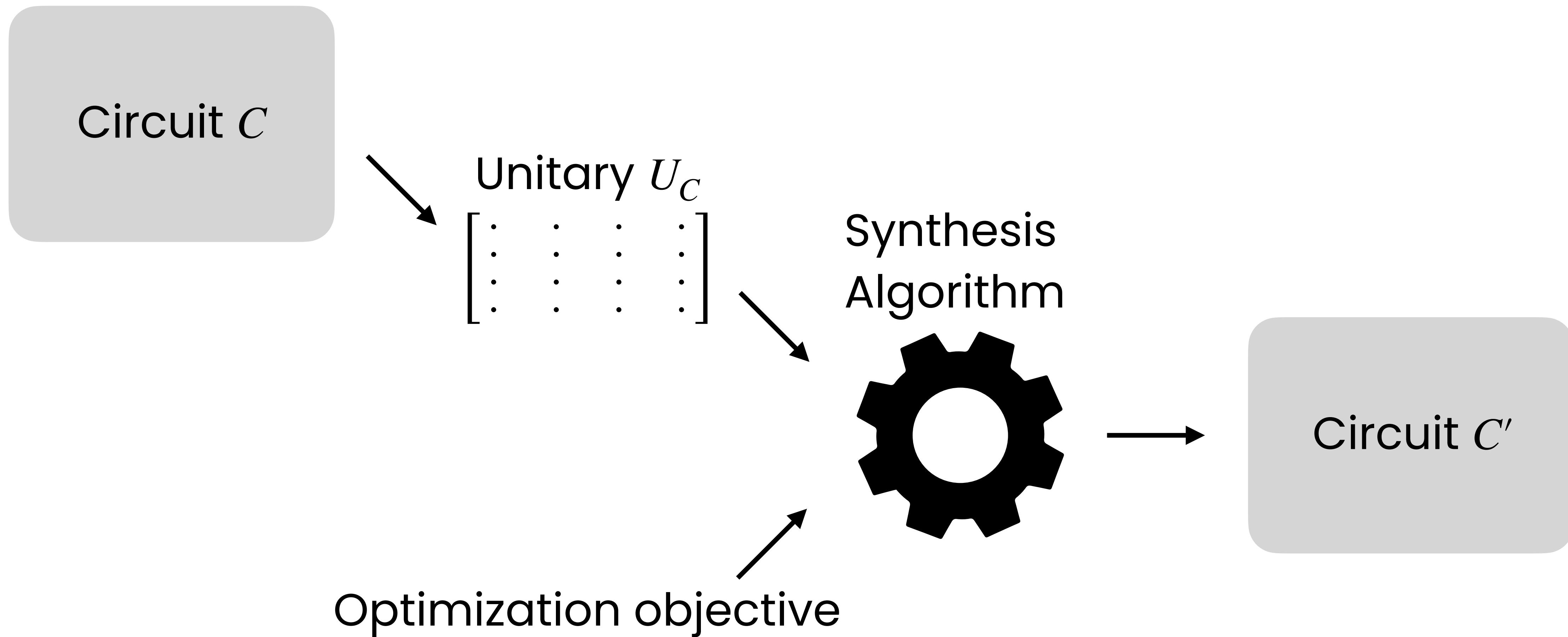
Circuit
 $C' \equiv_{\epsilon} C$

k fewer gates than C

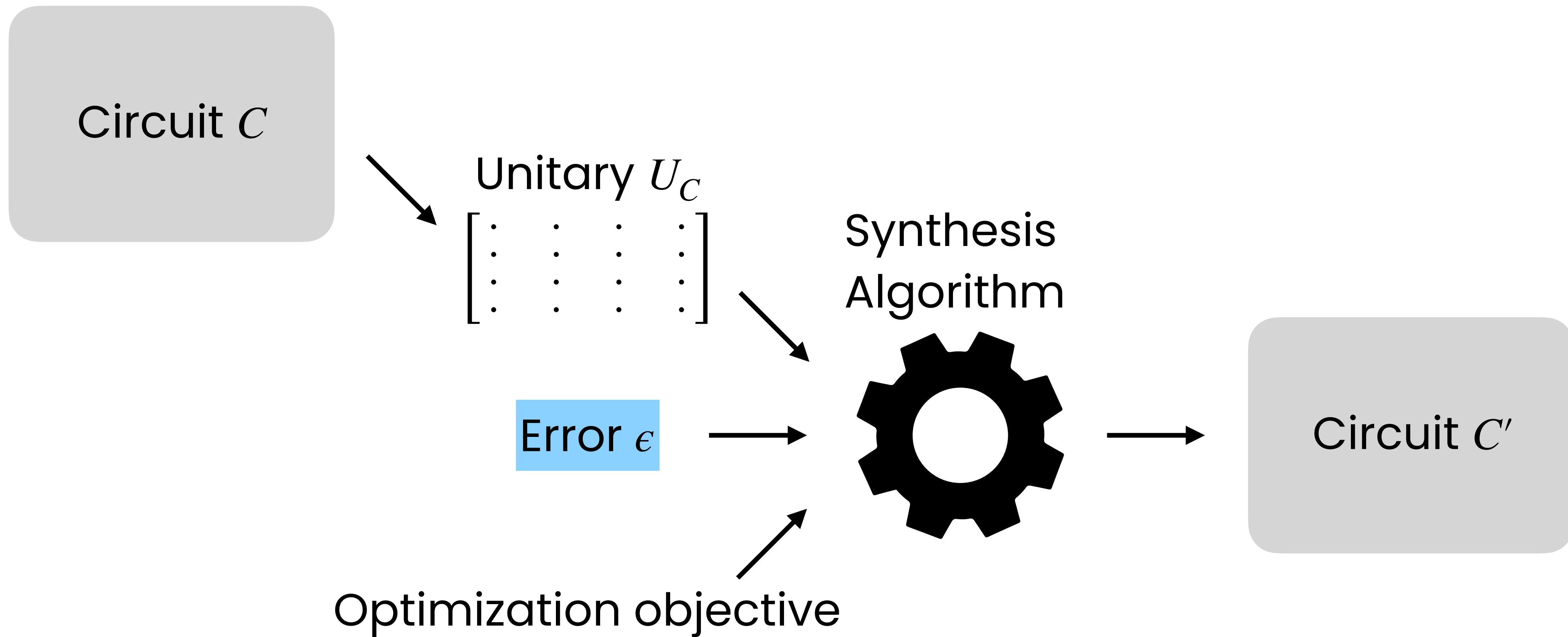
The hope: $U_{C'_{noisy}}$ closer to U_C than $U_{C_{noisy}}$

Noise from k gates $> \epsilon$

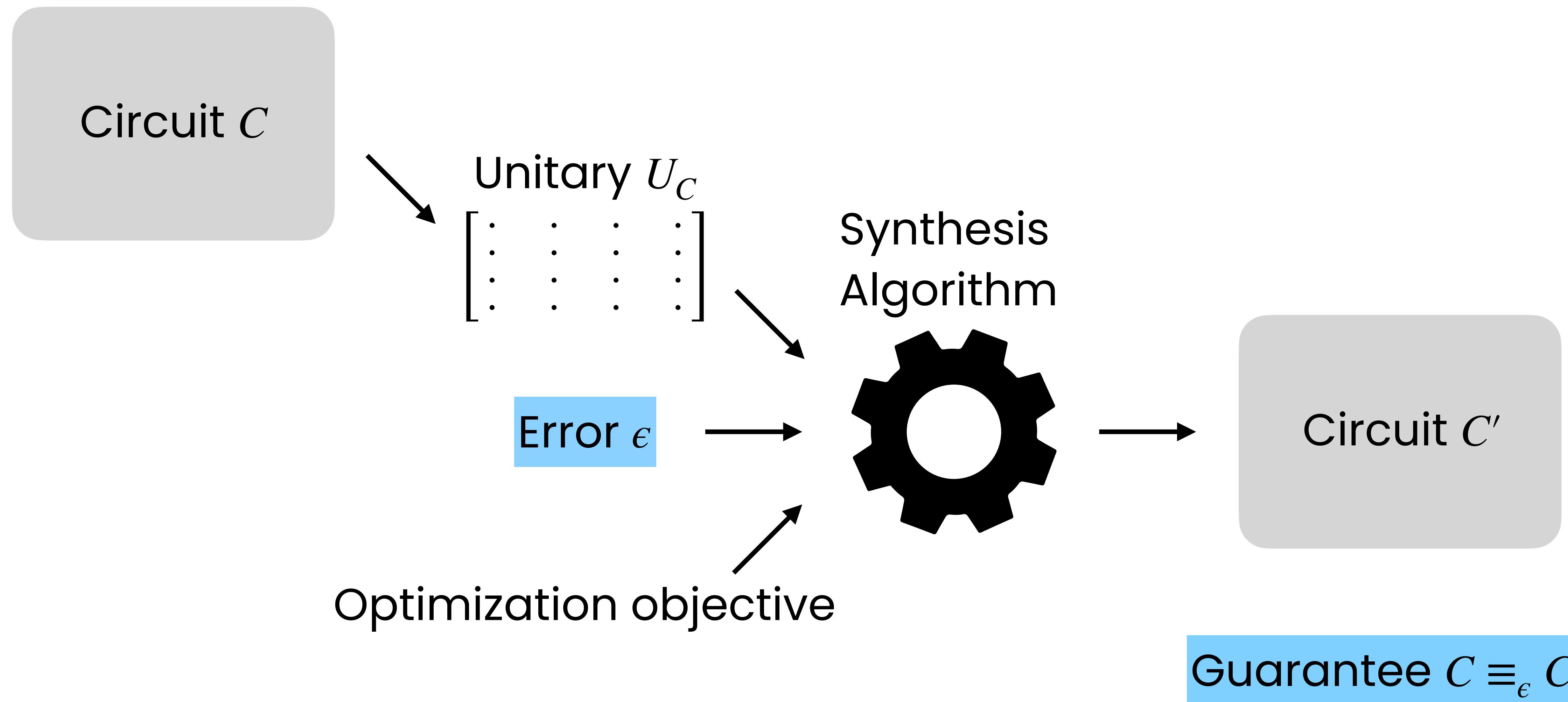
Unitary Synthesis



Unitary Synthesis



Unitary Synthesis



Rich Research Area

Rich Research Area

Synthesis of Quantum-Logic Circuits

Vivek V. Shende, Stephen S. Bullock, and Igor L. Markov, *Senior Member, IEEE*

Rich Research Area

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Towards Optimal Topology Aware Quantum Circuit Synthesis

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LEAP: Scaling Numerical Optimization Based Synthesis Using an Incremental Approach

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ED YOUNIS, LINDSAY BASSMAN OFTELIE, WIM LAVRIJSEN, and COSTIN IANCU,
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Exact synthesis of multiqubit Clifford+ T circuits

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HAKJOO OH*, Korea University, Republic of Korea

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SyntheIQ: Fast and Versatile Quantum Circuit Synthesis

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Berkeley Quantum Synthesis Toolkit

● OpenQASM ⭐ 132 📅 38

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Rich Research Area

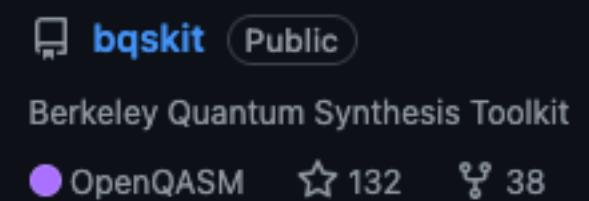
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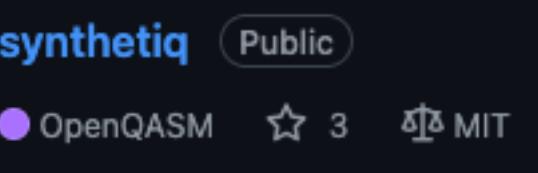
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Techniques:

- analytical

Rich Research Area

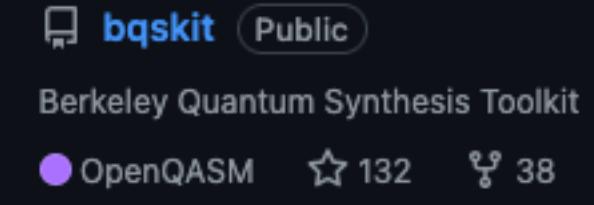
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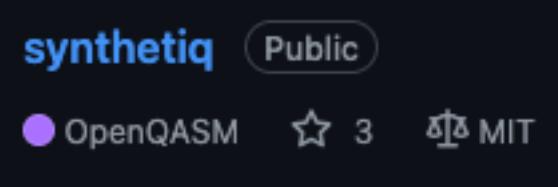
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Techniques:

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Rich Research Area

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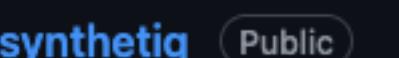
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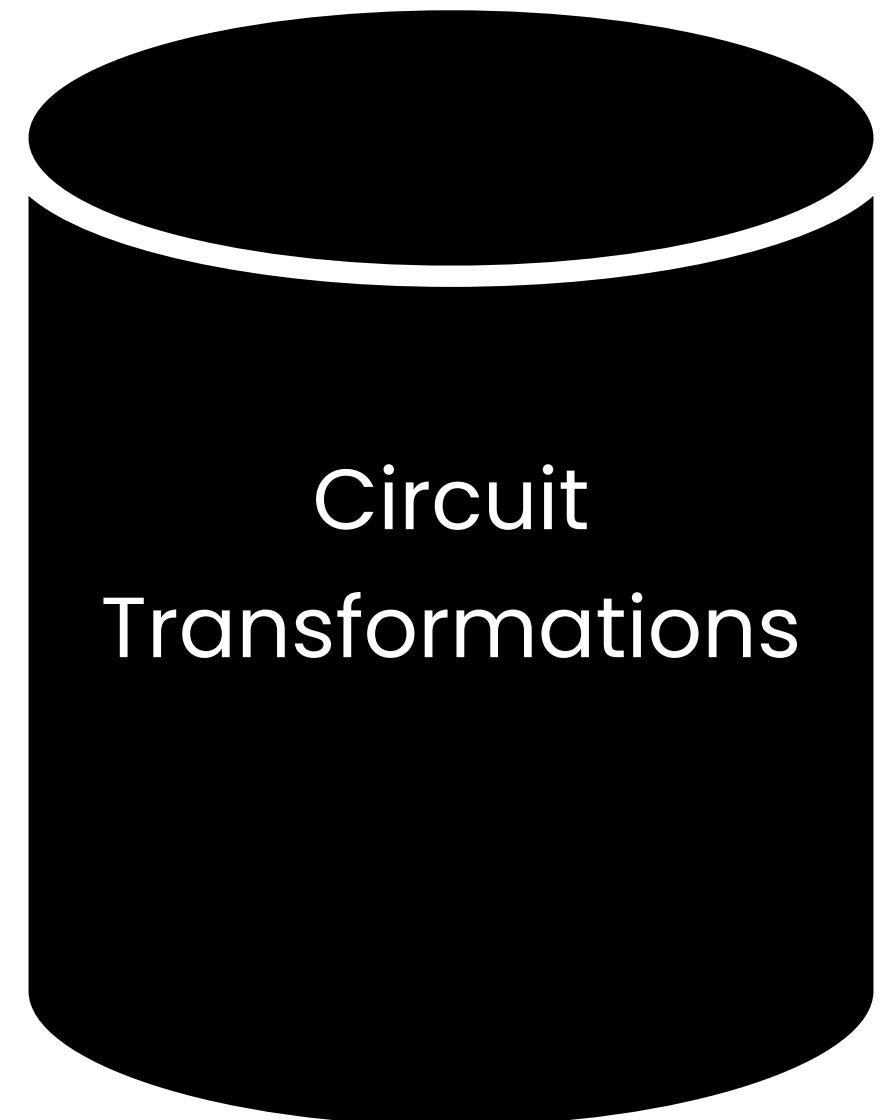
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Techniques:

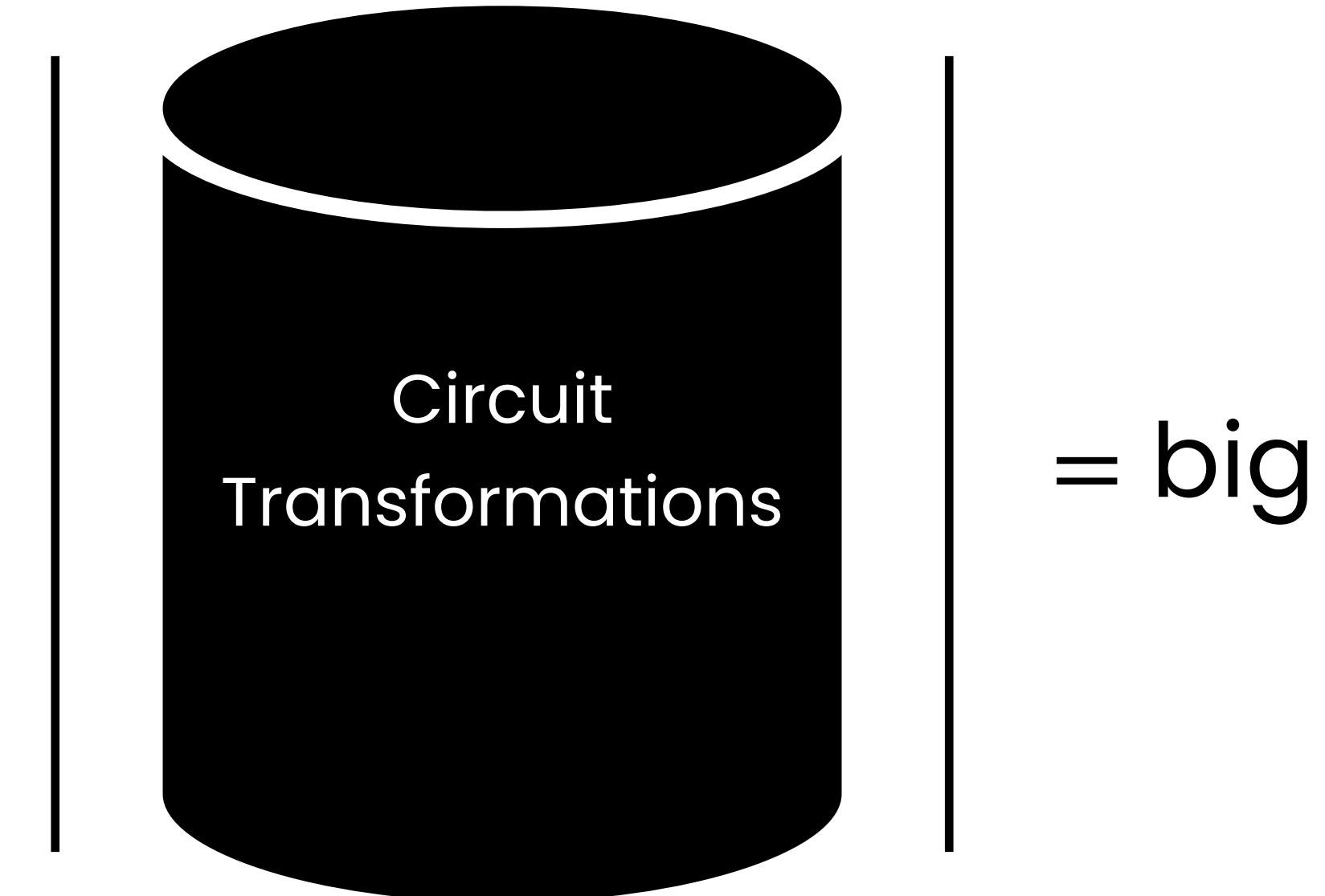
- analytical
- numerical optimization
- explicit search

Scheduling Transformations

Scheduling is hard

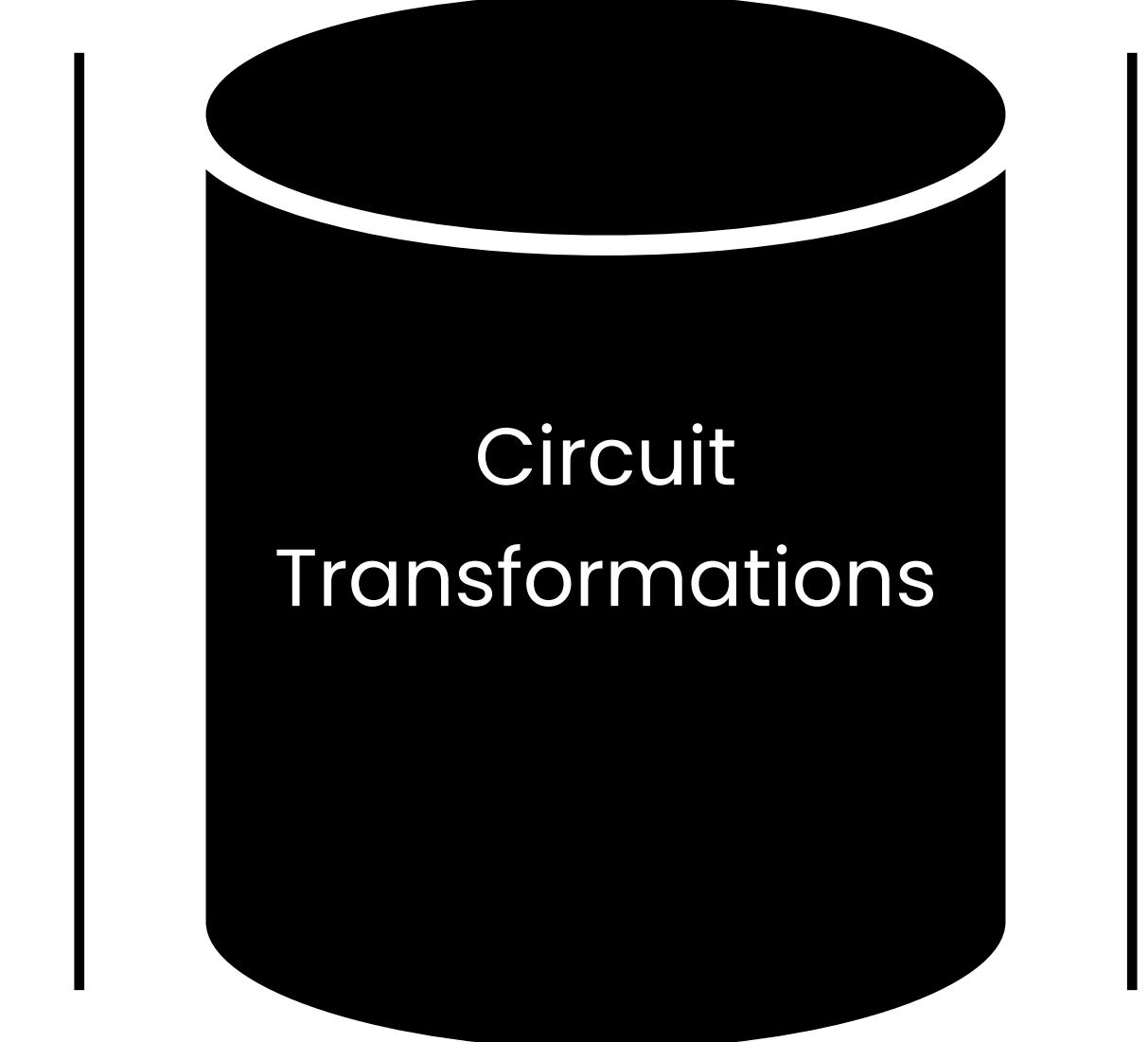


Scheduling is hard



Scheduling is hard

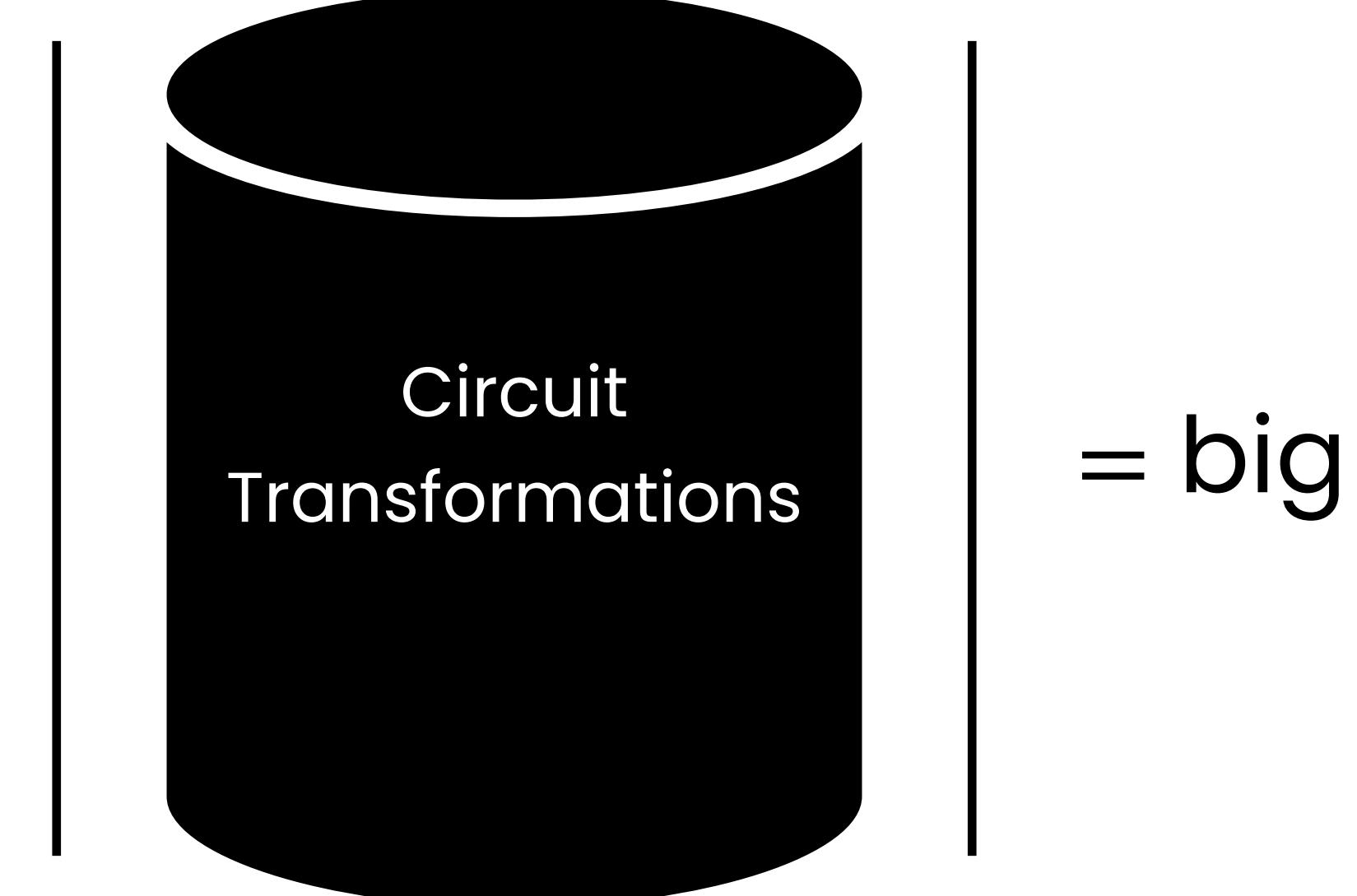
In what order to apply???



= big

Scheduling is hard

In what order to apply???

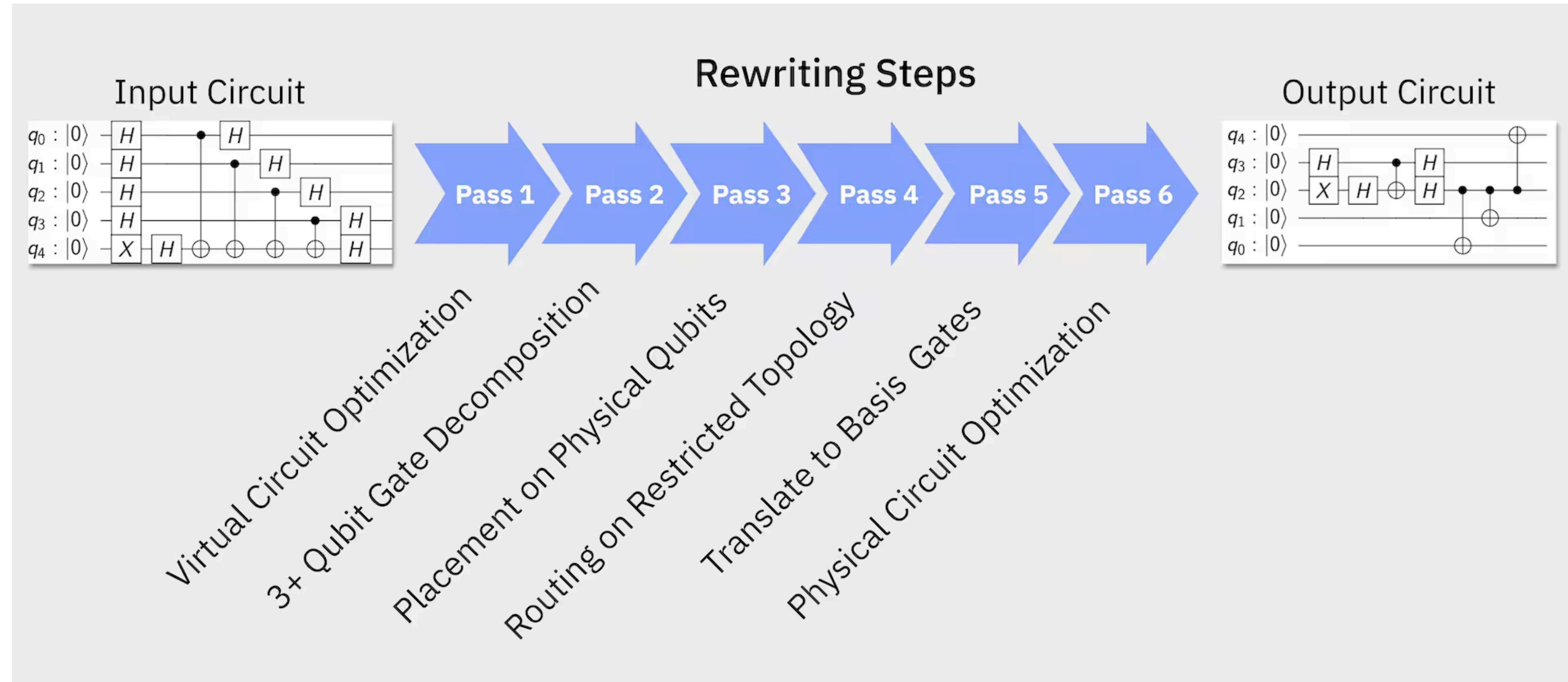


"phase-ordering problem"

Qiskit

<https://docs.quantum.ibm.com/api/qiskit/transpiler#optimization-stage>

https://github.com/Qiskit/qiskit/blob/stable/1.4/qiskit/transpiler/preset_passmanagers/level3.py



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[qiskit / qiskit / transpiler / preset_passmanagers / level3.py](#) ↑ Top

Code Blame 119 lines (104 loc) · 4.8 KB ·

Raw

```
26
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28     """Level 3 pass manager: heavy optimization by noise adaptive qubit mapping and
29     gate cancellation using commutativity rules and unitary synthesis.
30
31     This pass manager applies the user-given initial layout. If none is given, a search
32     for a perfect layout (i.e. one that satisfies all 2-qubit interactions) is conducted.
33     If no such layout is found, and device calibration information is available, the
34     circuit is mapped to the qubits with best readouts and to CX gates with highest fidelit
35
36     The pass manager then transforms the circuit to match the coupling constraints.
37     It is then unrolled to the basis, and any flipped cx directions are fixed.
38     Finally, optimizations in the form of commutative gate cancellation, resynthesis
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[qiskit / qiskit / transpiler / preset_passmanagers / level3.py](#) [↑ Top](#)

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Circuit Transformations

Rewrite rules Resynthesis

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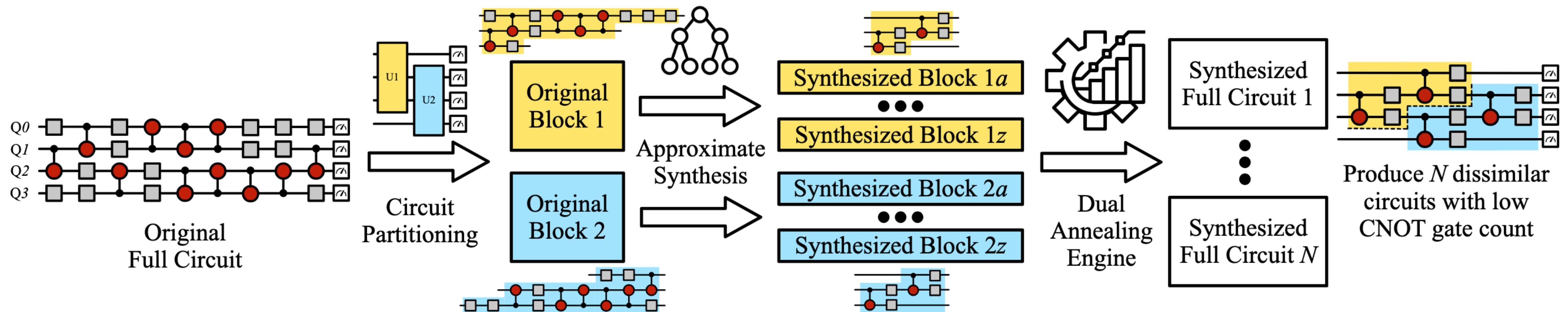
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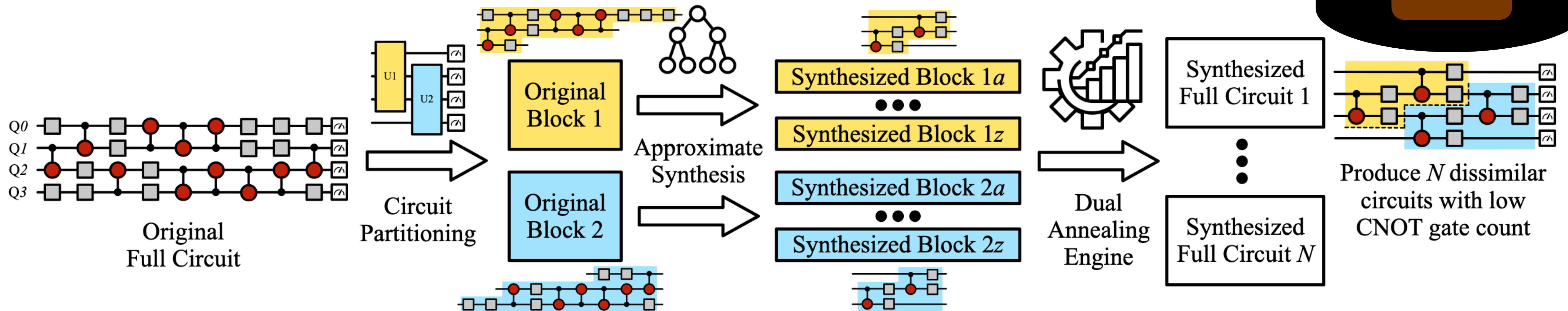
BQSKit



QUEST: systematically approximating quantum circuits for higher output fidelity [Patel et al. 2022]

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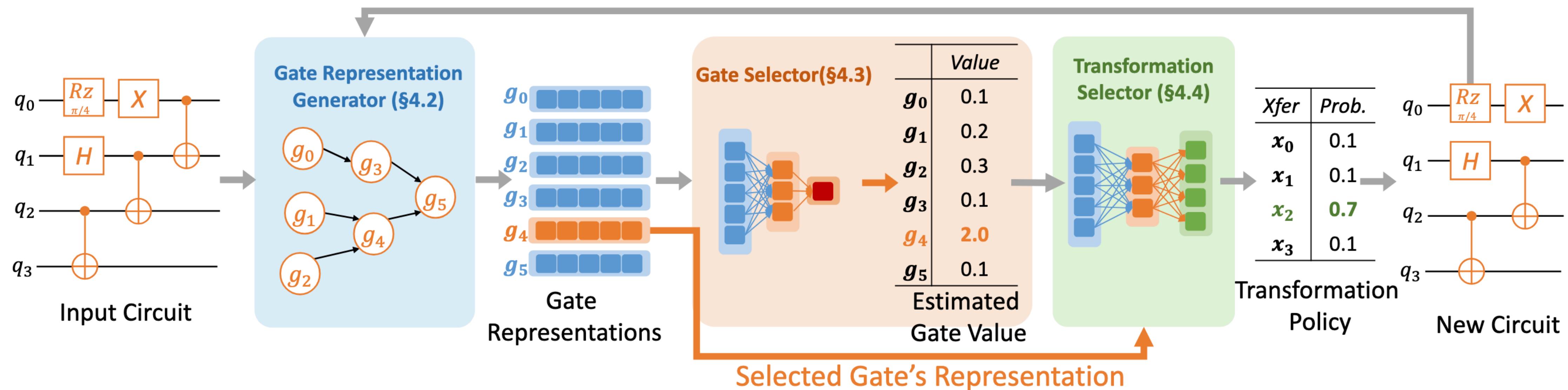
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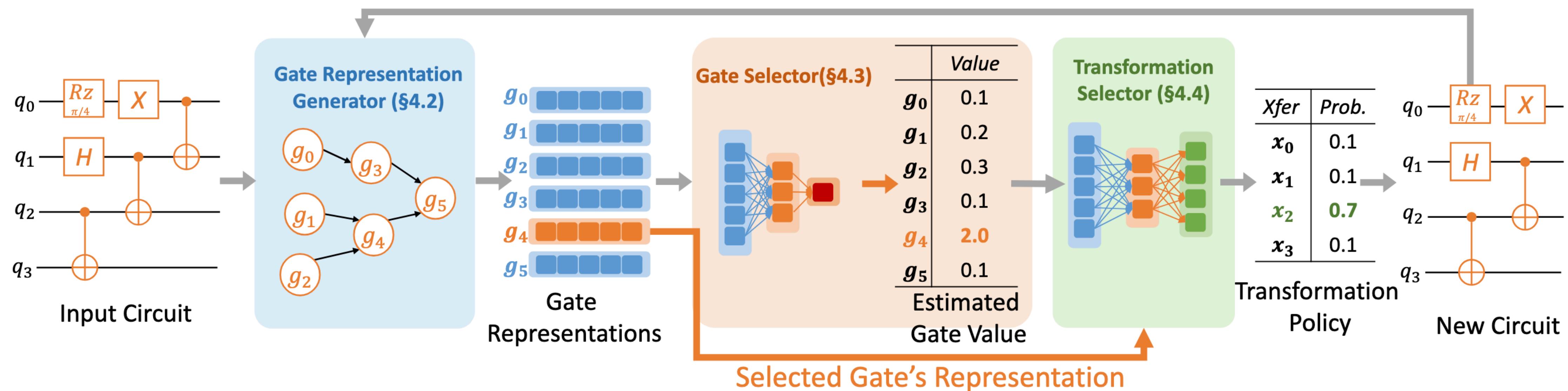
<https://github.com/BQSKit/bqskit>

Quarl



Quarl: A Learning-Based Quantum Circuit Optimizer [Li et al. 2024]

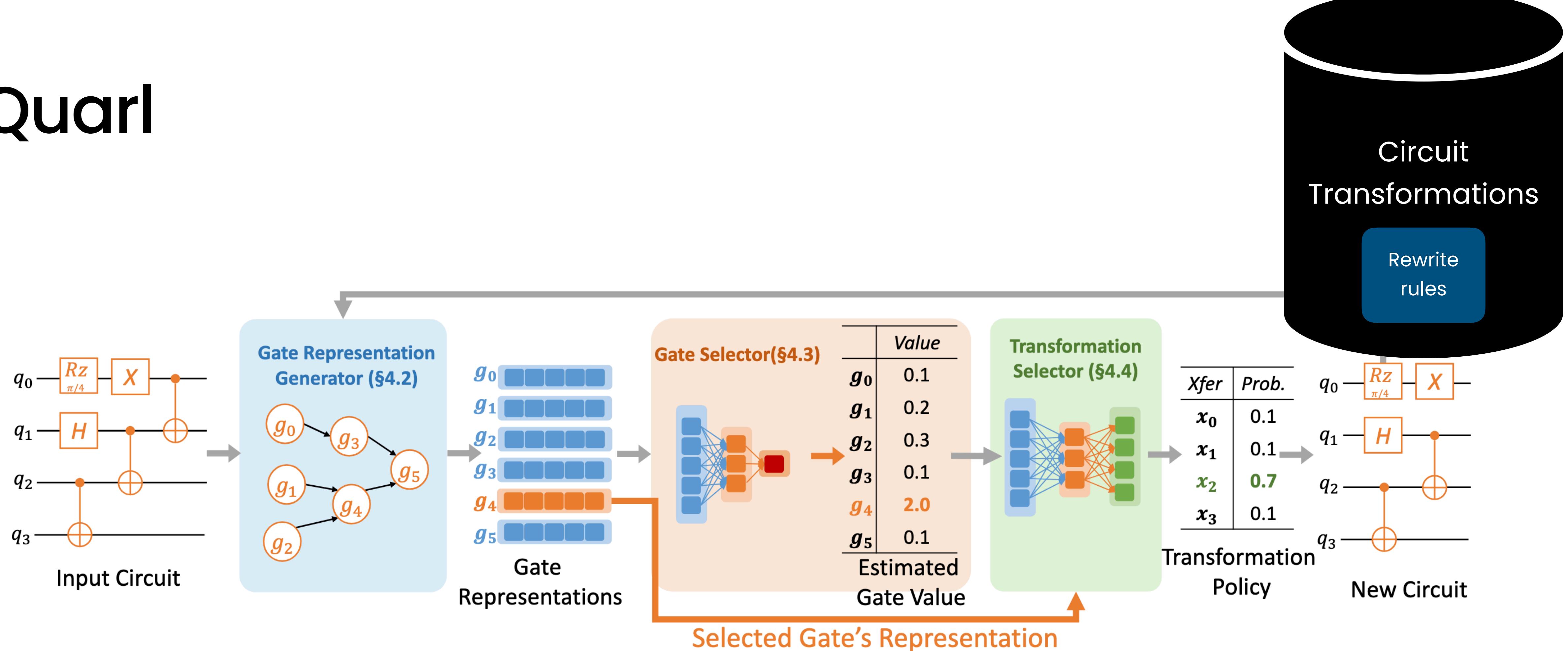
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GUOQ

Optimizing Quantum Circuits, Fast and Slow

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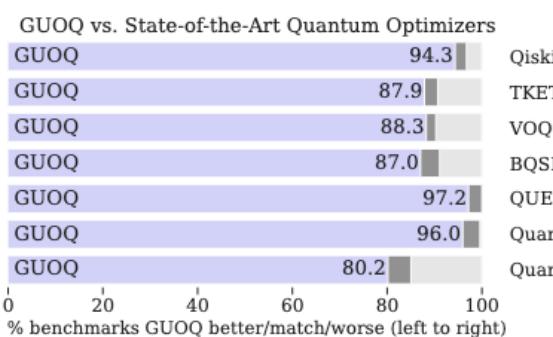
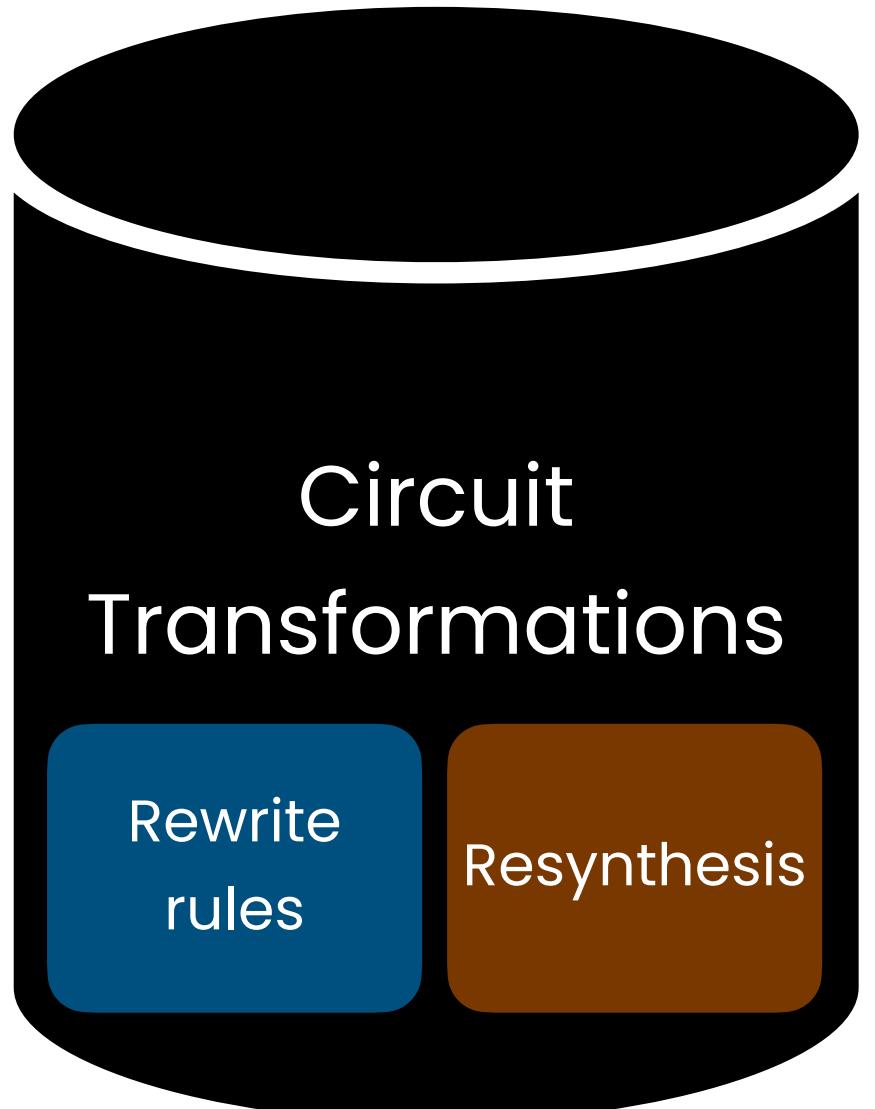


Figure 1. Summary of `guoq` compared to state-of-the-art on 2-qubit-gate reduction for the `IMMQ20` gate set. `guoq` and `BQSKit` are allowed to approximate the circuit up to $\epsilon = 10^{-8}$.
*Quarl requires an NVIDIA A100 (40GB) GPU to run.

ASPLOS talk on April 1st!



GUOQ

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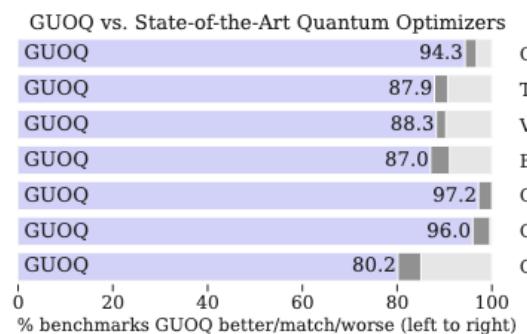
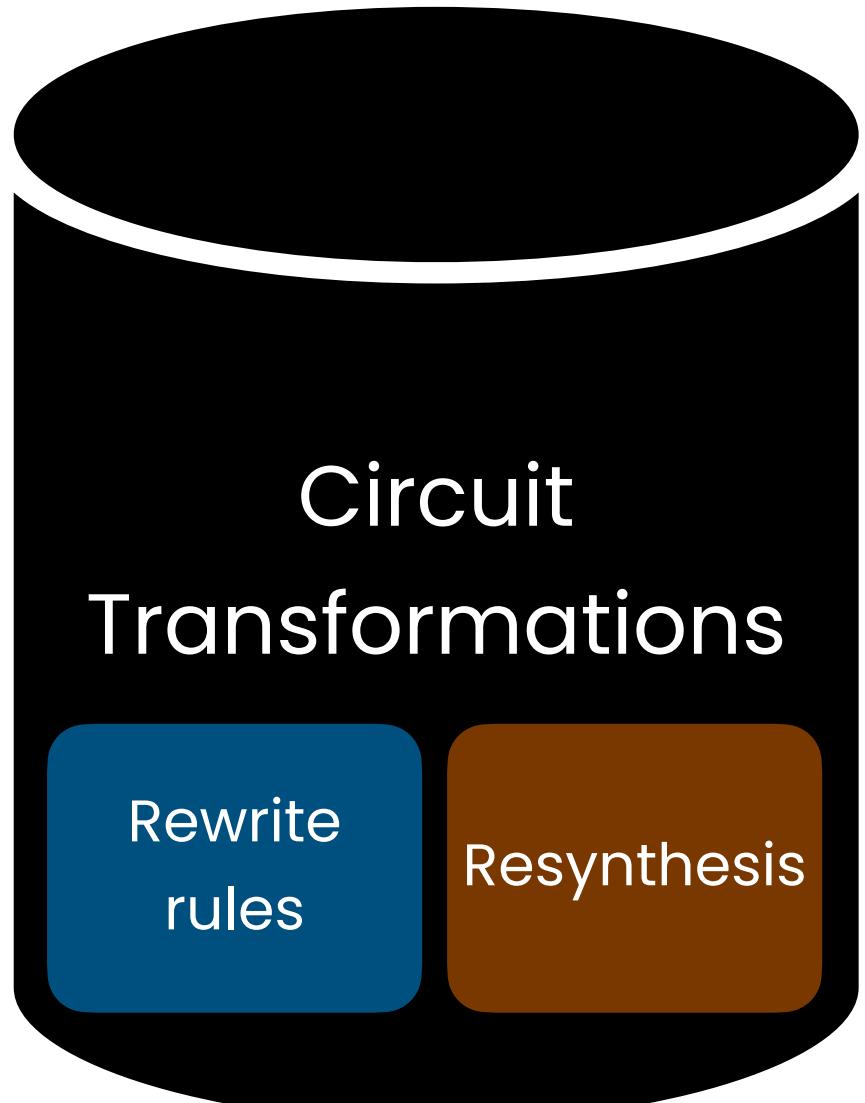


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1. Randomly pick a transformation.
2. Randomly pick a subcircuit to transform.
3. Accept the result if better than current.
Else, reject with high probability.

GUOQ

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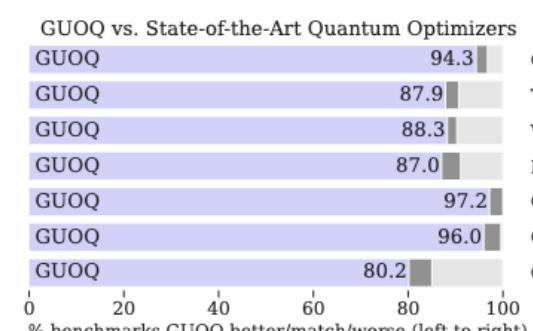
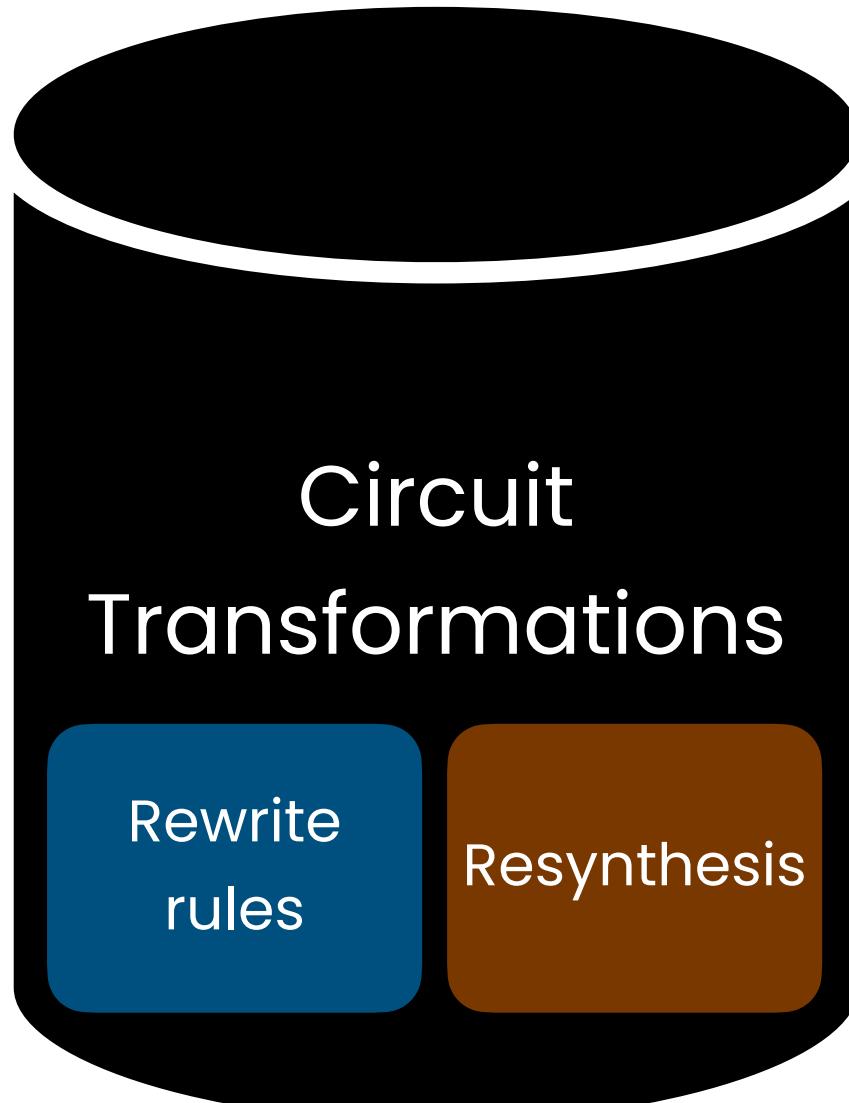
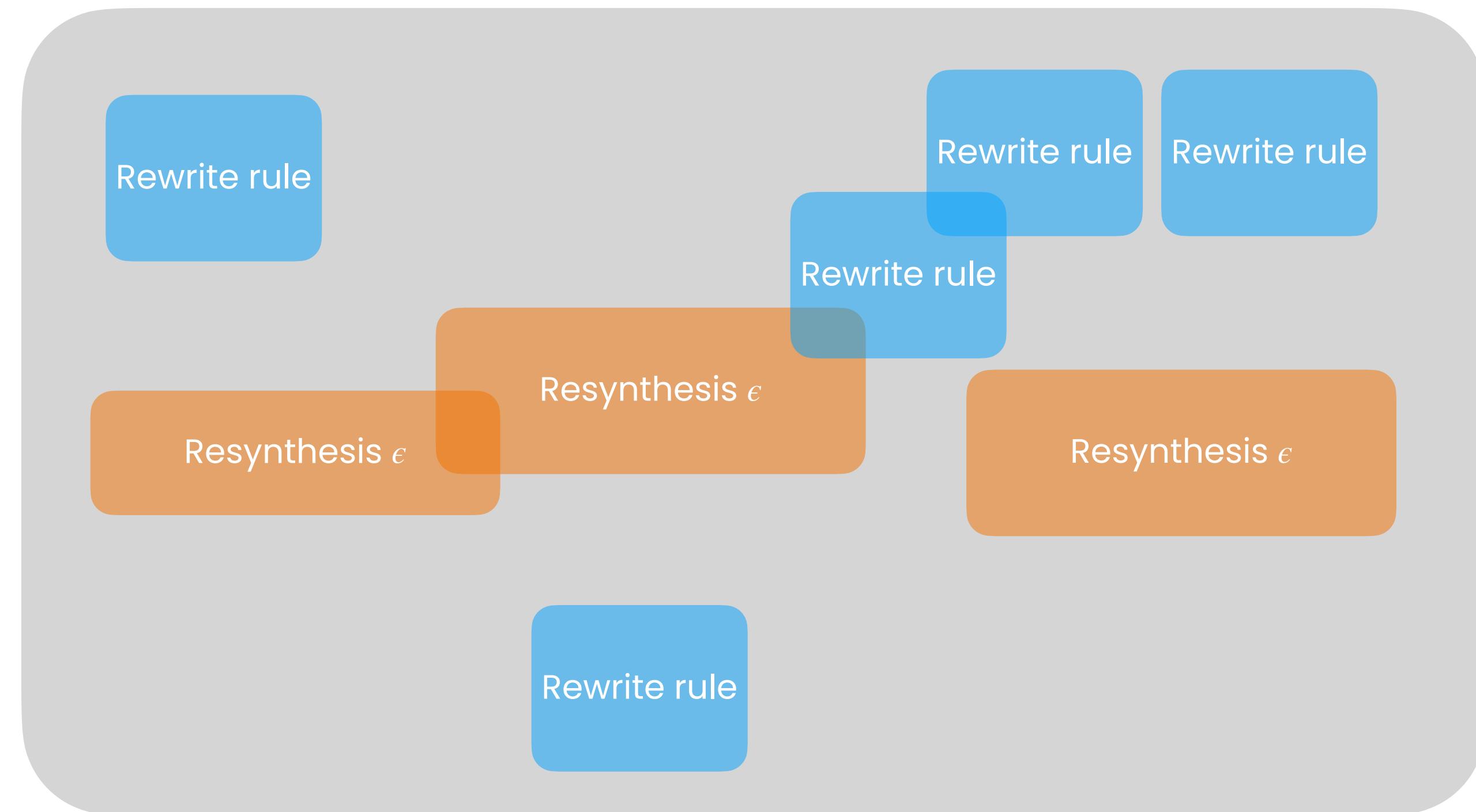


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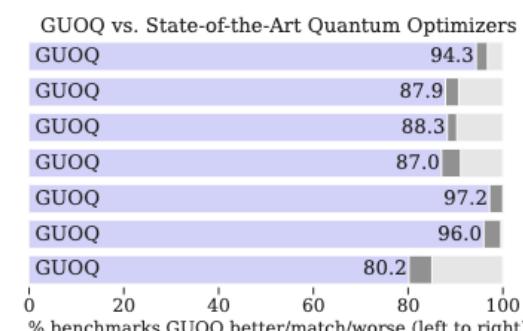
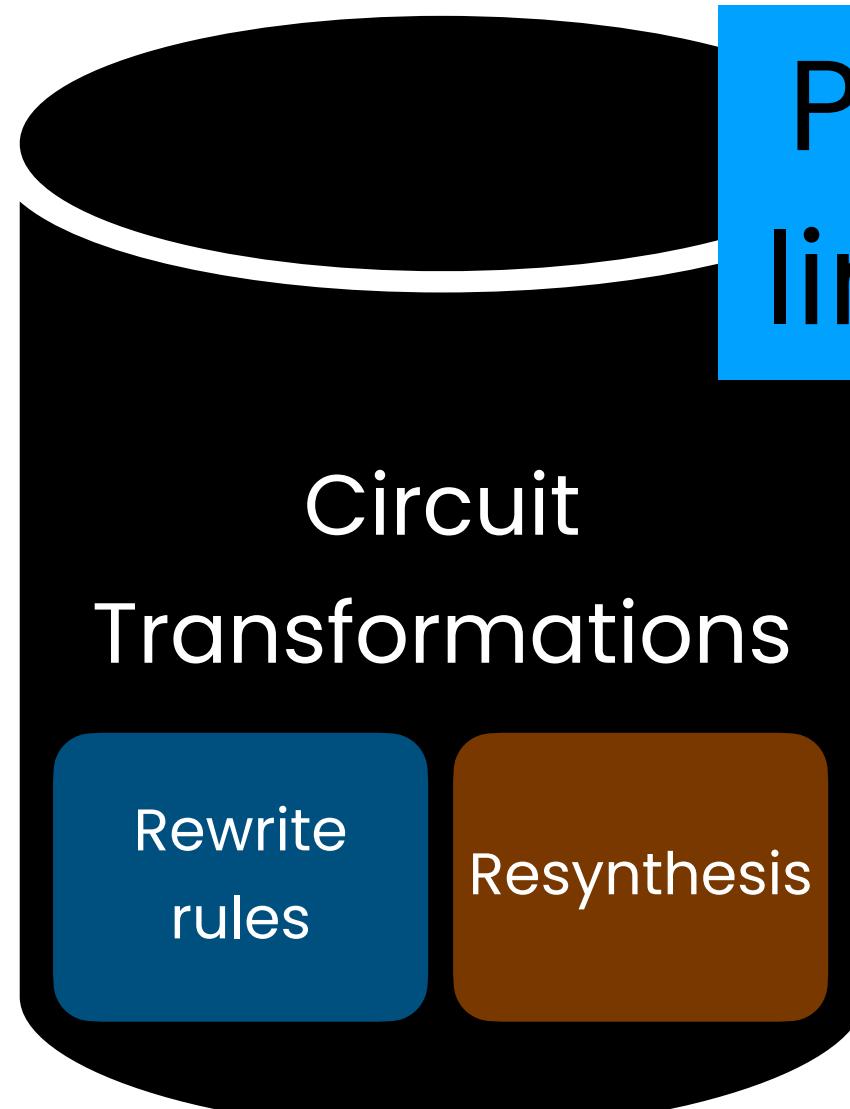


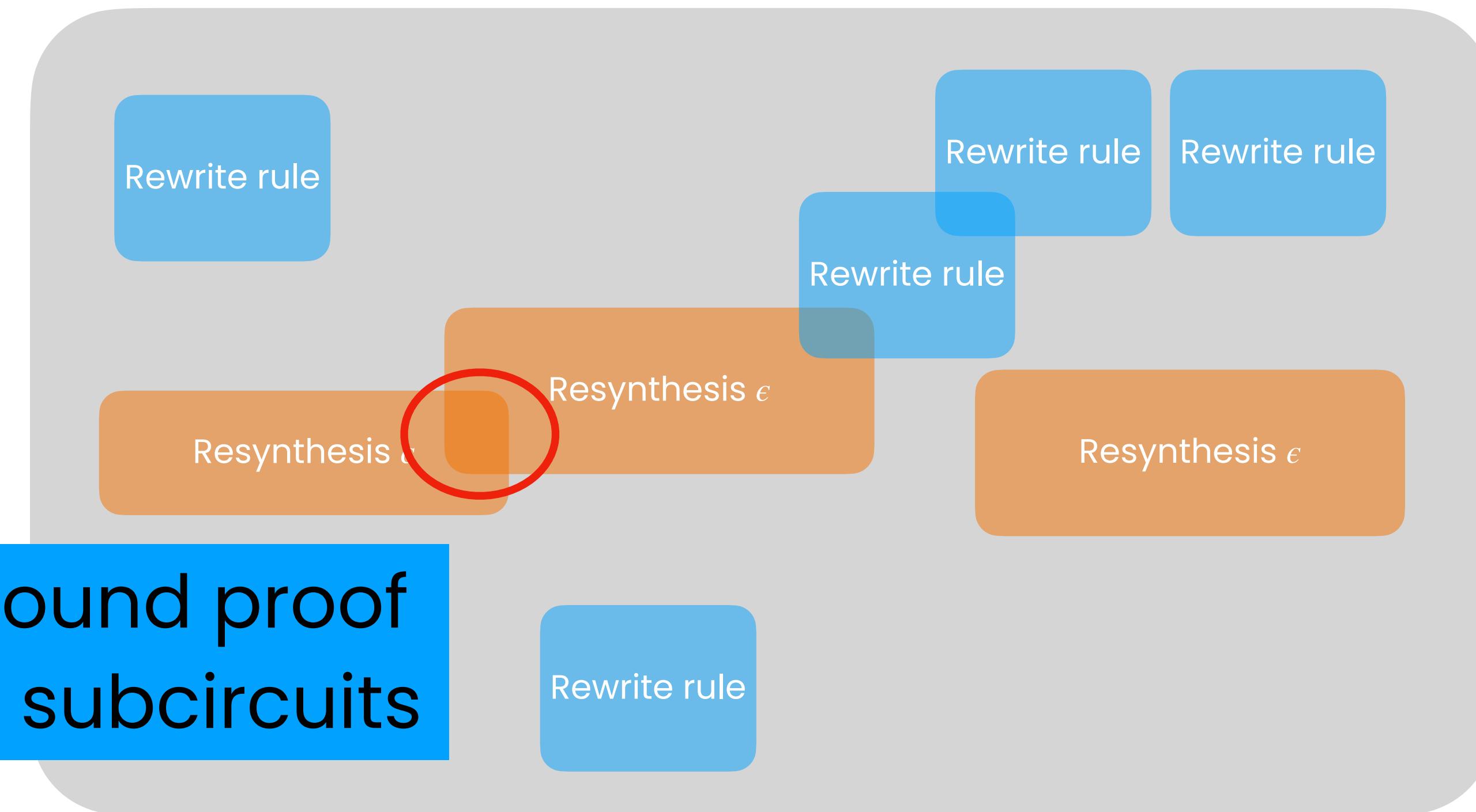
Figure 1. Summary of *guoq* compared to state-of-the-art on 2-qubit-gate reduction for the IBMQ20 gate set. *guoq* and BQSKit are allowed to approximate the circuit up to $\epsilon = 10^{-8}$.
*Quarl requires an NVIDIA A100 (40GB) GPU to run.

ASPLOS talk on April 1st!



Prior work: error bound proof limited to **disjoint** subcircuits

1. Randomly pick a transformation.
2. Randomly pick a subcircuit to transform.
3. Accept the result if better than current.
Else, reject with high probability.



GUOQ

Optimizing Quantum Circuits, Fast and Slow

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Abstract

Optimizing quantum circuits is critical: the number of quantum operations needs to be minimized for a successful evaluation of a circuit on a quantum processor. In this paper we unify two disparate ideas for optimizing quantum circuits, *rewrite rules*, which are fast standard optimizer passes, and *unitary synthesis*, which is slow, requiring a search through the space of circuits. We present a clean, unifying framework for thinking of rewriting and resynthesis as abstract circuit transformations. We then present a radically simple algorithm, *guoq*, for optimizing quantum circuits that exploits the synergies of rewriting and resynthesis. Our extensive evaluation demonstrates the ability of *guoq* to strongly outperform existing optimizers on a wide range of benchmarks.

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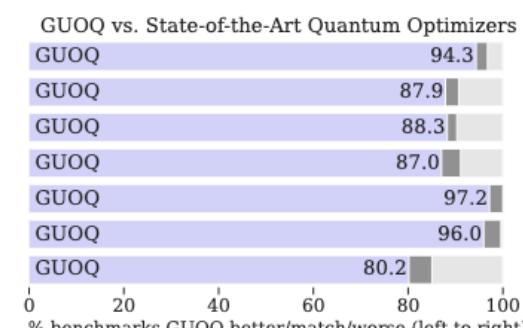
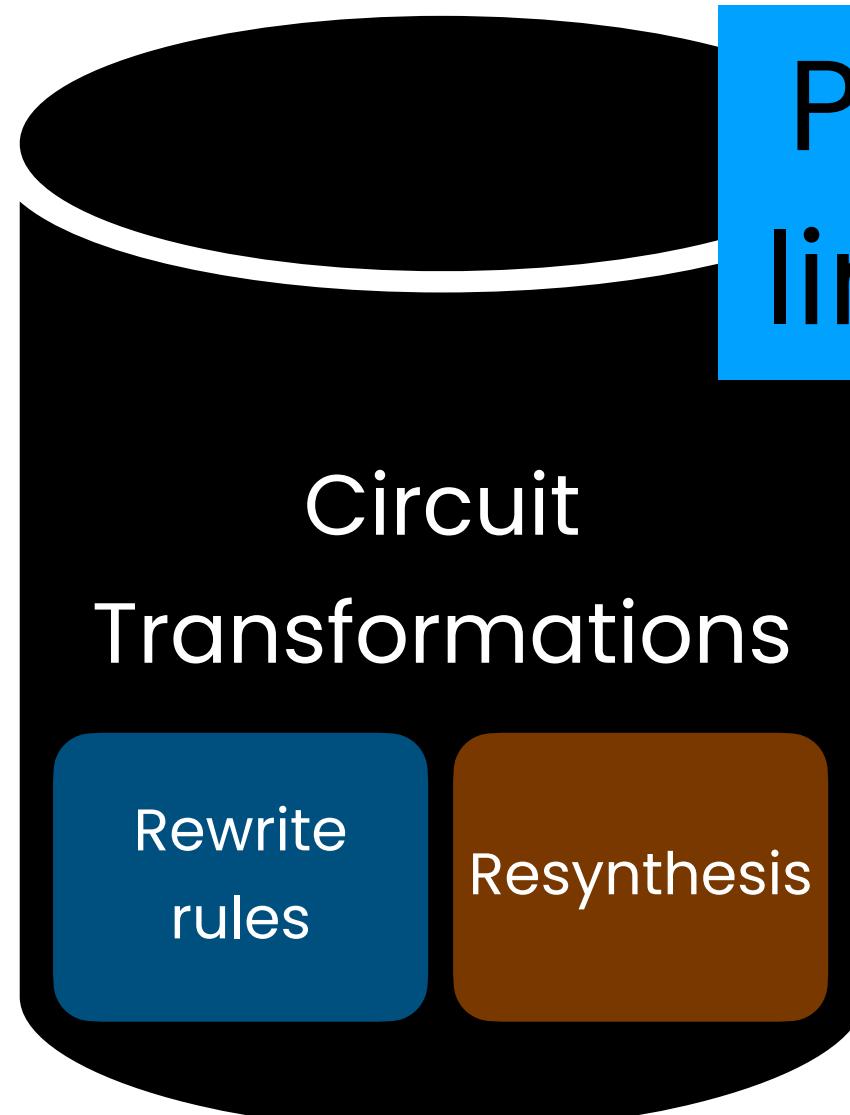
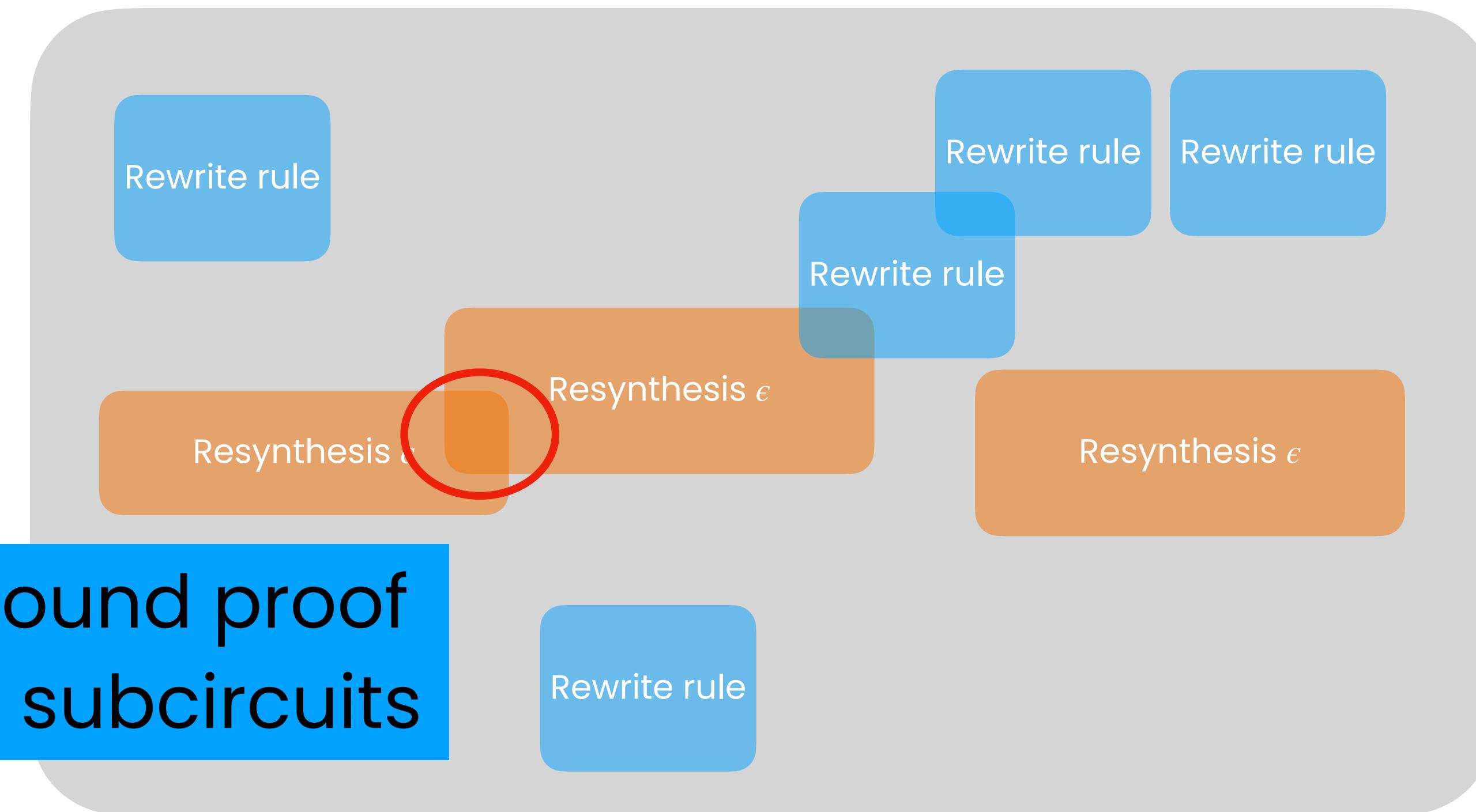


Figure 1. Summary of *guoq* compared to state-of-the-art on 2-qubit-gate reduction for the IBMQ20 gate set. *guoq* and BQSKit are allowed to approximate the circuit up to $\epsilon = 10^{-8}$.
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ASPLOS talk on April 1st!



1. Randomly pick a transformation.
2. Randomly pick a subcircuit to transform.
3. Accept the result if better than current.
Else, reject with high probability.



$$\epsilon_{total} \leq \epsilon + \epsilon + \epsilon$$

We extended to handle arbitrary subcircuits

GUOQ Motivation: Two Disparate Techniques

Rewrite Rules



GUOQ Motivation: Two Disparate Techniques

Rewrite Rules

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \\ - [R_z Q(k)] \quad H \quad \oplus \quad H \end{array} \quad \equiv \quad \begin{array}{c} \text{---} \\ | \quad | \quad | \\ - H \quad \oplus \quad H \quad [R_z Q(k)] \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ - [R_z Q(k)] \quad \oplus \quad [R_z Q(k')] \quad \oplus \quad | \end{array} \quad \equiv \quad \begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ - | \quad \oplus \quad [R_z Q(k')] \quad \oplus \quad [R_z Q(k)] \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \\ - [R_z Q(k)] \quad \oplus \quad | \end{array} \quad \equiv \quad \begin{array}{c} \text{---} \\ | \quad | \quad | \\ - | \quad \oplus \quad [R_z Q(k)] \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ - | \quad \oplus \quad | \quad \oplus \end{array} \quad \equiv \quad \begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ - | \quad \oplus \quad | \quad \oplus \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ - | \quad \oplus \quad | \quad \oplus \end{array} \quad \equiv \quad \begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ - | \quad \oplus \quad | \quad \oplus \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ - | \quad \oplus \quad H \quad \bullet \quad H \end{array} \quad \equiv \quad \begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ - H \quad \bullet \quad H \quad \oplus \end{array}$$

• • •



GUOQ Motivation: Two Disparate Techniques

Rewrite Rules

$$\begin{array}{c} \text{---} \\ | \\ \boxed{R_z Q(k)} \text{---} H \text{---} \oplus \text{---} H \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ \text{---} \text{---} \oplus \text{---} H \text{---} \boxed{R_z Q(k)} \text{---} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \\ \boxed{R_z Q(k)} \text{---} \oplus \text{---} \boxed{R_z Q(k')} \text{---} \oplus \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ \oplus \text{---} \boxed{R_z Q(k')} \text{---} \oplus \text{---} \boxed{R_z Q(k)} \text{---} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \\ \boxed{R_z Q(k)} \text{---} \oplus \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ \text{---} \text{---} \oplus \text{---} \boxed{R_z Q(k)} \text{---} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \\ \text{---} \text{---} \oplus \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ \text{---} \text{---} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \\ \text{---} \text{---} \oplus \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ \text{---} \text{---} \oplus \text{---} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \\ \oplus \text{---} \boxed{H} \text{---} \bullet \text{---} \boxed{H} \text{---} \oplus \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ \boxed{H} \text{---} \bullet \text{---} \boxed{H} \text{---} \oplus \text{---} \end{array}$$

• • •

Circuit Resynthesis



GUOQ Motivation: Two Disparate Techniques

Rewrite Rules

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \\ R_z Q(k) \quad H \quad \oplus \quad H \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \\ H \quad \oplus \quad H \quad R_z Q(k) \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ R_z Q(k) \quad \oplus \quad R_z Q(k') \quad \oplus \quad | \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ \oplus \quad R_z Q(k') \quad \oplus \quad R_z Q(k) \quad | \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \\ R_z Q(k) \quad | \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \\ | \quad \oplus \quad | \end{array}$$

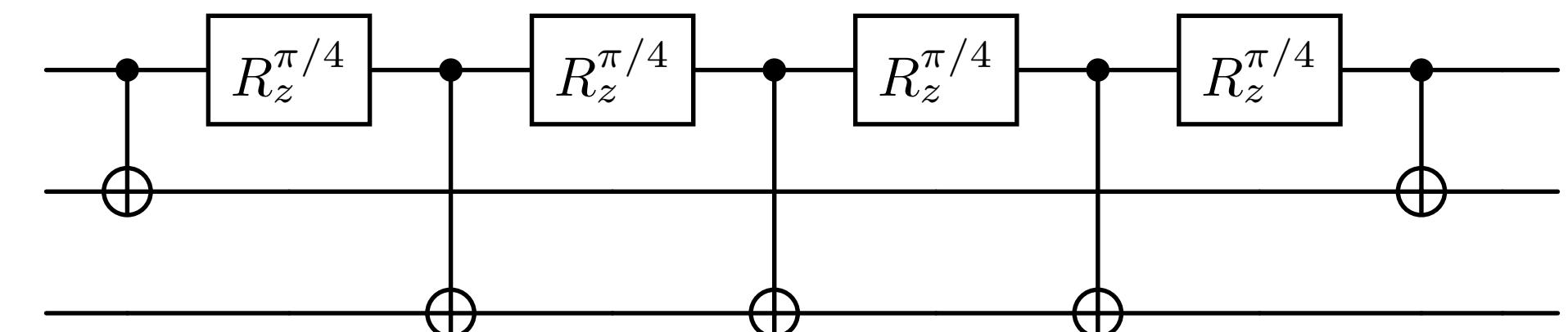
$$\begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ | \quad \oplus \quad | \quad \oplus \quad | \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ | \quad \oplus \quad | \quad \oplus \quad | \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ | \quad \oplus \quad | \quad | \quad | \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ | \quad | \quad | \quad \oplus \quad | \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ | \quad \oplus \quad H \quad | \quad H \quad | \quad | \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ | \quad H \quad | \quad \bullet \quad H \quad | \quad \oplus \quad | \end{array}$$

• • •

Circuit Resynthesis



GUOQ Motivation: Two Disparate Techniques

Rewrite Rules

$$\begin{array}{c} \text{---} \\ | \\ R_z Q(k) \end{array} \text{---} \text{---} H \oplus H \equiv \begin{array}{c} \text{---} \\ | \\ H \end{array} \text{---} \text{---} H \text{---} R_z Q(k)$$

$$\begin{array}{c} \text{---} \\ | \\ R_z Q(k) \end{array} \text{---} \oplus \begin{array}{c} \text{---} \\ | \\ R_z Q(k') \end{array} \text{---} \oplus \equiv \begin{array}{c} \text{---} \\ | \\ \oplus \end{array} \text{---} R_z Q(k') \text{---} \oplus \begin{array}{c} \text{---} \\ | \\ R_z Q(k) \end{array} \text{---} \oplus$$

$$\begin{array}{c} \text{---} \\ | \\ R_z Q(k) \end{array} \text{---} \text{---} \oplus \equiv \begin{array}{c} \text{---} \\ | \\ \bullet \end{array} \text{---} R_z Q(k) \text{---} \oplus$$

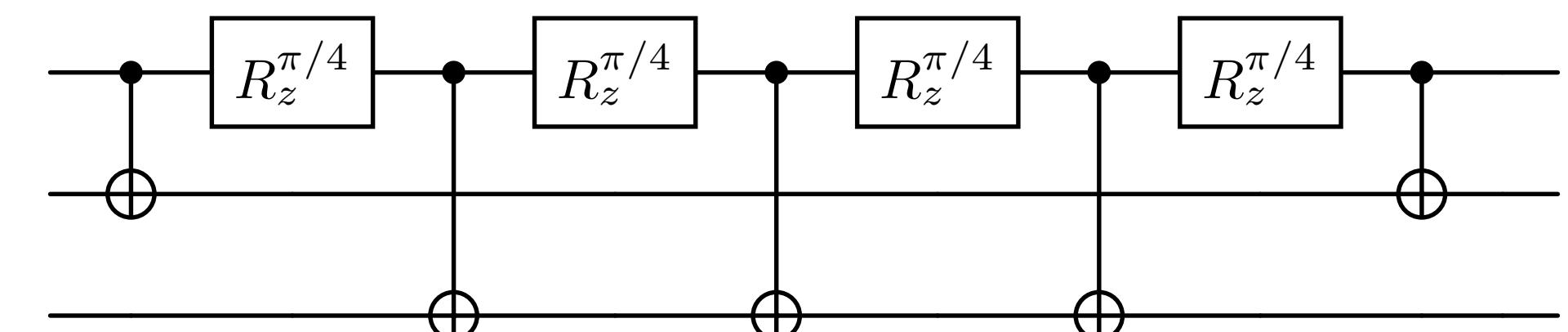
$$\begin{array}{c} \text{---} \\ | \\ \bullet \end{array} \text{---} \text{---} \oplus \oplus \equiv \begin{array}{c} \text{---} \\ | \\ \bullet \end{array} \text{---} \text{---} \oplus \oplus$$

$$\begin{array}{c} \text{---} \\ | \\ \bullet \bullet \end{array} \text{---} \text{---} \oplus \equiv \begin{array}{c} \text{---} \\ | \\ \bullet \bullet \end{array} \text{---} \text{---} \oplus$$

$$\begin{array}{c} \text{---} \\ | \\ \oplus \end{array} \text{---} H \text{---} H \text{---} \oplus \equiv \begin{array}{c} \text{---} \\ | \\ H \end{array} \text{---} \bullet \text{---} H \text{---} \oplus$$

• • •

Circuit Resynthesis



specification

GUOQ Motivation: Two Disparate Techniques

Rewrite Rules

$$\begin{array}{c} \text{---} \\ | \\ \boxed{R_z Q(k)} \quad H \quad \oplus \quad H \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ H \quad \oplus \quad H \quad \boxed{R_z Q(k)} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \\ \boxed{R_z Q(k)} \quad \oplus \quad \boxed{R_z Q(k')} \quad \oplus \quad \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ \oplus \quad \boxed{R_z Q(k')} \quad \oplus \quad \boxed{R_z Q(k)} \quad \text{---} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \\ \boxed{R_z Q(k)} \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ \bullet \quad \boxed{R_z Q(k)} \end{array}$$

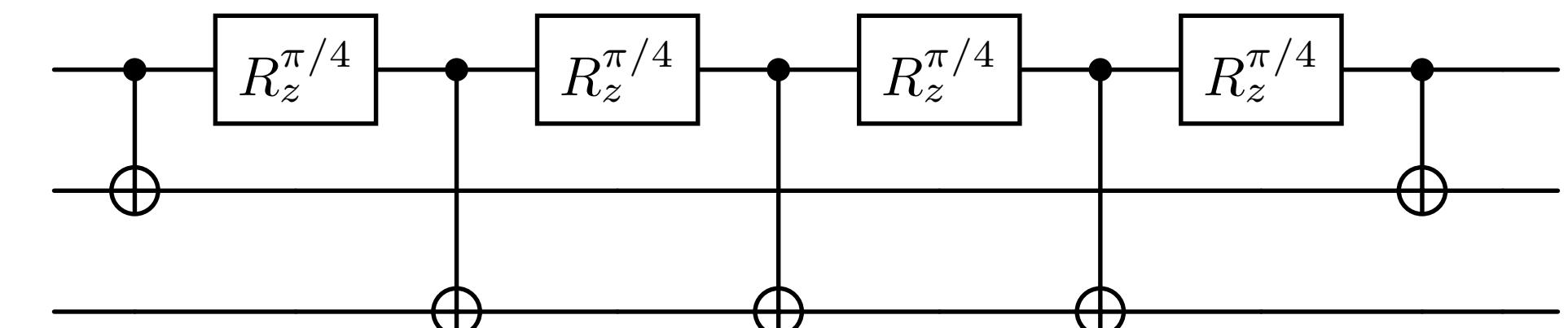
$$\begin{array}{c} \text{---} \\ | \\ \bullet \quad \bullet \\ \oplus \quad \oplus \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ \bullet \quad \bullet \\ \oplus \quad \oplus \end{array}$$

$$\begin{array}{c} \text{---} \\ | \\ \bullet \quad \bullet \\ \oplus \quad \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ \bullet \quad \bullet \\ \oplus \quad \text{---} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \\ \oplus \quad H \quad \bullet \quad H \end{array} \equiv \begin{array}{c} \text{---} \\ | \\ H \quad \bullet \quad H \quad \oplus \end{array}$$

• • •

Circuit Resynthesis



specification

synthesis

GUOQ Motivation: Two Disparate Techniques

Rewrite Rules

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \\ R_z Q(k) \quad H \quad \oplus \quad H \\ | \quad | \quad | \\ \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \\ H \quad \oplus \quad H \quad R_z Q(k) \\ | \quad | \\ \text{---} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ R_z Q(k) \quad \oplus \quad R_z Q(k') \quad \oplus \quad | \\ | \quad | \quad | \quad | \\ \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \quad | \quad | \\ \oplus \quad R_z Q(k') \quad \oplus \quad R_z Q(k) \\ | \quad | \quad | \quad | \\ \text{---} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \\ R_z Q(k) \quad | \\ | \quad | \\ \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \\ R_z Q(k) \\ | \quad | \\ \text{---} \end{array}$$

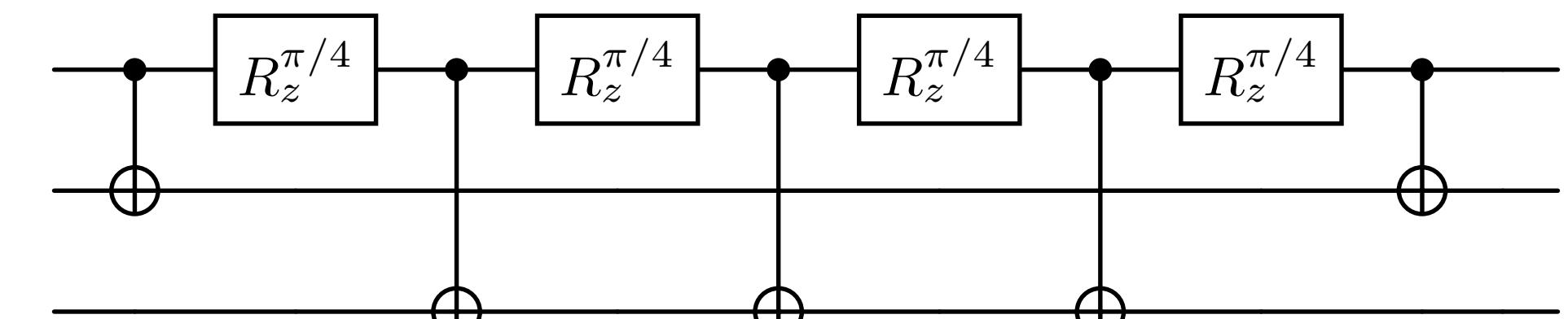
$$\begin{array}{c} \text{---} \\ | \quad | \quad | \\ | \quad \oplus \quad | \\ | \quad | \quad | \\ \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \quad | \\ | \quad \oplus \quad | \\ | \quad | \quad | \\ \text{---} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \\ | \quad | \quad | \\ | \quad \oplus \quad | \\ | \quad | \quad | \\ \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \quad | \\ | \quad | \quad | \\ | \quad \oplus \quad | \\ | \quad | \quad | \\ \text{---} \end{array}$$

$$\begin{array}{c} \text{---} \\ | \quad | \quad | \\ | \quad \oplus \quad | \quad H \quad | \quad H \quad | \quad | \\ | \quad | \quad | \quad | \quad | \quad | \\ \text{---} \end{array} \equiv \begin{array}{c} \text{---} \\ | \quad | \quad | \\ | \quad \oplus \quad | \quad H \quad | \quad H \quad \oplus \quad | \\ | \quad | \quad | \quad | \quad | \quad | \\ \text{---} \end{array}$$

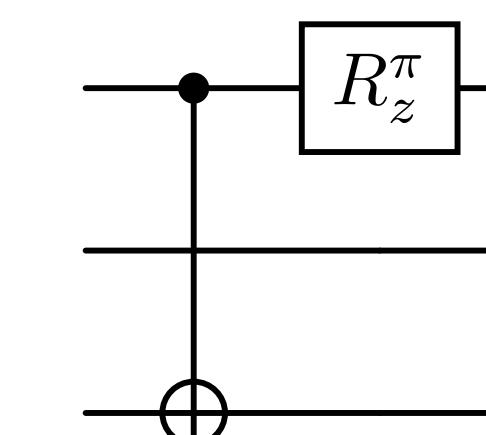
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Circuit Resynthesis



specification

synthesis

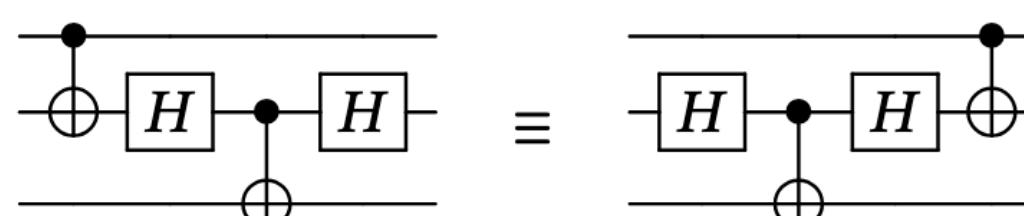
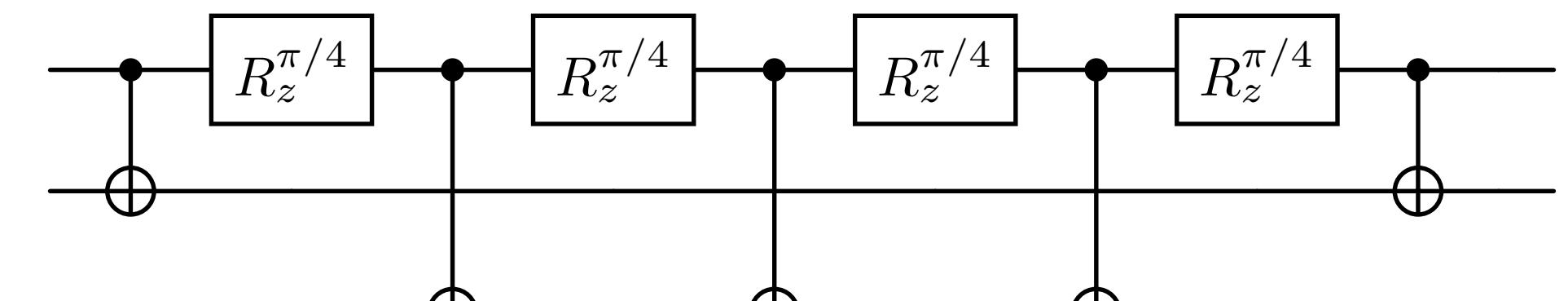


GUOQ Motivation: Two Disparate Techniques

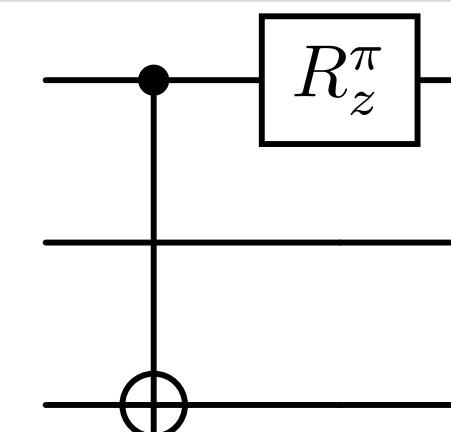
Rewrite Rules



Circuit Resynthesis

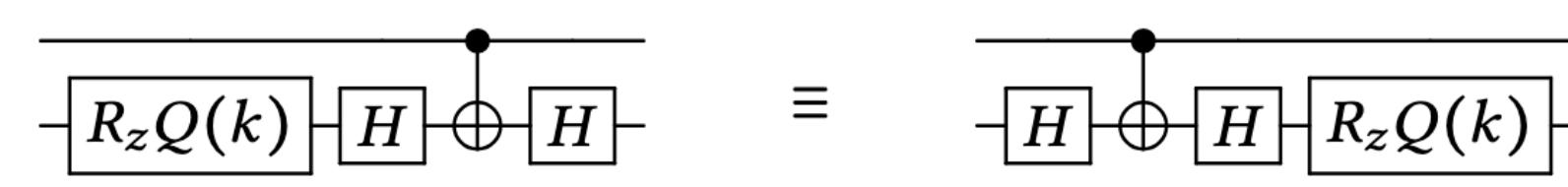


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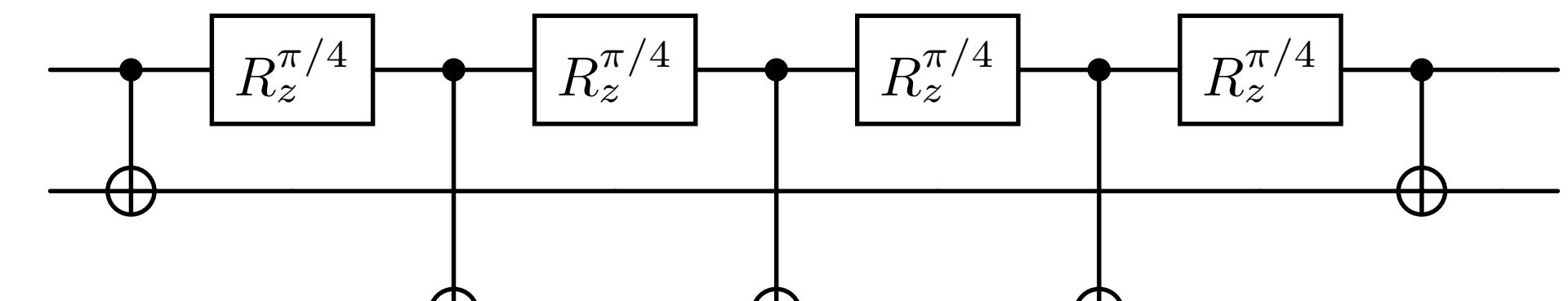


GUOQ Motivation: Two Disparate Techniques

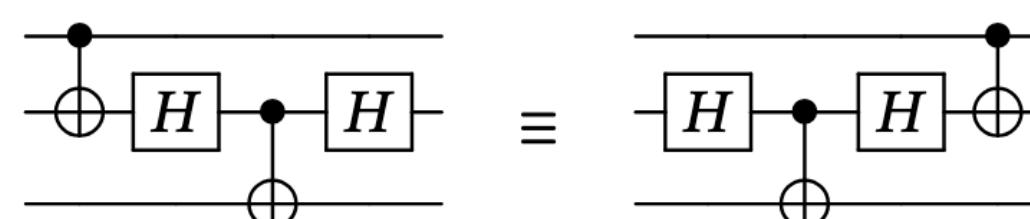
Rewrite Rules



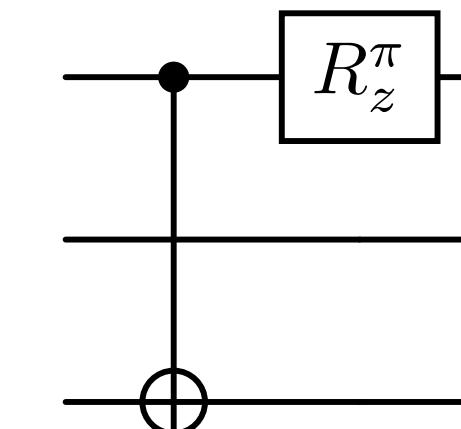
Circuit Resynthesis



Fast



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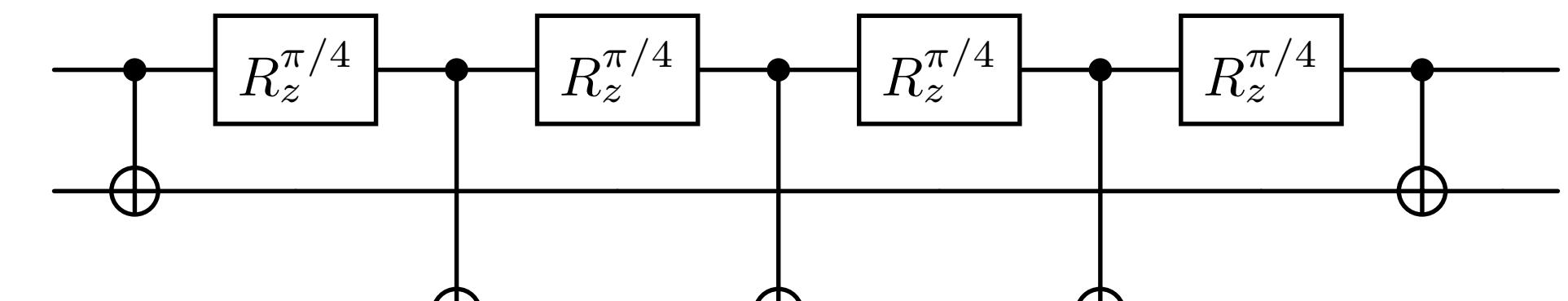


GUOQ Motivation: Two Disparate Techniques

Rewrite Rules

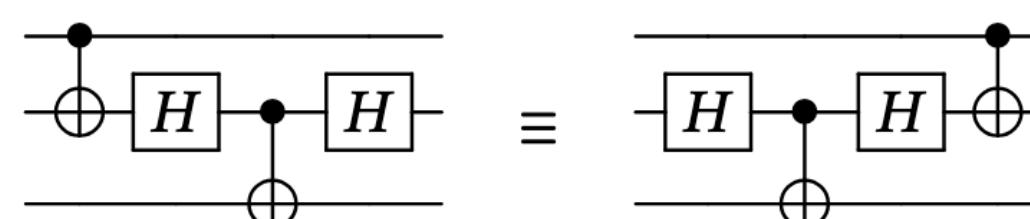


Circuit Resynthesis

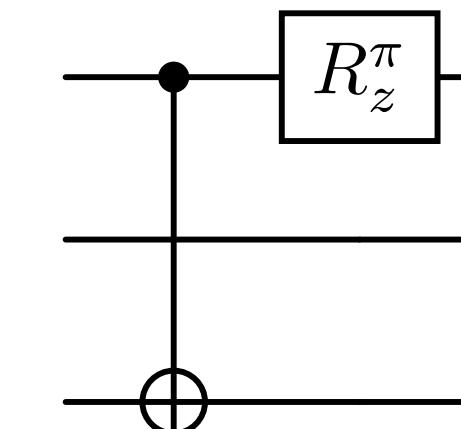


Fast

Slow

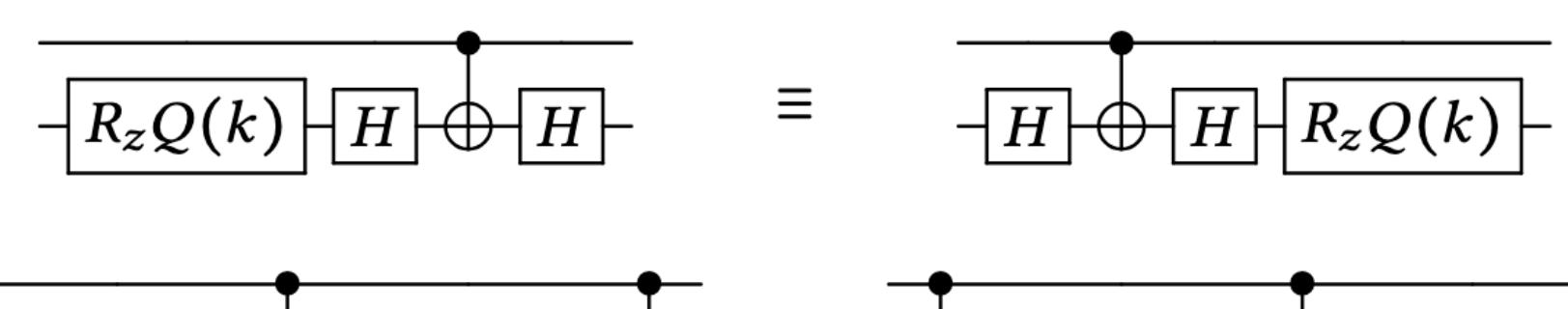


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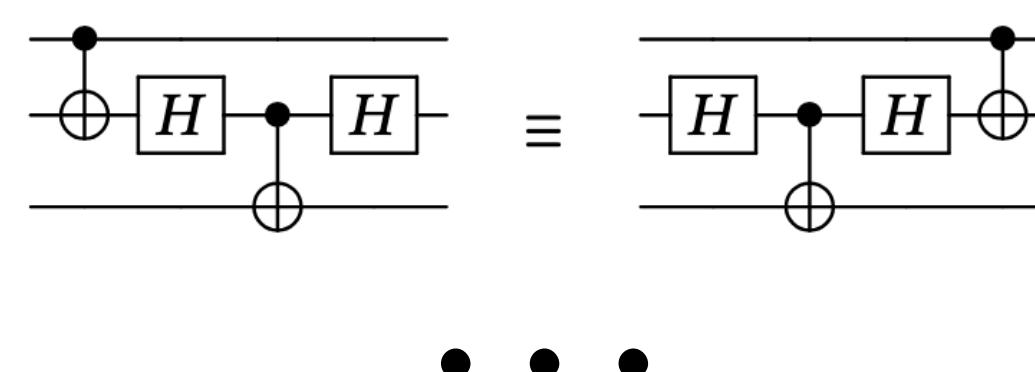
GUOQ Motivation: Two Disparate Techniques

Rewrite Rules

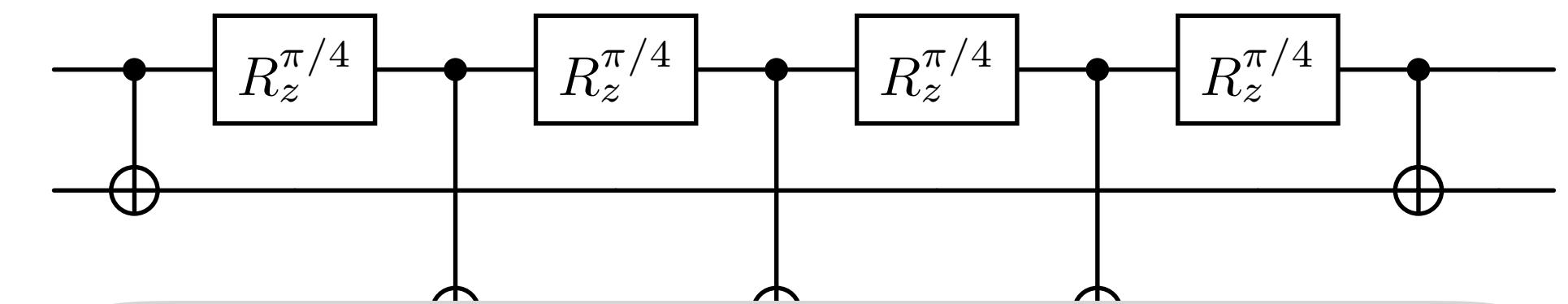


Fast

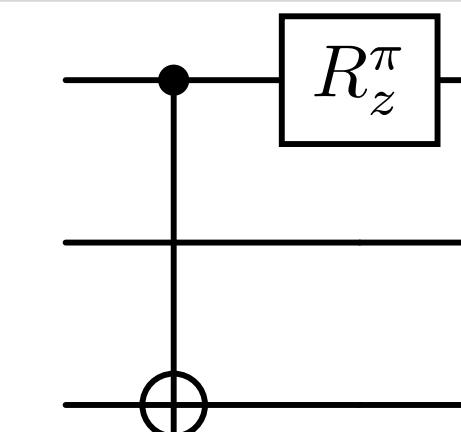
Limited by # gates



Circuit Resynthesis

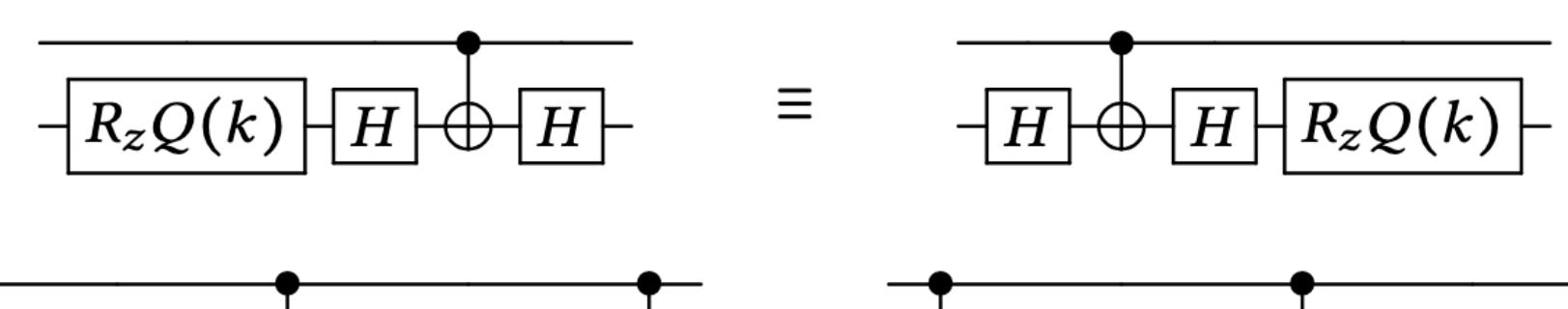


Slow



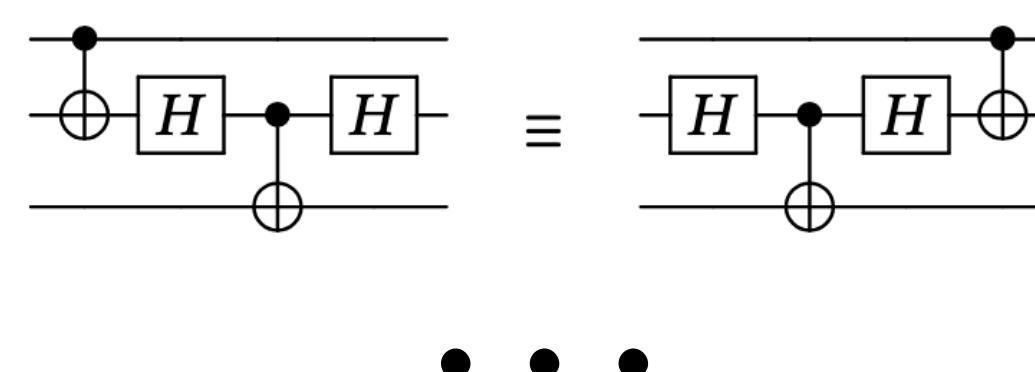
GUOQ Motivation: Two Disparate Techniques

Rewrite Rules

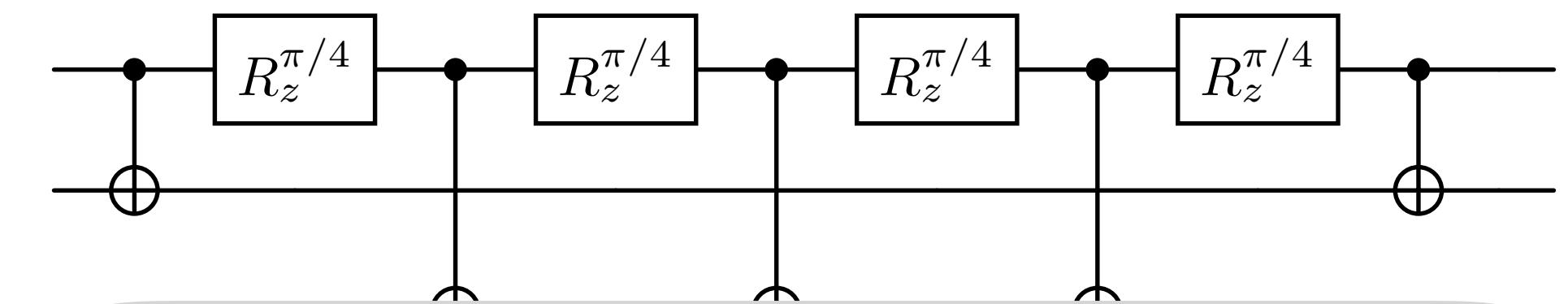


Fast

Limited by # gates

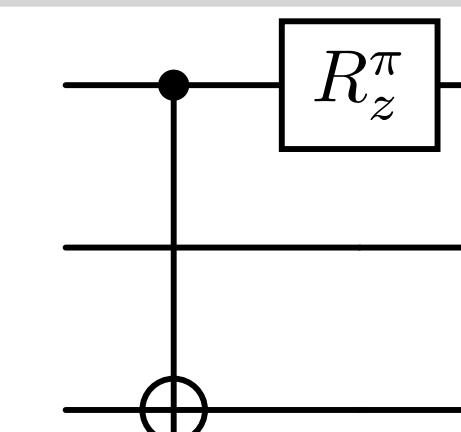


Circuit Resynthesis



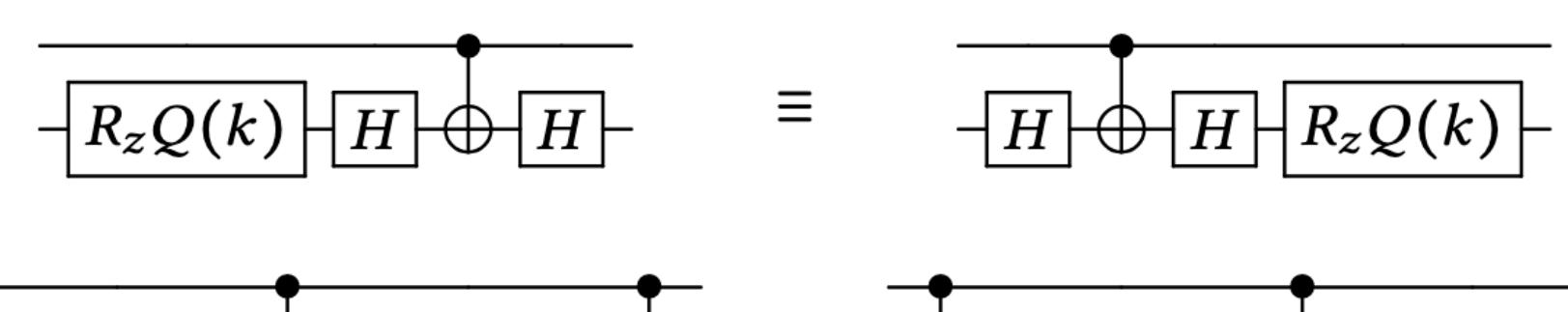
Slow

Limited by # qubits



GUOQ Motivation: Two Disparate Techniques

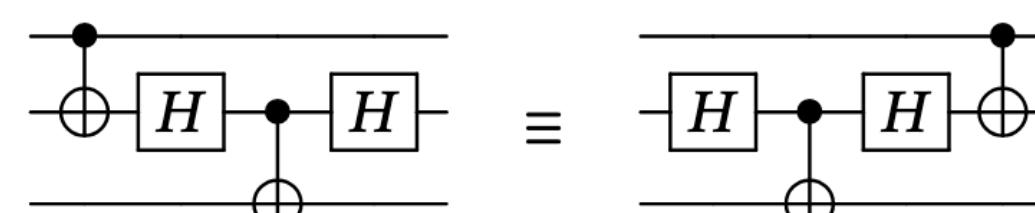
Rewrite Rules



Fast

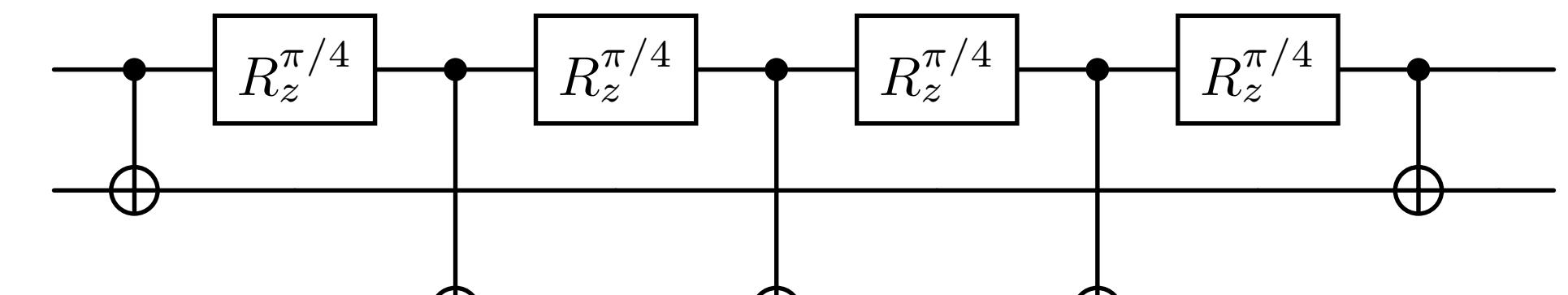
Limited by # gates

Exact



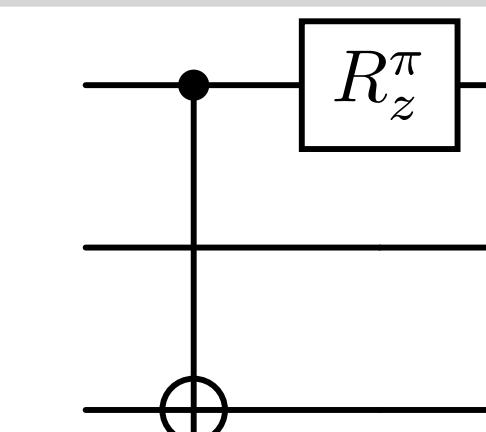
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Circuit Resynthesis



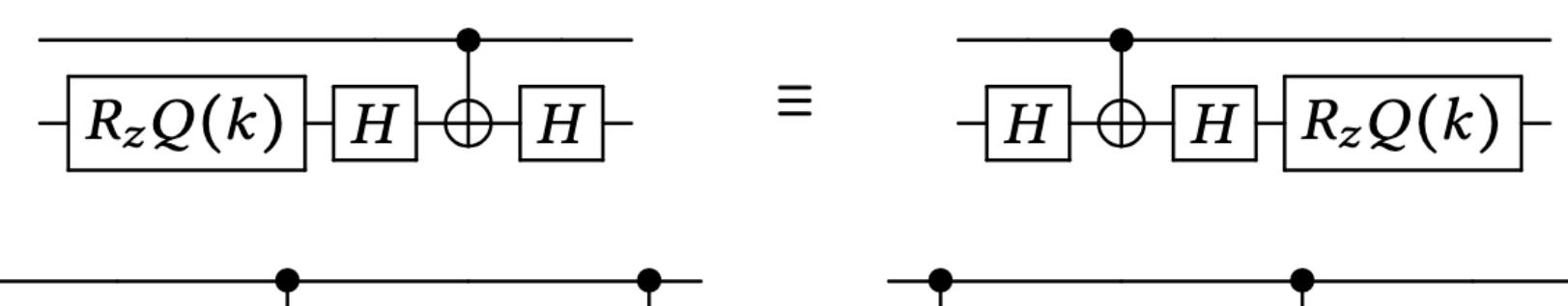
Slow

Limited by # qubits



GUOQ Motivation: Two Disparate Techniques

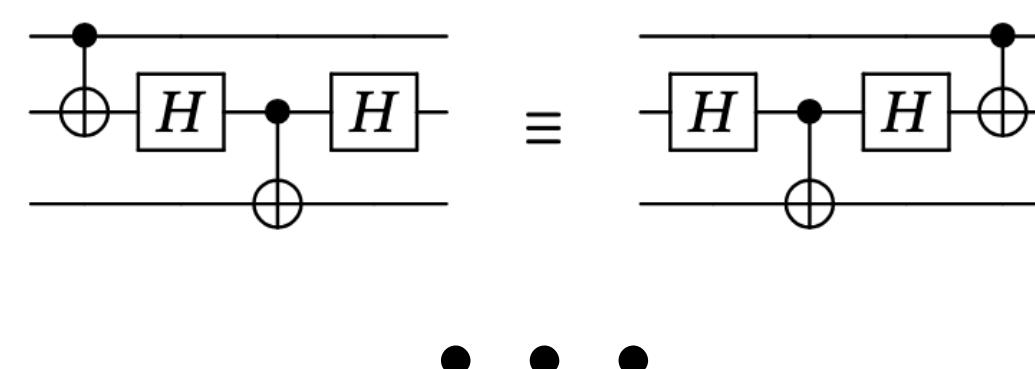
Rewrite Rules



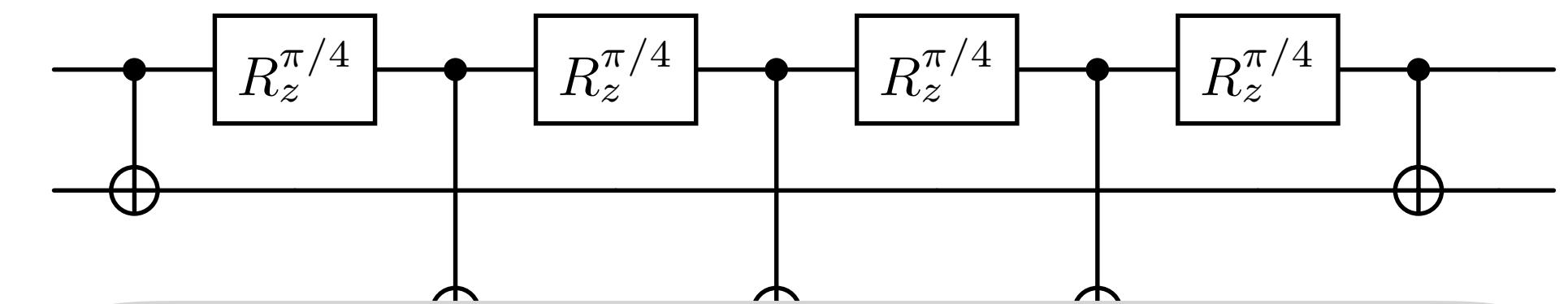
Fast

Limited by # gates

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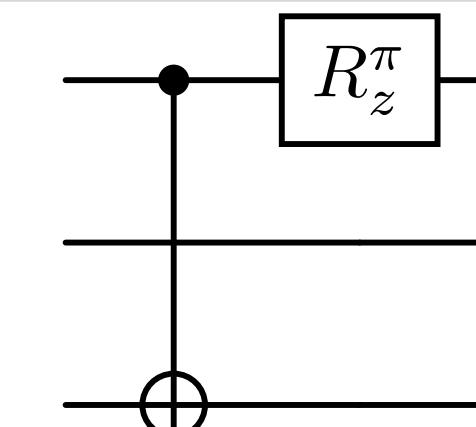
Circuit Resynthesis



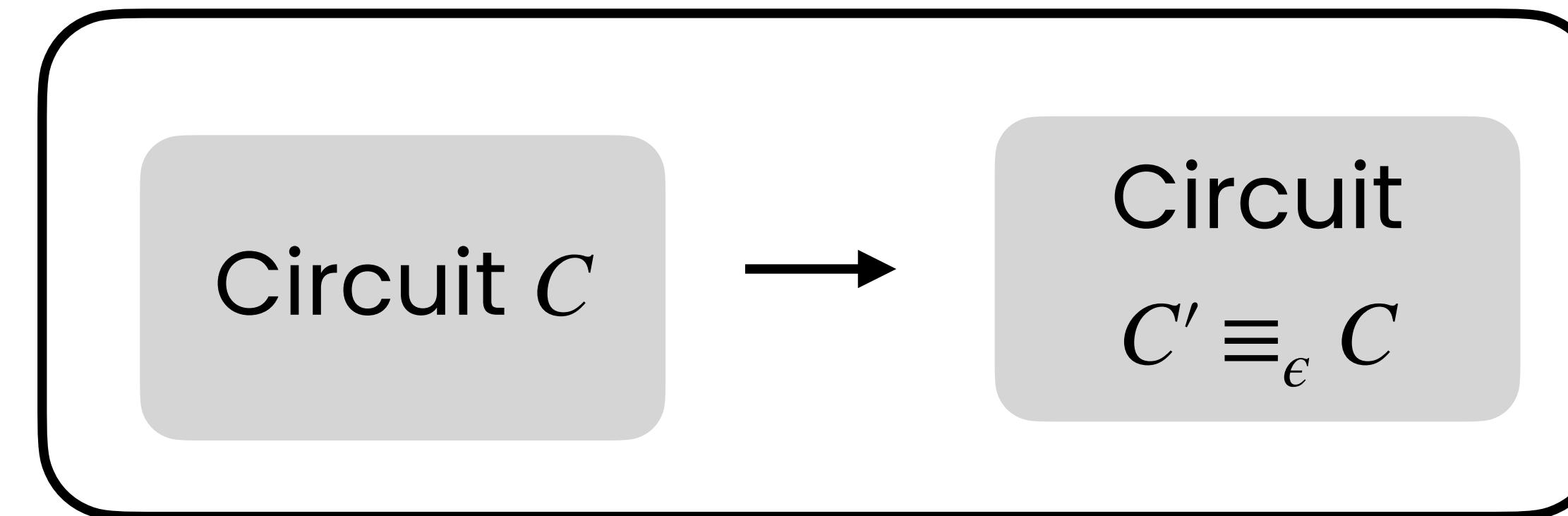
Slow

Limited by # qubits

Approximate

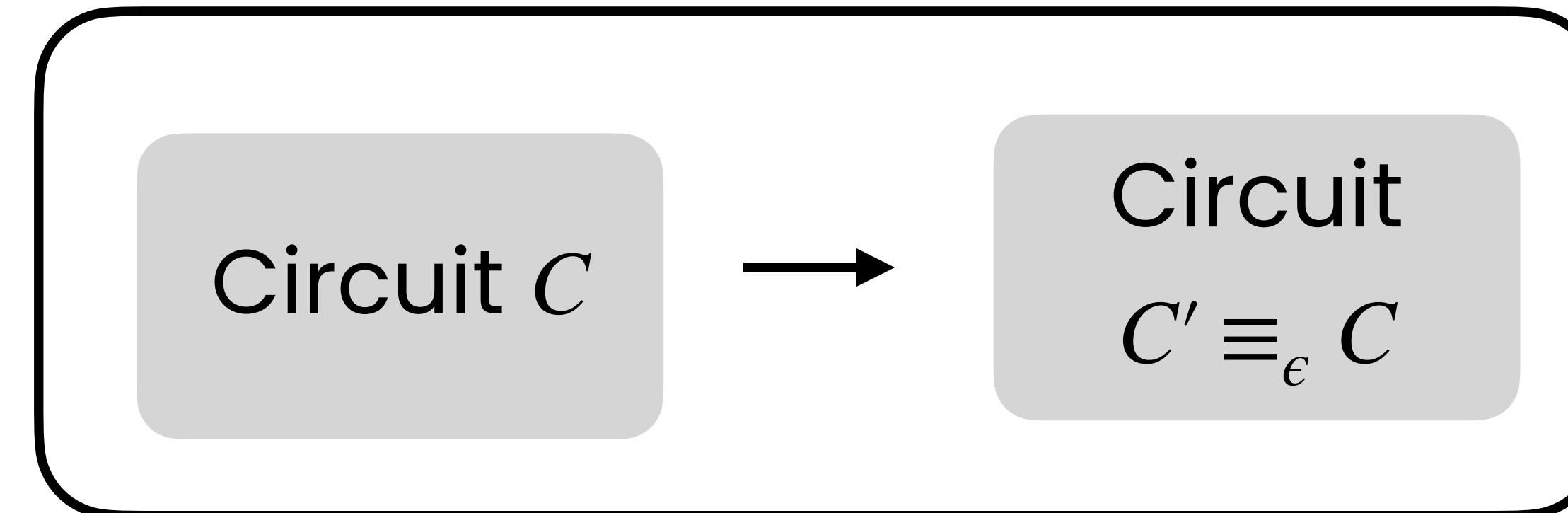


Unifying Abstract Circuit Transformations

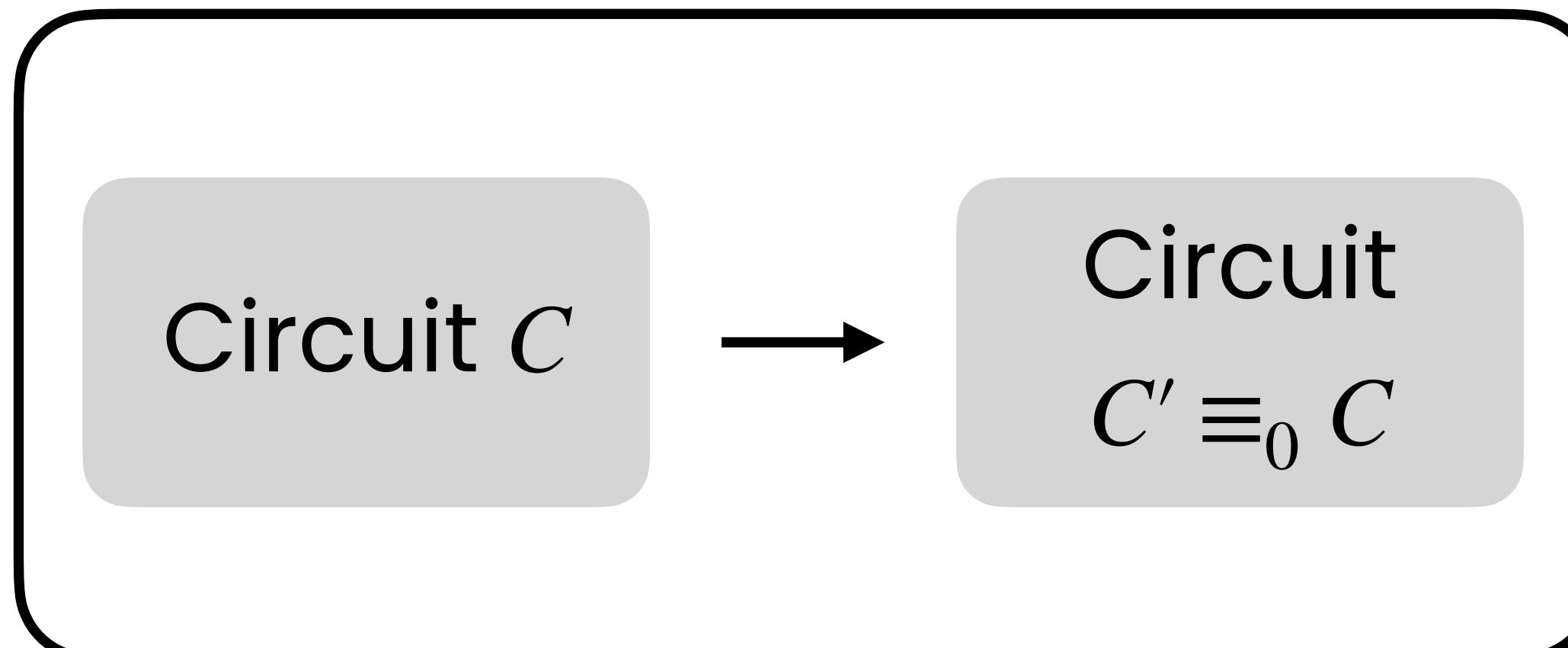


transformation $\tau_{\epsilon} : \mathcal{C} \rightarrow \mathcal{C}$

Unifying Abstract Circuit Transformations

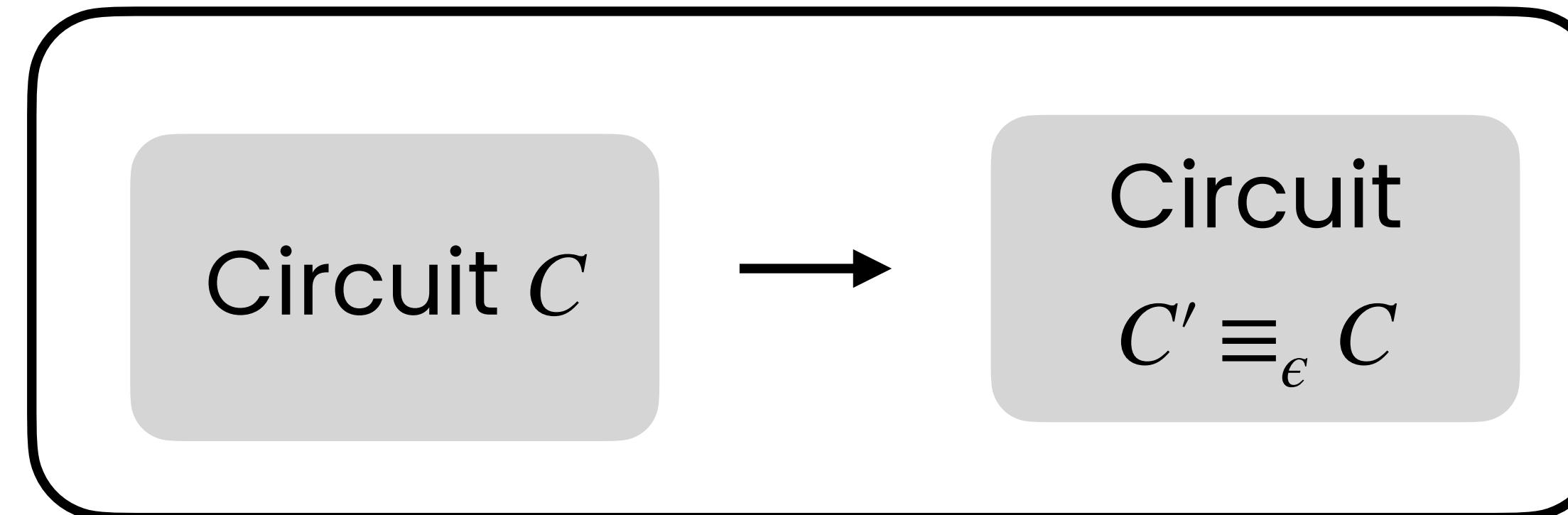


transformation $\tau_{\epsilon} : \mathcal{C} \rightarrow \mathcal{C}$

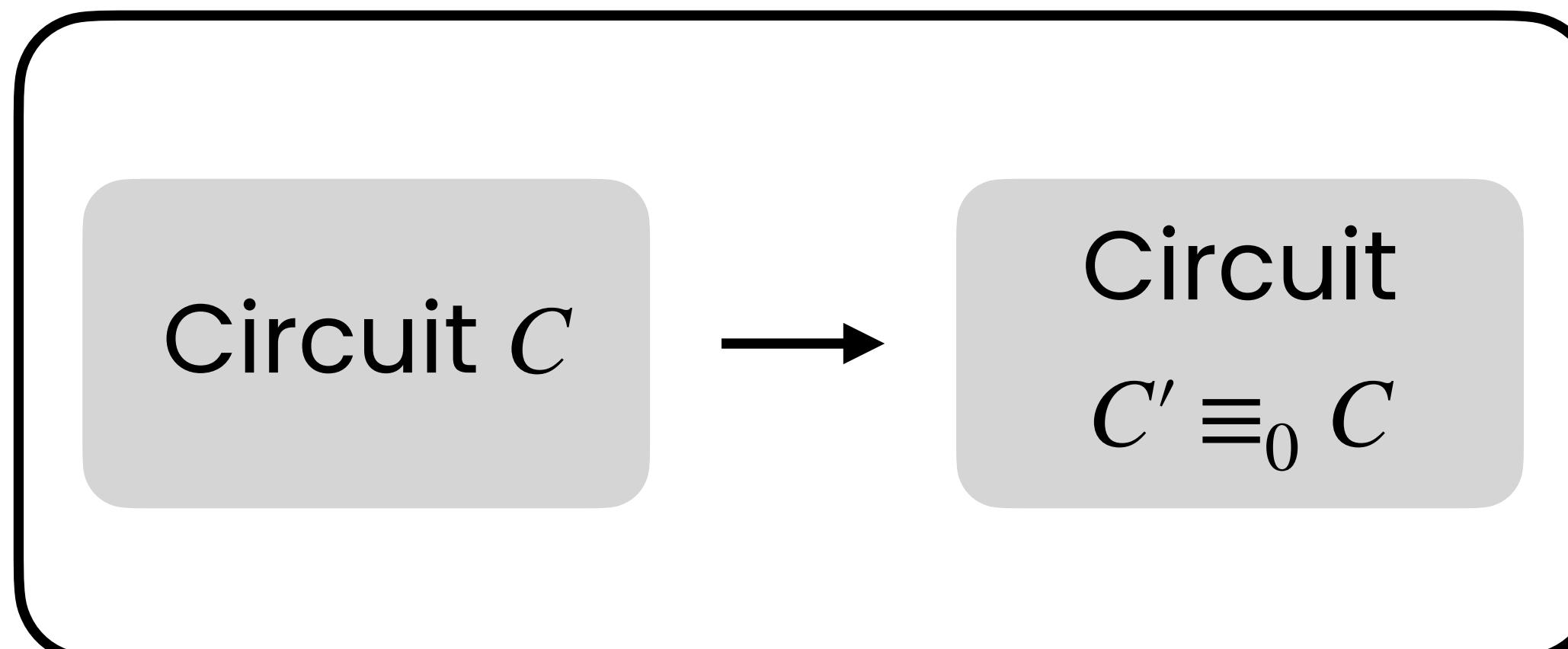


rewrite rule

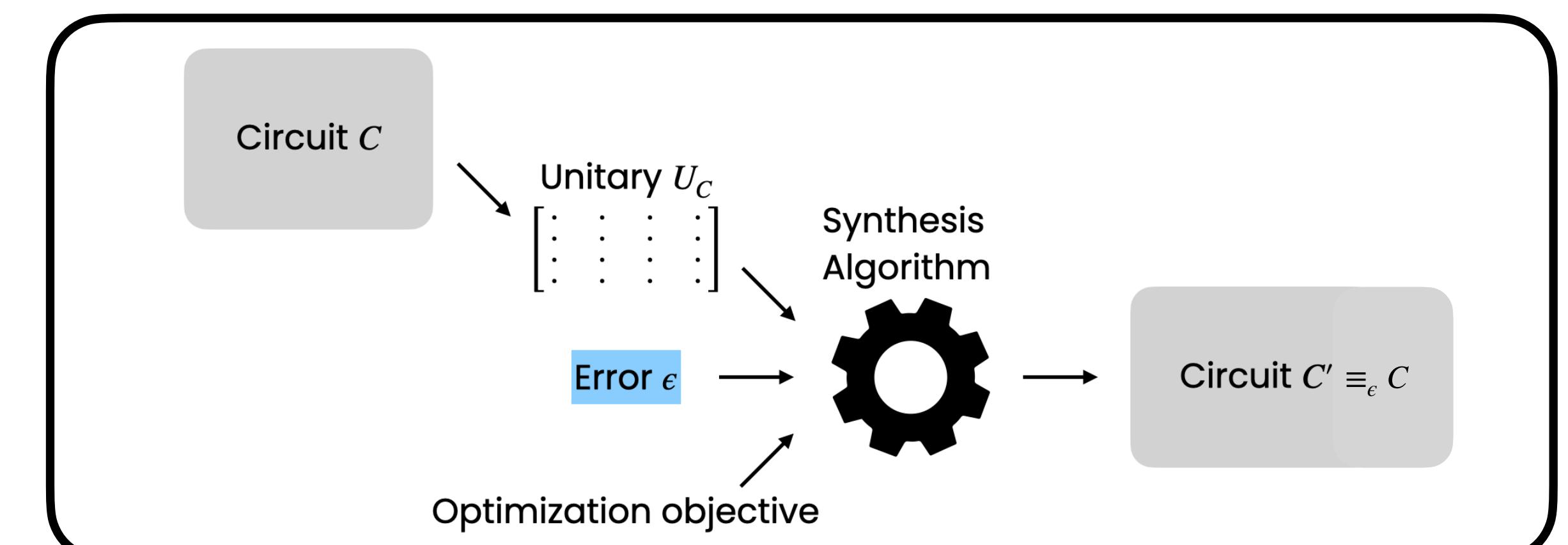
Unifying Abstract Circuit Transformations



transformation $\tau_{\epsilon} : \mathcal{C} \rightarrow \mathcal{C}$

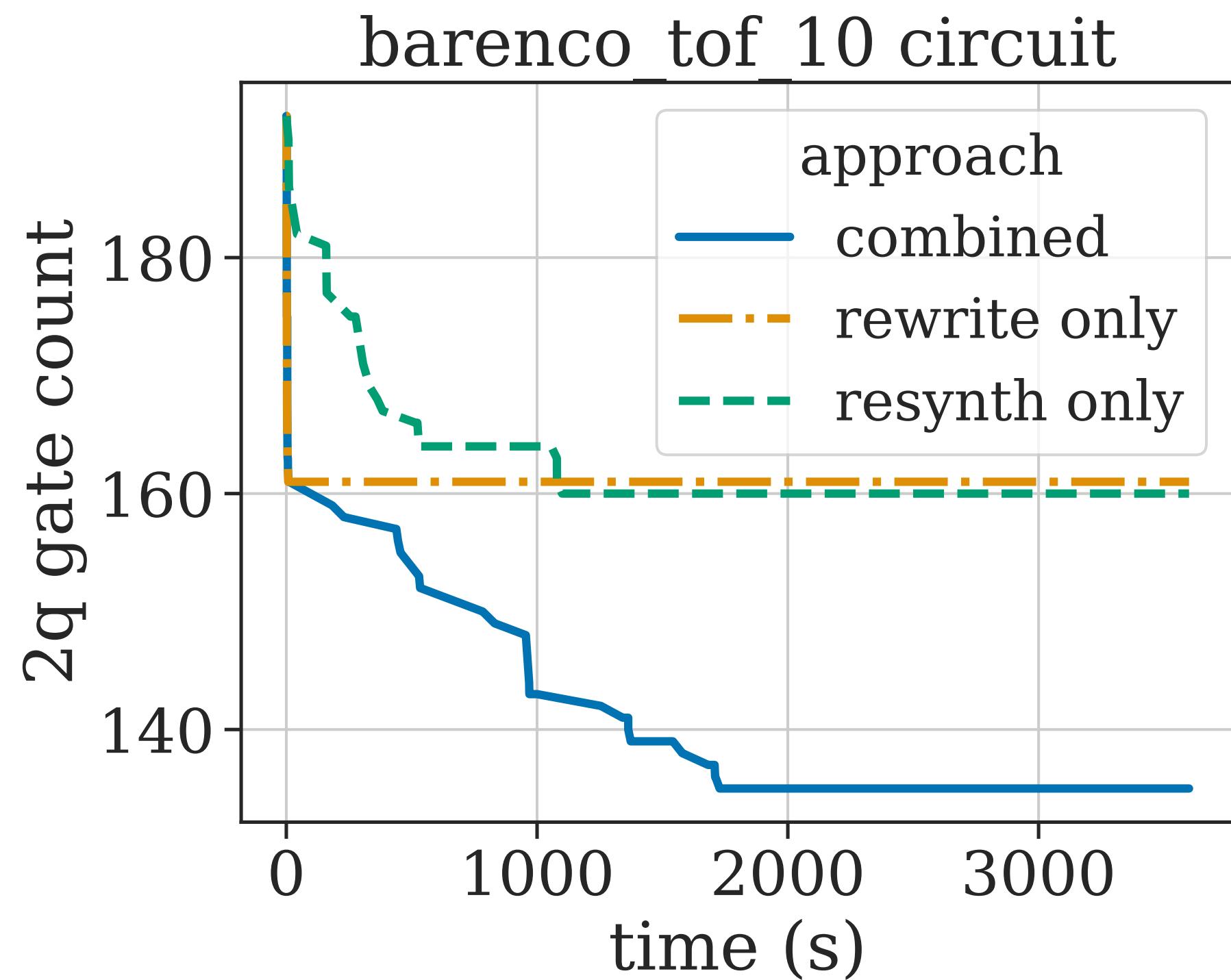


rewrite rule

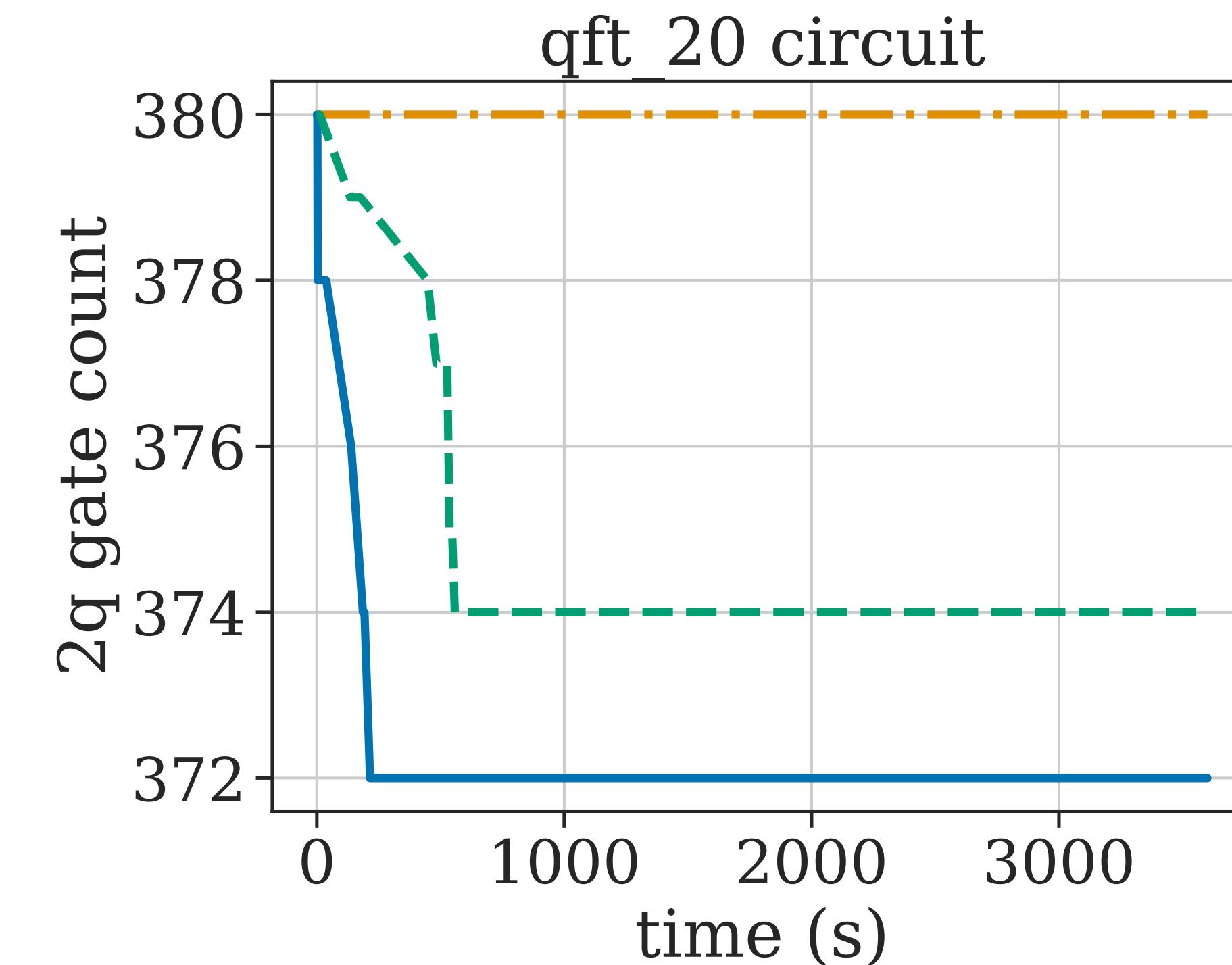
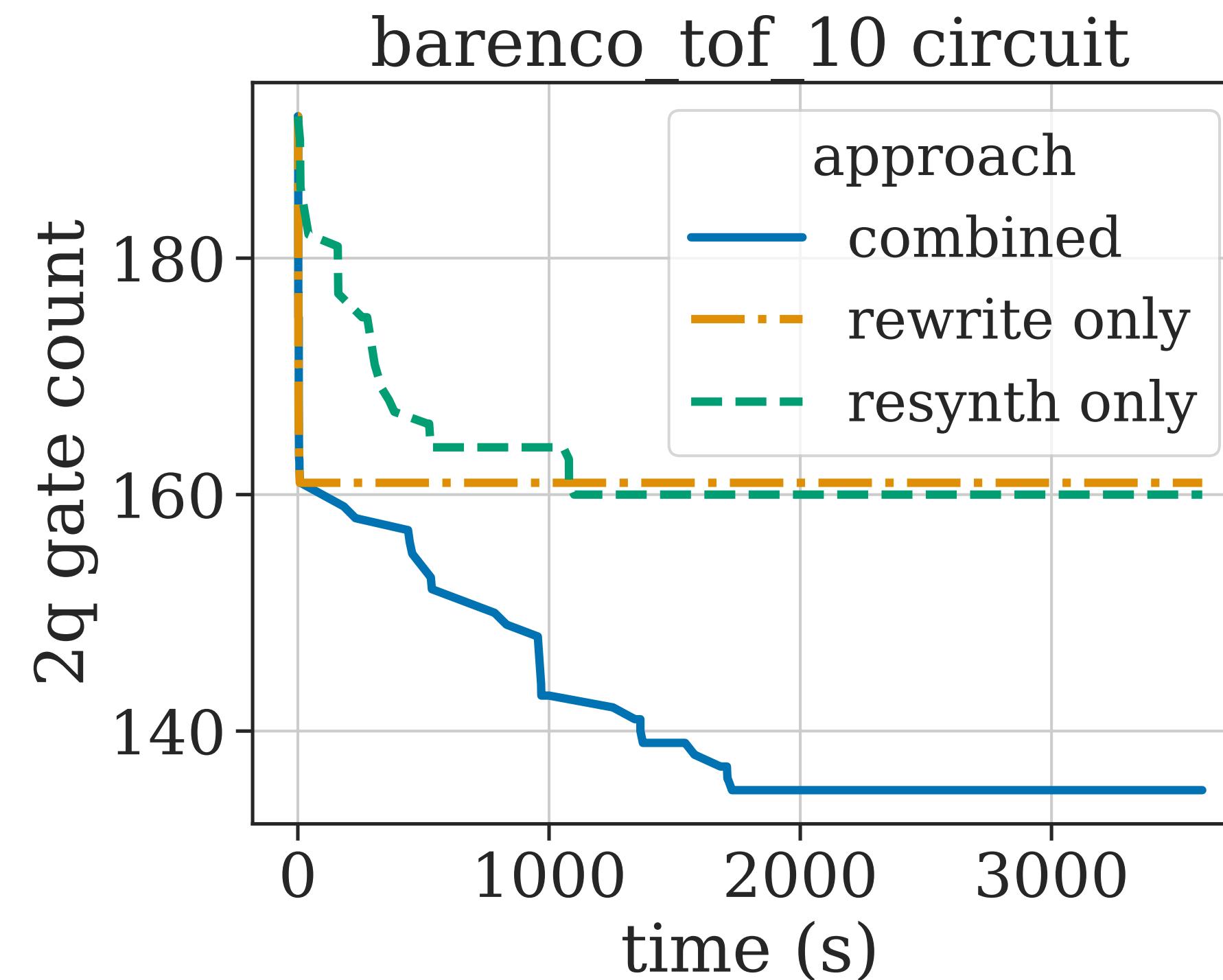


resynthesis

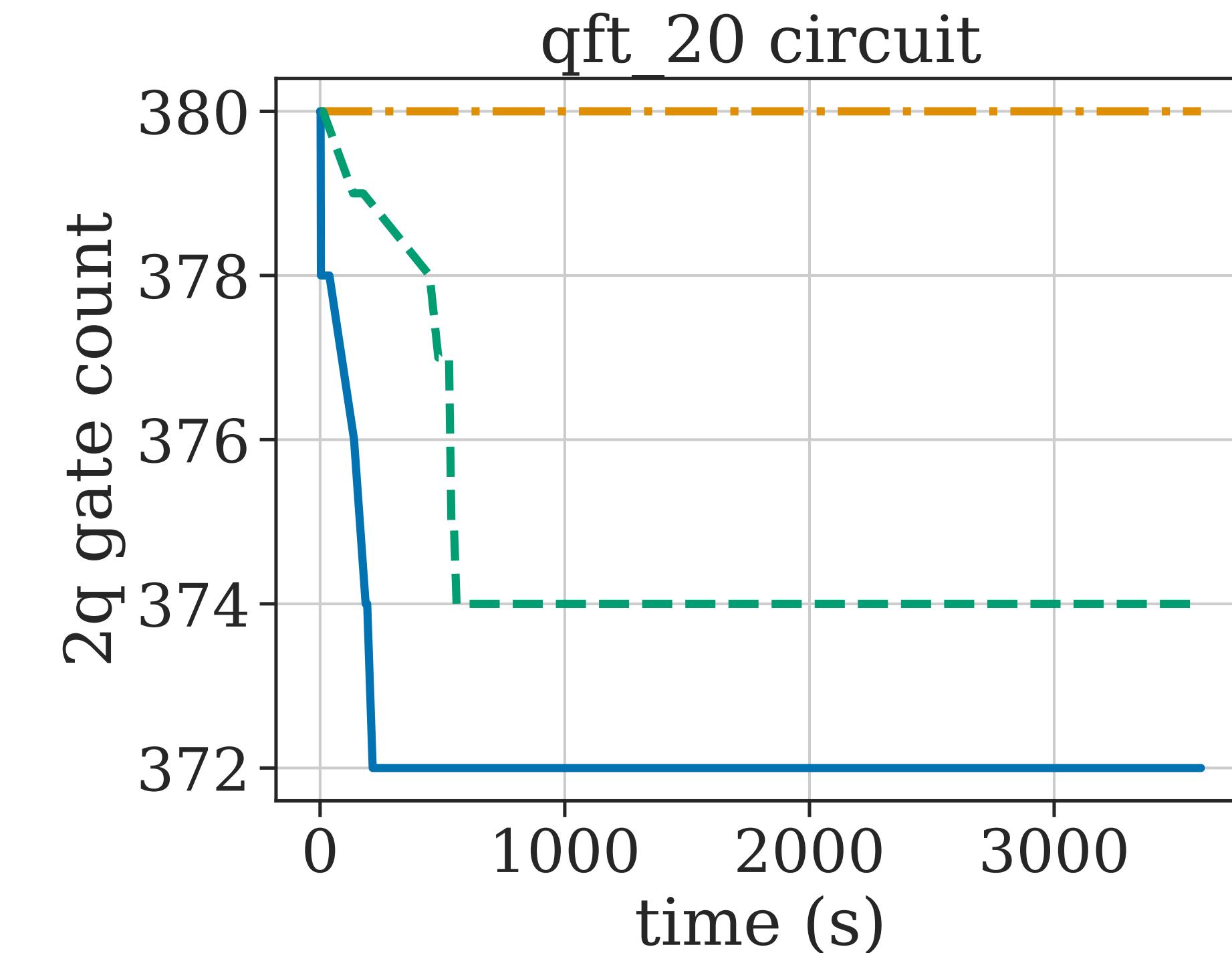
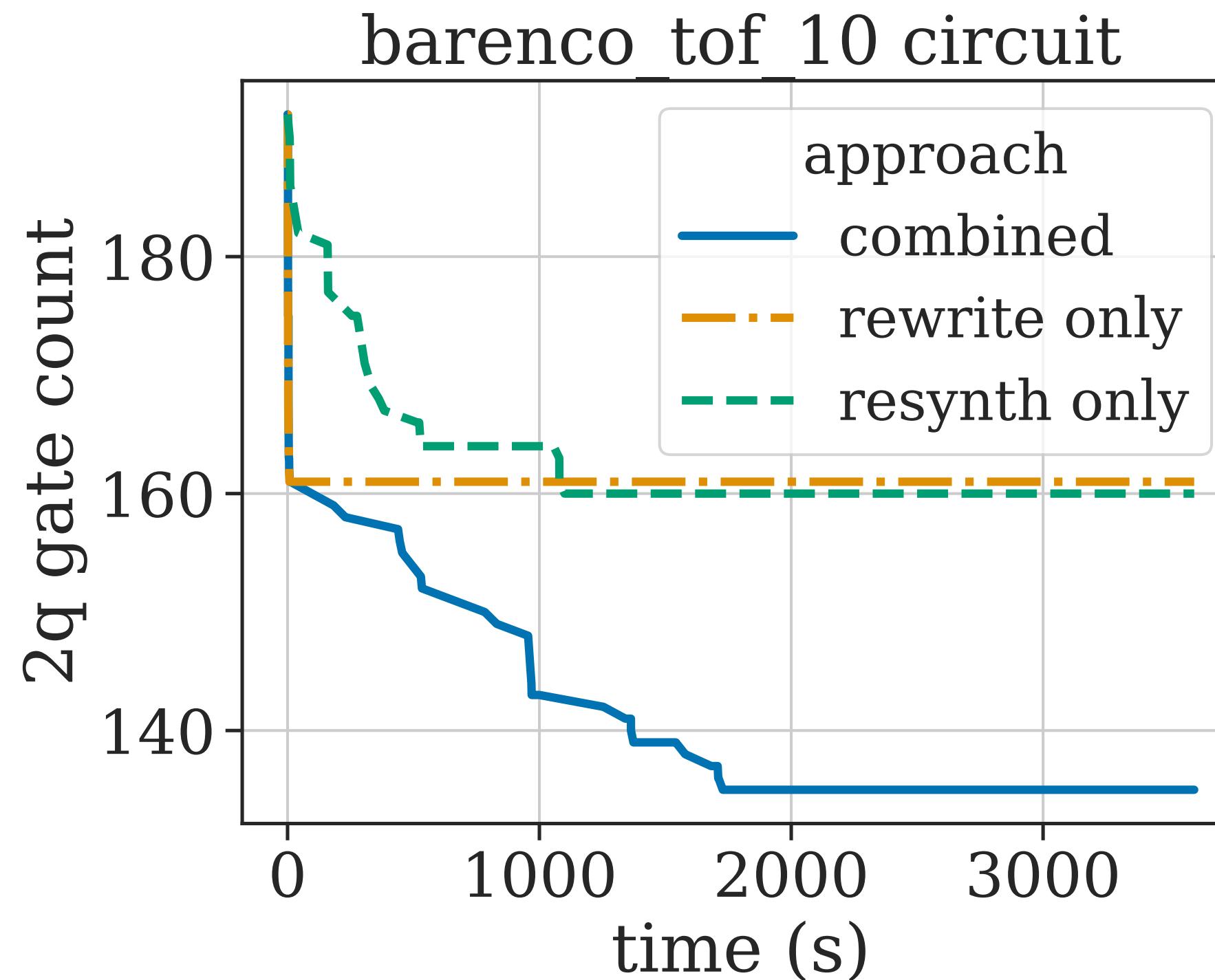
GUOQ Search Exploits Synergy



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Optimizing for FTQC

Optimizing for FTQC

Clifford + T gate set: $\{T, T^\dagger, S, S^\dagger, H, CX\}$

Optimizing for FTQC

Clifford + T gate set: $\{T, T^\dagger, S, S^\dagger, H, CX\}$

discrete

Optimizing for FTQC

Clifford + T gate set: $\{T, T^\dagger, S, S^\dagger, H, CX\}$

discrete

"magic state distillation"

Optimizing for FTQC

Clifford + T gate set: $\{T, T^\dagger, S, S^\dagger, H, CX\}$

discrete

"magic state distillation"

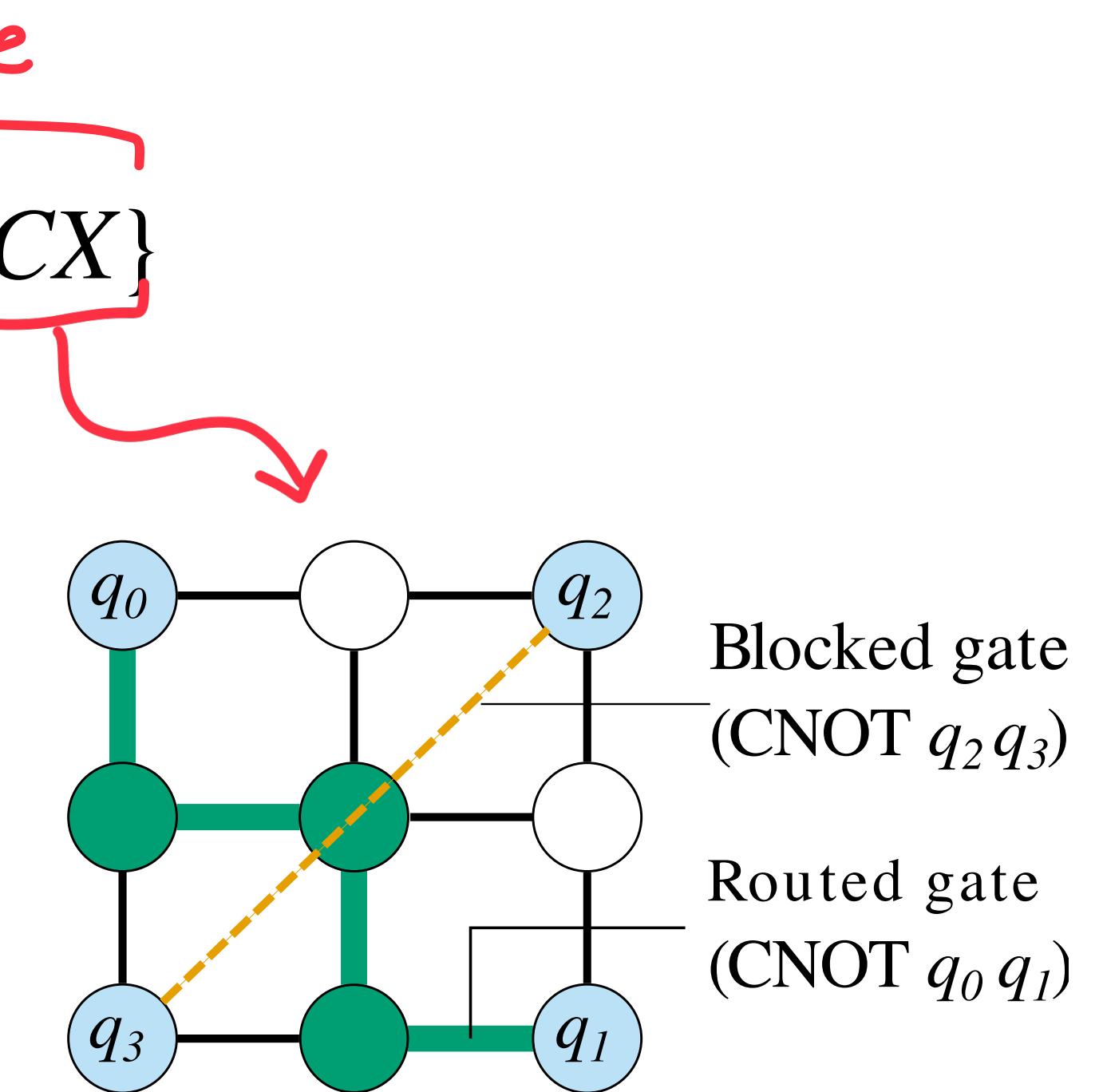
Eastin-Knill theorem: no transversal universal set

Optimizing for FTQC

Clifford + T gate set: $\{T, T^\dagger, S, S^\dagger, H, CX\}$

"magic state distillation"

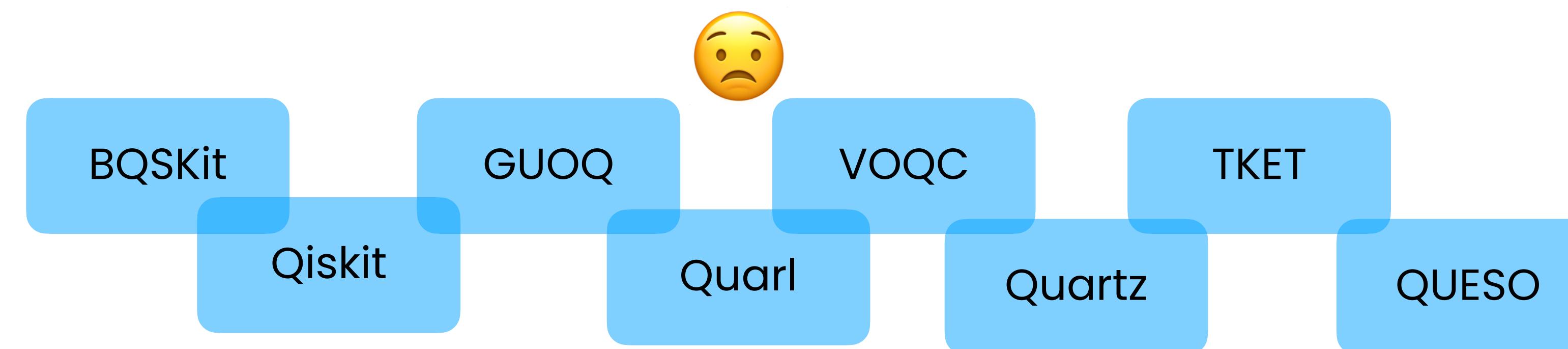
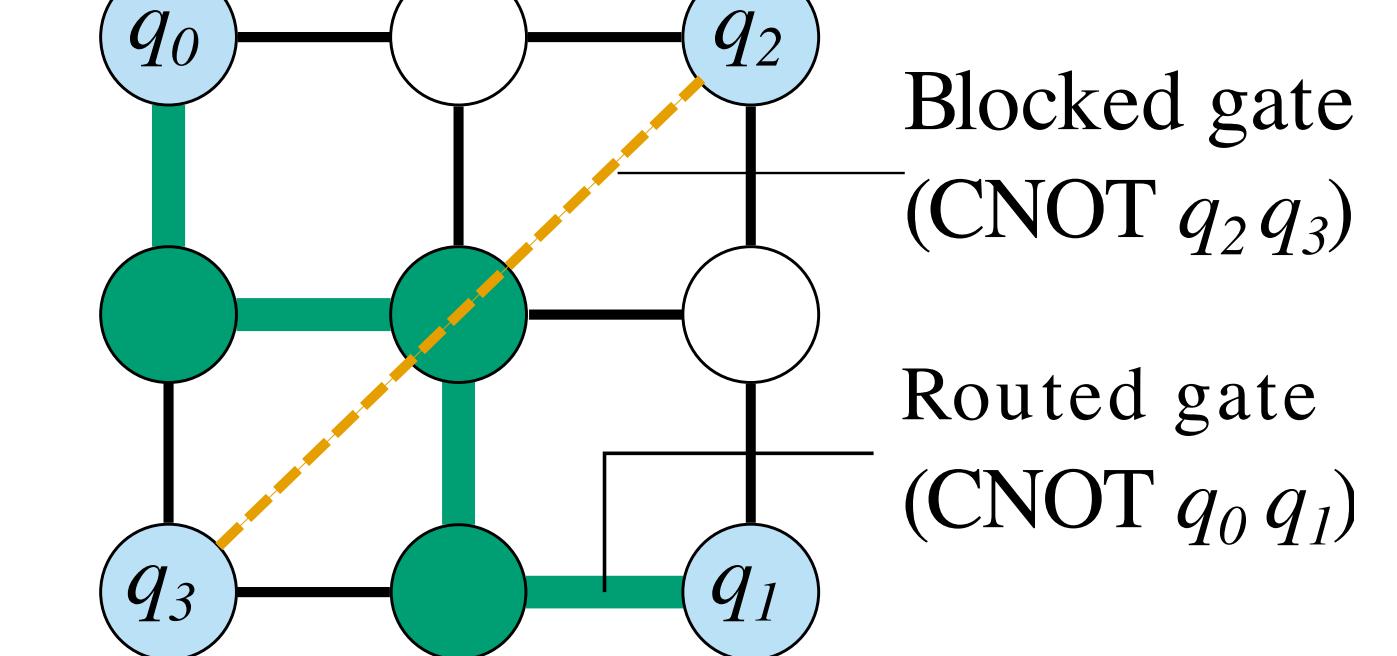
Eastin–Knill theorem: no transversal universal set



Optimizing for FTQC

Clifford + T gate set: $\{T, T^\dagger, S, S^\dagger, H, CX\}$ *discrete*
"magic state distillation"

Eastin–Knill theorem: no transversal universal set



Optimizing for FTQC

Open Research Question

Optimizing for FTQC

Open Research Question

Reducing T-count with the ZX-calculus

Aleks Kissinger and John van de Wetering

Radboud University Nijmegen

January 20, 2020



Linear and Non-linear Relational Analyses for Quantum Program Optimization

MATTHEW AMY, Simon Fraser University, Canada

JOSEPH LUNDERVILLE, Simon Fraser University, Canada