<u>Dashboard</u> / My courses / <u>CISC 530-91- O-2019/Summer</u> / Final Term Examination / <u>Final term PART 1: Chapters 6,7,8,9, and 10 Examination.</u>

Started on	Tuesday, August 6, 2019, 7:06 PM
State	Finished
Completed on	Tuesday, August 6, 2019, 9:03 PM
Time taken	1 hour 56 mins
Grade	18.00 out of 20.00 (90 %)
Question 1	
Correct	
1.00 points out of 1.00	
SRAM is a very fast	memory and it doesn't need to be refreshed like DRAM does.
Select one:	
● True	
False	
The correct answer	is 'True'.

Question 2	
Correct	
1.00 points out of 1.00	
Superscalar archit	rectures include a single execution unit such as specialized integer and floating-point adders and multipliers.
Select one:	
True	
• False 🗸	
Superscalar archit	ectures include multiple execution units such as specialized integer and floating-point adders and multipliers.
The correct answer	is 'False'.

Tigh	tly coupled multiprocessor systems
Sele	ct one:
	a. Share a common memory and different set of I/O devices
	b. None of the mentioned
•	c. Share a common memory and the same set of I/O devices
	d. Share different memories and different set of I/O devices

Embedded systems are resource constrained. Utilization of memory and power are critical. The economy of hardware and software is often paramount and can affect design decisions Select one: True False The correct answer is 'True'.	estion 4	
Embedded systems are resource constrained. Utilization of memory and power are critical. The economy of hardware and software is often paramount and can affect design decisions Select one: True False	rrect	
software is often paramount and can affect design decisions Select one: True ✓ False	0 points out of 1.00	
software is often paramount and can affect design decisions Select one: True ✓ False		
Select one: True ✓ False 		
True ✓False	oftware is often paramo	nt and can affect design decisions
○ False	Select one:	
	True ✓	
The correct answer is 'True'.	False	
The correct answer is 'True'.		
The correct answer is "True".		
	ne correct answer is True	

Question 5	
Correct	
1.00 points out	of 1.00
A serial da	ta transmission link uses
Select one	
a. Or	ne wire
O b. Ei	ght wires
O c. Ni	ne wires
• d. Tv	vo wire€∕
Your answ	er is correct.
The correct	t answer is: Two wires

Correct
1.00 points out of 1.00
In the context of operating systems, protected environments such as logical partitions are
Select one: a. Enabling technology consolidating few servers within the confines of a single large system b. Enabling technology consolidating hundreds of small servers within the confines of a single large system c. Enabling technology consolidating hundreds of large servers within the confines of multiple large systems d. Enabling technology consolidating large servers within the confines of a single large system

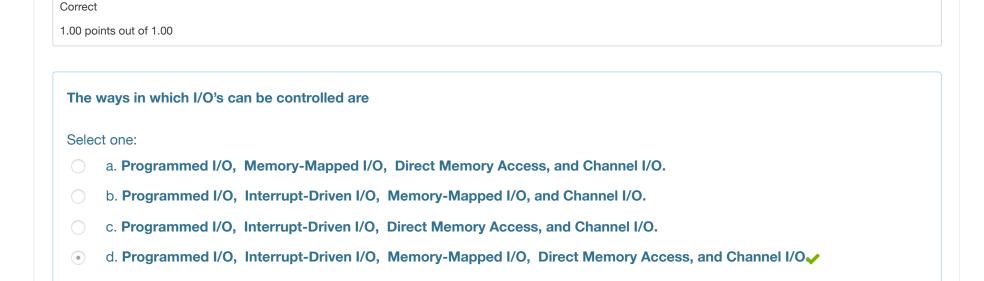
Your answer is correct.

Question 6

The correct answer is: Enabling technology consolidating hundreds of small servers within the confines of a single large system

Question 7
Correct
1.00 points out of 1.00
In a computer environment, slow I/O device(s) will impact system performance due to a so-called ripple effect
Select one:
True ✓
○ False
The correct answer is 'True'.
Question 8 Correct
1.00 points out of 1.00
Database physical schema is computer's view of the database that includes locations of physical files and indexes.
Select one:
True ✓
○ False
The correct angwer is 'True'
THE CORRECT ANSWER IS TIME.
The correct answer is 'True'.

Question 9
Correct
1.00 points out of 1.00
I/O subsystems comprise of
Select one:
a. Blocks of main memory, buses, and special cabling
b. Blocks of main memory, buses, and special compiler
c. Blocks of main memory, buses, interfaces and special cabling/
d. Blocks of main memory, buses, and special interpreter
Your answer is correct.
The correct answer is: Blocks of main memory, buses, interfaces and special cabling

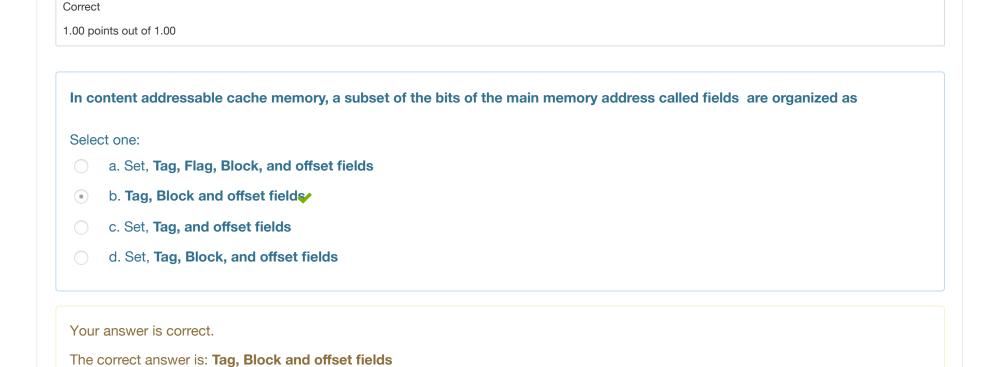


Your answer is correct.

Question 10

The correct answer is: Programmed I/O, Interrupt-Driven I/O, Memory-Mapped I/O, Direct Memory Access, and Channel I/O.

Question 11				
Correct				
.00 points out of 1.00				
Effective Access Ti	me is calculated using			
Select one:				
a. Acces times	s, hit rates, and miss pena	lties.		
b. Acces times	s, hit rates, and miss rates	→		
o. Hit rates, m	iss rates, and miss penalti	ies.		
d. Acces times	s, hit rates, miss rates, and	d miss penalties.		
Your answer is corre	ct.			
The correct answer i	s: Acces times, hit rates, a	and miss rates.		



Question 13					
Correct					
1.00 points out of 1.00					
In off-the-shelf embe	dded hardware, minima	al hardware custo	mization is possible	e	
Select one:					
True ✓					
False					
The correct answer is '	True'.				

Question 14 Incorrect

0.00 points out of 1.00

MIMD architectures may be viewed as

Select one:

- a. A collection of workstations where a group of workstations brought together to solve a general problem
- b. A collection of workstations where many workstations coordinates the actions of the others
- o. A collection of workstations where a cluster of homogenous systems will form a dedicated system
- d. A collection of workstations where one workstation coordinates the actions of the others

Your answer is incorrect.

The correct answer is: A collection of workstations where one workstation coordinates the actions of the others

Question **15**Correct 1.00 points out of 1.00

One of the process management tasks is

Select one:

- a. Creates processes, deletes processes, and deallocates resources that were allocated during process execution
- b. Schedules access to resources deletes processes, and deallocates resources that were allocated during process execution
- o. Schedules access to processes and deallocates resources that were allocated during process execution
- d. Creates processes, schedules their access to resources, deletes processes, and deallocates resources that were allocated during process execution

Your answer is correct.

The correct answer is: Creates processes, schedules their access to resources, deletes processes, and deallocates resources that were allocated during process execution

Question 16	
Incorrect	
0.00 points out of 1.00	
Embedded systems programmers need not understand every detail about the hardware	
Select one:	
True ▼	
○ False	
The correct answer is 'False'.	
Question 17	
Correct	
1.00 points out of 1.00	
Parallel processing is capable of economically increasing system throughput while providing better fault tolerance	
Select one:	
True ✓	
○ False	
The correct answer is 'True'.	

Correct 1.00 poi	nts out of 1.00
RISC	have, amongst other specifications,
Selec	t one:
•	a. Single-cycle instructions, fixed length instructions, with few addressing modes
	b. Single-cycle instructions, variable length instructions, with few addressing modes.
	c. Single-cycle instructions, fixed length instructions, parameter passing through memory, with few addressing modes.
	d. Single-cycle instructions, fixed length instructions, microprogrammed control, with few addressing modes.
Vour	answer is correct.

The correct answer is: Single-cycle instructions, fixed length instructions, with few addressing modes.

Correct	
1.00 points out of 1.00	
Virtual memory uses	
Select one:	
a. Part of main memory and none of the hard drive	
b. Part of main memory and all of the hard drive	
 c. Part of main memory and part of the hard driv 	
d. No part of main memory and all of the hard drive	
Your answer is correct.	

The correct answer is: Part of main memory and part of the hard drive

Question 20
Correct
1.00 points out of 1.00
For the software in embedded systems, memory limitations are almost always a software development constraint and this is due
to lack of input and output memory mapping
Select one:
○ True
False ✓
The correct answer is 'False'.