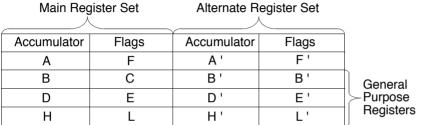
CPU Register Configuration



Special Purpose

Registers

Memory Refresh

Flag Regi	eral ose sters	l
	Flag	Regi

Flag Definitions				
Symbol	Field Name			
С	Carry Flag			
N	Add/Subtract			
P/V	Parity/Overflow Flag			
Н	Half Carry Flag			
Z	Zero Flag			
S	Sign Flag			
Χ	Not Used			

Floor Definitions

interrupt vector	Wichioly Helicon
l	R
Index Register	IX
Index Register	IY
Stack Pointer	SP
Program Counter	PC

Interrunt Vector

Flag Register Bit Positions

Bit	7	6	5	4	3	2	1	0
Position	S	Z	Χ	Н	Χ	P/V	N	С