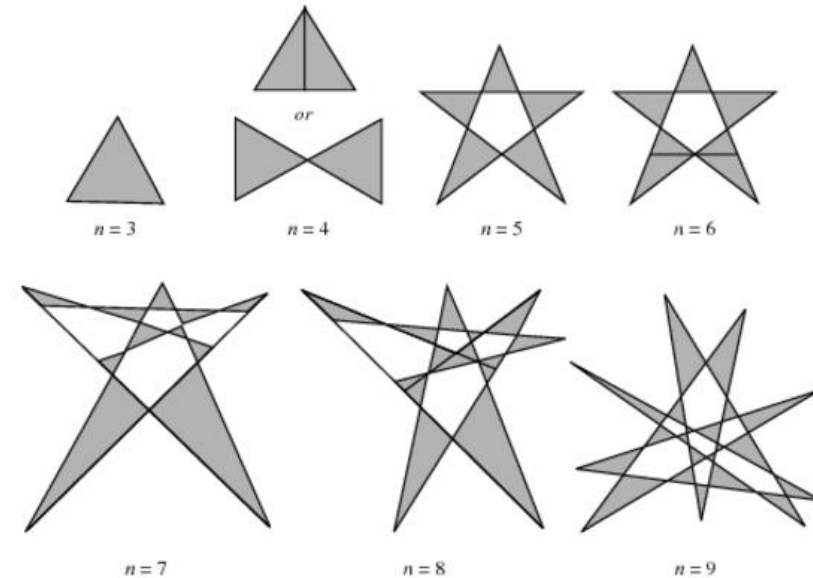


# Kobon Triangles

# The question is: What is the maximum number of non-overlapping triangles realizable by $n$ straight lines in a plane?

- It was originally stated in the book “The Tokyo Puzzle” by Kobon Fujimura, in 1978 [1-2].
- For up to six lines it’s easy to find  $K(n)$  and the corresponding optimal configurations, Figure 1.
- According to [3] up to now, this is an unsolved problem in combinatorial geometry.
- [1] K. Fujimura, “The Tokyo Puzzle”, Charles Scribner’s Sons, New York, 1978.
- [2] M. Gardner, “Wheels, Life, and Other Mathematical Amusements”. W. H. Freeman, New York, pp. 170-171 and 178, 1983.
- [3] G. Clément, J. Bader, “Tighter Upper Bound for the Number of Kobon Triangles”, Dec. 21, 2007



• Figure 1.

# Saburo Tamura upper bound proof

- On Table I, the number of non-overlapping triangles  $K(n)$  that the best known configuration of  $n$  straight lines realizes.
- The bold numbers reach the bound of Tamura, the (\*) indicates the configuration which reach the tighter bound (2), and therefore are maximum as well.
- Definition 1.- A perfect configuration is an arrangement of  $n$  pairwise intersecting lines, where each segment is the side of exactly one non-overlapping triangle and  $K(n)$  meets the upper bound (1). For example  $n=3$  or 5.

$n$	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
$(n \bmod 6)$	3	4	5	0	1	2	3	4	5	0	1	2	3	4	5	0	1	2
$K(n)$	<b>1</b>	<b>2</b>	<b>5</b>	7*	<b>11</b>	15*	<b>21</b>	25	32	38	<b>47</b>	53	<b>65</b>	72	<b>85</b>	93	104	115
Bound of Tamura	1	2	5	8	11	16	21	26	33	40	47	56	65	74	85	96	107	120
Authors' bound	1	2	5	7	11	15	21	26	33	39	47	55	65	74	85	95	107	119

Table I

- Saburo Tamura has proved an upper bound which is describe in (1)

$$K(n) \leq \left( \frac{n(n-2)}{3} \right) \quad \forall n \geq 3 \quad (1)$$

$$K(n) \leq \left\lfloor \frac{n(n-2)}{3} \right\rfloor - \mathbf{I}_{\{n|(n \bmod 6) \in \{0,2\}\}}(n) \quad (2)$$

# Saburo Tamura upper bound proof

- **Lemma 1.** If  $(n \bmod 3) \in \{0, 2\}$  then all configurations that meet the upper bound (1) are perfect configurations.
- In these cases  $n(n-2) \equiv 0 \pmod 3$ ; hence  $K(n) = n(n-2)/3$ .
- **Proof:** it is necessary to show BWOC that no configuration with common side triangles and  $(n \bmod 3) \in \{0, 2\}$  exist with  $K(n) = n(n-2)/3$ .
- This is equal to show that the number of line segments needed is larger than  $3K(n)$ .
- We all know that a triangle needs three line segments if they don't side another triangle.
- From Figure 2 We can see:
  - 1) A line segment can be the side of one or more triangles
  - 2) If a line segment is the side of two triangles then the corresponding line intersects at one of the two endpoints an existing point which belongs to both triangles.
  - 3) Every intersection point with more than two corresponding lines is part of at most two pairs of triangles that share a common side.

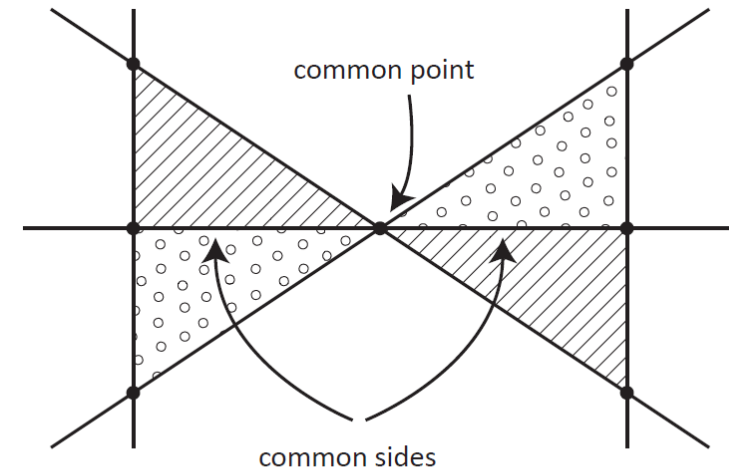


Figure 2. Two pairs of triangles share one common side each and a common point which is the intersection of (at least) three lines.

# Saburo Tamura upper bound proof

- By looking at Figure 3 we see that if a line intersects an existing point then the number of points decreases by 2 and the number of segments by 3. Hence we "save" at most two segments (the common ones) but we lose three due to the intersection of more than two lines in one point.
- Hence the number of line segments needed to build triangles increases if one side belongs to two triangles.
- Clearly, if a side does not belong to any triangle the number increases as well.
- Therefore the number of line segments needed is minimized, if each line segment belongs to exactly one triangle and cannot be reached in all other cases.

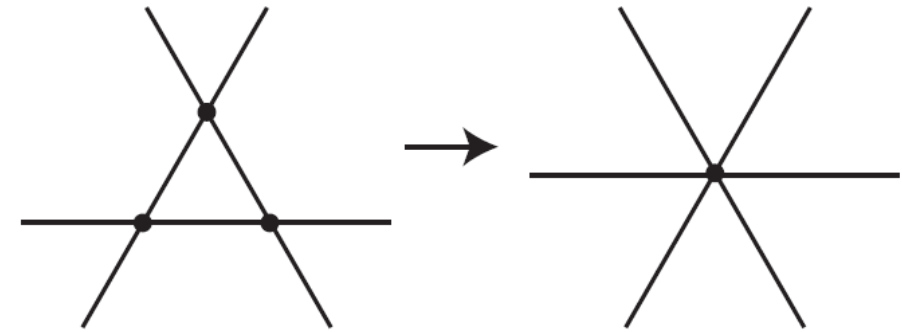


Figure 3. The number of line segments and points decreases by 3 and 2 respectively if a line intersects an existing point.

# Biplanar Crossing Number

# On the Biplanar Crossing Number [1]

- When designing printed circuit boards (PCBs) or integrated circuits (ICs) it is desirable to minimize the number of jumpers and via-holes.
- Replacing the components' pins with vertices and the paths between them with edges yields the linear graph  $G$ .
- We want to split  $G$  into two subgraphs  $G_1$  and  $G_2$  that correspond to the two sides of a PCB or to two layers of metallization on an IC.
- Our problem becomes minimize  $v_2(G)$ .

[1] A. Owens, "On the biplanar crossing number," in *IEEE Transactions on Circuit Theory*, vol.18, no.2, pp.277-280, Mar 1971

# On the Biplanar Crossing Number [1]

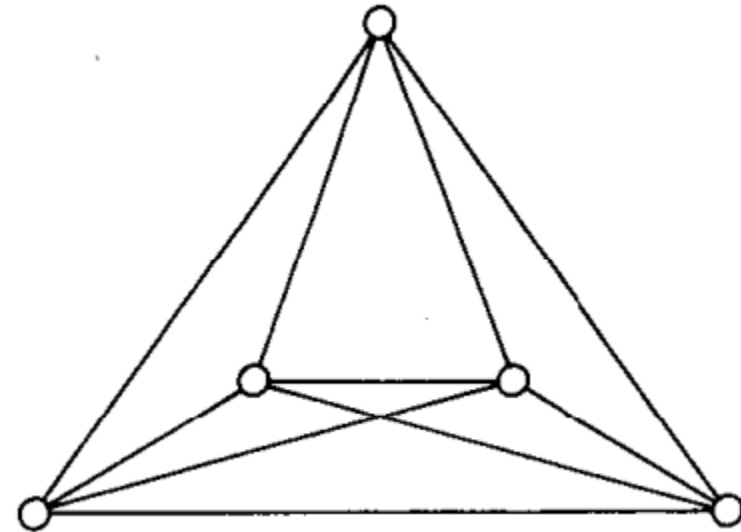
- The first step is to determine if  $G$  is planar. Which would imply  $\nu_2(G) = 0$ .
- The second step is to determine the thickness  $t$  of  $G$ . Given  $t$  and the  $t$  subgraphs we could construct a PCB with no crossings in  $t$  layers.
- The third step is to draw  $G$  in one plane so that the edges have the fewest number  $\nu_1(G)$  of crossings.
- The final step is to determine the fewest number of edge crossings when the graph is drawn on two planes.

[1] A. Owens, "On the biplanar crossing number," in *IEEE Transactions on Circuit Theory*, vol.18, no.2, pp.277-280, Mar 1971



# On the Biplanar Crossing Number [1]

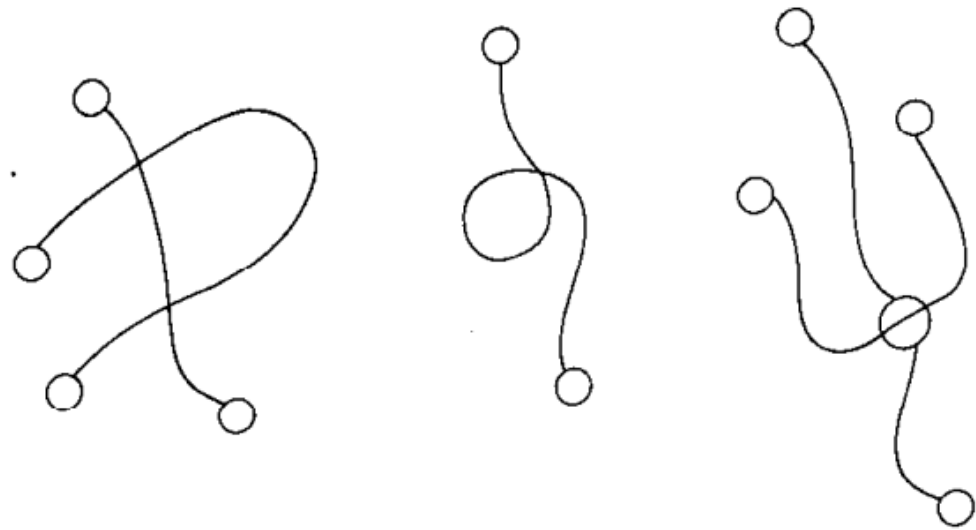
- Side note...
- $K_5$  drawn with one edge crossing, so
- $\nu_1(K_5) = 1$ .
- Since we know  $\nu_1(K_5) > 0$  and with the figure on the right we conclude  $\nu_1(K_5) = 1$ .



[1] A. Owens, "On the biplanar crossing number," in *IEEE Transactions on Circuit Theory*, vol.18, no.2, pp.277-280, Mar 1971

# On the Biplanar Crossing Number [1]

- Forbidden crossings:
- No crossings at vertices.
- No two edges may cross more than once.



[1] A. Owens, "On the biplanar crossing number," in *IEEE Transactions on Circuit Theory*, vol.18, no.2, pp.277-280, Mar 1971

# On the Biplanar Crossing Number [1]

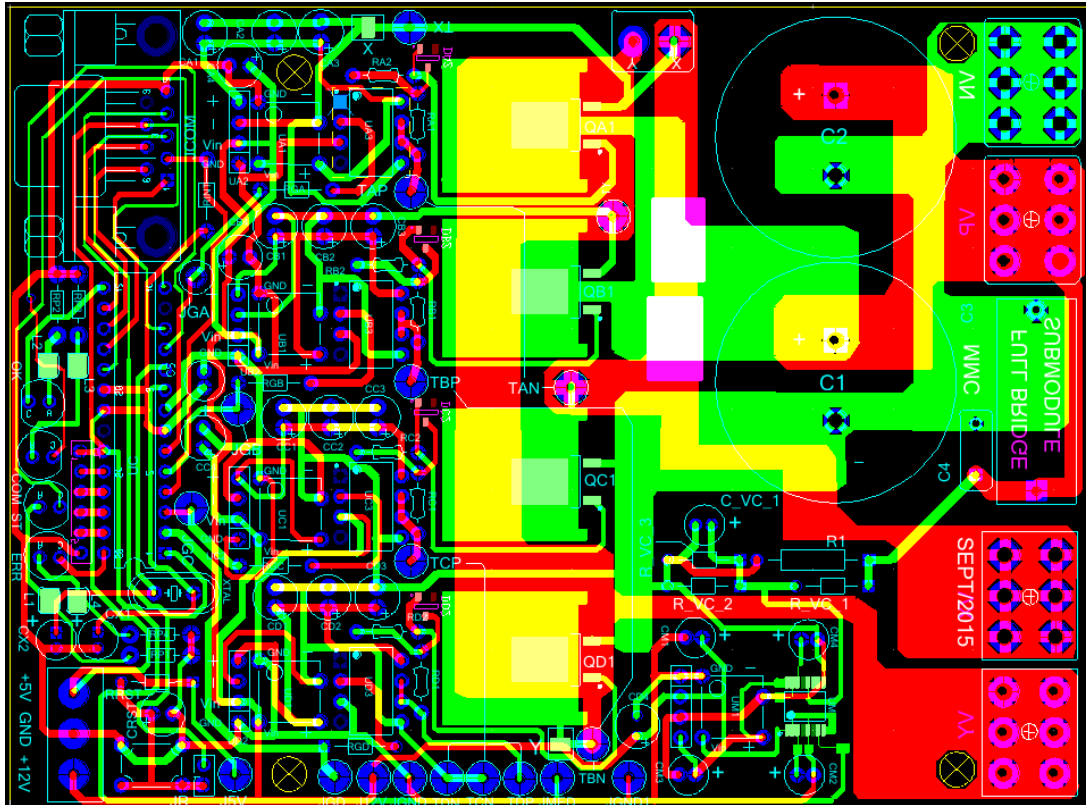
- Assuming that we have two subgraphs of  $G$  ( $G_1, G_2$ ) with  $G = G_1 \cup G_2$  we can define the biplanar crossing number  $\nu_2(G)$  as

$$\nu_2(G) = \min(\nu_1(G_1) + \nu_1(G_2))$$

- So the problem is to obtain these two graphs.
- The problem becomes more constrained given that some sets of vertices must have specific relative positions.

[1] A. Owens, "On the biplanar crossing number," in *IEEE Transactions on Circuit Theory*, vol.18, no.2, pp.277-280, Mar 1971

# On the Biplanar Crossing Number [1]



**Spreadsheet View**

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Ref...	Shape	Loc...	Trace d...	Value	Part spacing	Pin swap	Gate swap	Pi
RRST	RES900-30...	No	10.0000	10kΩ	Use design r...	Yes	Internal...	Ni
TAN	TEST_POIN...	No	10.0000	TP	Use design r...	Yes	Internal...	Ni
TAP	TEST_POIN...	No	10.0000	TP	Use design r...	Yes	Internal...	Ni
TBN	TEST_POIN...	No	10.0000	TP	Use design r...	Yes	Internal...	Ni
TBP	TEST_POIN...	No	10.0000	TP	Use design r...	Yes	Internal...	Ni
TCN	TEST_POIN...	No	10.0000	TP	Use design r...	Yes	Internal...	Ni
TCP	TEST_POIN...	No	10.0000	TP	Use design r...	Yes	Internal...	Ni
TDN	TEST_POIN...	No	10.0000	TP	Use design r...	Yes	Internal...	Ni
TDP	TEST_POIN...	No	10.0000	TP	Use design r...	Yes	Internal...	Ni
TX	TEST_POIN...	No	10.0000	TP	Use design r...	Yes	Internal...	Ni
TY	TEST_POIN...	No	10.0000	TP	Use design r...	Yes	Internal...	Ni
UA1	VBSD1_DC_...	No	10.0000	VB...	Use design r...	Yes	Internal...	Ni
UA2	POS_DC_D...	No	10.0000	PD...	Use design r...	Yes	Internal...	Ni
UA3	DIP8300	No	10.0000	FO...	Use design r...	Yes	Internal...	Ni
UB1	VBSD1_DC_...	No	10.0000	VB...	Use design r...	Yes	Internal...	Ni
UB2	POS_DC_D...	No	10.0000	PD...	Use design r...	Yes	Internal...	Ni
UB3	DIP8300	No	10.0000	FO...	Use design r...	Yes	Internal...	Ni
UC	SDIP28I	No	10.0000	AT...	Use design r...	Yes	Internal...	Ni
UC1	VBSD1_DC_...	No	10.0000	VB...	Use design r...	Yes	Internal...	Ni
UC2	POS_DC_D...	No	10.0000	PD...	Use design r...	Yes	Internal...	Ni
UC3	DIP8300	No	10.0000	FO...	Use design r...	Yes	Internal...	Ni

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[1] A. Owens, "On the biplanar crossing number," in *IEEE Transactions on Circuit Theory*, vol.18, no.2, pp.277-280, Mar 1971

# On the Biplanar Crossing Number [1]

- The authors propose to obtain a simple biplanar model for  $K_n$  by decomposing it into three subgraphs:

$$K_{[n/2]}; \quad K_{\{n/2\}}; \quad K_{[n/2],\{n/2\}}.$$

$[x]$  implies the ceiling function       $\{x\}$  implies the floor function

- The crossing numbers of the three graphs added would yield the crossing number of  $K_n$ .

$$v_2(K_n) = v_1(K_{[n/2]}) + v_1(K_{\{n/2\}}) + v_1(K_{[n/2],\{n/2\}})$$

[1] A. Owens, "On the biplanar crossing number," in *IEEE Transactions on Circuit Theory*, vol.18, no.2, pp.277-280, Mar 1971

# On the Biplanar Crossing Number [1]

- Using previous conjectures from other papers, the authors give an upper bound for this biplanar crossing number with  $n$  even as:

$$v_2(K_n) \leq \frac{1}{2} \left[ \frac{r}{2} \right] \left[ \frac{r-1}{2} \right] \left[ \frac{r-2}{2} \right] \left[ \frac{r-3}{2} \right] + \left[ \frac{r}{2} \right]^2 \left[ \frac{r-1}{2} \right]^2,$$

- where  $n = 2r$ .

[1] A. Owens, "On the biplanar crossing number," in *IEEE Transactions on Circuit Theory*, vol.18, no.2, pp.277-280, Mar 1971

# On the Biplanar Crossing Number [1]

- Furthermore, in the text the authors mention that  $K_9$  is the **smallest complete graph which cannot be drawn on two planes without crossings**.
- From this statement we imply  $v_2(K_{1\dots 8}) = 0$ .
- Applying the formula to  $K_{10}$  and  $K_{12}$  yields
$$v_2(K_{10}) \leq 34 \text{ and } v_2(K_{12}) \leq 81.$$

[1] A. Owens, "On the biplanar crossing number," in *IEEE Transactions on Circuit Theory*, vol.18, no.2, pp.277-280, Mar 1971

Questions?