



# LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING

(AUTONOMOUS)

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## Assignment Questions

1) In the latch circuit shown, the NAND gates have non-zero, but unequal propagation delays. The present input condition is:  $P = Q = "0"$ . If the input condition is changed simultaneously to  $P = Q = "1"$ , the outputs X and Y are.

2) Convert the following Decimal number system to Binary, Octal, Hexa Decimal Number system

(i)  $(4796)_{10}$

(ii)  $(1248.56)_{10}$

3) Evaluate Small signal CE Amplifier and explain the input and output waveforms

4) Obtain the decimal, Octal and Hexa Decimal equivalent of given binary numbers.

[i]  $(1101101)_2$

[ii]  $(1101110.011)_2$