

Section [Unit 1] 1 of 6 Question : 5 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Which of the following is correct about Multiplexer

- It is a data converter
- It can be represented as 4:1 MUX
- Both A and B
- None of the above

Finish

Clear Response

Next (1)

Once you click next/finish, you will not be able to come back to this question later.

Section [Unit 2] 2 of 6 Question : 2 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question mark for help

If the Instruction register IR = 7800, AR=123, DR= F800 then the type of instruction will be

- Direct memory reference
- indirect memory reference
- register reference
- Input/output

C

Finish ► Clear Response

Next (3)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 2] 2 of 6 Question : 4 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question mark for help

Which of the following forms the part of instruction set completeness

- functional instruction
- control instruction
- transfer instruction
- all of the above

D

Finish ► Clear Response

Next (2)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 2] 2 of 6 Question : 3 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question mark icon

Which of the following is incorrect about the control unit

- Control unit can be hardwired or programmed
- Hardwired controls provides faster mode of operation
- programmed controls are difficult to be modify
- Hardwired control used combinational and sequential circuits

Finish ► Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 2] 2 of 6 Question : 5 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

In a basic computer, the PC is of _____ bits

- 12
- 8
- 16
- 10

C

Finish ► Clear Response

Next (3)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 3] 3 of 6 Question : 1 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Which of the following is a NOT feature of RISC computers

- Few instructions
- Few addressing modes
- variable length instruction
- hardwired control unit

C

Finish ► Clear Response

Next (1)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 3] 3 of 6 Question : 2 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Which of the following is correct about CISC computers

- the instructions acts indirectly on memory address
- machine instructions are designed to match high level language
- both A and B
- none of the above

C

Finish ► Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 2] 2 of 6 Question : 1 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + X

Select the correct answer

Saved successfully.

In a basic computer, the machine instruction is executed in following manner

- decode, fetch, execute
- fetch, execute, decode
- fetch, decode, execute
- any of the above sequence

C

Finish ►

Clear Response

Next (0)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Home In Anywhere 11806347.st@ipu.in Logout

Section [Unit 3] 3 of 6 Question : 3 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer 100%

Select the correct answer Saved successfully.

RISC stands for

Recover Instruction Set Computer
 Reproduce Instruction Set Computer
 Reduced Instruction Set Computer
 Reuse Instruction Set Computer

Finish Clear Response Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ▶

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Section [Unit 3] 3 of 6 Question : 4 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Which of the following is a zero address instruction

- LOAD A
- PUSH A
- MOV R1, A
- ADD R1, A

B

Finish ► Clear Response

Next (3)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 1] 1 of 6 Question : 2 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

MUX can be used to design

D

- Adders only
- Fundamental Gates only
-
- Universal Gates only
- All of the above

Finish

Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later.

Home In Anywhere 11806347.st@ipu.in Logout

Press Esc to exit full screen

Section [Unit 1] 1 of 6 Question : 1 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Selective set operation can be executed by using _____

OR gate
 AND gate
 NOT gate
 none of the above

A

Finish This is the beginning of the test! Clear Response

Once you click next/finish, you will not be able to come back to this question later.

Start Test ▶

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Section [Unit 1] 1 of 6 Question : 3 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

To design a arithmetic circuit which can perform 7 different arithmetic operation _____ is used

- Encoder
- Decoder
- Multiplexer
- Demultiplexer

C

Finish ► Clear Response

Next (3)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 1] 1 of 6

Question : 1 of 5

Select the correct answer

Convert hexadecimal value 43 to decimal.

67_{10}

22_{10}

20_{10}

10_{10}

A

Start

This is the **beginning** of the test!

Clear R

Select the correct answer

Which of the following is NOT an example of Input-output command

- Control
- Status
- Data
- all of the above

Finish

Clear Response

Once you click



Select the correct answer

UART stands for

- United Asynchronous Receiver Transmitter
- Universal Asymmetric Receiver Transmitter
- Universal Asynchronous Receiver Transmitter
- Universal Asynchronous Repeater Transmitter



C

Finish

Clear Response

- Section [Unit] 6 of 8 Question 17 of 19 Marks for this Question: 1.0 Negative Marks: 0.0
- Select the correct answer
- Which of the following is not correct about ARM processor
- Low power consumption
 - Complex Circuit
 - It can't be used in Windows
 - Scheduling instructions is difficult
- B

Clear Response

Q1. Which of the following shared memory multiprocessor model is used in time critical applications.

- UMA
- NUMA
- COMA
- All of the above

Clear Response

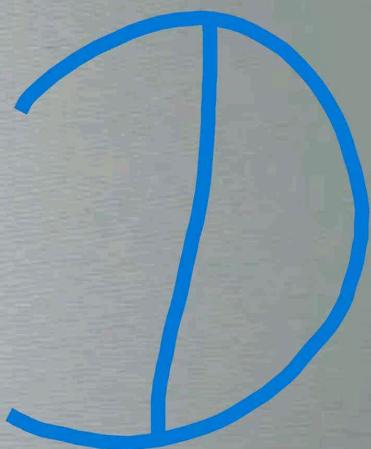
Once you click next/finish, you will not be able to change your answer.

Correct answer
Pipelining increases



of the processor

- Storage
- Latency
- Predictivity
- Throughput



Finish

Clear Response

Home > Anywhere

Section [Unit 5] 3 of 6

Question 18 of 10

Marks for this Question 1.0

Negative Marks 0.0

Time Remaining 00:00:00

Select the correct answer

Index field and Tag field is associated with which of the mapping technique in cache memory

- Direct Mapping
- Associative Mapping
- Set-associative Mapping
- Upset-associative Mapping

A

Clear Response

Once you click next/finish, you will not be able to change your answer.

Question 2 of 10

Question 2 of 10

Select the correct answer

mapping stores both address and the content of the memory word

- Direct Mapping
- Associative Mapping
- Set associative mapping
- None of the above

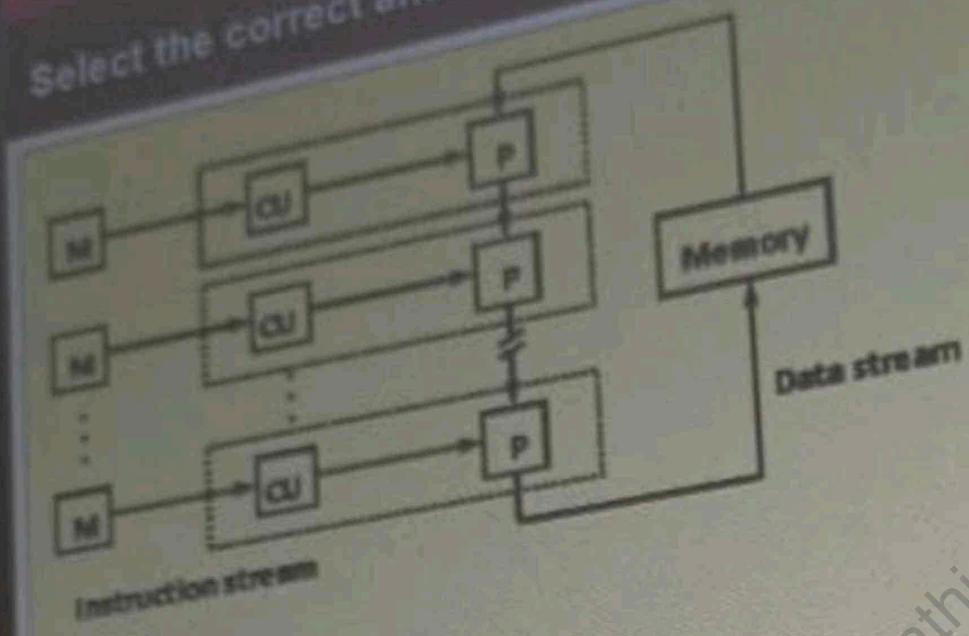
Clear Response

B

Once you click next/finish, you will

The image shows a computer monitor displaying a test interface. The question is about memory mapping, specifically asking which type stores both address and content. Four options are listed: Direct Mapping, Associative Mapping, Set associative mapping, and None of the above. A large, blue, hand-drawn letter 'B' is written diagonally across the page, likely indicating the correct answer. In the bottom right corner, there is a message: "Once you click next/finish, you will".

Select the correct answer



The above given diagram is an example of

- SISD
- SIMD
- MISD
- MIMD

C

Section [Unit 5] 5 of 6

Question : 9 of 10

Marks for this Question : 1

Negative Marks : -25% on wrong answer

Select the correct answer

Consider the following statements

- a) FIFO replacement policy is easy to implement
- b) In FIFO, under certain circumstances pages are removed and loaded from memory too frequently

- Only A is correct
- Only B is correct
- Both A and B are correct
- None of the above

Submit

Clear Response

Select the correct answer

Question : 9 of 10

Marks for this Question :

To design a 1024×8 RAM using 128×8 RAM , how many such

4

6

8

10

Address bits = 10
Data bits = 8
Control bits = 6

Clear Response

Select the correct answer

Memory Hierarchy in terms of access speed is

- Cache > Main Memory > Register
- Register > Cache > Main Memory
- Register > Main Memory > Cache
- Main Memory > Register > Cache

B

Select the correct answer

Parallel Processing is applicable at

- Job level only
- task level only
- instruction level only
- All of the above

D

In an asynchronous Data transfer

- a) Internal timing in CPU and interface is dependent
- b) It requires control signals be transmitted to indicate the time of data being transmitted

- Only A is correct
- Only B is correct
- Both A and B are correct
- None of the above

B

Section [Unit 4] 4 of 6

Question : 4 of 10

Marks for this Question : 1

Nega

Select the correct answer

Which of the following is incorrect about Input-Output Interface

- It decodes the device address
- It decodes the Command (Operations)
- Provides Signal for Peripheral Controller
- none of the above



Finish

Clear Response

Select the correct answer

Consider the following statements

- i. Memory mapped I/O is less efficient than isolated I/O
- ii. Memory mapped I/O is larger in size than isolated I/O

- Only i is correct
- only ii is correct
- both i and ii are correct
- None of the above

A



in Anywhere

Section [Unit 4] 4 of 6

Question : 4 of 10

Marks for this Question : 1

Negative Marks : -25% on

Select the correct answer

In programmed I/O method,

- a) CPU stays in loop until the I/O unit indicated that it is ready for the data transfer
- b) It is a time consuming process

- Only A is correct
- Only B is correct
- Both A and B are correct
- None of the above

C

Clear Response

Section [Unit 6] 6 of 6 Question : 3 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Which of the following statements is/are correct

- (A) Multiprocessor has separate memory and single CPU
(B) Multiprocessor has shared Memory with multiple CPUs
(C) The CPUs in Multiprocessor can have master-slave relationship only

- Only A is correct
- Only B is correct
- B & C are correct
- A , B are correct

C

Finish

Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later.

Section [Unit 5] 5 of 6 Question : 10 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Hardware organization of Content Addressable Memory includes

- Argument register only
- Key register
- Match register
- All of the above

D

Finish ► Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 6] 6 of 6 Question : 6 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

In "ARM Processor", ARM stands for

- Adaptive RISC Machine
- Advanced RISC Machine
- Analytical RISC Machine
- Additional RISC Machine

B

Finish ► Clear Response

Next (1)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 6] 6 of 6 Question : 1 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Time shared common Bus organization of a Multiprocessor has

- CPUs and the shared Memory only
- IOPs and Shared Memory only
- CPU, IOPs and Shared Memory
- none of the above

C

Finish ► Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 6] 6 of 6 Question : 2 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

The above given diagram is an example of

- SISD
- SIMD
- MISD
- MIMD

Finish  Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later.

Section [Unit 6] 6 of 6 Question : 10 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question link

Which of the following is not an example of the Physical forms available for interconnection network in a multiprocessor

- Time shared common bus
- Frequency shared common bus
- Cross bar Switch
- Multiport Memory

B

Finish ➔ Clear Response

This is the end of the test!

Next ➔

Once you click next/finish, you will not be able to come back to this question later.

Start Test ➔

Section [Unit 6] 6 of 6 Question : 8 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question mark icon

Which of the following is not correct about ARM processor

- Low power consumption
- Complex Circuit
- It can't be used in Windows
- Scheduling instructions is difficult

B



Finish ► Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 6] 6 of 6 Question : 9 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question mark for help

Which of the following is incorrect about GPU

- GPU is similar to CPU
- GPU has large number of cores for faster processing
- GPU has big cache, few threads
- GPU mainly rely on multi-threading

C

Finish ► Clear Response

Next (2)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 6] 6 of 6 Question : 5 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question mark icon

GPU stands for

- Gradual Processing Unit
- Graphics Processing Unit
- Grand Processing Unit
- None of the above

B



Finish ► Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 6] 6 of 6 Question : 7 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question limit left: 3

In multiprocessor, graceful degradation is

- Ability to continue working even if one processor fail
- Ability to continue working even if Shared Memory fail
- both A and B
- none of the above

A

Finish ► Clear Response

Next (3)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 6] 6 of 6 Question : 4 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Which of the following is not an example of shared memory multiprocessor models

- UMA
- NUMA
- PUMA
- COMA

C

Finish ► Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 4] 4 of 6 Question : 3 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Continue and finish

Which of the following statements is/are correct

- A) Isolated I/O is more efficient than the Memory mapped I/O
B) Memory mapped I/O has increased memory due to addition of I/O

- Only A is correct
- Only B is correct
- Both A and B are correct
- None of the above

A

Finish ➔ Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Section [Unit 4] 4 of 6 Question : 4 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + X

Select the correct answer

In programmed I/O method,

Your time is up on this question. Automatically saving the current question and navigating to the next question with time available.

- a) CPU stays in loop until the I/O unit indicated that it is ready for the data transfer
b) It is a time consuming process

Saved successfully.

Automatically navigated to this question based on time remaining.

- Only A is correct
- Only B is correct
- Both A and B are correct
- None of the above

C

Finish

Clear Response

Next (1)

Once you click next/finish, you will not be able to come back to this question later.

Section [Unit 4] 4 of 6 Question : 5 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question mark icon

Input-Output Interface resolves the difference between

- CPU and Memory
- Computer and Peripheral Devices
- Memory and Peripheral Device
- None of the above

B

Finish ► Clear Response

Next (3)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 4] 4 of 6 Question : 7 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Which of the following is an example of a Peripheral Device

- CPU
- Memory
- Input/Output
- All of the above

C

Finish ► Clear Response

Next (3)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 3] 3 of 6 Question : 5 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

What will the output of the given instruction : ADD R1, R2

- Add the content of R1 and R2 and move the result to R2
- Add the content of R1 and R2 and move the result to R1
- Add the content of R1 and R2 and move the result to Accumulator
- None of the above

B

Finish ►

Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 4] 4 of 6 Question : 10 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.  Continue and Next

Isolated input-output has

- common address and control bus but separate data bus
- common address and data bus but separate control bus
- common control and data bus but address control bus
- common address, data and control bus



Finish  Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test 

Section [Unit 5] 5 of 6 Question : 7 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Hit Ratio is associated with

- Cache Memory
- Virtual Memory
- Associative Memory
- Main Memory

A

Finish ► Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ▶

Section [Unit 5] 5 of 6 Question : 6 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Test time left: 54:59 Question time left: 01:39

If in a associative memory , Argument register A = 110 111000 and Key register K= 00011100 then the correct content in the match register M from the following will be

- 110 000111
- 101 111011
- 101 110 111
- 111101111

A

Finish ► Clear Response

Next >

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

5/5

Section [Unit 5] 5 of 6 Question : 9 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

In a associative Memory , the smallest unit cell C has

- J-K Flip flop
- D flip flop
- S-R flip flop
- T flip flop

C

Finish ► Clear Response

Next (2)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 4] 4 of 6 Question : 1 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question time left: 00:00:00

Which of the following is/are correct

- A) In programmed I/O, CPU stays in a program loop until the I/O unit indicate that it is ready for data transfer
B) The problem mentioned above can be solved by using Interrupts

- Only A is correct
- Only B is correct
- Both A and B are correct
- None of them is correct

[Finish](#) [Clear Response](#)

[Next \(3\)](#)

Once you click next/finish, you will not be able to come back to this question later

Section [Unit 4] 4 of 6 Question : 2 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Memory mapped I/O has

- Increased addressing memory due to the memory mapped I/O
- decreased addressing memory due to the memory mapped I/O
- Separate control line
- none of the above

B

Finish ► Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 5] 5 of 6 Question : 5 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Associative mapping procedure is related to

- Cache Memory
- Virtual Memory
- Associative memory
- Auxiliary Memory

A

Finish ► Clear Response

Next (3)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 5] 5 of 6 Question : 3 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question mark for help

Address space and Memory space is associated with which of the memory types

- Cache Memory
- Content Addressable Memory CAM
- Virtual Memory
- Auxiliary Memory

C

Finish ► Clear Response

Next (3)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 4] 4 of 6 Question : 9 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

Data transfer to and from peripheral can be handled using

- Programmed I/O only
- Interrupt-Initiated I/O only
- Direct Memory Access (DMA)
- All of the above

D

Finish ► Clear Response

Next (3)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 5] 5 of 6 Question : 9 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Test time left: 51:37 Question time left: 00:35

In a associative Memory , the smallest unit cell C has

- J-K Flip flop
- D flip flop
- S-R flip flop
- T flip flop

C

Finish ► Clear Response

Next >

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 5] 5 of 6 Question : 1 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully.

mapping stores both address and the content of the memory word

- Direct Mapping
- Associative Mapping
- Set associative mapping
- None of the above

B

Finish ► Clear Response

Next (4)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 4] 4 of 6 Question : 6 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Saved successfully. Question mark for help

Which of the following is an example of Output device

- Card Puncher
- Card Reader
- Optical Mark Reader
- Bar Code Reader

A

Finish ► Clear Response

Next (1)

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 5] 5 of 6 Question : 2 of 10 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

Test time left: 58:37 Question time left: 00:55

Tracks and sectors are associated with

- Magnetic tapes
- Magnetic Drives
- Pen Drives
- All of the above

B

Finish ► Clear Response

Next >

Once you click next/finish, you will not be able to come back to this question later

Start Test ►

Section [Unit 1] 1 of 6 Question : 4 of 5 Marks for this Question : 1 Negative Marks : -25% on wrong answer

100% + x

Select the correct answer

To perform a logical AND operation, which of the following symbol is used

<

>

^

+

C

Finish ➔ Clear Response

Next (3)

Once you click next/finish, you will not be able to come back to this question later

Start Test ➔