

Fig. 1. Satisfying assignments for simple gates

Fig. 2. Unfolding Transition Function of Mealy Finite State Machine

Fig. 3. f^{-1} and Parameters of o 's Finite Length Subsequence

Fig. 4. Circuit structure of E^{-1}

TABLE I
INFORMATION OF BENCHMARKS

TABLE II
RESULTS OF CHECKING THE PARAMETERIZED COMPLEMENTARY
CONDITION

TABLE III
RUN TIME OF BUILDING COMPLEMENTARY CIRCUITS

TABLE IV
COMPARING DECODER AREA

TABLE V
COMPARING CRITICAL PATH LATENCIES IN NANOSECOND