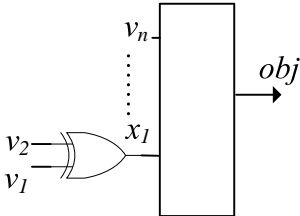


a) origin circuit with  
hidden XOR



b) new circuit with  
removed XOR