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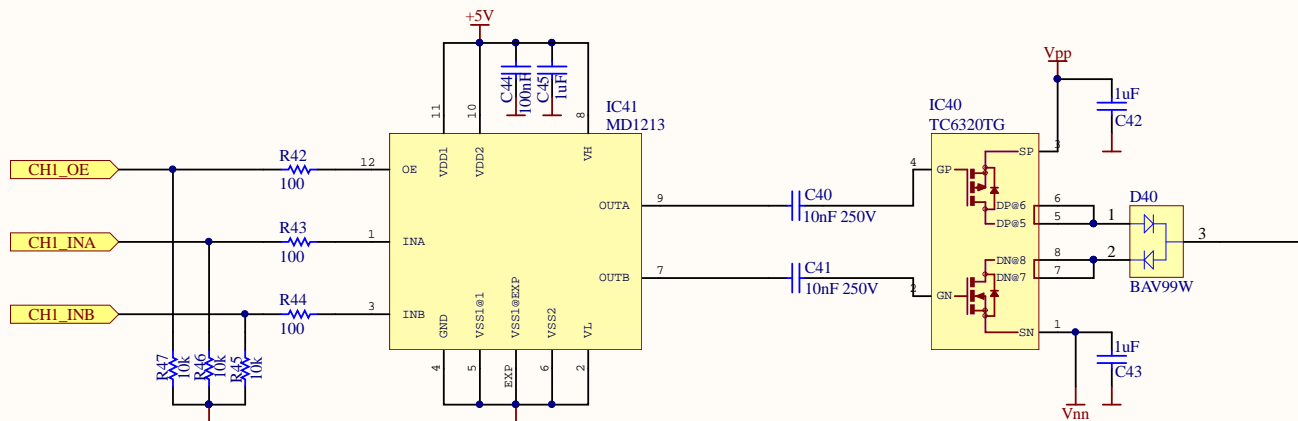
2

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A

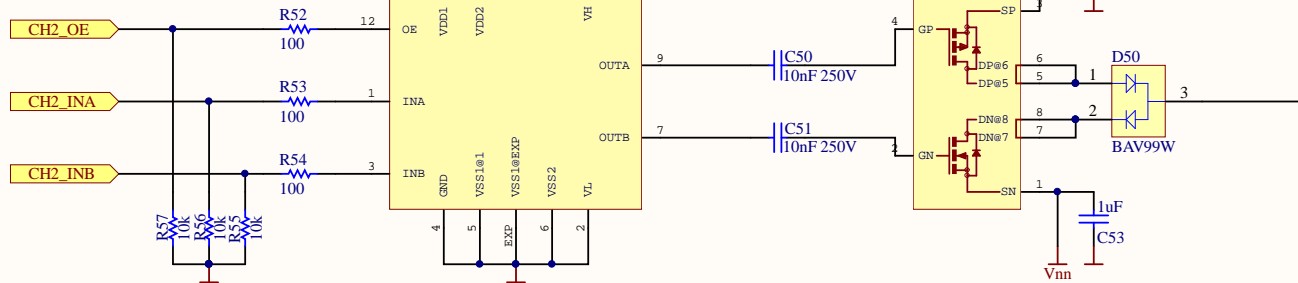
B

C



CH1_Out

Truth function table						
OE	INA	INB	OUTA	OUTB	OUT	
H	L	L	Vh	Vh	Vnn	
H	L	H	Vh	Vl	off	
H	H	L	Vl	Vh	conflict	
H	H	H	Vl	Vl	Vpp	
L	X	X	Vh	Vl	off	



CH2_Out

1k

R51

D51
BAV99W

IC52 MD0100N8

3

B

CON

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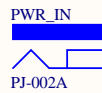
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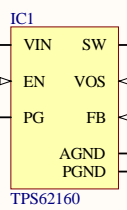
Vin 10.8 - 13.2V 1A



Vin



+12V



+12V



GND

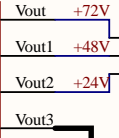
T1 Vin

Vin 10.8 - 13.2 V

DCDC_PP

+Vin	+72	+48	+24	0	Vout
	+48	+24	0	-24	Vout1
	+24	0	-24	-48	Vout2
	0	-24	-48	-72	Vout3
-Vin					
GND					

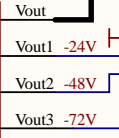
NMT1272SC



DCDC_NN

+Vin	+72	+48	+24	0	Vout
	+48	+24	0	-24	Vout1
	+24	0	-24	-48	Vout2
	0	-24	-48	-72	Vout3
-Vin					
GND					

NMT1272SC



SSW-104-01-F-D

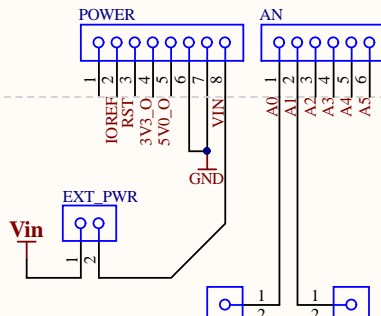
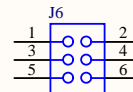
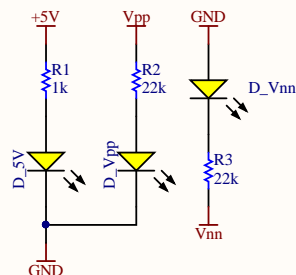
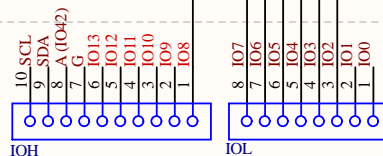
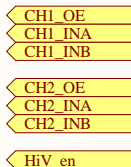
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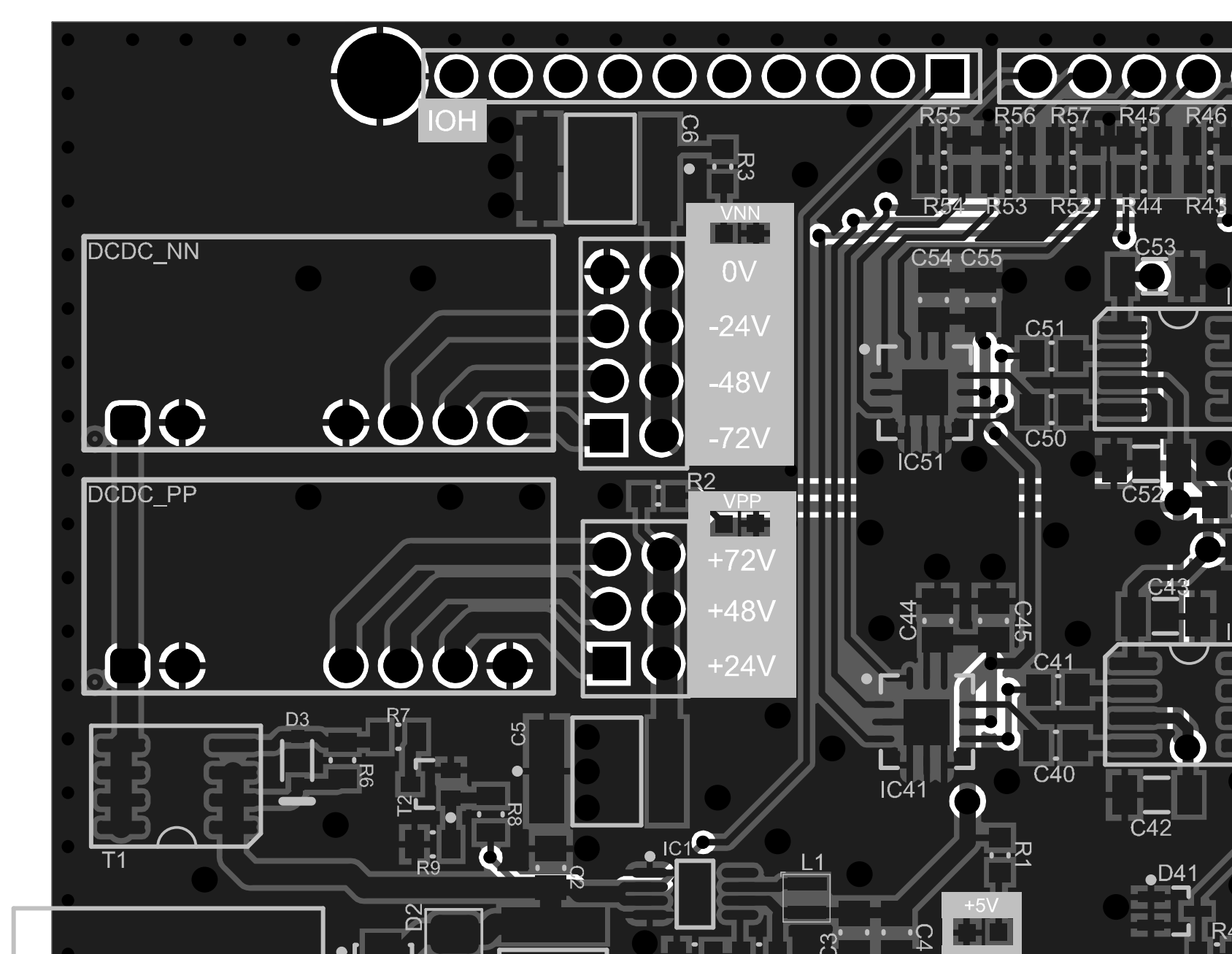
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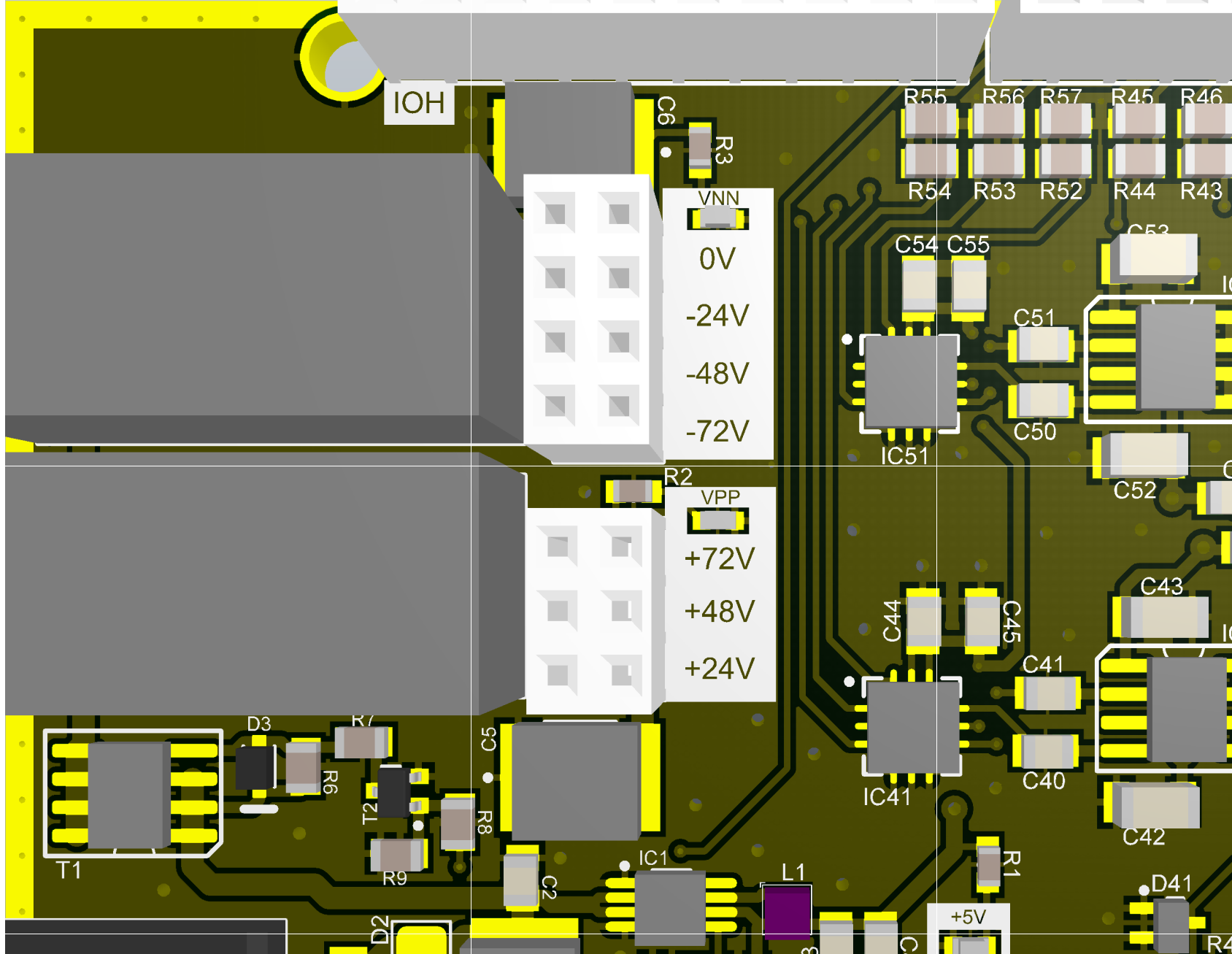
3

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1.8 - 5.0V inputs







IOH

C6

VNN

0V

-24V

-48V

-72V

R2

VPP

+72V

+48V

+24V

R55

R56

R57

R45

R46

R54

R53

R52

R44

R43

C54

C55

C53

C51

C50

IC51

C52

C5

C43

C44

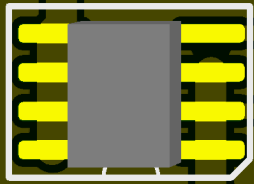
C45

C41

C40

IC41

C42



T1

D3

R6

R7

T2

R9

C5

R8

C2

IC1

L1

+5V

R1

D41

R4