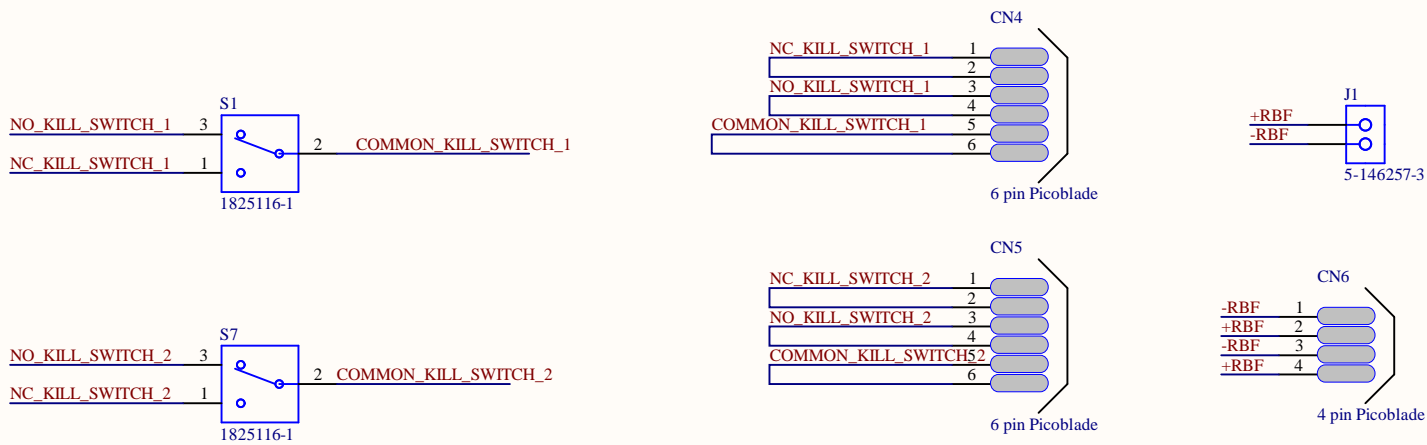


Title		
Size	Number	Revision
A4		
Date:	2/03/2021	Sheet of
File:	C:\Users\...\CAN_schematic.SchDoc	Drawn By:



RBF	Switch(NC)	Switch(NO)	MOSFET	EPS POWER
1	X	X	OPEN	Cut-off
0	1	0	CLOSE	ON
0	0	1	OPEN	Cut-off (launch moment)

Title			
Size	Number		Revision
A4			
Date:	2/03/2021	Sheet	of
File:	C:\Users\...\kill-switches_schematic.SchDoc		
Drawn By:			

A

B

C

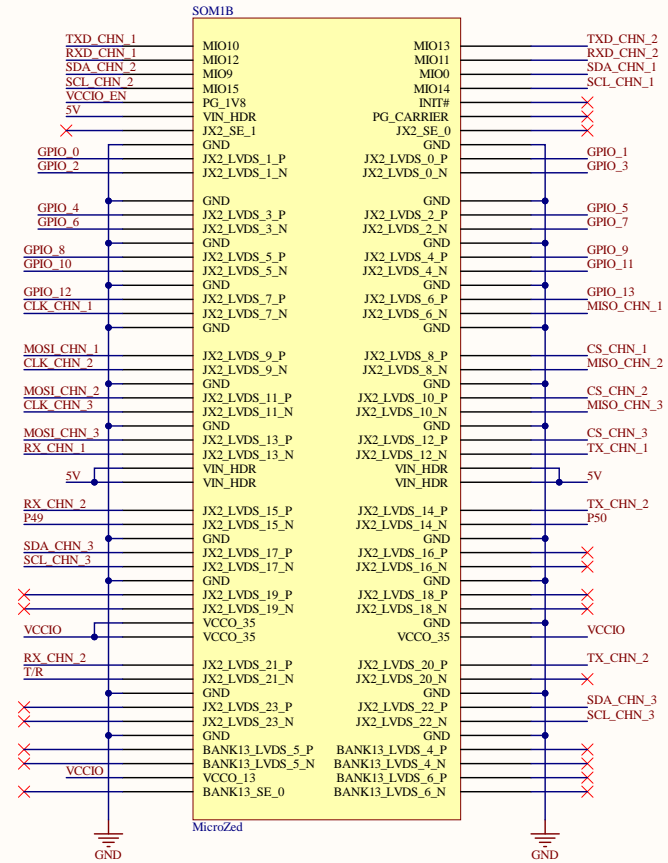
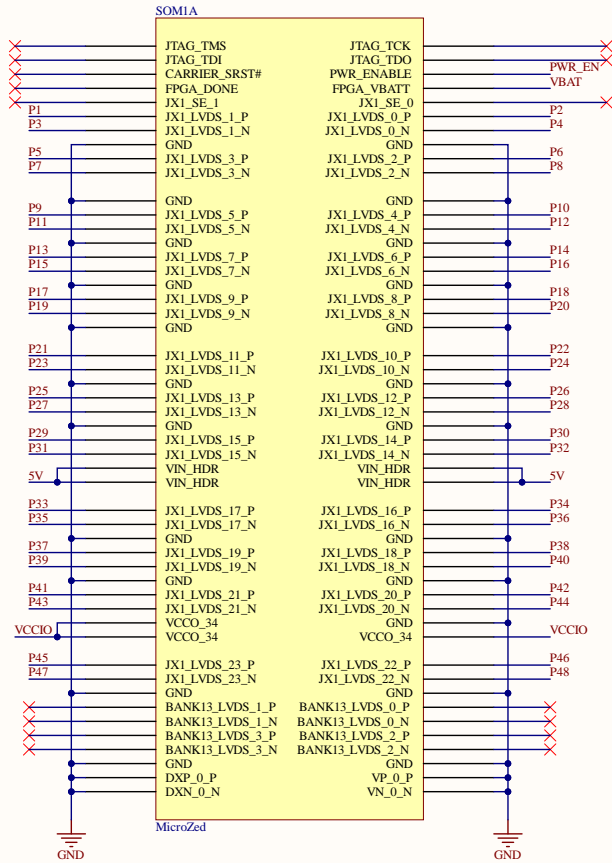
D

A

B

C

D



Title		
Size	Number	Revision
A3		
Date:	2/03/2021	Sheet of
File:	C:\Users\microzed_schematic.SchDoc	Drawn By:

## A



## C



## A

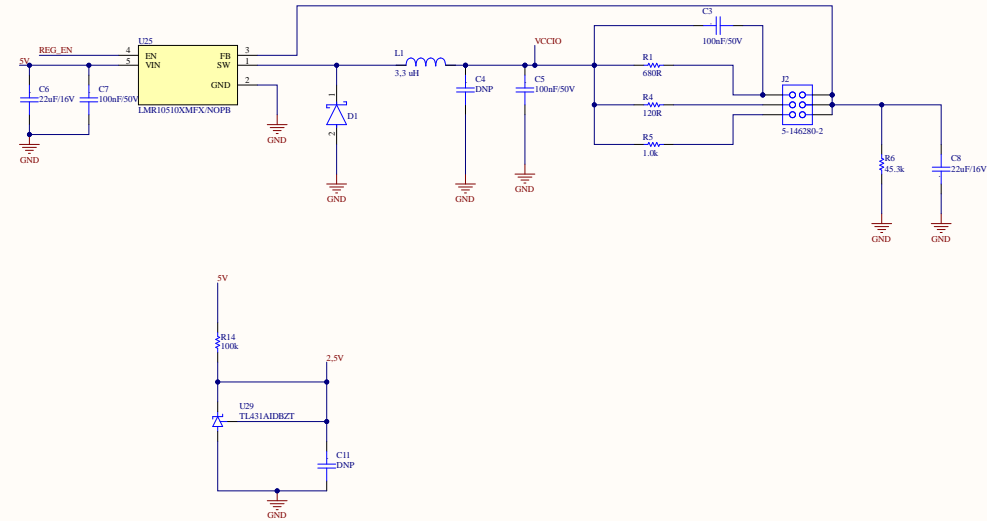


## C

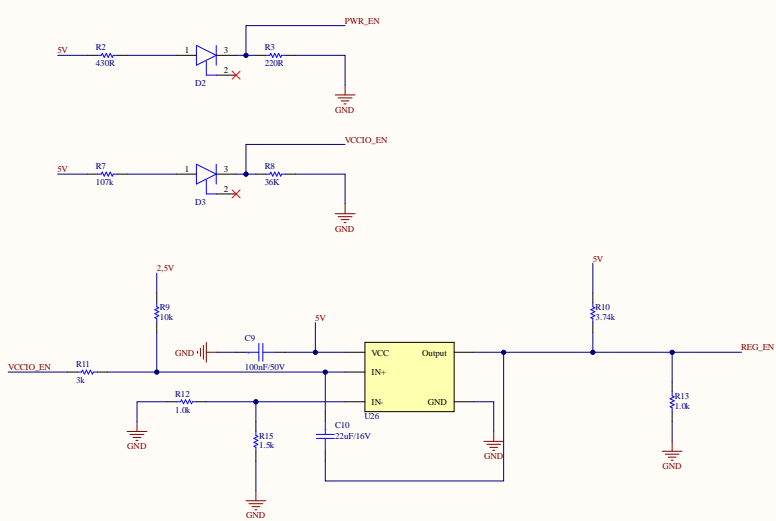


Title		
Size A2	Number	Revision
Date: File:	2/03/2021 C:\Users\...PC\104_schematic.SchDoc	Sheet of Drawn By:

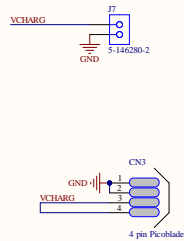
Reference signals



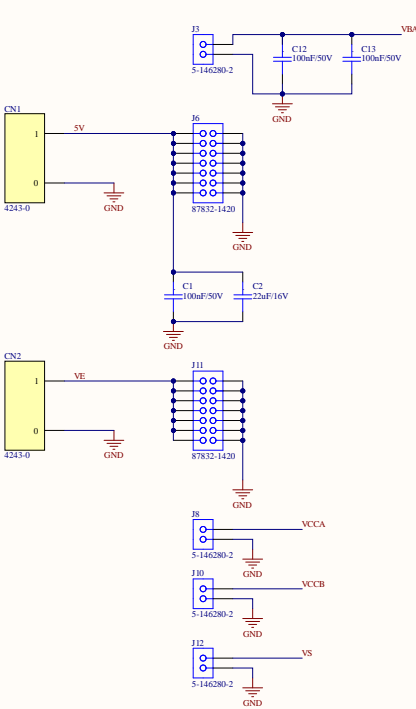
Enables



Batteries charge circuit

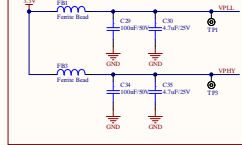


External voltage sources

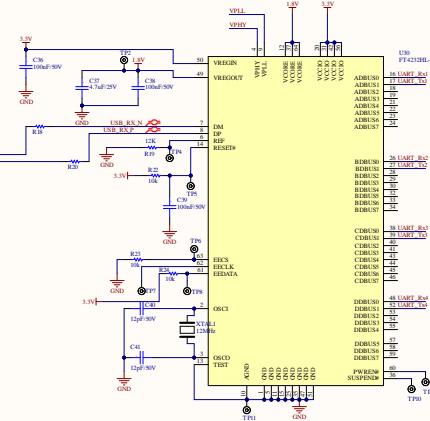
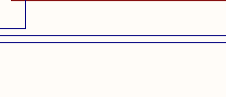
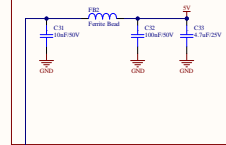


Title		
Size	Number	Revision
A2		
Date:	2023/02/01	Sheet of
File:	C:\Users\j...power_schematic.SchDoc	Drawn By:

Low pass LC filters for VPLL and VPHY



USB VBUS Filter



FT4232HL-REEL

FT4232HL-REEL

FT4232HL-REEL

FT4232HL-REEL

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FT4232HL-REEL

FT4232HL-REEL

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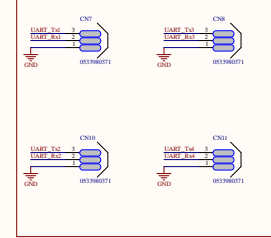
FT4232HL-REEL

FT4232HL-REEL

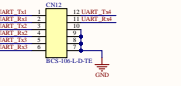
FT4232HL-REEL

FT4232HL-REEL

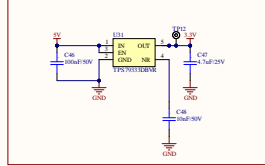
Debug interfaces



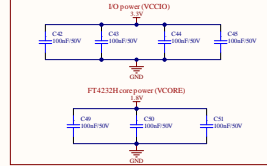
Redundant pin header



Voltage Regulator

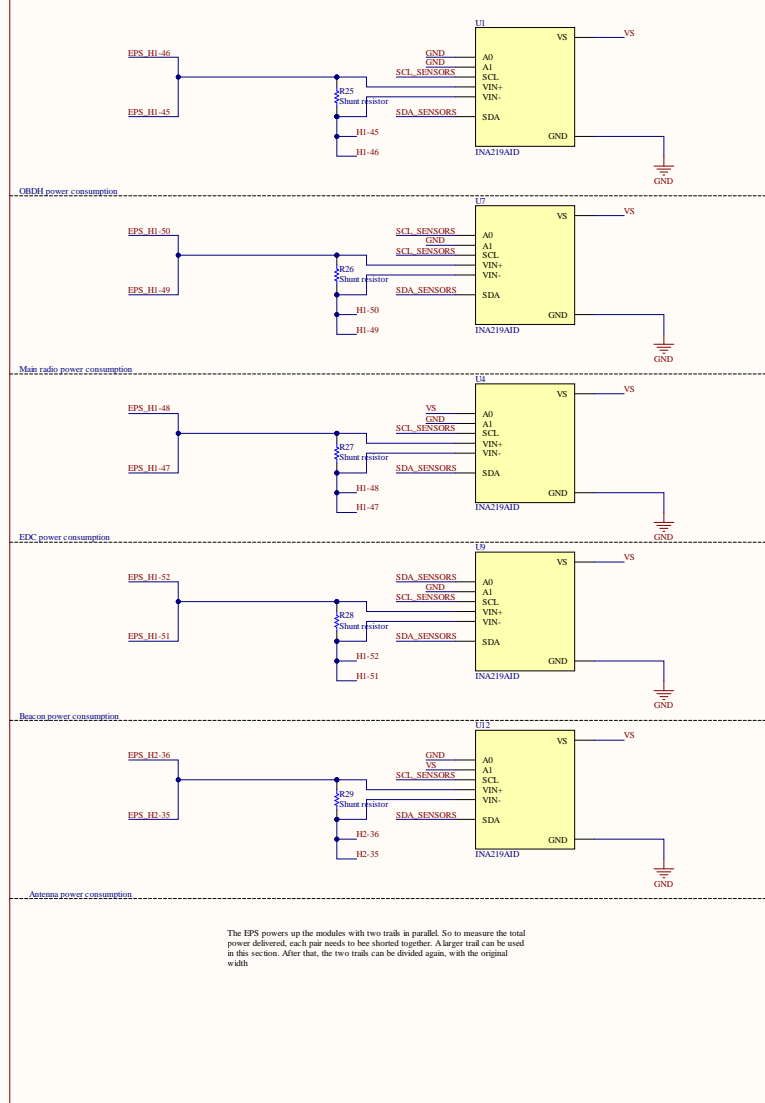


Decoupling capacitors



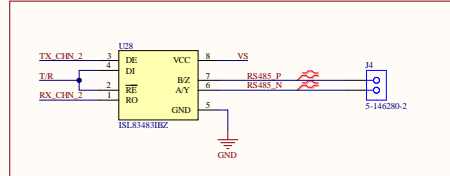
Title		
Rev	Number	Revision
A1		
USB-to-serial converter V3.00		
USB-to-serial converter V3.00		

## Current sensors

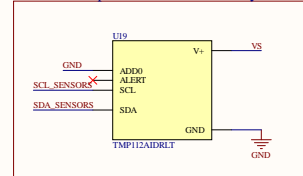


The EPS powers up the modules with two tracks in parallel. So to measure the total power delivered, each pair needs to be shorted together. A larger track can be used in this section. After that, the two tracks can be divided again, with the original width

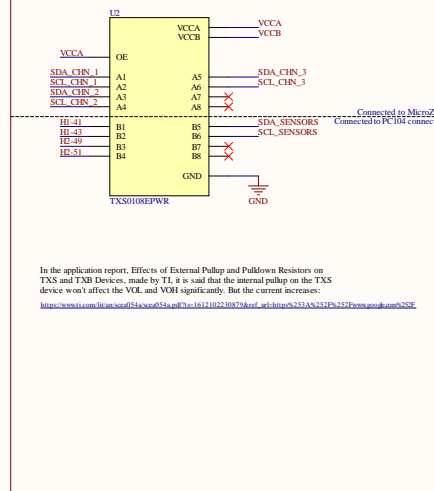
## RS485 module



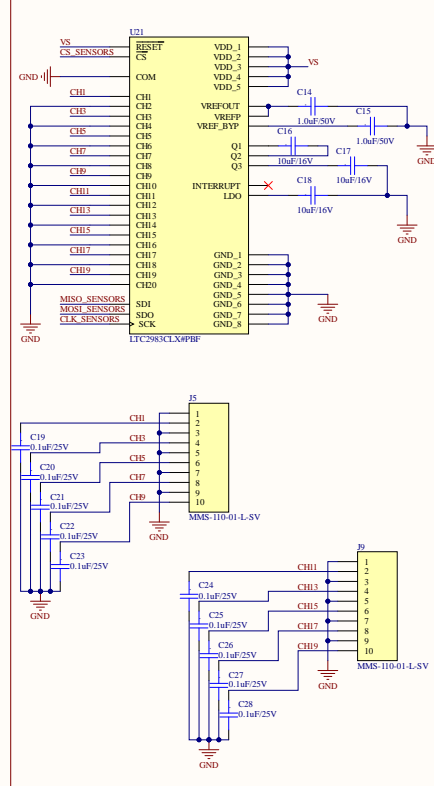
## Ambient temperature measurement system



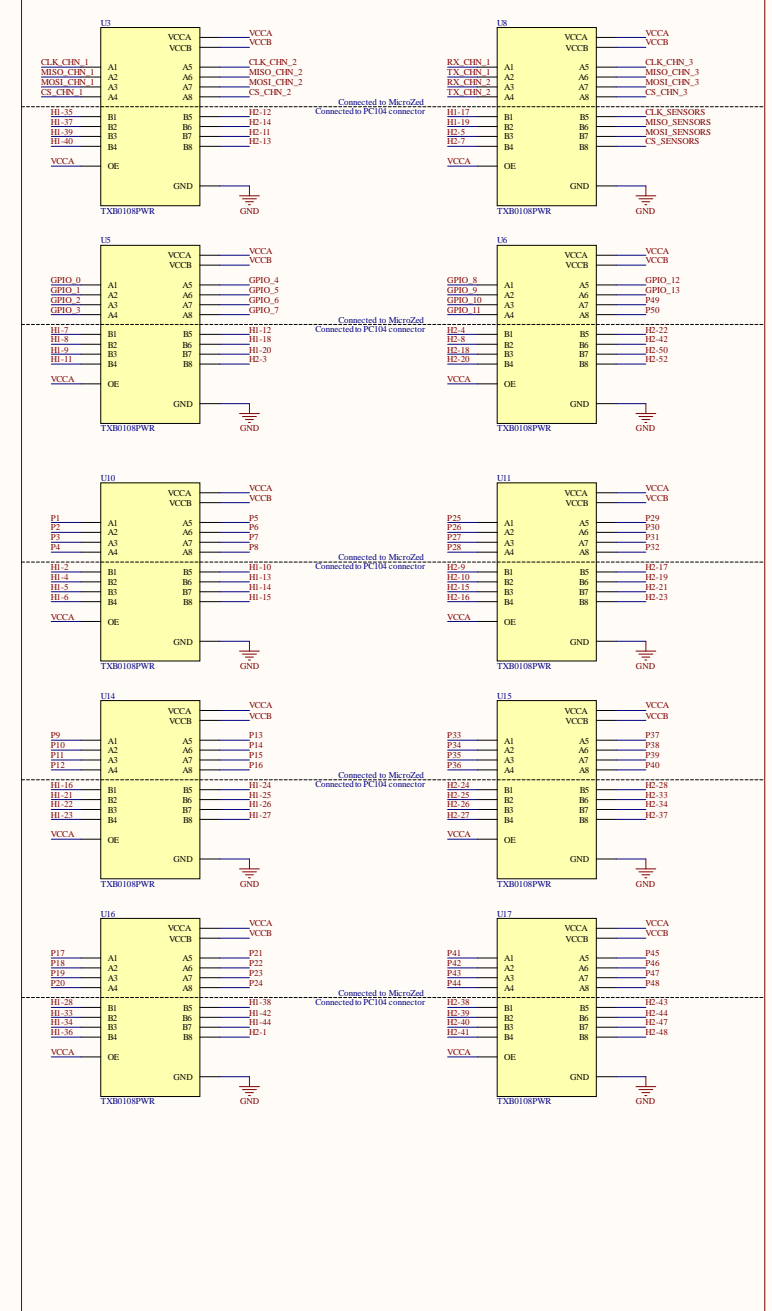
## Bidirectional voltage-level translator for PC



## Components temperature measurement system



## Generic bidirectional voltage-level translator



Title		
Size	Number	Revision
A2		
Date:	2020/2021	Sheet of
File:	C:\Users\volage-level translators, sda	10 of 10