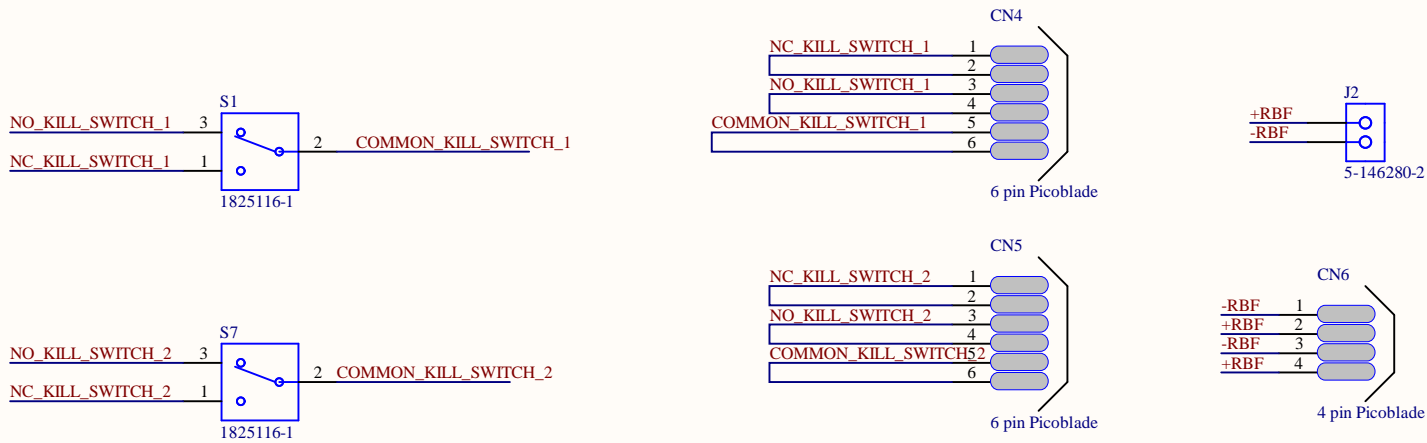
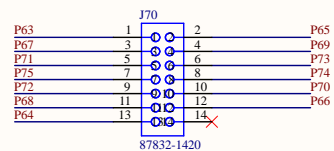
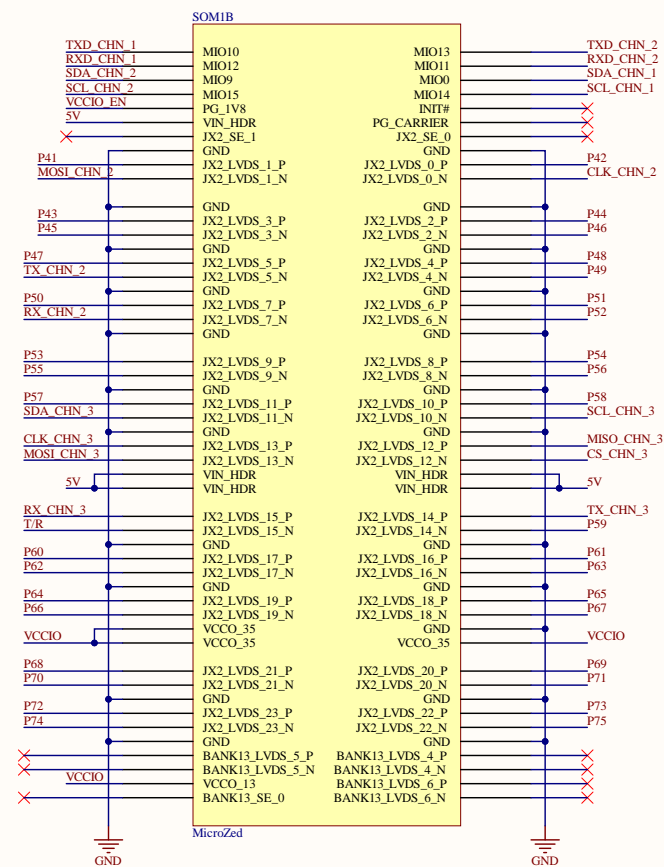
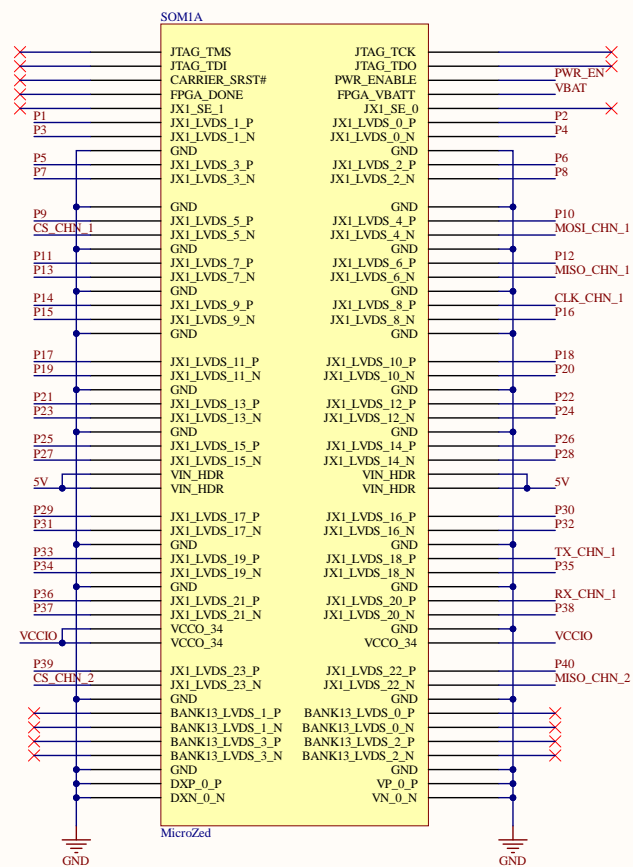


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| RBF | Switch(NC) | Switch(NO) | MOSFET | EPS POWER |
|-----|------------|------------|--------|-------------------------|
| 1 | X | X | OPEN | Cut-off |
| 0 | 1 | 0 | CLOSE | ON |
| 0 | 0 | 1 | OPEN | Cut-off (launch moment) |

| Title | | | |
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| Size | Number | | Revision |
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A



C



A

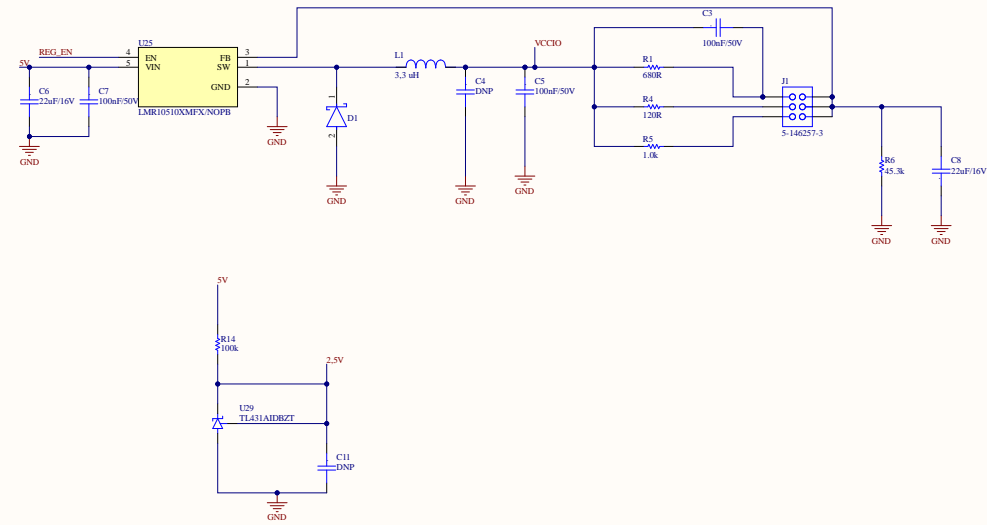


C

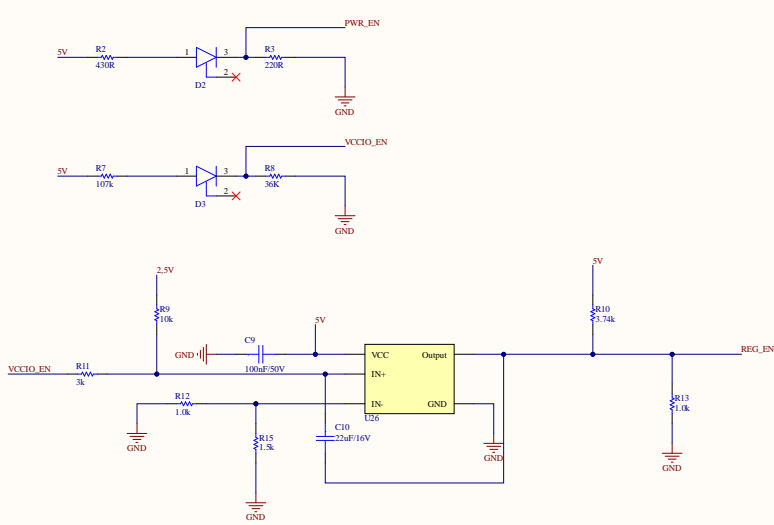


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| Size A2 | Number | Revision |
| Date: File: | 3/03/2021 C:\Users\j\PC104 schematic.SchDoc | Sheet of Drawn By: |

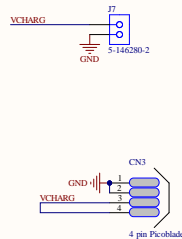
Reference signals



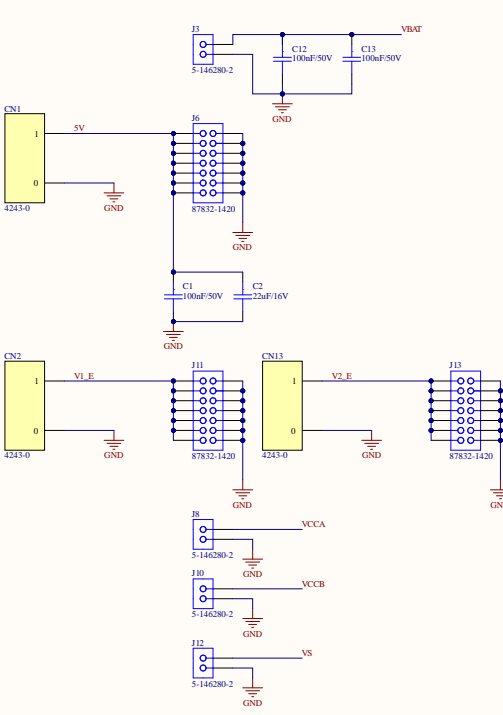
Enables



Batteries charge circuit



External voltage sources



| Title | | |
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Figure 10 shows the pin connections for the TMS320C6741. The diagram is divided into two sections: TX1 and TX2. Each section shows a pin header with pins 1 through 10. The TX1 header is connected to VCCA, VCCB, SDA_CRS_1, SCL_CRS_1, TX1_CRS_1, TX1_CRS_2, TX1_CRS_3, TX1_CRS_4, TX1_CRS_5, and TX1_CRS_6. The TX2 header is connected to VCCA, VCCB, SDA_CRS_2, SCL_CRS_2, TX2_CRS_1, TX2_CRS_2, TX2_CRS_3, TX2_CRS_4, TX2_CRS_5, and TX2_CRS_6. The TMS320C6741 chip has pins for VCCA, VCCB, OE, A[0-10], B[0-10], GND, and TX1_CRS[1-6].

Components temperature measurement system

Figure 10 shows eight pin connection diagrams for the Yocta board, labeled P1 through P8. Each diagram shows a pin header with pins 1-16 and a corresponding pin list. The diagrams are organized into two columns and four rows. Each diagram also shows connections for VCCA, VCCB, and GND. Some diagrams include notes about connections to the MicroBlaze and PC via connectors.

- P1:** Pin 1: A1, Pin 2: A2, Pin 3: A3, Pin 4: A4, Pin 5: A5, Pin 6: A6, Pin 7: A7, Pin 8: A8, Pin 9: A9, Pin 10: A10, Pin 11: A11, Pin 12: A12, Pin 13: A13, Pin 14: A14, Pin 15: A15, Pin 16: A16. VCCA is connected to pin 1, VCCB to pin 2, and GND to pin 3.
- P2:** Pin 1: A1, Pin 2: A2, Pin 3: A3, Pin 4: A4, Pin 5: A5, Pin 6: A6, Pin 7: A7, Pin 8: A8, Pin 9: A9, Pin 10: A10, Pin 11: A11, Pin 12: A12, Pin 13: A13, Pin 14: A14, Pin 15: A15, Pin 16: A16. VCCA is connected to pin 1, VCCB to pin 2, and GND to pin 3.
- P3:** Pin 1: A1, Pin 2: A2, Pin 3: A3, Pin 4: A4, Pin 5: A5, Pin 6: A6, Pin 7: A7, Pin 8: A8, Pin 9: A9, Pin 10: A10, Pin 11: A11, Pin 12: A12, Pin 13: A13, Pin 14: A14, Pin 15: A15, Pin 16: A16. VCCA is connected to pin 1, VCCB to pin 2, and GND to pin 3.
- P4:** Pin 1: A1, Pin 2: A2, Pin 3: A3, Pin 4: A4, Pin 5: A5, Pin 6: A6, Pin 7: A7, Pin 8: A8, Pin 9: A9, Pin 10: A10, Pin 11: A11, Pin 12: A12, Pin 13: A13, Pin 14: A14, Pin 15: A15, Pin 16: A16. VCCA is connected to pin 1, VCCB to pin 2, and GND to pin 3.
- P5:** Pin 1: A1, Pin 2: A2, Pin 3: A3, Pin 4: A4, Pin 5: A5, Pin 6: A6, Pin 7: A7, Pin 8: A8, Pin 9: A9, Pin 10: A10, Pin 11: A11, Pin 12: A12, Pin 13: A13, Pin 14: A14, Pin 15: A15, Pin 16: A16. VCCA is connected to pin 1, VCCB to pin 2, and GND to pin 3.
- P6:** Pin 1: A1, Pin 2: A2, Pin 3: A3, Pin 4: A4, Pin 5: A5, Pin 6: A6, Pin 7: A7, Pin 8: A8, Pin 9: A9, Pin 10: A10, Pin 11: A11, Pin 12: A12, Pin 13: A13, Pin 14: A14, Pin 15: A15, Pin 16: A16. VCCA is connected to pin 1, VCCB to pin 2, and GND to pin 3.
- P7:** Pin 1: A1, Pin 2: A2, Pin 3: A3, Pin 4: A4, Pin 5: A5, Pin 6: A6, Pin 7: A7, Pin 8: A8, Pin 9: A9, Pin 10: A10, Pin 11: A11, Pin 12: A12, Pin 13: A13, Pin 14: A14, Pin 15: A15, Pin 16: A16. VCCA is connected to pin 1, VCCB to pin 2, and GND to pin 3.
- P8:** Pin 1: A1, Pin 2: A2, Pin 3: A3, Pin 4: A4, Pin 5: A5, Pin 6: A6, Pin 7: A7, Pin 8: A8, Pin 9: A9, Pin 10: A10, Pin 11: A11, Pin 12: A12, Pin 13: A13, Pin 14: A14, Pin 15: A15, Pin 16: A16. VCCA is connected to pin 1, VCCB to pin 2, and GND to pin 3.

Pin configuration diagram for TMP112A0B02. The chip has pins for GND, SCL, SDA, and VS. The SCL pin is crossed out with a red X. The chip is labeled TMP112A0B02.

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