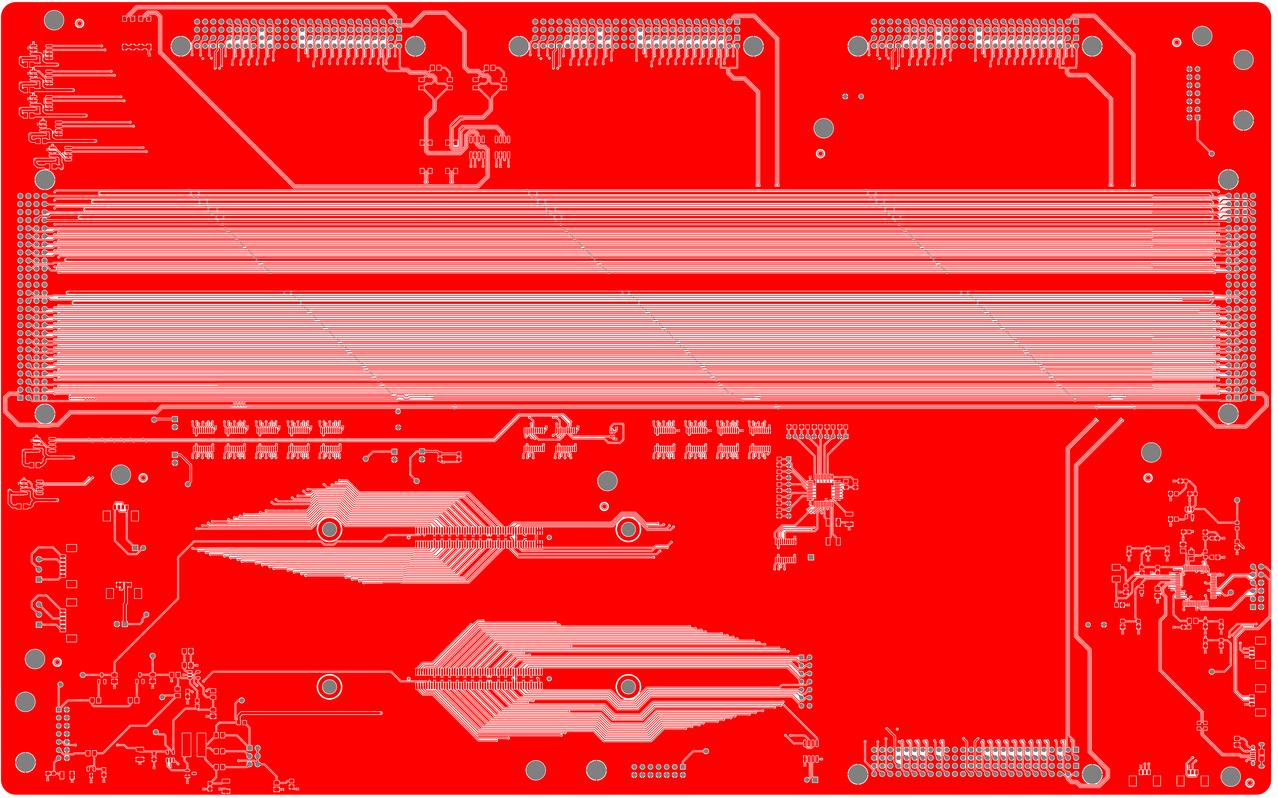
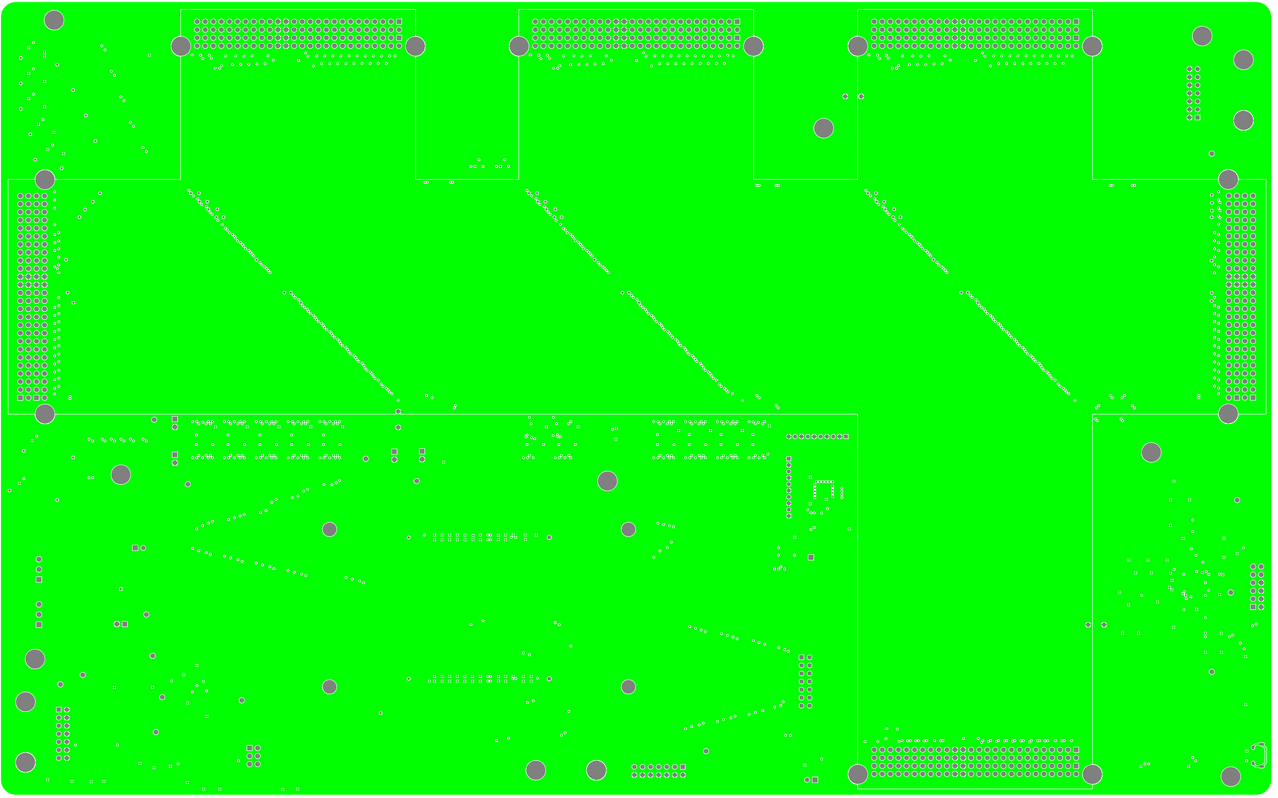
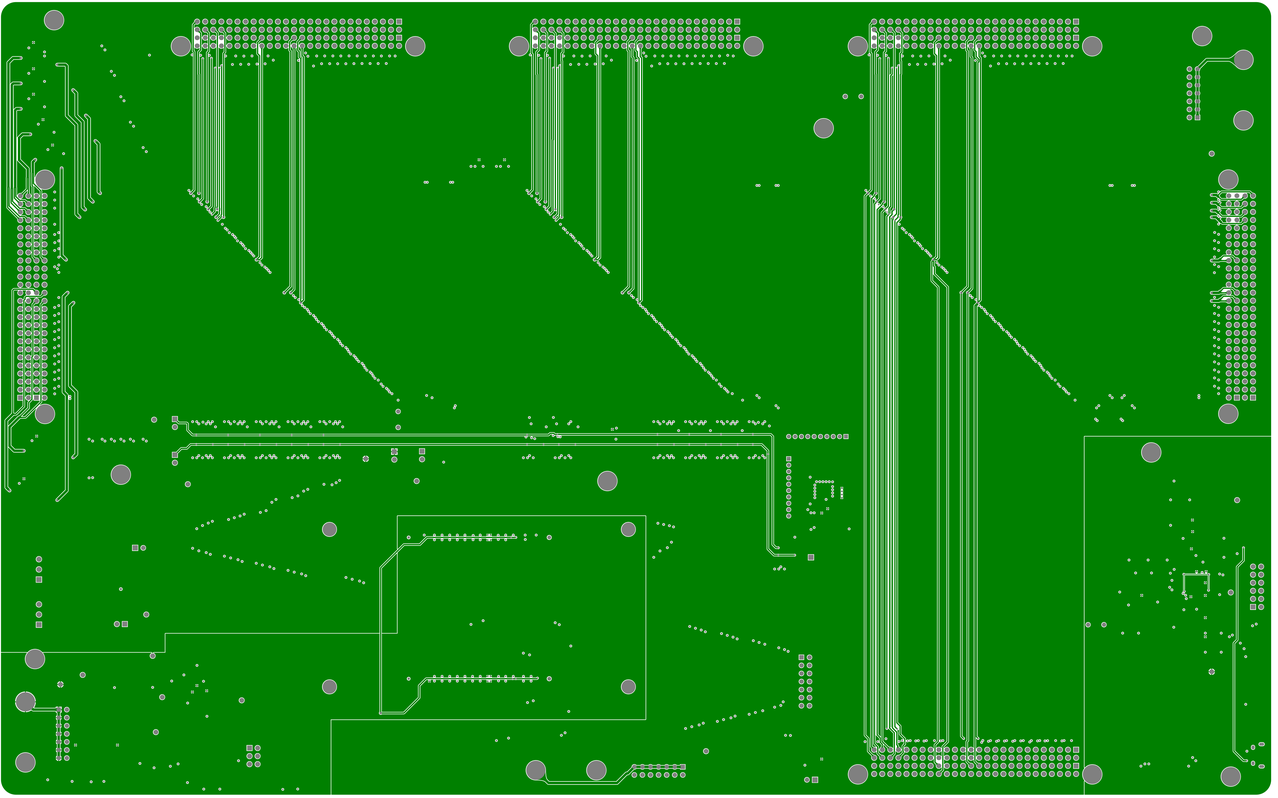
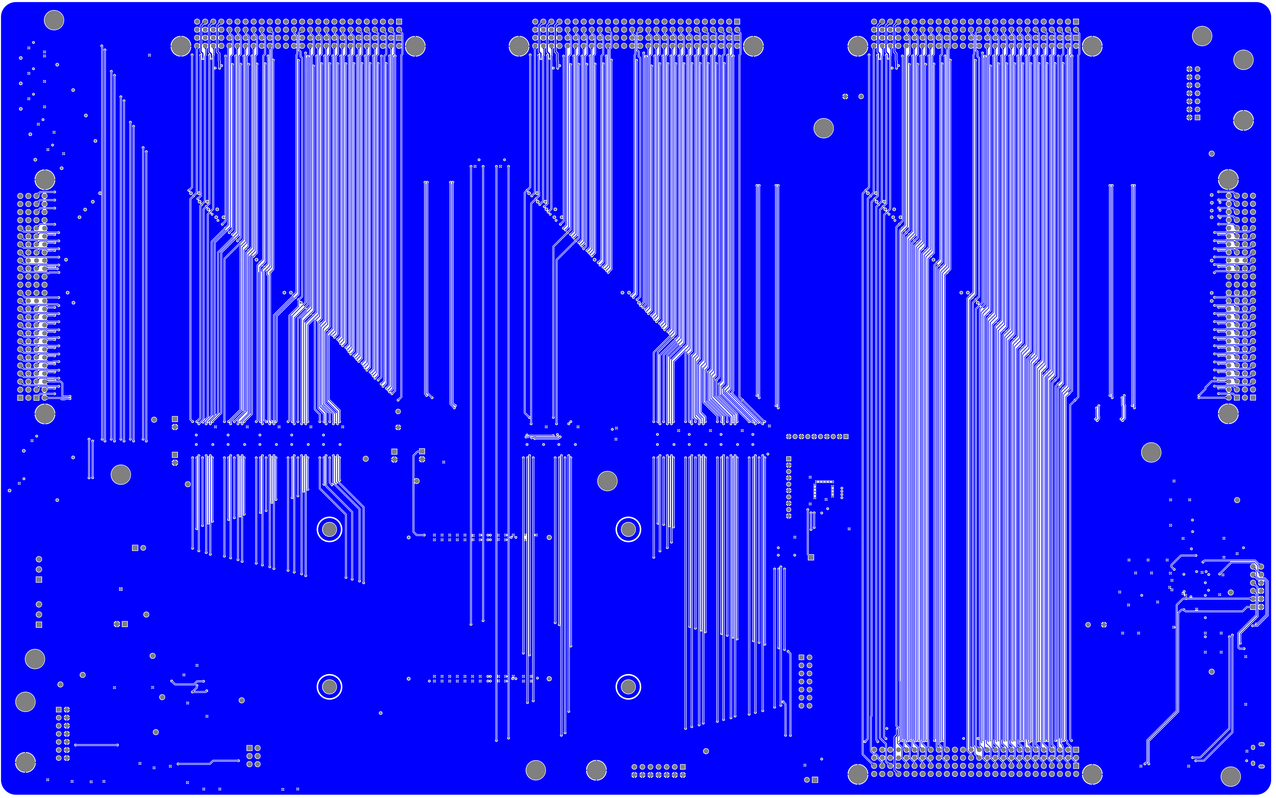








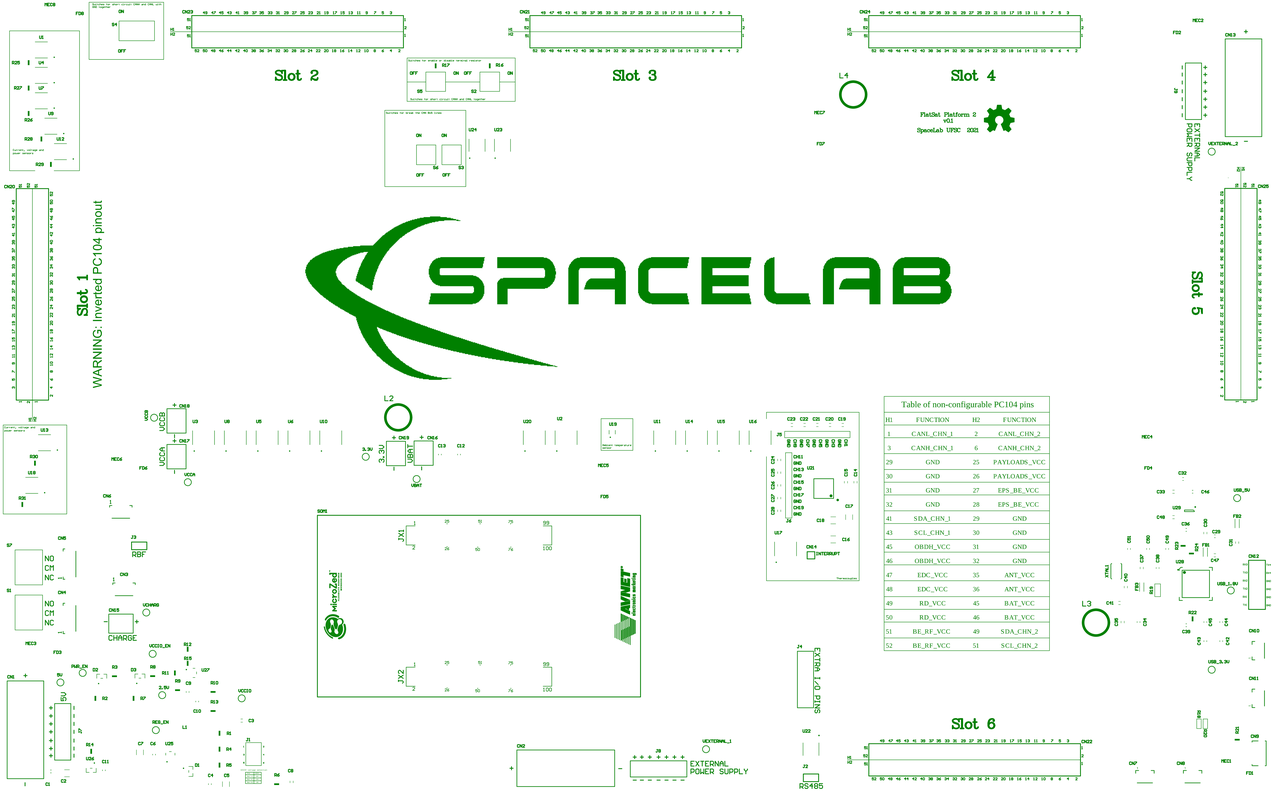
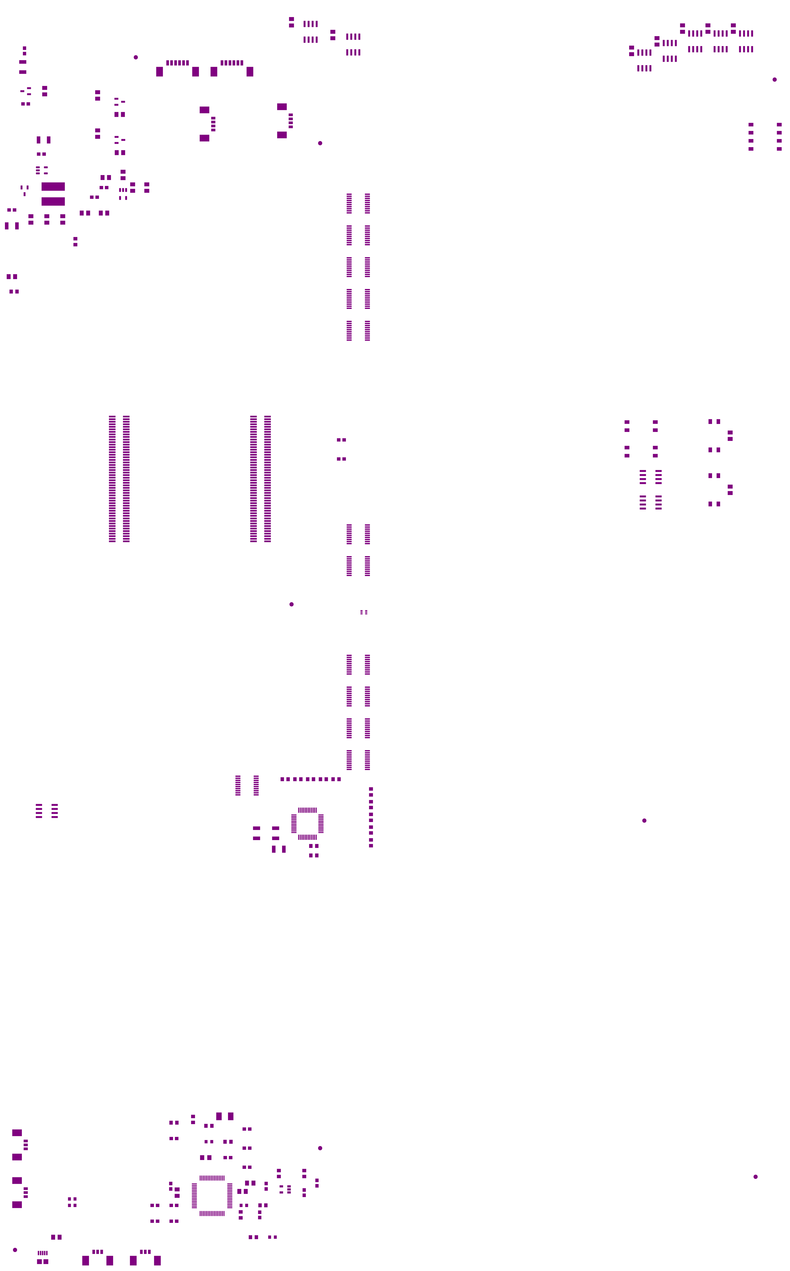
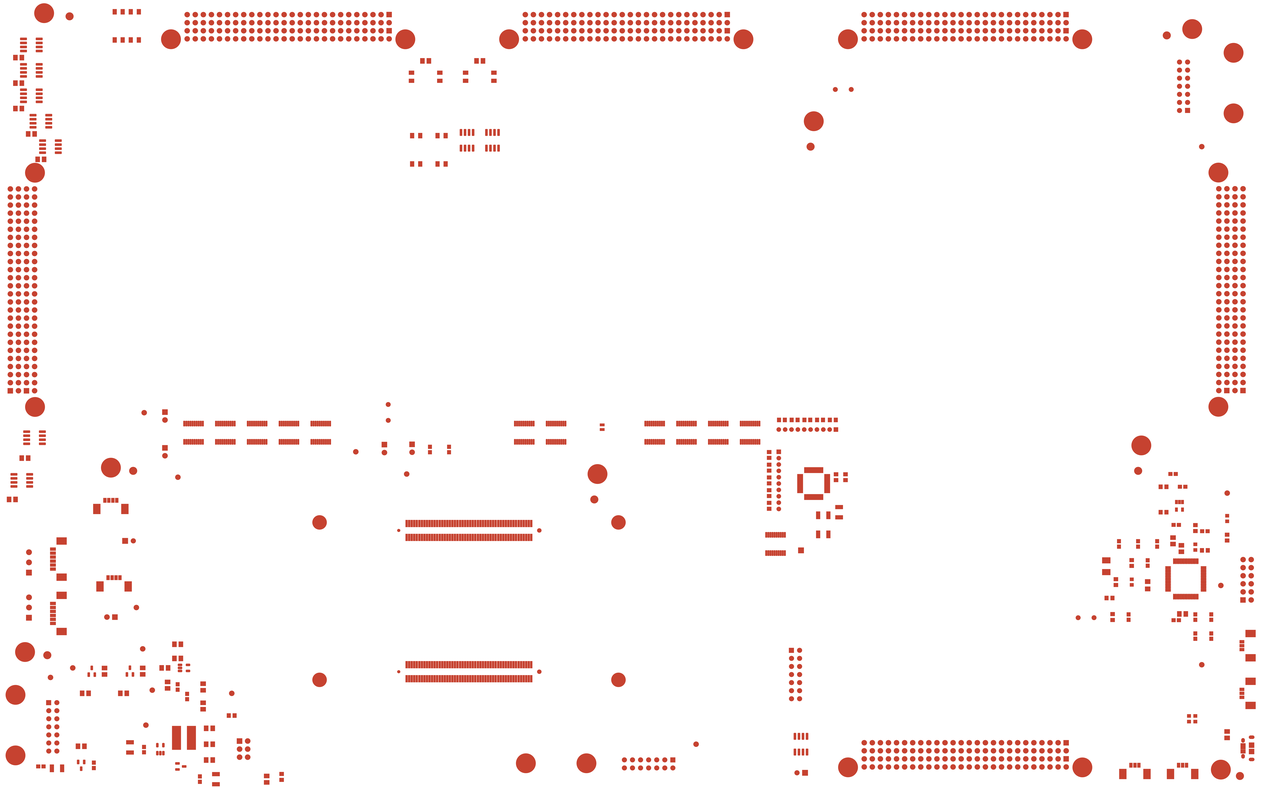
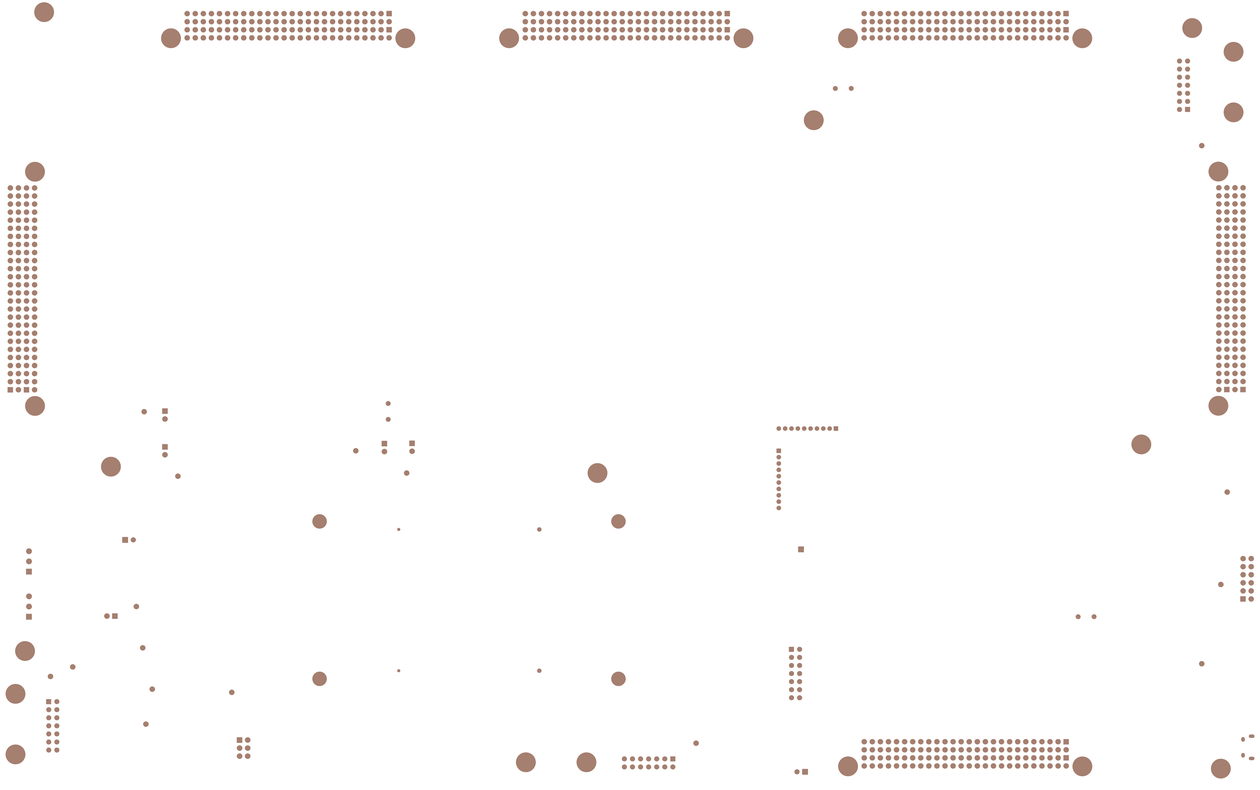


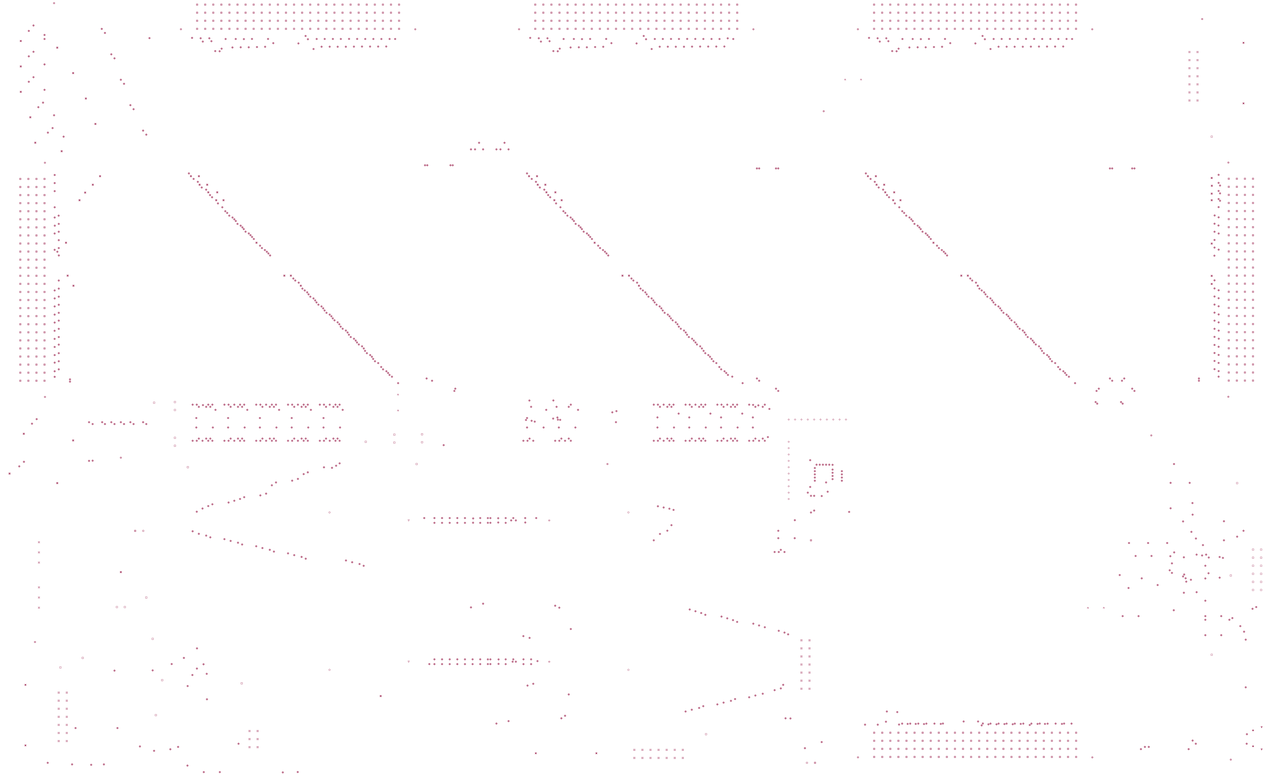
Table of non-configurable PC104 pins

H1	FUNCTION	H2	FUNCTION
1	CANL_CHN_1	2	CANL_CHN_2
3	CANH_CHN_1	6	CANH_CHN_2
29	GND	25	PAYLOADS_VCC
30	GND	26	PAYLOADS_VCC
31	GND	27	EPS_BE_VCC
32	GND	28	EPS_BE_VCC
41	SDA_CHN_1	29	GND
43	SCL_CHN_1	30	GND
45	OBDH_VCC	31	GND
46	OBDH_VCC	32	GND
47	EDC_VCC	35	ANT_VCC
48	EDC_VCC	36	ANT_VCC
49	RD_VCC	45	BAT_VCC
50	RD_VCC	46	BAT_VCC
51	BE_RF_VCC	49	SDA_CHN_2
52	BE_RF_VCC	51	SCL_CHN_2









Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template	Description	Hole Tolerance (+)	Hole Tolerance (-)	Hole Length	Routed Path Length
	2	19,69mil (0,500mm)	PTH	Slot	Top Layer - Bottom Layer	Pad	Rounded	r90_160h50_120r100m0mx0				47,24mil (1,200mm)	27,56mil (0,700mm)
	2	21,65mil (0,550mm)	PTH	Slot	Top Layer - Bottom Layer	Pad	Rounded	r125_95h55_85r100m0mx0				33,47mil (0,850mm)	11,81mil (0,300mm)
	2	32,01mil (0,813mm)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c76hn81m0mx0				-	-
	2	35,04mil (0,890mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c140h89m0mx0				-	-
	2	47,99mil (1,219mm)	NPTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c117hn122m0mx0				-	-
	4	125,00mil (3,175mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c432h318m0mx0				-	-
	6	32,28mil (0,820mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c132h82m0mx0				-	-
	6	42,13mil (1,070mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)				-	-
	6	177,17mil (4,500mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c600h450m0mx0				-	-
	20	27,99mil (0,711mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)				-	-
	20	125,98mil (3,200mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c600h320m0mx0				-	-
	38	39,37mil (1,000mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)				-	-
	62	40,16mil (1,020mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)				-	-
	66	17,72mil (0,450mm)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v76h45m0mx0				-	-
	627	35,43mil (0,900mm)	PTH	Round	Top Layer - Bottom Layer	Pad	(Mixed)	(Mixed)				-	-
	1073	11,81mil (0,300mm)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v51h30m0mx0				-	-
	1938 Total												

Slot definitions : **Routed Path Length** = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout