

TTC 2.0 Documentation

TTC 2.0 Documentation SpaceLab, Universidade Federal de Santa Catarina, Florianópolis - Brazil

TTC 2.0 Documentation *April*, 2021

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Revision Control:

Version	Author	Changes	Date
0.0	G. M. Marcelino	Document creation	2021/04/01



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Nomenclature

LEO Low Earth Orbit.

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CHAPTER 1

Introduction

. [1], [2] LEO

CHAPTER 2

Hardware

PC104 JTAG SPI/GPIO RF PC MSP430F6659 UART RF4463F30 Antenna I²C **♦** Voltage/Current $\mathbf{I}^2\mathbf{C}$ I^2C I²C Buffer Sensors JTAG SPI/GPIO RF UART MSP430F6659 RF4463F30 Antenna I²C **♦** Voltage/Current I^2C $\mathbf{I}^{2}\mathbf{C}$ I²C Buffer Sensors TTC Board

Figure 2.1: Block diagram of the TTC 2 hardware.

Bibliography

- [1] SpaceLab. Test, July 2020. Note.
- [2] Space Technology Research Laboratory (SpaceLab). *OBDH 2.0 Documentation*, 2020. Available at https://github.com/spacelab-ufsc/obdh2.