

TTC 2.0 Documentation

TTC 2.0 Documentation SpaceLab, Universidade Federal de Santa Catarina, Florianópolis - Brazil

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CHAPTER 1

Introduction

[1], [2] LEO

CHAPTER 2

Hardware

PC104 **QPIO** JTAG JTAG PC UART MSP430F6659 UART PC MSP430F6659 I²C ₩ I²C **♦** $\boldsymbol{\mathsf{I}^2\mathsf{C}}$ I^2C l²C SPI/GPIO SPI/GPIO I²C Buffer I²C Buffer Voltage/Current Voltage/Current Antenna Antenna Sensors Sensors RF RF RF4463F30 RF4463F30 TTC Board

Figure 2.1: Block diagram of the TTC 2 hardware.

Bibliography

- [1] SpaceLab. Test, July 2020. Note.
- [2] Space Technology Research Laboratory (SpaceLab). *OBDH 2.0 Documentation*, 2020. Available at https://github.com/spacelab-ufsc/obdh2.