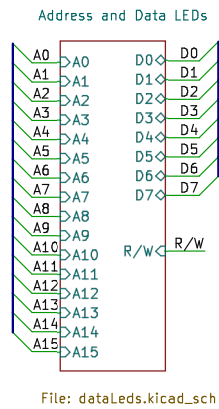


Power

Serial Interface

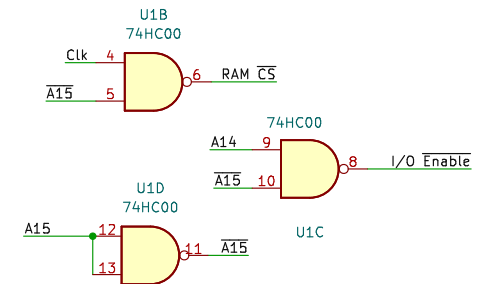
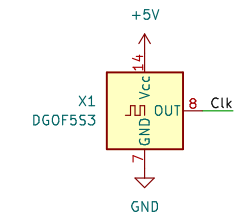
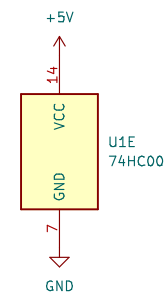
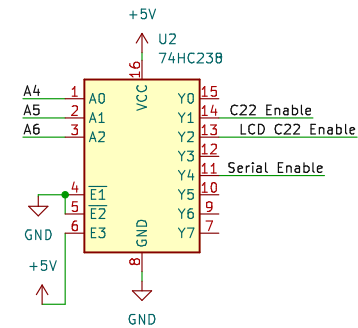
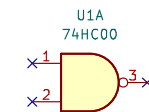
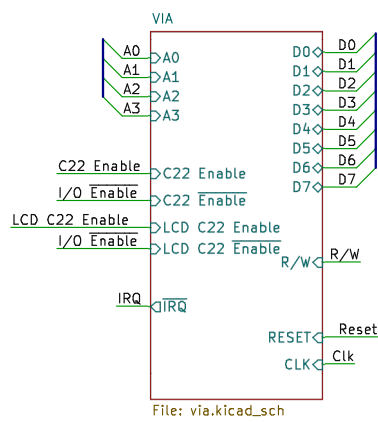
RAM and ROM

Main CPU and clock

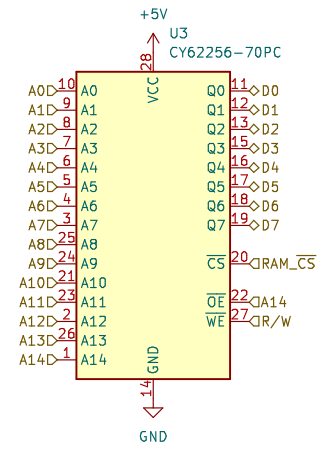


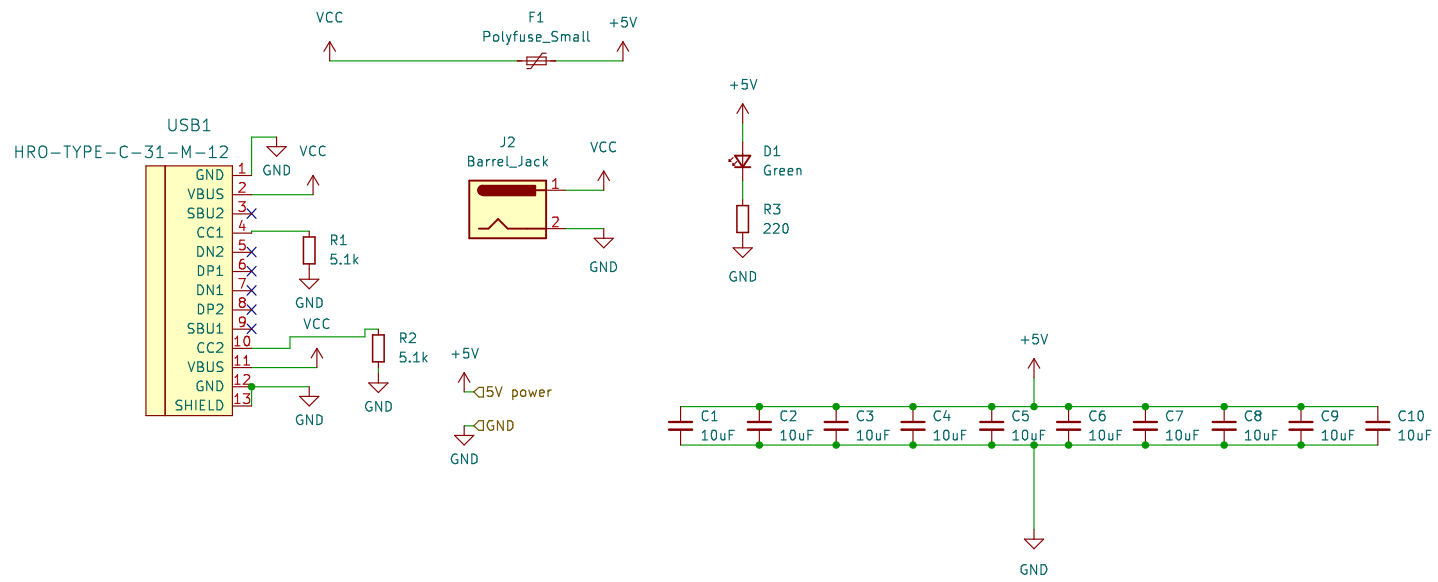
Debug LEDs

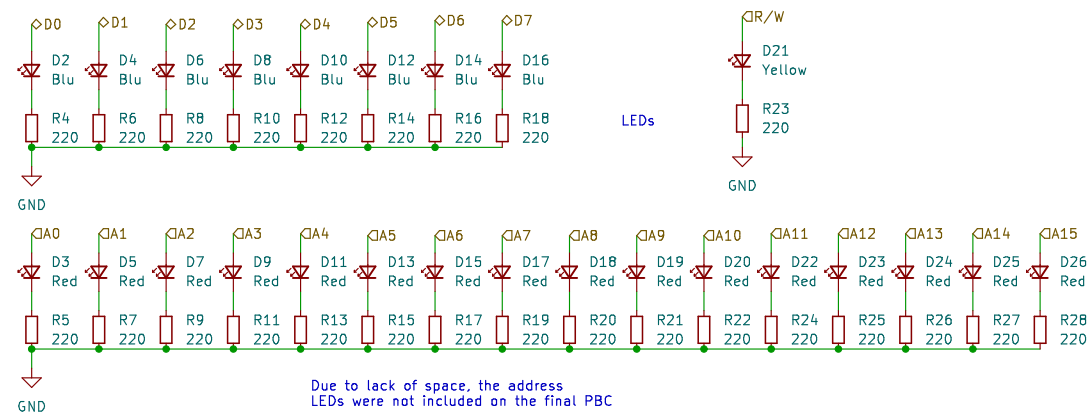
GPIO

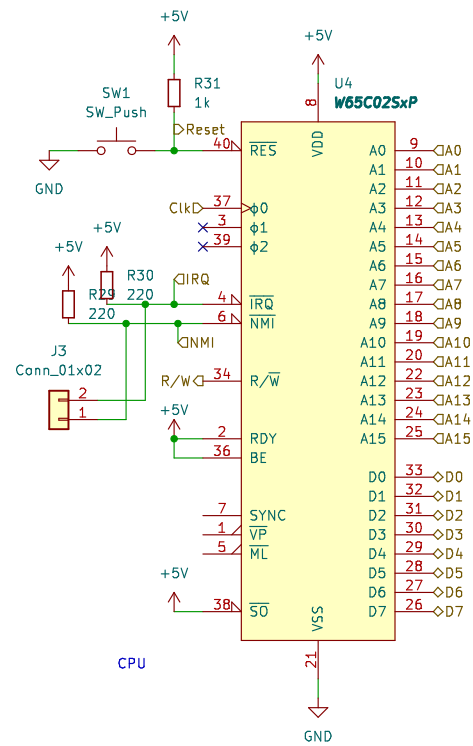


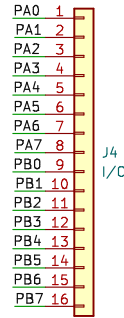
Address decode logic



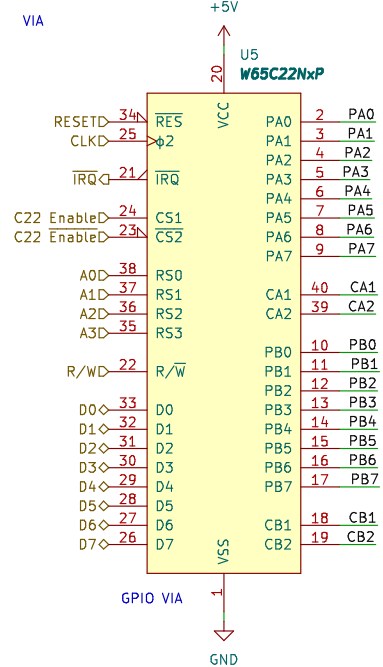






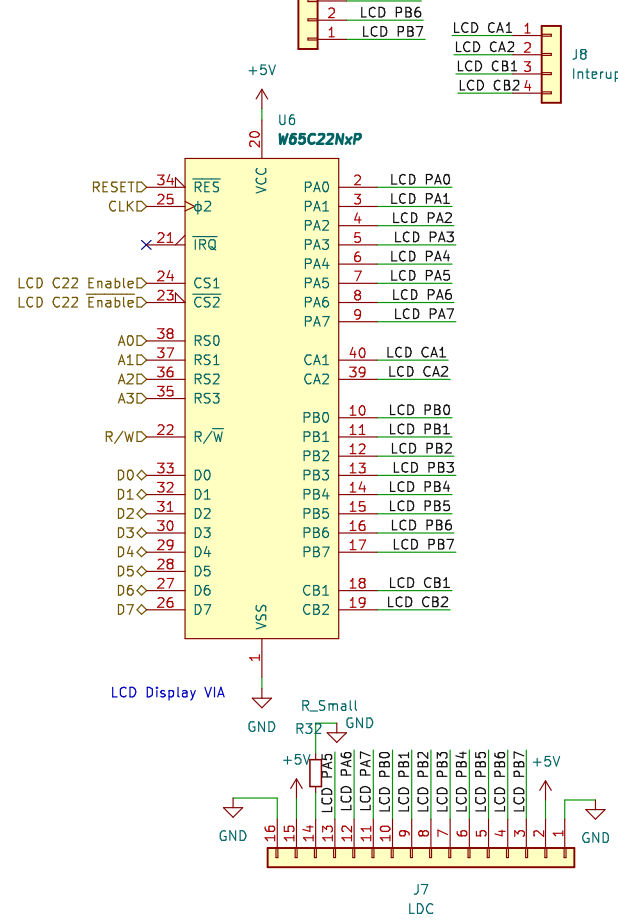


VIA I/O headers



GPIO VIA

GND



J7 LDC

