# CircuiTikZ

# version 0.2.2

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### 1 Introduction

After two years of little exposure only on my personal website<sup>1</sup>, I did a major rehauling of the code of CircuiTikZ, fixing several problems and converting everything to TikZ version 2.0.

I'm not too sure about the result, because my (La)TEX skills are much to be improved, but it seems it's time for more user feedback. So, here it is...

I know the documentation is somewhat scant. Hope to have time to improve it a bit.

#### 1.1 About

This package provides a set of macros for naturally typesetting electrical and (somewhat less naturally, perhaps) electronical networks.

It was born mainly for writing my own exercise book and exams sheets for the Elettrotecnica courses at Politecnico di Milano, Italy. I wanted a tool that was easy to use, with a lean syntax, native to LATEX, and supporting directly PDF output format.

So I based everything with the very impressive (if somewhat verbose at times) TikZ package.

### 1.2 Loading the package

\usepackage{circuitikz}

TikZ will be automatically loaded.

### 1.3 License

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#### 1.4 Feedback

Much appreciated: mredaelli@elet.polimi.it. Although I don't guarantee quick answers.

<sup>1</sup>http://home.dei.polimi.it/mredaelli.

### 1.5 Requirements

- tikz, version  $\geq 2$ ;
- xstring;
- siunitx, if using siunitx option.

### 1.6 Incompatible packages

None, as far as I know.

## 2 Options

- europeanvoltage: uses arrows to define voltages, and uses european-style voltage sources;
- americanvoltage: uses and + to define voltages, and uses americanstyle voltage sources;
- europeancurrent: uses european-style current sources;
- americancurrent: uses american-style current sources;
- europeanresistor: uses rectangular empty shape for resistors, as per european standards;
- americanresistor: uses zig-zag shape for resistors, as per american standards;
- europeaninductor: uses rectangular filled shape for inductors, as per european standards;
- americaninductor: uses coil shape for inductors, as per american standards:
- european: equivalent to europeancurrent, europeanvoltage, europeanresistor, europeaninductor;
- american: equivalent to americancurrent, americanvoltage, americanresistor, americaninductor;
- siunitx: integrates with SIunitx package. If labels, currents or voltages are of the form #1<#2> then what is shown is actually \SI{#1}{#2};
- nosiunitx: labels are not interpreted as above;
- fulldiode: the various diodes are drawn and filled by default, i.e. when using styles such as diode, D, sD, ... Un-filled diode can always be forced with Do, sDo, ...
- emptydiode: the various diodes are drawn *but not* filled by default, i.e. when using styles such as diode, D, sD, ... Filled diode can always be forced with D\*, sD\*, ...

- arrowmos: pmos and nmos have arrows analogous to those of pnp and npn transistors;
- noarrowmos: pmos and nmos do not have arrows analogous to those of pnp and npn transistors.

Loading the package with no options is equivalent to my own personal liking, that is to the following options:

[european current, european voltage, american resistor, american inductor, nosiunitx, noarrowmos].

# 3 The components

Here follows the list of all the shapes defined by CircuiTikZ. These are all pgf nodes, so they are usable in both pgf and TikZ.

Each bipole is shown using the following command, where #1 is the name of the component<sup>2</sup>:

```
\begin{center}\begin{circuitikz} \draw
  (0,0) to[ #1 ] (2,0)
; \end{circuitikz} \end{center}
```

The other shapes are shown with:

\begin{center}\begin{circuitikz} \draw
 (0,0) node[ #1 ] {}
; \end{circuitikz} \end{center}

### 3.1 Monopoles

• Ground (ground)



### 3.2 Bipoles

#### Instruments

• Ammeter (ammeter)



• Voltmeter (voltmeter)



 $<sup>^2</sup>$ If #1 is the name of the bipole/the style, then the actual name of the shape is #1shape.

### Basic resistive bipoles

• Short circuit (short)

\_\_\_\_

• Open circuit (open)

• Potentiometer (pR, or potentiometer)



• Lamp (lamp)



• Generic (symmetric) bipole (generic)



• Generic asymmetric bipole (ageneric)



• Generic asymmetric bipole (fullgeneric)



The resistor If europeanresistor option is active (or the style [european resistor] is used), the resistor is displayed as follows:

• Resistor (R, or resistor)



If instead (default behaviour) americanresistor option is active (or the style [american resistor] is used), the resistor is displayed as follows:

• Resistor (R, or resistor)



### Stationary sources

• Battery (battery)



• Voltage source (european style) (european voltage source)



• Voltage source (american style) (american voltage source)



• Current source (european style) (european current source)



• Current source (american style) (american current source)



The options europeancurrent [resp. europeanvoltage] (the default) and americancurrent [resp. americanvoltage] define which current [resp. voltage] source is selected by default when the abbreviated styles current source, csource, I [resp. voltage source, vsource, V] are used.

One can also use the related styles [european current] [resp. [european voltage]] and [american current] [resp. [american voltage]].

### Diodes and such

• Empty diode (empty diode, or Do)



• Empty Schottky diode (empty Schottky diode, or sDo)



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• Empty Zener diode (empty Zener diode, or zDo)



• Empty tunnel diode (empty tunnel diode, or tDo)



• Empty photodiode (empty photodiode, or pDo)



• Empty led (empty led, or leDo)



• Empty varcap (empty varcap, or VCo)



• Full diode (full diode, or D\*)



• Full Schottky diode (full Schottky diode, or sD\*)



• Full Zener diode (full Zener diode, or zD\*)



• Full tunnel diode (full tunnel diode, or tD\*)



• Full photodiode (full photodiode, or pD\*)



• Full led (full led, or leD\*)



• Full varcap (full varcap, or VC\*)



The options fulldiode and emptydiode (and the styles [full diode] and [full diode]) define which shape will be used by abbreviated commands such that D, sD, zD, tD, pD, leD, and VC.

### Basic dynamical bipoles

• Capacitor (capacitor, or C)



If europeaninductor option is active (or the style [european inductor] is used), the inductor is displayed as follows:

• Inductor (L, or inductor)



If instead (default behaviour) americaninductor option is active (or the style [american inductor] is used), the inductor is displayed as follows:

• Inductor (L, or inductor)



**Sinusoidal sources** Here because I was asked for them. But how do you distinguish one from the other?!

 $\bullet$  Sinusoidal voltage source (sinusoidal voltage source, or vsourcesin, sV)



• Sinusoidal current source (sinusoidal current source, or isourcesin, sI)



#### Switch

• Closing switch (closing switch, or cspst)



• Opening switch (opening switch, or ospst)



### 3.3 Tripoles

Controlled sources Admittedly, graphically they are bipoles. But I couldn't...

• Controlled voltage source (european style) (european controlled voltage source)



• Controlled voltage source (american style) (american controlled voltage source)



• Controlled current source (european style) (european controlled current source)



• Controlled current source (american style) (american controlled current source)



The options europeancurrent [resp. europeanvoltage] (the default) and americancurrent [resp. americanvoltage] define which controlled current [resp. voltage] source is selected by default when the abbreviated styles are used.

One can also use the related styles [european current] [resp. [european voltage]] and [american current] [resp. [american voltage]].

• Controlled sinusoidal voltage source (controlled sinusoidal voltage source, or controlled vsourcesin, cvsourcesin, csV)



• Controlled sinusoidal current source (controlled sinusoidal current source, or controlled isourcesin, cisourcesin, csI)



### Transistors

• nmos (nmos)



 $\bullet$  pmos (pmos)



• npn (npn)



• pnp (pnp)



If the option arrowmos is used (or after the commant  $\text{ctikzset}\{tripoles/mos style/arrows\}$  is given), this is the output:

• nmos (nmos)



• pmos (pmos)



# 3.4 Double bipoles

• Transformer (transformer)



• Gyrator (gyrator)



# 3.5 Logic gates

• AND port (and port)



• OR port (or port)



• NOT port (not port)



• NAND port (nand port)



• NOR port (nor port)



• XOR port (xor port)

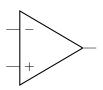


• XNOR port (xnor port)



# 3.6 Operational Amplifier

• Operational amplifier (op amp)



# 3.7 Support shapes

• Arrows (current and voltage) (currarrow)

•

• Connected terminal (circ)

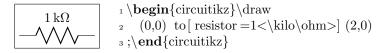
•

• Unconnected terminal (ocirc)

0

## 4 Usage

Long names/styles for the bipoles can be used:



#### 4.1 Labels



### 4.2 Currents

$$\begin{array}{c|c}
 & \text{$^{i_1}$} & \text{$^{i_1}$} \\
 & \text{$^{i_1}$} & \text{$^{i_1}$} \\
 & \text{$^{i_1}$} & \text{$^{i_1}$} \\
 & \text{$^{i_1}$} & \text{$^{i_1}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} \\
 & \text{$^{i_1}$} & \text{$^{i_2}$} & \text{$^$$

```
1 \begin{circuitikz}
                        \text{draw } (0,0) \text{ to } [R, i^<=\$i_1\$] (2,0);
                   3 \end{circuitikz}
                   1 \begin{circuitikz}
                      \text{draw } (0,0) \text{ to } [R, i_{=} = i_{1}] (2,0);
                   3 \end{circuitikz}
                   1 \begin{circuitikz}
                      \text{draw } (0,0) \text{ to } [R, i>^=\$i_1\$] (2,0);
                   3 \end{circuitikz}
                   1 \begin{circuitikz}
                        \text{draw } (0,0) \text{ to } [R, i> =\$i_1\$] (2,0);
                   3 \end{circuitikz}
                   1 \begin{circuitikz}
                        \text{draw } (0,0) \text{ to } [R, i<^=\$i_1\$] (2,0);
                   3 \end{circuitikz}
                   _{1} \setminus \mathbf{begin}\{\mathbf{circuitikz}\}
                        \text{draw } (0,0) \text{ to } [R, i < =\$i_1\$] (2,0);
                   3 \end{circuitikz}
Also
                   _{1} \setminus \mathbf{begin}\{\mathbf{circuitikz}\}
                   draw (0,0) to [R, i <= i_1] (2,0);
                   3 \end{circuitikz}
                   1 \begin{circuitikz}
                        \text{draw } (0,0) \text{ to } [R, i>=\$i_1\$] (2,0);
                   3 \end{circuitikz}
                   1 \begin{circuitikz}
                        \text{draw } (0,0) \text{ to } [R, i^=\$i_1\$] (2,0);
                   3 \end{circuitikz}
                   1 \begin{circuitikz}
                        \det (0,0) \ \text{to}[R, i_{=}$i_{1}$] (2,0);
                   3 \end{circuitikz}
```

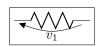
### 4.3 Voltages

European style The default, with arrows. Use option europeanvoltage or style [european voltage].

$$\begin{array}{c|c} v_1 & \text{$^1 \over $} & \text{$^1 \over $} & \text{$^1 \over $} & \text{$^2 \over $} & \text{$^2 \over $} & \text{$^2 \over $} & \text{$^3 \over $} & \text{$$$

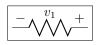
```
\boxed{ \color{red} \bullet \color{black} v_1}
```

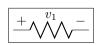
$$\boxed{ \bigvee_{v_1}}$$



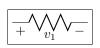
1 \begin{circuitikz}[european voltage]
2 \draw (0,0) to [R, v\_<=\$v\_1\$] (2,0);
3 \end{circuitikz}

American style For those who like it (not me). Use option americanvoltage or set [american voltage].









1 \begin{circuitikz}[american voltage]
2 \draw (0,0) to [R, v\_<=\$v\_1\$] (2,0);
3 \end{circuitikz}

### 4.4 Nodes

$$\boxed{\bigcirc - \bigvee \bigvee - \bigcirc}$$









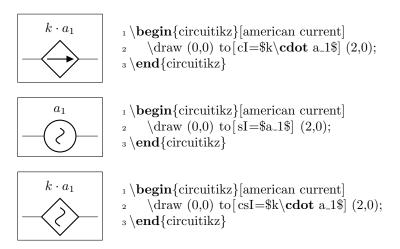
$$\begin{array}{ll} {}_{1} \backslash \mathbf{begin} \{ \mathrm{circuitikz} \} \\ {}_{2} \quad \backslash \mathrm{draw} \ (0,0) \ \mathrm{to} [\mathrm{R}, \ -*] \ (2,0); \\ {}_{3} \backslash \mathbf{end} \{ \mathrm{circuitikz} \} \end{array}$$

### 4.5 Special components

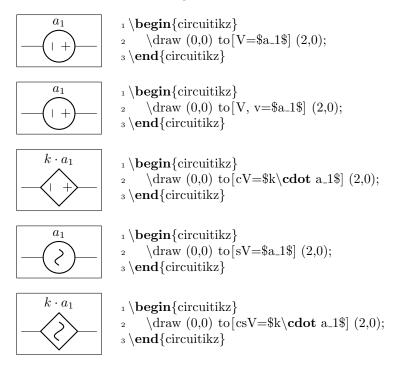
For some components label, current and voltage behave as one would expect:

```
a_1
                  1 \begin{circuitikz}
                        \text{draw } (0,0) \text{ to } [I=\$a_1\$] (2,0);
                  3 \end{circuitikz}
                  _{1} \setminus \mathbf{begin}\{\mathbf{circuitikz}\}
 a_1
                         \det (0,0) \ to[I, i=\$a_1\$] (2,0);
                  3 \end{circuitikz}
k \cdot a_1
                  1 \begin{circuitikz}
                      \text{draw } (0,0) \text{ to } [cI=\$k \setminus \textbf{cdot } a_1\$] (2,0);
                  3 \end{circuitikz}
  a_1
                  1 \begin{circuitikz}
                        \text{draw } (0,0) \text{ to } [sI=\$a_1\$] (2,0);
                  3 \end{circuitikz}
k \cdot a_1
                  1 \begin{circuitikz}
                         \text{draw } (0,0) \text{ to } [\text{csI}=\$k \setminus \text{cdot a.1}\$] (2,0);
                  3 \end{circuitikz}
```

The following results from using the option  ${\tt americancurrent}$  or using the style [american current].



The same holds for voltage sources:



The following results from using the option  ${\tt americanvoltage}$  or the style  ${\tt [american\ voltage]}.$ 

```
 \begin{array}{c|c} a_1 & \text{$^1$ begin{circuitikz}[american voltage]}\\ & \text{$^2$ draw (0,0) to[V=\$a\_1\$] (2,0);}\\ & \text{$^3$ end{circuitikz}$} \\ \hline \\ & a_1 & \text{$^1$ begin{circuitikz}[american voltage]}\\ & \text{$^2$ draw (0,0) to[V, v=\$a\_1\$] (2,0);}\\ & \text{$^3$ end{circuitikz}$} \\ \hline \end{array}
```

### 4.6 Integration with siunitx

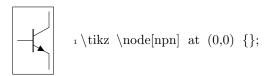
If the option siunitx is active, then the following are equivalent:

```
1 \begin{circuitikz}
1\,\mathrm{k}\Omega
                      \operatorname{draw}(0,0) \text{ to}[R, l=1<\text{kilo}\circ m>](2,0);
                3 \end{circuitikz}
                1 \begin{circuitikz}
1 \, \mathrm{k}\Omega
                      \operatorname{draw}(0,0) \text{ to}[R, l=\$SI\{1\}{\kappa(0,0)};
                3 \end{circuitikz}
                   1 \begin{circuitikz}
     √1 mA
                        \operatorname{draw}(0,0) \text{ to}[R, i=1<\min[] (2,0);
                   3 \end{circuitikz}
                   1 \begin{circuitikz}
     1 \,\mathrm{mA}
                        \operatorname{draw}(0,0) \text{ to}[R, i=\$SI\{1\}{\text{milli}}] (2,0);
                   3 \end{circuitikz}
                1 \begin{circuitikz}
                      \text{draw } (0,0) \text{ to } [R, v=1<\text{volt}] (2,0);
                3 \end{circuitikz}
                1 \begin{circuitikz}
                      \text{draw } (0,0) \text{ to } [R, v=\$SI\{1\}\{\text{volt}\}\] (2,0);
                3 \end{circuitikz}
```

### 4.7 Putting them together

## 5 Not only bipoles

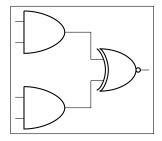
Since only bipoles can be placed "along a line", components with more than two terminals are placed as nodes:



### 5.1 Anchors

In order to allow connections with other components, all components define anchors.

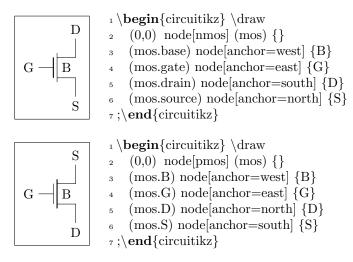
**Logical ports** All logical ports, except NOT, have to inputs and one output. They are called respectively in 1, in 2, out:



```
1 \begin{circuitikz} \draw
2      (0,2) node[and port] (myand1) {}
3      (0,0) node[and port] (myand2) {}
4      (2,1) node[xnor port] (myxnor) {}
5      (myand1.out) -| (myxnor.in 1)
6      (myand2.out) -| (myxnor.in 2)
7 ;\end{circuitikz}
```

In the case of NOT, there are only in and out:

**Transistors** For MOS transistors one has base, gate, source and drain anchors (which can be abbreviated with B, G, S and D):



For BJT transistors the anchors are base, emitter and collector anchors (which can be abbreviated with B, E and C):

```
C
| 1 \begin{circuitikz} \draw |
| 2 (0,0) node[npn] (npn) {}
| 3 (npn.base) node[anchor=east] {B}
| 4 (npn.collector) node[anchor=south] {C}
| 5 (npn.emitter) node[anchor=north] {E}
| 6 ;\end{circuitikz}

| 1 \begin{circuitikz} \draw |
| 2 (0,0) node[pnp] (pnp) {}
| 3 (pnp.B) node[anchor=east] {B}
| 4 (pnp.C) node[anchor=north] {C}
| 5 (pnp.E) node[anchor=south] {E}
| 6 ;\end{circuitikz}
```

Here is one composite example (please notice that the xscale=-1 style would also reflect the label of the transistors, so here a new node is added and its text is used, instead of that of pnp1):

**Operational amplifier** The op amp defines the inverting input (-), the non-inverting input (+) and the output (out) anchors:

```
v_{-} - v_{o}
v_{+} - v_{o
```

There are also two more anchors defined, up and down, for the power supplies:

```
v_{-} = v_{-} - v_{0}
v_{+} = v_{0} - v_{0}
v_{+
```

**Double bipoles** All the (few, actually) double bipoles/quadrupoles have the four anchors, two for each port. The first port, to the left, is port A, having the anchors A1 (up) and A2 (down); same for port B. They also expose the base anchor, for labelling:

```
1 \begin{circuitikz} \draw
                            (0,0) node[transformer] (T) \{\}
                 B1
A1
         K
                            (T.A1) node[anchor=east] {A1}
                            (T.A2) node[anchor=east] \{A2\}
                            (T.B1) node[anchor=west] \{B1\}
                            (T.B2) node[anchor=west] \{B2\}
                            (T.base) node\{K\}
                 B2
A2
                         s ;\end{circuitikz}
                         1 \begin{circuitikz} \draw
                            (0,0) node[gyrator] (G) {}
A1
         Κ
                 · B1
                            (G.A1) node[anchor=east] \{A1\}
                            (G.A2) node[anchor=east] \{A2\}
                            (G.B1) node[anchor=west] {B1}
                            (G.B2) node[anchor=west] {B2}
                            (G.base) node{K}
                 B2
A2
                         8 ;\end{circuitikz}
```

### 6 Customization

### 6.1 Parameters

Pretty much all Circui $\mathrm{Ti}k\mathrm{Z}$  relies heavily on pgfkeys for value handling and configuration. Indeed, at the beginning of circuitikz.sty a series of key definitions can be found that modify all the graphical characteristics of the package.

All can be varied using the \ctikzset command, anywhere in the code:

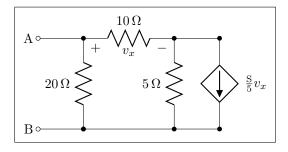
```
1\,\Omega
                _1 \times (0,0)  to [R=1<\ohn>] (2,0); \mathbf{par}
                2 \ ctikzset { bipoles / resistor / height=.6}
1\Omega
                _{3} \text{ tikz } \text{ draw } (0,0) \text{ to } [R=1<\text{ohm}] (2,0);
1\,\mathrm{F}
                1 \tikz \draw (0,0) to [C=1<\farad>] (2,0); \par
                2 \ ctikzset { bipoles/thickness=1}
1\,\mathrm{F}
                _3 \times (0,0)  to C=1<\operatorname{arad}(2,0);
                  _1 \text{ tikz } \text{ draw } (0,0) \text{ to } [R, v=1<\text{volt}] (2,0); \text{ } \text{par}
                  2 \ ctikzset {voltage/distance from node=.1}
                  \frac{1}{3} \text{ tikz } \text{ draw } (0,0) \text{ to } [R, v=1<\text{volt}] (2,0);
            _1 \times (0,0) \text{ node[nand port] } 
            <sup>2</sup>\ctikzset { tripoles /nand port/input height=.2}
            3 \ ctikzset { tripoles /nand port/port width=.2}
            _4 \text{ tikz } \text{ draw } (0,0) \text{ node}[\text{nand port}] \{\};
                1 \tikz \draw (0,0) to [C, i=$\imath$] (2,0); \par
                _2 \setminus \text{ctikzset} \{ \text{current/distance} = .2 \}
                _3 \text{ tikz } \text{ draw } (0,0) \text{ to } [C, i=\$\mathbf{math}] (2,0);
```

Admittedly, not all graphical properties have understandable names, but for the time it will have to do:

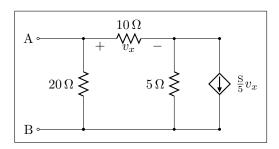


### 6.2 Components size

Perhaps the most important parameter is \circuitikzbasekey/bipoles/length, which can be interpreted as the length of a resistor (including reasonable connections): all other lengths are relative to this value. For instance:



```
 \begin{circuitikz} [scale=1.4cm] \\ 2 \begin{circuitikz} [scale=1.2] \draw \\ 3 & (0,0) & node[anchor=east] \{B\} \\ 4 & to[short, o-*] & (1,0) \\ 5 & to[R=20<\ohn>, *-*] & (1,2) \\ 6 & to[R=10<\ohn>, v=\$v_x\$] & (3,2) & -- & (4,2) \\ 7 & to[cI=\$\{rac\{\simeq\}\}\{5\} & v_x\$, *-*] & (4,0) & -- & (3,0) \\ 8 & to[R=5<\ohn>, *-*] & (3,2) \\ 9 & (3,0) & -- & (1,0) \\ 10 & (1,2) & to[short, -o] & (0,2) & node[anchor=east]\{A\} \\ 11 & ; \end\{circuitikz\} \\ \end{circuitikz}
```

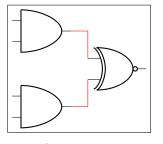


```
 \begin{circuitikz} [scale=1.2] \draw \\ (0,0) \ node[anchor=east] \{B\} \\ (0,0) \ node[anchor=east] \{A\} \\ (0,0) \ node[anchor=
```

### 6.3 Colors

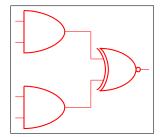
The color of the components is stores in the key  $\circuitikzbasekey/color$ . CircuiTikZ tries to follow the color set in TikZ, although sometimes it fails. If you change color in the picture, please do not use just the color name as a style, like [red], but rather assign the style [color=red].

Compare for instance



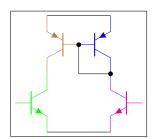
```
 \begin{circuitikz} \draw[red] \\ (0,2) \ node[and port] \ (myand1) \ \{\} \\ (0,0) \ node[and port] \ (myand2) \ \{\} \\ (2,1) \ node[xnor port] \ (myxnor) \ \{\} \\ (myand1.out) -| \ (myxnor.in 1) \\ (myand2.out) -| \ (myxnor.in 2) \\ (theorem example of the continuous of
```

and



```
1 \begin{circuitikz} \draw[color=red]
2 (0,2) node[and port] (myand1) {}
3 (0,0) node[and port] (myand2) {}
4 (2,1) node[xnor port] (myxnor) {}
5 (myand1.out) -| (myxnor.in 1)
6 (myand2.out) -| (myxnor.in 2)
7 ;\end{circuitikz}
```

One can of course change the color in medias res:



The all-in-one stream of bipoles poses some challanges, as only the actual body of the bipole, and not the connecting lines, will be rendered in the specified color. Also, please notice the curly braces around the to:

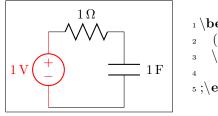
```
 \begin{array}{|c|c|c|c|c|}\hline & & & & & & & \\ & & & & & \\ & & & & \\ & & & & \\ & & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & & \\ & \\ & & \\ & \\ & & \\ & \\ & & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\ & \\
```

Which, for some bipoles, can be frustrating:

```
\begin{array}{c|c}
1\Omega & & & 1 \\
\hline
1V & + & & \\
\hline
1V & + & \\
\hline
1V & + & \\
\hline
1V & & \\
2 & (0,0) & \text{to} \\
3 & & \text{to} \\
4 & & \text{to} \\
5 & \text{;} & \text{end} & \text{circ}
\end{array}
```

```
 \begin{circuitikz} \draw \\ 2 & (0,0) \{to[V=1<\volt>, color=red] (0,2) \} \\ 3 & to[R=1<\volt>, color=red] (0,2) \} \\ 4 & to[C=1<\farad>] (2,0) -- (0,0) \\ 5 & ; \end{circuitikz}
```

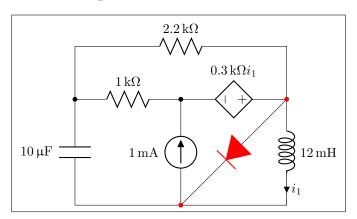
The only way out is to specify different paths:



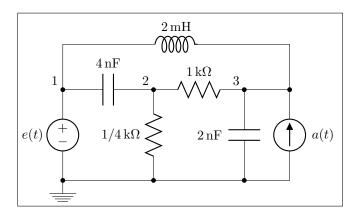
```
 \begin{array}{lll} \label{eq:continuity} $$ \operatorname{lone}(circuitikz) \leq (0,0) $$ to[V=1<\volt>, color=red] (0,2); $$ draw (0,2) to[R=1<\ohn>] (2,2) $$ to[C=1<\farad>] (2,0) -- (0,0) $$ $$ $$ to circuitikz$
```

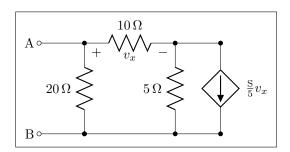
And yes: this is a bug and *not* a feature...

## 7 Examples

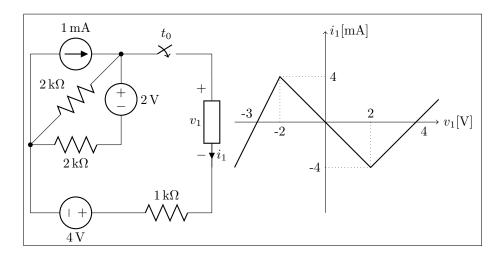


```
 \begin{array}{lll} \begin\{circuitikz\}[scale=1.4] \ draw \\ 2 & (0,0) \ to[C, l=10<\mbox{micro}\farad>] \ (0,2) \ --- \ (0,3) \\ 3 & to[R, l=2.2<\kilo\ohm>] \ (4,3) \ --- \ (4,2) \\ 4 & to[L, l=12<\mbox{milli}\henry>, i=\$i.1\$] \ (4,0) \ --- \ (0,0) \\ 5 & (4,2) \ \{ \ to[D*, *-*, color=red] \ (2,0) \ \} \\ 6 & (0,2) \ to[R, l=1<\kilo\ohm>, *-] \ (2,2) \\ 7 & to[cV, v=\$\SI\{.3\}\{\kilo\ohm\} \ i.1\$] \ (4,2) \\ 8 & (2,0) \ to[I, i=1<\mbox{milli}\ampere>, -*] \ (2,2) \\ 9 \ \end\{circuitikz\} \end{array}
```





```
 \begin{array}{lll} \label{localization} & \begin{array}{lll} \textbf{begin} \{ circuitikz \} [ scale = 1.2 ] \backslash draw \\ 2 & (0,0) & node [ anchor = east ] \ \{B\} \\ 3 & to [ short, \ o-* ] \ (1,0) \\ 4 & to [ R = 20 < \backslash ohm >, \ *-* ] \ (1,2) \\ 5 & to [ R = 10 < \backslash ohm >, \ v = \$v \_x\$ ] \ (3,2) \ -- \ (4,2) \\ 6 & to [ cI = \$ \backslash frac \{ \backslash siemens \} \{5\} \ v \_x\$, \ *-* ] \ (4,0) \ -- \ (3,0) \\ 7 & to [ R = 5 < \backslash ohm >, \ *-* ] \ (3,2) \\ 8 & (3,0) \ -- \ (1,0) \\ 9 & (1,2) \ to [ short, \ -o ] \ (0,2) \ node [ anchor = east ] \{A\} \\ 10 \ ; \backslash end \{ circuitikz \} \\ \end{array}
```



```
1 \begin{circuitikz}[scale=1.2, american]\draw
    (0,2) to [I=1<\milli\ampere>] (2,2)
           to[R, l_=2<\kilo\ohm>, *-*] (0,0)
           to[R, l=2<\langle kilo \rangle] (2,0)
           to[V, v=2<\volt>](2,2)
           to [cspst, l=\$t_0\$] (4,2) -- (4,1.5)
           to [generic, i=\$i_1\$, v=\$v_1\$] (4,-.5) -- (4,-1.5)
    (0,2) -- (0,-1.5) \text{ to}[V, v_=4<\text{volt}] (2,-1.5)
           to [R, l=1<\langle kilo \rangle ) (4,-1.5);
10
     \begin{scope}[xshift=6.5cm, yshift=.5cm]
11
       \frac{-1}{2} (-2,0) -- (2.5,0) node[anchor=west] {$v_1 [\volt]$};
12
      \langle \text{draw} [->] (0,-2) -- (0,2) \text{ node}[\text{anchor=west}] \{ i_1 [\SI\{\} \{ \text{milli} \neq \}] \} \}
13
      \frac{-1,0}{\text{oran}} node[anchor=north] \{-2\} (1,0) node[anchor=south] \{2\}
14
             (0,1) node[anchor=west] \{4\} (0,-1) node[anchor=east] \{-4\}
15
             (2,0) node[anchor=north west] \{4\}
16
             (-1.5,0) node[anchor=south east] \{-3\};
17
      \label{eq:draw} $$ [thick] (-2,-1) -- (-1,1) -- (1,-1) -- (2,0) -- (2.5,.5);
18
      \det[\det[(-1,1)] - (-1,0)(1,-1) - (1,0)
19
             (-1,1) -- (0,1) (1,-1) -- (0,-1);
20
     \ensuremath{\mbox{end}}
22 \end{circuitikz}
```

# 8 Revision history

 $version \ 0.2.2 \ (20090520).$ 

- 1. Added the shape for lamps.
- 2. Added options europeanresistor, europeaninductor, americanresistor and americaninductor, with corresponding styles.
- 3. **Fixed**: error in transistor arrow positioning and direction under negative xscale and yscale.

 $version \ 0.2.1 \ (20090503).$ 

- 1. Op-amps added.
- 2. Added options arrowmos and noarrowmos.

### $version\ 0.2$ First public release on CTAN (20090417).

- 1. **Backward incompatibility**: labels ending with : angle are not parsed for positioning anymore.
- 2. Full use of TikZ keyval features.
- 3. White background is not filled anymore: now the network can be drawn on a background picture as well.
- 4. Several new components added (logical ports, transistors, double bipoles,  $\dots$ ).
- 5. Color support.
- 6. Integration with siunitx.
- 7. Voltage, american style.
- 8. Better code, perhaps. General cleanup at the very least.

version 0.1 First public release (2007).