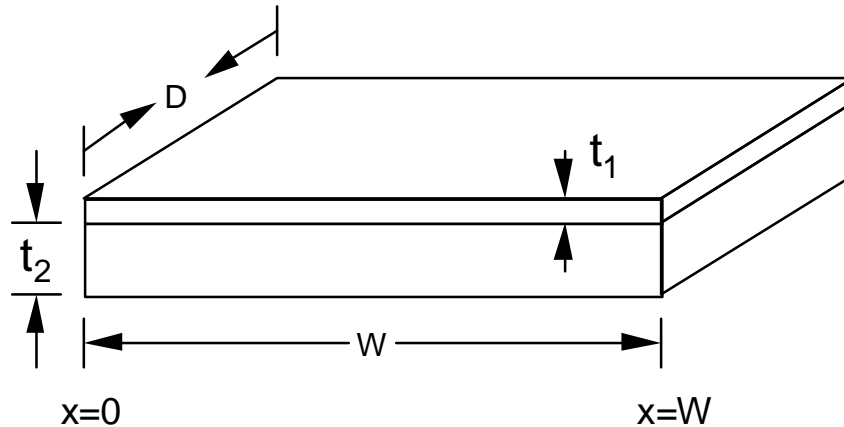


Due: Monday, April 21, 2003

1. A bipolar junction transistor uses a current variation in the base region to modulate the emitter-base voltage and hence a modulation on the injected current leading to a variation in the collector output current. So it is a current control mechanism. Field-effect transistors employ a different mechanism to control the output current by changing the charge density in the active region. The following problem is designed to illustrate the effect of changing charge density on the output current. Assume that a rectangular bar of silicon shown below consists of two layers, each of thickness t_1 and t_2 , respectively. The dimensions are $D=100\mu\text{m}$, $W=5\mu\text{m}$, $t_2=1\mu\text{m}$ and $t_1=200\text{\AA}$. The t_1 layer is uniformly doped with N_{d1}/cm^3 donors and $N_{d2}=10^{11}/\text{cm}^3$ donors are also uniformly doped in the t_2 layer. A bias of 1 V is applied between $x=0$ and $x=W$. Determine and plot the output current for different carrier densities in the t_1 layer. Specify the current for $N_{d1}=3\times 10^{13}/\text{cm}^3$, $3\times 10^{15}/\text{cm}^3$, $3\times 10^{17}/\text{cm}^3$, and $3\times 10^{19}/\text{cm}^3$. For simplicity, use an average electron mobility of $1000\text{ cm}^2/\text{V}\cdot\text{s}$ for both layers and various carrier densities to estimate the conductivity. In real field-effect transistors, the variation in carrier density in the t_1 layer is accomplished by charges induced by a vertical electric field.



2. Redraw Figure 6-12 of band diagrams for the ideal MOS structure in an n-type silicon at (a) thermal equilibrium (b) electron accumulation (c) electron depletion and (d) strong inversion. Also, in the drawing show a simple circuit illustrating how the biasing is applied in each case.
3. For an ideal MOS structure, the SiO_2 thickness is 200 \AA , and the substrate is doped with $5\times 10^{16}/\text{cm}^3$ acceptors. Determine the threshold voltage, V_T , required to achieve strong inversion and find the electric field in the oxide when the applied bias $V=V_T$. Repeat for a different MOS where the SiO_2 thickness is 40 \AA but the substrate doping is the same.