ATmega328P Code

main.cpp

```
// Configuration and initialization of the analog-to-digital converter:
#include "ADC.hpp"
// Capacitive touch sensing:
#include "Touch.hpp"
          // PID controller:
         #include "Controller.hpp"
// Configuration of PWM and Timer2/0 for driving the motor:
#include "Motor.hpp"
         // Reference signal for testing the performance of the controller: #include "Reference.hpp"
 10
          // Helpers for low-level AVR Timer2/0 and ADC registers:
 11
          #include "Registers.hpp"
         // Parsing incoming messages over Serial using SLIP packets:
#include "SerialSLIP.hpp"
 13
         #include <Arduino.h> // setup, loop, analogRead #include <Arduino_Helpers.h> // EMA.hpp #include <Wire.h> // I^2C slave
 16
 19
          #include "SMA.hpp"
                                                                    // SMA filter
 20
          #include <AH/Filters/EMA.hpp> // EMA filter
 21
 22
          // ------ Description -----//
 23
         /\!/ This sketch drives up to four motorized faders using a PID controller. The /\!/ motor is disabled when the user touches the knob of the fader.
 25
 26
 27
         // Everything is driven by Timer2, which runs (by default) at a rate of // 31.250 kHz. This high rate is used to eliminate audible tones from the PWM // drive for the motor. Timer0 is used for the PWM outputs of faders 3 and 4. // Every 30 periods of Timer2 (960 µs), each analog input is sampled, and // this causes the PID control loop to run in the main loop function.
 28
 29
 31
         // Capacitive sensing is implemented by measuring the RC time on the touch pin // in the Timer2 interrupt handler. The "touched" status is sticky for >20 ms // to prevent interference from the 50 Hz mains.
 34
 35
         // There are options to (1) follow a test reference (with ramps and jumps), (2) // to receive a target position over \rm I^2 C, or (3) to run experiments based on // commands received over the serial port. The latter is used by a Python script // that performs experiments with different tuning parameters for the
 37
 38
 39
 40
          // controllers.
 41
         // ----- Hardware ----- //
 43
 44
 45
         // - A0: wiper of the potentiometer
// - D8: touch pin of the knob
// - D2: input 1A of L293D dual H-bridge 1
 46
                                                                                                      (ADCO)
 47
                                                                                                      (PB0)
 49
               - D3: input 2A of L293D dual H-bridge 1
                                                                                                      (OC2B)
 50
         //
          // Fader 1:
 51
               - A1: wiper of the potentiometer
- D9: touch pin of the knob
- D13: input 3A of L293D dual H-bridge 1
                                                                                                      (ADC1)
 53
                                                                                                      (PB1)
                                                                                                      (PB5)
 55
               - D11: input 4A of L293D dual H-bridge 1
                                                                                                      (0C2A)
 56
         //
 57
          // Fader 2:
               - A2: wiper of the potentiometer
- D10: touch pin of the knob
- D4: input 1A of L293D dual H-bridge 2
- D5: input 2A of L293D dual H-bridge 2
                                                                                                      (ADC2)
 59
                                                                                                      (PB2)
 60
                                                                                                      (PD4)
                                                                                                      (OCOB)
 62
         11
         // Fader 3:
 63
         // - A3: wiper of the potentiometer
// - D12: touch pin of the knob
// - D7: input 3A of L293D dual H-bridge 2
// - D6: input 4A of L293D dual H-bridge 2
                                                                                                      (ADC3)
                                                                                                      (PB4)
 65
                                                                                                      (PD7)
 66
                                                                                                      (0C0A)
 68
         // If fader 1 is unused:
 69
 70
                - D13: LED or scope as overrun indicator
                                                                                                      (PB5)
 71
         11
 72
         // For communication:
               - D0: UART TX
- D1: UART RX
                                                                                                       (TXD)
 74
         11
                                                                                                      (RXD)
 75
                - A4: I2C data
                                                                                                      (SDA)
          // - A5: I2C clock
                                                                                                      (SCL)
 77
         // Connect the outer connections of the potentiometers to ground and Vcc, it's 
// recommended to add a 100 nF capacitor between each wiper and ground. 
// Connect the 1,2EN and 3,4EN enable pins of the L293D chips to Vcc. 
// Connect a 500kΩ pull-up resistor between each touch pin and Vcc.
 78
 80
 81
         // Connect a 500KD pull-up resistor between each touch pin and vcc.
// On an Arduino Nano, you can set an option to use pins A6/A7 instead of A2/A3.
// Note that D13 is often pulsed by the bootloader, which might cause the fader
// to move when resetting the Arduino. You can either disable this behavior in
// the bootloader, or use a different pin (e.g. A3 or A4 on an Arduino Nano).
// The overrun indicator is only enabled if the number of faders is 1, because
// it conflicts with the motor driver pin of Fader 1. You can choose a different
 83
 84
 86
 87
 89
                                    ----- Configuration -----//
 90
         struct Config {
   // Print the control loop and interrupt frequencies to Serial at startup:
 92
 93
                  static constexpr bool print_frequencies = true;
                 // Print the setpoint, actual position and control signal to Serial.
// Note that this slows down the control loop significantly, it probably
// won't work if you are using more than one fader without increasing
 95
 96
 98
                  // `interrupt_counter`:
                 static constexpr bool print_controller_signals = false;
static constexpr uint8_t controller_to_print = 0;
 99
                 // Follow the test reference trajectory (true) or receive the target
// position over I<sup>2</sup>C or Serial (false):
101
102
```

```
103
               static constexpr bool test_reference = false;
               // Increase this divisor to slow down the test reference:
static constexpr uint8_t test_reference_speed_div = 4;
// Allow control for tuning and starting experiments over Serial:
104
105
106
               static constexpr bool serial_control = true;
// I²C slave address (zero to disable I²C):
108
               static constexpr uint8_t i2c_address = 8;
109
               // Number of faders, must be between 1 and 4:
static constexpr size_t num_faders = 1;
// Actually drive the motors:
111
112
               static constexpr bool enable_controller = true;
// Use analog pins (A0, A1, A6, A7) instead of (A0, A1, A2, A3), useful for
// saving digital pins on an Arduino Nano:
114
115
116
               static constexpr bool use_A6_A7 = true;
// Use pin A2 instead of D13 as the motor driver pin for the second fader.
// Can only be used if `use_A6_A7` is set to true:
117
118
119
               static constexpr bool fader_1_A2 = true;
120
121
122
                // Capacitive touch sensing RC time threshold.
               // Increase this time constant if the capacitive touch sense is too // sensitive or decrease it if it's not sensitive enough:
123
124
               125
126
127
128
129
               // Use phase-correct PWM (true) or fast PWM (false), this determines the // timer interrupt frequency, prefer phase-correct PWM with prescaler 1 on // 16 MHz boards, and fast PWM with prescaler 1 on 8 MHz boards, both result // in a PWM and interrupt frequency of 31.250 kHz // (fast PWM is twice as fast):
130
131
132
133
134
               static constexpr bool phase_correct_pwm = true;
// The fader position will be sampled once per `interrupt_counter` timer
// interrupts, this determines the sampling frequency of the control loop.
135
136
137
               // Some examples include 20 \rightarrow 320~\mu s, 30 \rightarrow 480~\mu s, 60 \rightarrow 960~\mu s, // 90 \rightarrow 1,440~\mu s, 124 \rightarrow 2,016~\mu s, 188 \rightarrow 3,008~\mu s, 250 \rightarrow 4,000~\mu s. // 60 is the default, because it works with four faders. If you only use // a single fader, you can go as low as 20 because you only need a quarter // of the computations and ADC time:
138
139
140
141
142
               // Of the computations and ADC time:
static constexpr uint8_t interrupt_counter = 60 / (1 + phase_correct_pwm);
// The prescaler for the timer, affects PWM and control loop frequencies:
static constexpr unsigned prescaler_fac = 1;
// The prescaler for the ADC, affects speed of analog readings:
143
144
145
146
               static constexpr uint8_t adc_prescaler_fac = 64;
147
148
                // Turn off the motor after this many seconds of inactivity:
149
150
               static constexpr float timeout = 2;
151
               // EMA filter factor for fader position filters:
static constexpr uint8_t adc_ema_K = 2;
// SMA filter length for setpoint filters, improves tracking of ramps if the
// setpoint changes in steps (e.g. when the DAW only updates the reference
// every 20 ms). Powers of two are significantly faster (e.g. 32 works well):
152
153
154
155
156
157
               static constexpr uint8_t setpoint_sma_length = 0;
158
               // ----- Computed Ouantities -----//
160
               161
162
163
164
               constexpr static float interrupt_freq =
    1. * F_CPU / prescaler_fac / 256 / (1 + phase_correct_pwm);
// Clock speed of the ADC:
constexpr static float adc_clock_freq = 1. * F_CPU / adc_prescaler_fac;
165
166
167
168
               // Pulse pin D13 if the control loop took too long:
constexpr static bool enable_overrun_indicator =
    num_faders < 2 || fader_1_A2;</pre>
169
170
171
172
               173
174
175
                                      "and analog input at the same time");
176
177
        constexpr uint8_t Config::touch_masks[];
178
        constexpr float Ts = Config::Ts;
179
180
         // ----- ADC, Capacitive Touch State and Motors ------//
181
        ADCManager<Config> adc;
TouchSense<Config> touch;
182
183
184
        Motors<Config> motors;
185
186
                               ------ Setpoints and References -----//
187
188
         // Setpoints (target positions) for all faders:
189
        Reference<Config> references[Config::num_faders];
190
        // ----- Controllers ----- //
191
192
        // The main PID controllers. Need tuning for your specific setup:
193
194
        PID controllers[] {
195
                // This is an example of a controller with very little overshoot
196
197
                                 // Kp: proportional gain
198
                     2, // Ki: integral gain
0.035, // Kd: derivative gain
Ts, // Ts: sampling time
199
200
                     Ts,
201
                      60, // fc: cutoff frequency of derivative filter (Hz), zero to disable
203
               }, // This one has more overshoot, but less ramp tracking error
204
205
```

```
206
                             // Kp: proportional gain
                   11, // Ki: integral gain
0.028, // Kd: derivative gain
Ts, // Ts: sampling time
207
208
209
                   40, // fc: cutoff frequency of derivative filter (Hz), zero to disable
211
             },
// This is a very aggressive controller
212
                   8.55, // Kp: proportional gain
440, // Ki: integral gain
0.043, // Kd: derivative gain
Ts, // Ts: sampling time
214
215
217
                   70, // fc: cutoff frequency of derivative filter (Hz), zero to disable
218
             },
// Fourth controller
220
221
                  6, // Kp: proportional gain
2, // Ki: integral gain
0.035, // Kd: derivative gain
Ts, // Ts: sampling time
222
223
224
225
                   60, // fc: cutoff frequency of derivative filter (Hz), zero to disable
226
227
228
      };
229
230
       template <uint8 t Idx>
       void printControllerSignals(int16_t setpoint, int16_t adcval, int16_t control) {
   // Send (binary) controller signals over Serial to plot in Python
   if (Config::serial_control && references[Idx].experimentInProgress()) {
      const int16_t data[3] {setpoint, adcval, control};
      SLIPSender(Serial).writePacket(reinterpret_cast<const uint8_t *>(data),
      sizenf(data));
231
232
233
234
235
236
                                                                sizeof(data));
237
238
             239
240
                   Serial.print(setpoint);
Serial.print('\t');
Serial.print(adcval);
241
242
243
                   Serial.print('\t');
Serial.print((control + 256) * 2);
Serial.println();
244
245
246
247
             }
248
       }
249
250
       template <uint8_t Idx>
       void updateController(uint16_t setpoint, int16_t adcval, bool touched) {
   auto &controller = controllers[Idx];
251
252
253
             // Prevent the motor from being turned off after being touched
254
             if (touched) controller.resetActivityCounter();
255
256
             // Set the target position
if (Config::setpoint_sma_length > 0) {
257
258
                   static SMA<Config::setpoint_sma_length, uint16_t, uint32_t> sma;
uint16_t filtsetpoint = sma(setpoint);
260
                   controller.setSetpoint(filtsetpoint);
261
263
                   controller.setSetpoint(setpoint);
264
266
             // Update the PID controller to get the control action
             int16_t control = controller.update(adcval);
267
268
269
              // Apply the control action to the motor
             if (Config::enable_controller) {
   if (touched) // Turn off motor if knob is touched
270
271
272
                         motors.setSpeed<Idx>(0);
273
                   else
                         motors.setSpeed<Idx>(control);
275
276
             printControllerSignals<Idx>(controller.getSetpoint(), adcval, control);
278
       }
279
280
       template <uint8_t Idx>
       void readAndUpdateController() {
   // Read the ADC value for the given fader:
   int16_t adcval = adc.read(Idx);
281
282
283
             // If the ADC value was updated by the ADC interrupt, run the control loop: if (adcval >= 0) {
    // Check if the fader knob is touched
284
285
286
                   bool touched = touch read(Idx);
// Read the target position
uint16_t setpoint = references[Idx].getNextSetpoint();
287
288
289
                   // Run the control loop
290
                   updateController<Idx>(setpoint, adcval, touched);
// Write -1 so the controller doesn't run again until the next value is
291
292
                   // available:
293
                   adc.write(Idx, -1);
if (Config::enable_overrun_indicator)
294
295
                         cbi(PORTB, 5); // Clear overrun indicator
297
             }
298
       }
       // ----- Setup & Loop ----- //
300
301
       void onRequest();
       void onReceive(int);
void updateSerialIn();
303
304
       void setup() {
    // Initialize some globals
306
307
              for (uint8_t i = 0; i < Config::num_faders; ++i) {</pre>
308
```

```
309
                    // all fader positions for the control loop start of as -1 (no reading)
                   // all Tader positions for the current fader position to prevent transients
adc.write(i, -1);
// reset the filter to the current fader position to prevent transients
adc.writeFiltered(i, analogRead(adc.channel_index_to_mux_address(i)));
// after how many seconds of not touching the fader and not changing
// the reference do we turn off the motor?
***Tablesfil setActivitvTimeout(Config::timeout);
310
311
312
314
315
                    controllers[i].setActivityTimeout(Config::timeout);
317
              // Configure the hardware
318
              if (Config::print_frequencies || Config::print_controller_signals ||
320
                    Config::serial_control)
321
                    Serial.begin(1000000);
322
323
              if (Config::enable_overrun_indicator) sbi(DDRB, 5); // Pin 13 output
324
325
              adc.begin();
326
              touch.begin()
327
              motors.begin();
328
329
              if (Config::print_frequencies) {
                    Serial.println();
Serial.print(F("Interrupt frequency (Hz): "));
330
331
                   Serial.printl(("Interrupt frequency (H2): "));
Serial.println(Config::interrupt_freq);
Serial.printl(F("Controller sampling time (µs): "));
Serial.println(Config::Ts * 1e6);
Serial.println(F("ADC clock rate (Hz): "));
Serial.println(Config::adc_clock_freq);
Serial.println(F("ADC sampling rate (Sps): "));
Serial.println(adc.adc_rate);
332
333
334
335
336
337
338
339
340
              if (Config::print_controller_signals) {
                    Serial.println();
Serial.println(F("Reference\tActual\tControl\t-"));
341
342
343
                    Serial.println(F("0\t0\t0\t0\t0\t0\t0\t1024"));
344
             }
345
346
              // Initalize I<sup>2</sup>C slave and attach callbacks
             if (Config::i2c_address) {
    Wire.begin(Config::i2c_address);
347
348
349
                    Wire.onRequest(onRequest);
350
                    Wire.onReceive(onReceive);
351
352
353
              // Enable Timer2 overflow interrupt, this starts reading the touch sensitive
              /\!/ knobs and sampling the ADC, which causes the controllers to run in the /\!/ main loop
354
355
356
              sbi(TIMSK2, TOIE2);
357
       }
358
       void loop() {
   if (Config::num_faders > 0) readAndUpdateController<0>();
   if (Config::num_faders > 1) readAndUpdateController<1>();
   if (Config::num_faders > 2) readAndUpdateController<2>();
   if (Config::num_faders > 3) readAndUpdateController<3>();
   if (Config::serial control) updateSerialIn();
359
360
361
362
363
364
365
       }
366
        // ------ Interrupts ----- //
367
368
369
        // Fires at a constant rate of `interrupt_freq`.
       ISR(TIMER2_OVF_vect) {
   // We don't have to take all actions at each interupt, so keep a counter to
   // know when to take what actions.
370
372
373
374
375
              adc.update(counter);
376
              touch.update(counter);
377
378
              ++counter:
              if (counter == Config::interrupt_counter) counter = 0;
379
380
       }
381
        /\!/ Fires when the ADC measurement is complete. Stores the reading, both before /\!/ and after filtering (for the controller and for user input respectively).
382
383
384
        ISR(ADC_vect) { adc.complete(); }
385
386
                 ----- Wire ----- //
387
        // Send the touch status and filtered fader positions to the master.
388
389
        void onRequest() {
390
              uint8_t touched = 0;
              for (uint8_t i = 0; i < Config::num_faders; ++i)
    touched |= touch.touched[i] << i;</pre>
391
392
             Wire.write(touched);
for (uint8_t i = 0; i < Config::num_faders; ++i) {
    uint16_t filt_read = adc.filtered_readings[i];
    Wire.write(reinterpret_cast<const uint8_t *>(&filt_read), 2);
393
394
395
396
397
             }
398
       }
399
        // Change the setpoint of the given fader based on the value in the message
400
401
        // received from the master.
        void onReceive(int count) {
402
              if (count < 2) return;
if (Wire.available() < 2) return;</pre>
403
404
              uint16_t data = Wire.read();
405
              data |= uint16_t(Wire.read()) << 8;
uint8_t idx = data >> 12;
406
407
408
              data &= 0x03FF
409
              if (idx < Config::num_faders) references[idx].setMasterSetpoint(data);</pre>
410
       }
411
```

```
412
       // ------ Serial -----//
       /\!/ Read SLIP messages from the serial port that allow dynamically updating the /\!/ tuning of the controllers. This is used by the Python tuning script.
414
415
417
       // Message format: <command> <fader> <value>
       // Commands:
418
               - p: proportional gain Kp
              - i: integral gain Ki
- d: derivative gain Kd
- c: derivative filter cutoff frequency f_c (Hz)
420
421
        // - m: maximum absolute control output
// - s: start an experiment, using getNextExperimentSetpoint
// Fader index: up to four faders are addressed using the characters '0' - '3'.
423
424
426
        // Values: values are sent as 32-bit little Endian floating point numbers.
427
       /// For example the message <code>'c0\x00\x20\x42'</code> sets the derivative filter // cutoff frequency of the first fader to 40.
429
430
431
        void updateSerialIn() {
              static SLIPParser parser;
static char cmd = '\0';
432
433
              static uint8_t fader_idx = 0;
              static uint8_t buf[4];
static_assert(sizeof(buf) == sizeof(float), "");
// This function is called if a new byte of the message arrives:
435
436
437
438
              auto on_char_receive = [&](char new_byte, size_t index_in_packet) {
                    if (index_in_packet == 0) {
   cmd = new_byte;
} else if (index_in_packet == 1) {
   fader_idx = new_byte - '0';
} else if (index_in_packet << 6) {</pre>
439
440
441
442
443
444
                          buf[index_in_packet - 2] = new_byte;
445
446
447
              // Convert the 4-byte buffer to a float:
              auto as_f32 = [&] {
448
449
                    float f;
                    memcpy(&f, buf, sizeof(float));
450
451
                    return f;
452
453
              // Read and parse incoming packets from Serial:
              while (Serial.available() > 0) {
   uint8_t c = Serial.read();
454
455
456
                    auto msg_size = parser.parse(c, on_char_receive);
                    // If a complete message of 6 bytes was received, and if it addresses // a valid fader:
457
458
459
                    if (msg_size == 6 && fader_idx < Config::num_faders) {</pre>
                           // Execute the command:
460
                          // EXECUTE THE COMMINION.

switch (cmd) {
    case 'p': controllers[fader_idx].setKp(as_f32()); break;
    case 'i': controllers[fader_idx].setKi(as_f32()); break;
    case 'd': controllers[fader_idx].setKd(as_f32()); break;
    case 'c': controllers[fader_idx].setEMACutoff(as_f32()); break;
    case 'm': controllers[fader_idx].setMaxOutput(as_f32()); break;
}
461
463
464
                                case 'm': controllers[fader_idx].setEMACUtoff(as_f32()); break;
case 's':
466
467
                                       references[fader_idx].startExperiment(as_f32());
469
                                       controllers[fader_idx].resetIntegral();
470
                                break;
default: break;
472
                         }
473
                    }
              }
475
       }
```

ADC.hpp

```
#pragma once
        #include "Registers.hpp"
#include <avr/interrupt.h>
        #include <util/atomic.h>
        #include <Arduino_Helpers.h> // EMA.hpp
  6
        #include <AH/Filters/EMA.hpp> // EMA filter
 10
        template <class Config>
       struct ADCManager {

/// Evenly distribute the analog readings in the control loop period.

/// Evenly distribute the analog readings in the control loop period.
 11
 12
              constexpr static uint8_t adc_start_count =
   Config::interrupt_counter / Config::num_faders;
 13
 14
             CONTIG::Interrupt_counter / Conig:.num_lauers,

/// The rate at which we're sampling using the ADC.

constexpr static float adc_rate = Config::interrupt_freq / adc_start_count;

// Check that this doesn't take more time than the 13 ADC clock cycles it

// takes to actually do the conversion. Use 14 instead of 13 just to be safe.

static_assert(adc_rate <= Config::adc_clock_freq / 14, "ADC too slow");
 15
 16
 18
 19
 20
 21
              /// Enable the ADC with Vcc reference, with the given prescaler, auto
 22
              /// trigger disabled, ADC interrupt enabled.
/// Called from main program, with interrupts enabled.
 23
 24
              void begin();
 25
              /// Start an ADC conversion on the given channel. /// Called inside an ISR.
 26
 27
 28
              void startConversion(uint8_t channel);
 29
 30
              /// Start an ADC conversion at the right intervals.
 31
32
              /// @param counter
                                Counter that keeps track of how many times the timer interrupt
              /// fired, between
 33
                                 fired, between 0 and Config::interrupt_counter - 1.
 34
              void update(uint8_t counter);
 35
 36
 37
38
              /// Read the latest ADC result. /// Called inside an ISR.
 39
              void complete();
 40
              /// Get the latest ADC reading for the given index. /// Called from main program, with interrupts enabled.
 41
 42
              int16_t read(uint8_t idx);
/// Get the latest filtered ADC reading for the given index.
/// Called from main program, with interrupts enabled.
int16_t readFiltered(uint8_t idx);
 43
 44
 45
 46
 47
              /// Write the ADC reading for the given index.
/// Called from main program, with interrupts enabled.
 48
 49
              void write(uint8 t idx, int16 t val);
/// Write the filtered ADC reading for the given index.
/// Called only before ADC interrupts are enabled.
 50
 51
 52
 53
              void writeFiltered(uint8_t idx, int16_t val);
 54
 55
              /// Convert the channel index between 0 and Config::num_faders - 1 to the
              /// actual ADC multiplexer address.
constexpr static inline uint8_t
 56
 57
 58
              channel_index_to_mux_address(uint8_t adc_mux_idx) {
 59
                    60
 61
                                      : adc_mux_idx;
 62
              }
 63
              /// Index of the ADC channel currently being read.
 65
              uint8_t channel_index = Config::num_faders;
/// Latest ADC reading of each fader (updated in ADC ISR). Used for the
 66
 67
              /// control loop.
              volatile int16_t readings[Config::num_faders];
/// Filters for ADC readings.
 68
 69
              EMA<Config::adc_ema_K, uint16_t> filters[Config::num_faders];
/// Filtered ADC readings. Used to output over MIDI.
volatile uint16_t filtered_readings[Config::num_faders];
 70
 71
 72
 73
 74
 75
        template <class Config>
        inline void ADCManager<Config>::begin() {
   constexpr auto prescaler = factorToADCPrescaler(Config::adc_prescaler_fac);
   static_assert(prescaler != ADCPrescaler::Invalid, "Invalid prescaler");
 77
 78
 79
 80
              ATOMIC BLOCK(ATOMIC FORCEON) {
                    cbi(ADCSRA, ADEN); // Disable ADC
 81
 82
 83
                    cbi(ADMUX, REFS1); // Vcc reference
sbi(ADMUX, REFS0); // Vcc reference
 84
 85
 86
                    cbi(ADMUX, ADLAR); // 8 least significant bits in ADCL
 87
                    setADCPrescaler(prescaler);
 89
                    cbi(ADCSRA, ADATE); // Auto trigger disable
sbi(ADCSRA, ADIE); // ADC Interrupt Enable
sbi(ADCSRA, ADEN); // Enable ADC
 90
 92
 93
              }
       }
 95
        template <class Config>
 96
        inline void ADCManager<Config>::update(uint8_t counter) {
             if (Config::num_faders > 0 && counter == 0 * adc_start_count)
    startConversion(0);
 98
 99
              else if (Config::num_faders > 1 && counter == 1 * adc_start_count)
                    startConversion(1);
101
              else if (Config::num_faders > 2 && counter == 2 * adc_start_count)
102
```

```
startConversion(2);
else if (Config::num_faders > 3 && counter == 3 * adc_start_count)
103
104
105
                startConversion(3);
      }
106
108
109
      template <class Config>
inline void ADCManager<Config>::startConversion(uint8_t channel) {
           channel_index = channel;
           ADMUX &= 0xF0;
ADMUX |= channel_index_to_mux_address(channel);
sbi(ADCSRA, ADSC); // ADC Start Conversion
111
112
113
114
      }
115
      116
117
118
119
120
121
122
123
124
125
      }
126
      template <class Config>
inline int16_t ADCManager<Config>::read(uint8_t idx) {
127
128
129
           int16_t v;
           ATOMIC_BLOCK(ATOMIC_FORCEON) { v = readings[idx]; }
130
131
           return v;
132
      }
133
134
      template <class Config>
      inline void ADCManager<Config>::write(uint8_t idx, int16_t val) {
   ATOMIC_BLOCK(ATOMIC_FORCEON) { readings[idx] = val; }
135
136
137
138
      template <class Config>
inline int16_t ADCManager<Config>::readFiltered(uint8_t idx) {
139
140
141
           ATOMIC_BLOCK(ATOMIC_FORCEON) { v = filtered_readings[idx]; }
142
143
           return v;
144
      }
145
146
      template <class Config>
147
      inline void ADCManager<Config>::writeFiltered(uint8_t idx, int16_t val) {
           filters[idx].reset(val);
filtered_readings[idx] = val;
148
149
150
```

Touch.hpp

```
#pragma once
       #include <avr/interrupt.h>
       #include <avr/io.h>
       #include <util/atomic.h>
       template <class Config>
       struct TouchSense {
             /// The actual threshold as a number of interrupts instead of seconds:
             static constexpr uint8_t touch_sense_thres =
    Config::interrupt_freq * Config::touch_rc_time_threshold;
/// Ignore mains noise by making the "touched" status stick for longer than
10
11
12
             /// the mains period:
13
             static constexpr float period_50Hz = 1. / 50;
/// Keep the "touched" status active for this many periods (see below):
            16
19
20
21
            /// The combined bit mask for all touch GPIO pins.
static constexpr uint8_t gpio_mask =
   (Config::num_faders > 0 ? Config::touch_masks[0] : 0) |
   (Config::num_faders > 1 ? Config::touch_masks[1] : 0) |
   (Config::num_faders > 2 ? Config::touch_masks[2] : 0) |
22
23
25
26
                    (Config::num_faders > 3 ? Config::touch_masks[3] : 0);
27
28
             /// Initialize the GPIO pins for capacitive sensing. /// Called from main program, with interrupts enabled.
30
31
32
             void begin();
33
             /// Check which touch sensing knobs are being touched.
             /// @param counter
34
35
                                Counter that keeps track of how many times the timer interrupt
             /// fired, betwee
/// Called inside an ISR.
                                fired, between 0 and Config::interrupt_counter - 1.
37
38
             void update(uint8_t counter);
39
             /// Get the touch status for the given index. /// Called from main program, with interrupts enabled.
40
41
42
             bool read(uint8_t idx);
43
44
             // Timers to take into account the stickiness.
45
             uint8_t touch_timers[Config::num_faders] {};
46
             // Whether the knobs are being touched.
47
             volatile bool touched[Config::num_faders];
49
50
      template <class Config>
51
       void TouchSense<Config>::begin() {
52
            ATOMIC_BLOCK(ATOMIC_FORCEON)
                   PORTB &= ~gpio_mask; // low
DDRB |= gpio_mask; // output mode
53
54
55
56
      }
57
      // 0. The pin mode is "output", the value is "low".
      // 1. Set the pin mode to "input", touch_timer = 0.
// 2. The pin will start charging through the external pull-up resistor.
59
60
               After a fixed amount of time, check whether the pin became "high": if this is the case, the RC-time of the knob/pull-up resistor circuit was smaller than the given threshold. Since R is fixed, this can be used
62
63
      to infer C, the capacitance of the knob: if the capacitance is lower than the threshold (i.e. RC-time is lower), this means the knob was not touched.

// 5. Set the pin mode to "output", to start discharging the pin to 0V again.

// 6. Some time later, the pin has discharged, so switch to "input" mode and start charging again for the next RC-time measurement.
65
66
68
69
70
      // The "touched" status is sticky: it will remain set for at least
      // touch_sense_stickiness ticks. If the pin never resulted in another "touched"
// measurement during that period, the "touched" status for that pin is cleared.
71
72
74
       template <class Config>
75
       void TouchSense<Config>::update(uint8_t counter) {
             if (counter == 0) {
             DDRB &= -gpio_mask; // input mode, start charging } else if (counter == touch_sense_thres) {
77
78
                   uint8_t touched_bits = PINB;
DDRB |= gpio_mask; // output mode, start discharging
for (uint8_t i = 0; i < Config::num_faders; ++i) {
    bool touch_i = (touched_bits & Config::touch_masks[i]) == 0;
}</pre>
80
81
83
                         if (touch_i) {
                               touch_timers[i] = touch_sense_stickiness;
touched[i] = true;
84
86
                         } else if (touch_timers[i] > 0) {
    --touch_timers[i];
87
                                if (touch_timers[i] == 0) touched[i] = false;
89
                         }
90
                   }
92
      }
93
       template <class Config>
95
      bool TouchSense<Config>::read(uint8_t idx) {
96
             bool t:
             ATOMIC_BLOCK(ATOMIC_FORCEON) { t = touched[idx]; }
98
      }
99
```

Controller.hpp

```
1
       #pragma once
       #include <stddef.h>
  3
       #include <stdint.h>
       /// @see @ref horner(float,float,const float(&)[N]) constexpr inline float horner_impl(float xa, const float *p, size_t count, float t) {
  6
             return count == 0 ? p[count] + xa * t
  q
                                        : horner_impl(xa, p, count - 1, p[count] + xa * t);
 10
 11
       }
 12
 13
       /// Evaluate a polynomial using
       /// [Horner's method](https://en.wikipedia.org/wiki/Horner%27s_method).
        template <size_t N>
       constexpr inline float horner(float x, float a, const float (&p)[N]) {
   return horner_impl(x - a, p, N - 2, p[N - 1]);
 16
 18
 19
       /// Compute the weight factor of a exponential moving average filter
 20
       /// with the given cutoff frequency.
/// @see https://tttapa.github.io/Pages/Mathematics/Systems-and-Control-Theory/Digital-
 21
 22
       filters/Exponential%20Moving%20Average/Exponential-Moving-Average.html#cutoff-frequency
                     for the formula.
       inline float calcAlphaEMA(float f_n) {
 24
             // Taylor coefficients of 

// \alpha(f_n) = \cos(2\pi f_n) - 1 + \sqrt{(\cos(2\pi f_n)^2 - 4\cos(2\pi f_n) + 3)}

// at f_n = 0.25
 25
 26
 27
             // at T<sub>n</sub> = 0.25

constexpr static float coeff[] {

+7.3205080756887730e-01, +9.7201214975728490e-01,

-3.7988125051760377e+00, +9.5168450173968860e+00,

-2.0829320344443730e+01, +3.0074306603814595e+01,

-1.6446172139457754e+01, -8.0756002564633450e+01,

+3.2420501524111750e+02, -6.5601870948443250e+02,
 28
 29
 30
 31
 32
 33
 34
 35
              return horner(f_n, 0.25, coeff);
       }
 36
 37
       /// Standard PID (proportional, integral, derivative) controller. Derivative /// component is filtered using an exponential moving average filter.
 38
 39
       class PID {
 40
          public:
 41
             PID() = default;
 42
             /// @param
 43
                               kp
 44
                               Proportional gain
             /// @param
 45
                               ki
              ///
                               Integral gain
 46
 47
              /// @param
                               kd
 48
              ///
                               Derivative gain
             /// @param Ts
 49
 50
                               Sampling time (seconds)
 51
              /// @param fc
                               Cutoff frequency of derivative EMA filter (Hertz), zero to disable the filter entirely
 52
             111
 53
             PID(float kp, float ki, float kd, float Ts, float f_c = 0, float maxOutput = 255)
 54
 55
 56
                    : Ts(Ts), maxOutput(maxOutput) {
                    setKp(kp);
 57
 58
                   setKi(ki):
 59
                   setKd(kd);
 60
                    setEMACutoff(f_c);
 61
             }
 62
 63
              /// Update the controller: given the current position, compute the control
 64
              /// action.
 65
              float update(uint16_t input) {
                   // The error is the difference between the reference (setpoint) and the 
// actual position (input) 
int16_t error = setpoint - input; 
// The integral or sum of current and previous errors 
int32_t newIntegral = integral + error; 
// Compute the difference between the current and the previous input,
 66
 67
 68
 69
 70
 71
                    // but compute a weighted average using a factor \alpha \in (0,1]
 73
74
                   float diff = emaAlpha * (prevInput - input);
// Update the average
 75
                   prevInput -= diff;
 76
77
                    // Check if we can turn off the motor
                    if (activityCount >= activityThres && activityThres) {
                         float filtError = setpoint - prevInput;
if (filtError >= -errThres && filtError <= errThres) {
    errThres = 2; // hysteresis
    integral = newIntegral;
}</pre>
 79
 80
 82
 83
                               return 0:
                         } else {
 85
                               errThres = 1;
 86
                   } else {
 88
                         ++activityCount;
 89
                         errThres = 1:
 91
 92
                   bool backward = false;
                    int32_t calcIntegral = backward ? newIntegral : integral;
 94
 95
                    // Standard PID rule
                    float output = kp * error + ki_Ts * calcIntegral + kd_Ts * diff;
 97
 98
                    // Clamp and anti-windup
                   if (output > maxOutput)
  output = maxOutput;
100
101
                   else if (output < -maxOutput)</pre>
```

```
102
                             output = -maxOutput;
103
104
                             integral = newIntegral;
105
                      return output;
107
               }
108
               void setKp(float kp) { this->kp = kp; } ///< Proportional ga void setKi(float ki) { this->ki_Ts = ki * this->Ts; } ///< Integral gain void setKd(float kd) { this->kd_Ts = kd / this->Ts; } ///< Derivative gain
110
111
112
                \label{linear_float_getkp()} \begin{array}{lll} & & & ///< & Proportional gain \\ & & float getKi() & const \{ & return & ki\_Ts & / & Ts; \} & ///< & Integral gain \\ & float getKd() & const \{ & return & kd\_Ts & * & Ts; \} & ///< & Derivative gain \\ \end{array}
113
114
115
116
                /// Set the cutoff frequency (-3 dB point) of the exponential moving average /// filter that is applied to the input before taking the difference for
117
118
               /// computing the derivative term.
void setEMACutoff(float f_c) {
   float f_n = f_c * Ts; // normalized sampling frequency
   this->emaAlpha = f_c == 0 ? 1 : calcAlphaEMA(f_n);
119
120
121
122
123
124
125
                /// Set the reference/target/setpoint of the controller.
                void setSetpoint(uint16_t setpoint) {
   if (this->setpoint != setpoint) this->activityCount = 0;
126
127
128
                       this->setpoint = setpoint;
129
                }
/// @see @ref setSetpoint(int16_t)
130
131
                uint16_t getSetpoint() const { return setpoint; }
132
133
                      Set the maximum control output magnitude. Default is 255, which clamps
134
                /// the control output in [-255, +255].
void setMaxOutput(float maxOutput) { this->maxOutput = maxOutput; }
135
136
                /// @see @ref setMaxOutput(float)
137
                float getMaxOutput() const { return maxOutput; }
138
139
                /// Reset the activity counter to prevent the motor from turning off.
                void resetActivityCounter() { this->activityCount = 0; }
/// Set the number of seconds after which the motor is turned off, zero to
/// keep it on indefinitely.
140
141
142
143
                void setActivityTimeout(float s) {
144
                      if (s == 0)
145
                             activityThres = 0;
146
                             activityThres = uint16_t(s / Ts) == 0 ? 1 : s / Ts;
147
148
149
               /// Reset the sum of the previous errors to zero. void resetIntegral() { integral = 0; }
150
151
152
            private:
  float Ts = 1;
153
                                                               ///< Sampling time (seconds)
154
                float maxOutput = 255;
                                                               ///< Maximum control output magnitude
155
                float kp = 1;
float ki_Ts = 0;
156
                                                               ///< Proportional gain
                                                                ///< Integral gain times Ts
157
                float k1_TS = 0; ///< Integral gain times TS
float kd_TS = 0; ///< Derivative gain divided by TS
float emaAlpha = 1; ///< Weight factor of derivative EMA filter.
float prevInput = 0; ///< (Filtered) previous input for derivative.
uint16_t activityCount = 0; ///< How many ticks since last setpoint change.
159
160
                uint16_t activityThres = 0; ///< Threshold for turning off the output.
uint8_t errThres = 1; ///< Threshold with hysteresis.
int32_t integral = 0; ///< Sum of previous errors for integral.</pre>
162
163
165
                uint16_t setpoint = 0;
                                                               ///< Position reference.
        };
166
```

Motor.hpp

```
#pragma once
       #include "Registers.hpp"
#include <avr/interrupt.h>
       #include <util/atomic.h>
       /// Configure Timer0 in either phase correct or fast PWM mode with the given
  6
      /// prescaler, enable output compare B.
inline void setupMotorTimer0(bool phase_correct, Timer0Prescaler prescaler) {
            ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
    setTimer0WGMode(phase_correct ? Timer0WGMode::PWM
 10
 11
                                                             TimerOWGMode::FastPWM);
 12
                  setTimer0Prescaler(prescaler);
                  sbi(TCCR0A, COM0B1); // Table 14-6, 14-7 Compare Output Mode sbi(TCCR0A, COM0A1); // Table 14-6, 14-7 Compare Output Mode
 13
 15
 16
      }
       /// Configure Timer2 in either phase correct or fast PWM mode with the given
 19
       /// prescaler, enable output compare B.
inline void setupMotorTimer2(bool phase_correct, Timer2Prescaler prescaler) {
 20
            ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
    setTimer2WGMode(phase_correct ? Timer2WGMode::PWM
 21
 22
 23
                                                           : Timer2WGMode::FastPWM);
 24
                  setTimer2Prescaler(prescaler);
                  sbi(TCCR2A, COM2B1); // Table 14-6, 14-7 Compare Output Mode sbi(TCCR2A, COM2A1); // Table 14-6, 14-7 Compare Output Mode
 25
 26
 27
      }
 28
 29
 30
       \ensuremath{///} Configure the timers for the PWM outputs.
 31
32
       template <class Config>
       template <class Conig>
inline void setupMotorTimers() {
   constexpr auto prescaler0 = factorToTimer0Prescaler(Config::prescaler_fac);
   static_assert(prescaler0 != Timer0Prescaler::Invalid, "Invalid prescaler");
   constexpr auto prescaler2 = factorToTimer2Prescaler(Config::prescaler_fac);
 33
 34
 35
 36
            static_assert(prescaler2 != Timer2Prescaler::Invalid, "Invalid prescaler");
 37
38
            if (Config::num faders > 0)
 39
                  setupMotorTimer2(Config::phase_correct_pwm, prescaler2);
            if (Config::num_faders > 2)
    setupMotorTimer0(Config::phase_correct_pwm, prescaler0);
 40
 41
 42
 43
       /// Class for driving up to 4 DC motors using PWM.
 44
 45
       template <class Config>
 46
       struct Motors {
 47
            void begin();
 48
            template <uint8_t Idx>
 49
            void setSpeed(int16_t speed);
 50
 51
            template <uint8_t Idx>
            void setupGPIO();
template <uint8_t Idx>
 52
 53
 54
            void forward(uint8_t speed);
 55
            template <uint8_t Idx>
            void backward(uint8_t speed);
 56
 57
 58
 59
       template <class Config>
       inline void Motors<Config>::begin() {
 60
 61
            setupMotorTimers<Config>();
 62
 63
            if (Config::num_faders > 0) {
                  sbi(DDRD, 2);
sbi(DDRD, 3);
 64
 65
 66
 67
            if (Config::num_faders > 1) {
                 if (Config::fader_1_A2)
sbi(DDRC, 2);
 68
 69
                 else
    sbi(DDRB, 5);
 70
 71
72
 73
74
75
            if (Config::num_faders > 2) {
                  sbi(DDRD, 4);
sbi(DDRD, 5);
 76
 77
78
            if (Config::num_faders > 3) {
                  sbi(DDRD, 7);
 79
                  sbi(DDRD, 6);
 80
 81
      }
 83
       // Fast PWM (Table 14-6):
 84
             Clear OCOB on Compare Match, set OCOB at BOTTOM (non-inverting mode).
      // Phase Correct PWM (Table 14-7):
// Clear OCOB on compare match when up-counting. Set OCOB on compare match
 86
 87
             when down-counting.
 89
       template <class Config>
template <uint8_t Idx>
 90
       inline void Motors<Config>::forward(uint8_t speed) {
 92
            if (Idx >= Config::num_faders)
                 return;
 93
            else if (Idx == 0)
                 ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
  cbi(TCCR2A, COM2B0);
  cbi(PORTD, 2);
 95
 96
 98
                       OCR2B = speed;
 99
            }
else if (Idx == 1)
                 ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
101
                       cbi(TCCR2A, COM2A0);
102
```

```
103
                          if (Config::fader_1_A2)
104
                                cbi(PORTC, 2);
105
                          else
                                cbi(PORTB, 5);
106
                          OCR2A = speed;
             108
109
                          cbi(TCCROA, COMOBO);
cbi(PORTD, 4);
111
112
                          OCROB = speed;
113
              }
else if (Idx == 3)
ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
114
115
116
                          cbi(TCCROA, COMOAO);
cbi(PORTD, 7);
117
118
119
                          OCROA = speed;
120
                    }
       }
121
122
       // Fast PWM (Table 14-6):
// Set OCOB on Compare Match, clear OCOB at BOTTOM (inverting mode).
// Phase Correct PWM (Table 14-7):
123
124
125
126
               Set OCOB on compare match when up-counting. Clear OCOB on compare match
127
               when down-counting.
       // when down-counting.
template <class Config>
template <uint8_t Idx>
inline void Motors<Config>::backward(uint8_t speed) {
   if (Idx >= Config::num_faders)
128
129
130
131
              return;
else if (Idx == 0)
ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
132
133
134
135
                          sbi(TCCR2A, COM2B0);
sbi(PORTD, 2);
OCR2B = speed;
136
137
138
              else if (Idx == 1)
   ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
139
140
                          sbi(TCCR2A, COM2A0);
if (Config::fader_1_A2)
    sbi(PORTC, 2);
141
142
143
144
                          sbi(PORTB, 5);
OCR2A = speed;
145
146
147
              else if (Idx == 2)
   ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
148
149
                          sbi(TCCR0A, COM0B0);
sbi(PORTD, 4);
OCR0B = speed;
150
151
152
153
             }
else if (Idx == 3)
ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
    sbi(TCCR0A, COM0A0);
    sbi(PORTD, 7);
    OCR0A = speed;
}
154
155
156
157
158
                    }
160
       }
161
       template <class Config>
template <uint8_t Idx>
inline void Motors<Config>::setSpeed(int16_t speed) {
   if (speed >= 0)
162
163
164
165
166
                    forward<Idx>(speed);
              else
167
                    backward<Idx>(-speed);
168
169
       }
```

Reference.hpp

```
#include <avr/pgmspace.h>
     #include <stddef.h>
     #include <stdint.h>
     #include <util/atomic.h>
     /// Reference signal for testing the controller.
     const uint8_t reference_signal[] PROGMEM = {
         // Ramp up
         21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39, 40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57, 58,
10
11
         59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76,
12
         78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95, 96 97, 98, 99, 100, 101, 102, 103, 104, 105, 106, 107, 108, 109, 110, 111, 11 113, 114, 115, 116, 117, 118, 119, 120, 121, 122, 123, 124, 125, 126, 127,
13
                                                                               112
15
                                                                        141,
16
         128, 129, 130, 131, 132, 133, 134, 135, 136, 137, 138, 139, 140,
                                                                             142
         143, 144, 145, 146, 147, 148, 149, 150, 151, 152, 153,
                                                              154,
                                                                   155,
                                                                        156, 157,
                                                                   170,
                                                                        171,
                                                                             172,
         158, 159, 160, 161, 162, 163,
                                     164,
                                           165, 166, 167, 168, 169,
18
                                                         183,
                                                                   185,
19
         173, 174, 175,
                       176, 177,
                                 178.
                                      179,
                                           180,
                                               181,
                                                    182,
                                                              184
                                                                        186,
                                                                             187
         188, 189, 190,
                       191, 192,
                                           195,
                                               196, 197,
                                                         198, 199,
20
                                 193,
                                      194,
                                                                   200, 201, 202,
21
         203, 204, 205,
                       206, 207, 208,
                                     209,
                                           210, 211, 212, 213, 214, 215, 216, 217,
                                           225,
                                                         228,
                                                                   230,
                       221, 222,
236, 237,
22
         218, 219, 220,
                                 223,
                                      224,
                                               226, 227,
                                                              229,
                                                                        231,
                                                                             232
                                           240, 241, 242, 243, 244, 245, 246, 247,
         233, 234, 235,
                                 238,
                                     239,
23
         248, 249, 250, 251, 252, 253,
                                     254,
25
         // Max
         26
27
         // Ramp down
28
         255, 254, 253, 252, 251,
                                 250, 249,
                                           248, 247, 246, 245, 244, 243, 242,
         240, 239, 238, 237, 236,
29
                                 235, 234,
                                           233, 232, 231, 230, 229, 228, 227, 226,
30
         225, 224, 223, 222, 221,
                                 220,
                                      219,
                                           218,
                                               217, 216, 215, 214,
                                                                   213,
                                                                        212,
                                                                             211,
                                                    201,
                                                         200,
                                                                   198,
                                                                        197,
                                 205,
                                      204,
                                                              199,
31
         210, 209, 208,
                       207, 206,
                                           203,
                                                202,
                                                                             196
                                      189,
32
         195, 194, 193,
                       192, 191,
                                 190,
                                           188,
                                               187,
                                                    186,
                                                         185,
                                                              184,
                                                                   183,
                                                                        182,
                                                                             181,
33
         180, 179, 178,
                       177, 176,
                                 175,
                                      174,
                                           173,
                                               172, 171, 170, 169, 168, 167, 166,
                                                         155,
                                                              154,
                                                                   153,
                                                                        152,
                                 160,
145,
                                           158,
143,
34
         165, 164, 163, 162, 161,
                                      159,
                                               157,
                                                    156,
                                                                             151
         150, 149, 148, 147, 146,
                                               142, 141, 140, 139, 138, 137, 136,
                                      144,
35
             134, 133, 132, 131, 130,
36
         135,
                                     129,
37
         // Middle
         38
         39
40
         41
42
43
         // Jump low
         44
45
46
         10, 10, 10, 10,
47
         10, 10, 10,
         // Jump middle
49
         50
51
52
53
         127.
         127, 127, 127
// Jump high
                  127, 127,
54
         55
56
57
                                                                   245,
58
         245, 245, 245,
                       245, 245, 245, 245,
                                           245,
                                               245,
                                                    245,
                                                         245,
                                                              245,
                                                                        245.
59
         245.
             245,
                  245, 245,
         // Jump middle
60
         127, 127, 127,
                                                              127,
         62
63
                                      127,
                                                         127,
         127, 127, 127,
                       127, 127, 127,
                                           127,
                                                127,
                                                    127,
                                                              127,
                                                              127,
                                                                   127,
                                                                        127,
         65
66
         127, 127, 127, 127, 127, 127, 127,
                                          127,
                                                127, 127, 127,
        68
                                           127, 127, 127, 127, 127, 127, 127, 127,
69
 70
71
         // Ramp down
        77 Kamp down 127, 126, 125, 124, 123, 122, 121, 120, 119, 118, 117, 116, 115, 114, 113, 112, 111, 110, 109, 108, 107, 106, 105, 104, 103, 102, 101, 100, 99, 98, 97, 96, 95, 94, 93, 92, 91, 90, 89, 88, 87, 86, 85, 84, 83, 82, 81, 80, 79, 78, 77, 76, 75, 74, 73, 72, 71, 70, 69, 68, 67, 66, 65, 64, 63, 62, 61, 60, 59,
72
74
75
         58, 57, 56, 55, 54, 53, 52, 51, 50, 49, 48, 47, 46, 45, 44, 43, 42, 41, 40,
         39,
77
            38, 37, 36, 35, 34, 33, 32, 31, 30, 29, 28, 27, 26, 25, 24, 23, 22, 21, 19, 18, 17, 16, 15, 14, 13, 12, 11, 10, 9, 8, 7, 6, 5, 4, 3, 2, 1, 0,
78
         20.
           Low
                                     Θ,
                                        Θ,
80
              0, 0,
                    0, 0, 0,
                             Θ,
                                0, 0,
                                           0, 0, 0, 0, 0,
                                                          0, 0,
                                                                Θ,
81
              0, 0, 0, 0, 0, 0, 0, 0,
                                        0, 0,
                                              0, 0, 0, 0, 0, 0, 0,
                                      0.
                               Θ,
                                  Θ,
                                     Θ,
                                           Θ,
                                                          Θ,
                                        Θ,
                                              Θ,
                                                 Θ,
                                                    Θ,
83
                    0, 0,
                          0, 0,
                                                       Θ,
84
              0.0.
                    0. 0.
                          0.0.
                                Θ.
                                   0.
                                      Θ.
                                         0.
                                           Θ.
                                              Θ.
                                                 0.
                                                    Θ.
                                                       Θ,
                                                          0.
                                                             0.
                                                                Θ.
85
                                  Θ,
                                     Θ,
                                        Θ,
                                                    Θ,
                                                          Θ,
         Θ,
              Θ,
                 Θ,
                                                             Θ,
                                                                Θ,
86
                    0, 0, 0, 0,
                                Θ,
                                           Θ,
                                              Θ,
                                                 Θ,
                                                       Θ,
87
                                                    0, 0,
         Θ.
           Θ.
              0.0.
                    0. 0.
                          0, 0,
                                0.0.
                                      0.0.
                                           0.
                                              0.0.
                                                          0.0.
                                                                0.0.
                                                                      0.0.
                          0, 0, 0, 0,
                                      0, 0,
                                           Θ,
                                              Θ,
         Θ,
89
              0, 0,
                    0, 0, 0, 0, 0, 0, 0, 0, 0,
                                              0, 0, 0, 0, 0, 0, 0, 0,
90
         0, 0, 0, 0, 0, 0;
92
     \ensuremath{/\!/}\xspace Get the number of elements in the given array.
93
     template <class T, size t N>
     constexpr size_t len(const T (&)[N]) {
95
        return N;
96
98
     /// Class that handles the three main references/setpoints:
          1. Sequence programmed in PROGMEM
99
           2. Test sequence for tuning experiments (activated over Serial)
101
          3. Setpoint set by the I<sup>2</sup>C master (for real-world use)
102
     template <class Config>
```

```
103 | class Reference {
104
          /// Called from ISR
105
           void setMasterSetpoint(uint16_t setpoint) {
106
                this->master_setpoint = setpoint;
108
109
           void startExperiment(float speed_div) {
                this->experiment_speed_div = speed_div;
this->index = 0;
111
112
                this->seq_idx = 0;
113
114
          }
115
116
           bool experimentInProgress() const { return experiment_speed_div > 0; }
117
           uint16_t getNextProgmemSetpoint() {
118
119
                uint16_t setpoint = pgm_read_byte(reference_signal + index) * 4;
120
                ++seq_idx;
                if (seq_idx >= Config::test_reference_speed_div) {
121
122
                    seq_idx = 0;
123
                    ++index;
                    if (index == len(reference_signal)) index = 0;
124
125
126
                return setpoint;
127
128
129
           uint16_t getNextExperimentSetpoint() {
               constexpr uint16_t RAMPUP = 0xFFFF;
auto rampup = [](uint16_t idx, uint16_t duration) {
130
131
                    return uint32_t(1024) * idx / duration;
132
133
134
                constexpr uint16_t RAMPDOWN = 0xFFFE;
135
                auto rampdown = [&](uint16_t idx, uint16_t duration) {
   return 1023 - rampup(idx, duration);
136
137
138
                struct TestSeq {
                    uint16_t setpoint;
uint16_t duration;
139
140
               141
142
143
144
                                                                 {512, 256},
145
146
147
148
                static uint8_t seq_index = 0;
static uint16_t index = 0;
149
150
                uint16_t duration = seqs[seq_index].duration * experiment_speed_div;
151
               uint16_t seq_setpoint = seqs[seq_index].setpoint;
uint16_t setpoint;
152
153
                switch (seq_setpoint) {
                    case RAMPUP: setpoint = rampup(index, duration); break; case RAMPDOWN: setpoint = rampdown(index, duration); break;
154
155
                    default: setpoint = seq_setpoint;
156
157
                ++index:
158
                if (index == duration) {
160
                    index = 0;
                     ++sea index:
161
                    if (seq_index == len(seqs)) {
162
                         seq_index = 0;
experiment_speed_div = 0;
163
164
165
                    }
166
                return setpoint:
167
168
169
           /// Called from main program with interrupts enabled
170
           uint16_t getNextSetpoint() {
171
172
               uint16_t setpoint;
               if (Config::serial_control && experiment_speed_div > 0)
    // from the tuning experiment reference
173
174
175
                    setpoint = getNextExperimentSetpoint();
               else if (Config::test_reference)
// from the test reference
176
177
178
                    setpoint = getNextProgmemSetpoint();
                else
179
                    // from the I^2C master
180
181
                    ATOMIC_BLOCK(ATOMIC_FORCEON) { setpoint = master_setpoint; }
                return setpoint;
182
183
          }
184
        private:
185
          uint16_t index = 0;
186
187
           uint8_t seq_idx = 0;
           float experiment_speed_div = 0;
volatile uint16_t master_setpoint = 0;
188
189
190
      };
```

Registers.hpp

```
#pragma once
  3
       #include <avr/io.h>
#include <avr/sfr defs.h>
       #include <util/delay.h> // F_CPU
       // ------ Utils ----- //
       \# ifndef ARDUINO // Ensures that my IDE sees the correct frequency
       #undef F_CPU
#define F_CPU 16000000UL
 10
 11
 13
       #ifndef sbi
 15
       /// Set bit in register.
       template <class R>
inline void sbi(R &reg, uint8_t bit) {
   reg |= (1u << bit);</pre>
 16
 19
       #define sbi sbi
 20
 21
       #endif
       #ifndef cbi
 22
       /// Clear bit in register.
template <class R>
 23
       inline void cbi(R &reg, uint8_t bit) {
   reg &= ~(1u << bit);</pre>
 25
 26
 27
       #define cbi cbi
 28
 29
       #endif
 30
       /// Write bit in register.
        template <class R>
 31
       inline void wbi(R &reg, uint8_t bit, bool value) {
  value ? sbi(reg, bit) : cbi(reg, bit);
 32
 33
 34
 35
 36
       // ----- Timer0 ----- //
 37
38
       /// Timer 0 clock select (Table 14-9).
       enum class TimerOPrescaler : uint8_t {
   None = 0b000,
 39
 40
             S1 = 0b001,
S8 = 0b010,
 41
 42
             S64 = 0b011,
S256 = 0b100,
 43
 44
 45
             S1024 = 0b101
             ExtFall = 0b110,
ExtRise = 0b111,
 46
 47
             Invalid = 0xFF,
 49
 50
 51
        /// Timer 0 waveform generation mode (Table 14-8).
       enum class TimerOWGMode : uint8_t {
   Normal = 0b000,
 52
 53
             PWM = 0b001,
CTC = 0b010,
FastPWM = 0b011,
PWM_OCRA = 0b101,
 54
 55
 56
 57
 58
             FastPWM_OCRA = 0b111,
 59
       };
 60
       // Convert the prescaler factor to the correct bit pattern to write to the
// TCCROB register (Table 14-9).
constexpr inline TimerOPrescaler factorToTimerOPrescaler(uint16_t factor) {
 62
 63
                       factor == 1 2 Timer@Prescaler::S1 : factor == 8 ? Timer@Prescaler::S8 : factor == 64 ? Timer@Prescaler::S64 : factor == 256 ? Timer@Prescaler::S256 : factor == 1024 ? Timer@Prescaler::S1024 : Timer@Prescaler::Invalid;
             return factor == 1
 65
 66
 68
 69
 70
       }
 71
72
       /// Set the clock source/prescaler of Timer0 (Table 14-9).
       inline void setTimer0Prescaler(Timer0Prescaler ps) {
 74
            if (ps == Timer0Prescaler::Invalid)
    return;
 75
 76
             wbi(TCCR0B, CS02, static_cast<uint8_t>(ps) & (1u << 2));
 77
             wbi(TCCR0B, CS01, static_cast<uint8_t>(ps) & (1u << 1));
wbi(TCCR0B, CS00, static_cast<uint8_t>(ps) & (1u << 0));</pre>
 78
 79
 80
        /// Set the wavefrom generation mode of Timer0 (Table 14-8).
 81
       /// Set the wavefrom generation mode of limero (table 14-8).
inline void setTimeroWGMode(TimeroWGMode mode) {
   wbi(TCCROB, WGMO2, static_cast<uint8_t>(mode) & (1u << 2));
   wbi(TCCROA, WGMO1, static_cast<uint8_t>(mode) & (1u << 1));
   wbi(TCCROA, WGMO0, static_cast<uint8_t>(mode) & (1u << 0));</pre>
 83
 84
 85
 86
       }
 87
       // ----- Timer2 ----- //
 89
       /// Timer 0 clock select (Table 17-9).
 90
       enum class Timer2Prescaler : uint8_t {
             None = 0b000,
S1 = 0b001,
S8 = 0b010,
 92
 93
             S32 = 0b011,

S64 = 0b100,
 95
 96
             S128 = 0b101
             S256 = 0b110,
S1024 = 0b111
 98
 99
              Invalid = 0xFF
101
       };
102
```

```
103
       /// Timer 0 waveform generation mode (Table 17-8).
       enum class Timer2WGMode : uint8_t {
   Normal = 0b000,
105
             PWM = 0b001,
106
             CTC = 0b010,
             FastPWM = 0\dot{b}011,
PWM_OCRA = 0\dot{b}101,
108
109
             FastPWM_OCRA = 0b111,
111
       };
112
       \ensuremath{//} Convert the prescaler factor to the correct bit pattern to write to the
       // TCCROB register (Table 17-9).
constexpr inline Timer2Prescaler factorToTimer2Prescaler(uint16_t factor) {
114
115
                                               ? Timer2Prescaler::S1
? Timer2Prescaler::S8
? Timer2Prescaler::S32
             return factor == 1
                       : factor == 8
117
                        : factor == 32
118
                       : factor == 64 ? Timer2Prescaler::S64
: factor == 128 ? Timer2Prescaler::S128
: factor == 256 ? Timer2Prescaler::S256
119
120
121
                       : factor == 1024 ? Timer2Prescaler::S1024
: Timer2Prescaler::Invalid;
122
123
       }
124
125
       /// Set the clock source/prescaler of Timer2 (Table 17-9).
inline void setTimer2Prescaler(Timer2Prescaler ps) {
126
127
128
             if (ps == Timer2Prescaler::Invalid)
129
                   return;
             wbi(TCCR2B, CS22, static_cast<uint8_t>(ps) & (1u << 2));
wbi(TCCR2B, CS21, static_cast<uint8_t>(ps) & (1u << 1));</pre>
130
131
132
             wbi(TCCR2B, CS20, static_cast<uint8_t>(ps) & (1u << 0));
       }
133
134
135
       136
             wbi(TCCR2B, WGM22, static_cast<uint8_t>(mode) & (1u << 2));</pre>
137
138
             wbi(TCCR2A, WGM21, static_cast<uint8_t>(mode) & (lu << l));
139
             wbi(TCCR2A, WGM20, static_cast<uint8_t>(mode) & (1u << 0));</pre>
140
141
       // ----- ADC ----- //
142
143
       /// ADC prescaler select (Table 23-5).
enum class ADCPrescaler : uint8_t {
    S2 = 0b000,
144
145
146
147
             S2_2 = 0b001
             S4 = 0b010,
148
             S8 = 0b011,
149
150
             S16 = 0b100,
S32 = 0b101,
151
             S64 = 0b110,
152
             S128 = 0b111
153
154
             Invalid = 0xFF,
155
       };
       // Convert the prescaler factor to the correct bit pattern to write to the
// ADCSRA register (Table 23-5).
constexpr inline ADCPrescaler factorToADCPrescaler(uint8_t factor) {
157
158
            return factor == 2 ? ADCPrescaler::S2_2
: factor == 4 ? ADCPrescaler::S4
: factor == 8 ? ADCPrescaler::S8
: factor == 16 ? ADCPrescaler::S16
: factor == 32 ? ADCPrescaler::S32
: factor == 64 ? ADCPRESCALER::S64
: factor == 128 ? ADCPRESCALER::S128
160
161
163
164
166
                                              : ADCPrescaler::Invalid;
167
168
169
       /// Set the prescaler of the ADC (Table 23-5)
170
       inline void setADCPrescaler(ADCPrescaler ps) {
171
             if (ps == ADCPrescaler::Invalid)
    return;
172
173
             wbi(ADCSRA, ADPS2, static_cast<uint8_t>(ps) & (1u << 2));</pre>
174
             wbi(ADCSRA, ADPS1, static_cast<uint8_t>(ps) & (1u << 1));
wbi(ADCSRA, ADPS0, static_cast<uint8_t>(ps) & (1u << 0));</pre>
175
176
```

SerialSLIP.hpp

```
#include <Arduino.h>
  1
       namespace SLIP_Constants {
const static uint8_t END = 0300;
const static uint8_t ESC = 0333;
  3
       const static uint8_t ESC_END = 0334;
const static uint8_t ESC_ESC = 0335;
       } // namespace SLIP_Constants
  q
 10
       /// Parses SLIP packets: https://datatracker.ietf.org/doc/html/rfc1055
       class SLIPParser {
 11
 12
         public:
 13
             template <class Callback>
             size_t parse(uint8_t c, Callback callback);
 14
 15
             void reset() {
 16
                  size = 0;
 17
                   escape = false;
 18
 19
 20
 21
          private:
 22
             size_t size = 0;
bool escape = false;
 23
 24
 25
 26
       template <class Callback>
 27
        size_t SLIPParser::parse(uint8_t c, Callback callback) {
 28
             using namespace SLIP_Constants;
 29
              * handle bytestuffing if necessary
 30
 31
32
             switch (c) {
                  /*

* if it's an END character then we're done with
 33
 34
                       the packet
 35
 37
38
                  case END: {
                       e END: {
   /*
   * a minor optimization: if there is no
   * data in the packet, ignore it. This is
   * meant to avoid bothering IP with all
   * the empty packets generated by the
   * duplicate END characters which are in
   * turn sent to try to detect line noise.
   //
   //
 39
 40
 41
 42
 43
 44
 45
 46
                         auto packetLen = size;
 47
                         reset();
                         if (packetLen) return packetLen;
 49
                  } break;
 50
                  /*
 * if it's the same code as an ESC character, wait
 51
 52
 53
 54
                       what to store in the packet based on that.
 55
                  case ESC: {
 56
 57
                        escape = true;
 58
                  } break;
 59
 60
                    * here we fall into the default handler and let
 61
 62
                       it store the character for us
 63
 64
                   default: {
 65
                        if (escape) {
 66
                               * if "c" is not one of these two, then we
 67
                               * have a protocol violation. The best bet
* seems to be to leave the byte alone and
 68
 69
 70
                                  just stuff it into the packet
 71
72
                              switch (c) {
   case ESC_END: c = END; break;
   case ESC_ESC: c = ESC; break;
   default: break; // LCOV_EXCL_LINE (protocol violation)
 74
 75
 76
 77
                              escape = false:
 78
 79
                         callback(c, size);
 80
                         ++size;
 81
                  }
 83
             return 0:
 84
       }
 85
       /// Sends SLIP packets: https://datatracker.ietf.org/doc/html/rfc1055  
 86
       class SLIPSender {
 87
 88
 89
             SLIPSender(Stream &stream) : stream(stream) {}
 90
             size_t beginPacket() { return stream.write(SLIP_Constants::END); }
size_t endPacket() { return stream.write(SLIP_Constants::END); }
 92
 93
             size_t write(const uint8_t *data, size_t len);
size_t writePacket(const uint8_t *data, size_t len) {
    size_t sent = 0;
 95
 96
 97
                   sent += beginPacket();
                  sent += write(data, len);
sent += endPacket();
 98
 99
100
                   return sent;
101
             }
102
```

```
private:
103
104
            Stream &stream;
      };
105
106
       inline size_t SLIPSender::write(const uint8_t *data, size_t len) {
   // https://datatracker.ietf.org/doc/html/rfc1055
   using namespace SLIP_Constants;
108
109
             size_t sent = 0;
            /* * for each byte in the packet, send the appropriate character * sequence
111
112
113
114
115
            116
117
118
119
120
121
122
                             sent += stream.write(ESC);
sent += stream.write(ESC_END);
123
124
125
                             break;
126
127
                       /*
 * if it's the same code as an ESC character,
 * we send a special two character code so as not
 * to make the receiver think we sent an ESC
128
129
130
131
                       case ESC:
    sent += stream.write(ESC);
    sent += stream.write(ESC_ESC);
132
133
134
135
136
                       137
138
139
140
                  }
data++;
141
142
143
144
             return sent;
       }
145
```

SMA.hpp

```
#pragma once
3
     #include <Arduino_Helpers.h>
     #include <AH/Math/Divide.hpp>
     #include <AH/STL/algorithm> // std::fill
#include <AH/STL/cstdint>
      * @brief Simple Moving Average filter.
10
11
12
        Returns the average of the N most recent input values.
13
15
         y[n] = \frac{1}{N} \sum_{i=0}^{N-1}x[n-i]
16
18
        @see
                    https://tttapa.github.io/Pages/Mathematics/Systems-and-Control-Theory/Digital-
     filters/Simple%20Moving%20Average/Simple-Moving-Average.html
19
20
                    The number of samples to average.
21
22
        @tparam input_t
23
                    The type of the input (and output) of the filter.
        @tparam sum_t
24
                   The type to use for the accumulator, must be large enough to fit N times the maximum input value.
25
27
28
     template <uint8_t N, class input_t = uint16_t, class sum_t = uint32_t>
     class SMA {
30
31
       public:
          /// Default constructor (initial state is initialized to all zeros).
32
          SMA() = default;
33
34
          /// Constructor (initial state is initialized to given value).
          /// @param initialValue
35
36
37
                        ///
39
40
          SMA(input_t initialValue) : sum(N * (sum_t)initialValue) {
   std::fill(std::begin(previousInputs), std::end(previousInputs),
42
43
                           initialValue);
          }
          /// Update the internal state with the new input @f$ x[n] @f$ and return the /// new output @f$ y[n] @f$.
45
46
47
          /// @param input
/// The n
48
          /// The new input @f$ x[n] @f$.
/// @return The new output @f$ y[n] @f$.
input_t operator()(input_t input) {
49
50
51
52
               sum -= previousInputs[index];
sum += input;
53
               previousInputs[index] = input;
if (++index == N) index = 0;
return AH::round_div<N>(sum);
54
55
56
57
58
59
60
          uint8_t index = 0;
          input_t previousInputs[N] {};
sum_t sum = 0;
61
62
```