ATmega328P Code

main.cpp

```
\label{lem:configuration} \mbox{// Configuration and initialization of the analog-to-digital converter:} \\
       #include "ADC.hpp'
       // Capacitive touch sensing:
       #include "Touch.hpp"
       // PID controller:
 5
       #include "Controller.hpp"
 6
       // Configuration of PWM and Timer2/0 for driving the motor:
       #include "Motor.hpp"
 8
       // Reference signal for testing the performance of the controller:
       #include "Reference.hpp"
10
       // Helpers for low-level AVR Timer2/0 and ADC registers:
       #include "Registers.hpp"
12
13
       \ensuremath{//} Parsing incoming messages over Serial using SLIP packets:
14
       #include "SerialSLIP.hpp"
15
       \label{eq:minclude} \begin{tabular}{ll} \#include & \begin{tabular}{ll} \begin{tabular}{ll} \#include & \begin{tabular}{ll} \b
16
       #include <Arduino.h>
17
       #include <Wire.h>
                                                       // I<sup>2</sup>C slave
18
19
20
       #include "SMA.hpp"
                                                          // SMA filter
       #include <AH/Filters/EMA.hpp> // EMA filter
22
23
       // ------ Description -----//
24
      // This sketch drives up to four motorized faders using a PID controller. The
25
      // motor is disabled when the user touches the knob of the fader.
26
27
      // Everything is driven by Timer2, which runs (by default) at a rate of
       // 31.250 kHz. This high rate is used to eliminate audible tones from the PWM
29
       // drive for the motor. Timer0 is used for the PWM outputs of faders 3 and 4.
31
       // Every 30 periods of Timer2 (960 \mu s), each analog input is sampled, and
       // this causes the PID control loop to run in the main loop function.
32
      // Capacitive sensing is implemented by measuring the RC time on the touch pin // in the Timer2 interrupt handler. The "touched" status is sticky for >20 ms // to prevent interference from the 50 Hz mains.
33
34
35
36
37
       // There are options to (1) follow a test reference (with ramps and jumps), (2)
       // to receive a target position over I^2C, or (3) to run experiments based on
38
       // commands received over the serial port. The latter is used by a Python script // that performs experiments with different tuning parameters for the
40
41
       // controllers.
42
43
      // ----- Hardware ----- //
44
45
      // Fader 0:
      // - A0: wiper of the potentiometer
// - D8: touch pin of the knob
46
                                                                                        (ADCO)
47
                                                                                        (PB0)
      // - D2: input 1A of L293D dual H-bridge 1
48
      // - D3: input 2A of L293D dual H-bridge 1
                                                                                        (0C2B)
       // Fader 1:
51
      // - A1: wiper of the potentiometer
// - D9: touch pin of the knob
52
                                                                                        (ADC1)
53
                                                                                        (PB1)
      // - D7: input 3A of L293D dual H-bridge 1
54
                                                                                        (PD7)
      // - D11: input 4A of L293D dual H-bridge 1
55
                                                                                        (0C2A)
      //
56
      // Fader 2:
57
      // - A2: wiper of the potentiometer
                                                                                        (ADC2)
58
      // - D10: touch pin of the knob
59
                                                                                        (PB2)
60
      // - D4: input 1A of L293D dual H-bridge 2
                                                                                        (PD4)
      // - D5: input 2A of L293D dual H-bridge 2
                                                                                        (OCOB)
61
62
      // Fader 3:
63
      // - A3: wiper of the potentiometer
// - D12: touch pin of the knob
                                                                                        (ADC3)
64
65
                                                                                        (PB4)
            - D13: input 3A of L293D dual H-bridge 2
66
                                                                                        (PB5)
67
      // - D6: input 4A of L293D dual H-bridge 2
                                                                                        (0C0A)
      // If fader 1 is unused:
69
70
      // - D13: LED or scope as overrun indicator
                                                                                        (PB5)
71
      //
72
      // For communication:
      // - D0: UART TX
73
                                                                                        (TXD)
      //
            - D1: UART RX
74
                                                                                        (RXD)
75
            - A4: I2C data
                                                                                        (SDA)
76
      // - A5: I2C clock
                                                                                        (SCL)
77
      // Connect the outer connections of the potentiometers to ground and Vcc, it's
78
79
       // recommended to add a 100 nF capacitor between each wiper and ground.
       // Connect the 1,2EN and 3,4EN enable pins of the L293D chips to \ensuremath{\text{Vcc.}}
80
      // Connect a 500k\Omega pull-up resistor between each touch pin and Vcc. // On an Arduino Nano, you can set an option to use pins A6/A7 instead of A2/A3.
81
82
      // Note that D13 is often pulsed by the bootloader, which might cause the fader // to move when resetting the Arduino. You can either disable this behavior in
83
84
      // the bootloader, or use a different pin (e.g. A2 or A3 on an Arduino Nano).
// The overrun indicator is only enabled if the number of faders is less than 4,
85
       // because it conflicts with the motor driver pin of Fader 1. You can choose a
87
       // different pin instead.
88
89
90
      // ------ Configuration ----- //
91
       // Enable MIDI input/output.
```

```
93 | #define WITH_MIDI 0
      // Print to the Serial monitor instead of sending actual MIDI messages.
 94
      #define MIDI_DEBUG 0
 95
 97
      struct Config {
 98
           // Print the control loop and interrupt frequencies to Serial at startup:
 99
           static constexpr bool print_frequencies = true;
100
           // Print the setpoint, actual position and control signal to Serial.
           // Note that this slows down the control loop significantly, it probably // won't work if you are using more than one fader without increasing
101
102
103
           // `interrupt_divisor
104
           static constexpr bool print_controller_signals = false;
           static constexpr uint8_t controller_to_print = 0;
           // Follow the test reference trajectory (true) or receive the target
           // position over I<sup>2</sup>C or Serial (false):
static constexpr bool test_reference = false;
107
108
109
           // Increase this divisor to slow down the test reference:
           static constexpr uint8_t test_reference_speed_div = 4;
// Allow control for tuning and starting experiments over Serial:
110
111
           static constexpr bool serial_control = true;
112
           // I<sup>2</sup>C slave address (zero to disable I<sup>2</sup>C):
113
           static constexpr uint8_t i2c_address = 8;
           // The baud rate to use for the Serial interface (e.g. for MIDI_DEBUG,
115
116
           // print_controller_signals, serial_control, etc.)
117
           static constexpr uint32_t serial_baud_rate = 10000000;
           // The baud rate to use for MIDI over Serial.
// Use 31'250 for MIDI over 5-pin DIN, HIDUINO/USBMidiKliK.
// Hairless MIDI uses 115'200 by default.
118
119
120
           // The included python/SerialMIDI.py script uses 1'000'000.
121
122
           static constexpr uint32_t midi_baud_rate = serial_baud_rate;
123
124
           // Number of faders, must be between 1 and 4:
           static constexpr size_t num_faders = 1;
126
           // Actually drive the motors. If set to false, runs all code as normal, but
           \ensuremath{\text{//}} doesn't turn on the motors.
127
128
           static constexpr bool enable_controller = true;
           // Use analog pins (A0, A1, A6, A7) instead of (A0, A1, A2, A3), useful for
129
           // saving digital pins on an Arduino Nano: static constexpr bool use_A6_A7 = false;
130
131
           // Use pin A2 instead of D13 as the motor driver pin for the fourth fader.
           // Allows D13 to be used as overrun indicator, and avoids issues with the
           // bootloader blinking the LED.
// Can only be used if `use_A6_A7` is set to true.
134
135
           static constexpr bool fader_3_A2 = false;
136
137
           // Capacitive touch sensing RC time threshold.
138
           // Increase this time constant if the capacitive touch sense is too
// sensitive or decrease it if it's not sensitive enough:
139
140
           static constexpr float touch_rc_time_threshold = 150e-6; // seconds
141
           // Bit masks of the touch pins (must be on port B):
142
           static constexpr uint8_t touch_masks[] = {0, 0, 0, 0};
143
144
145
           // Use phase-correct PWM (true) or fast PWM (false), this determines the
146
           // timer interrupt frequency, prefer phase-correct PWM with prescaler 1 on
147
           // 16 MHz boards, and fast PWM with prescaler 1 on 8 MHz boards, both result
148
           // in a PWM and interrupt frequency of 31.250 kHz // (fast PWM is twice as fast):
149
           static constexpr bool phase_correct_pwm = true;
// The fader position will be sampled once per `interrupt_divisor` timer
150
151
152
           // interrupts, this determines the sampling frequency of the control loop.
           // Some examples include 20 \rightarrow 320 \mu s, 30 \rightarrow 480 \mu s, 60 \rightarrow 960 \mu s, // 90 \rightarrow 1,440 \mu s, 124 \rightarrow 2,016 \mu s, 188 \rightarrow 3,008 \mu s, 250 \rightarrow 4,000 \mu s. // 60 is the default, because it works with four faders. If you only use
155
156
           \ensuremath{/\!/} a single fader, you can go as low as 20 because you only need a quarter
157
           // of the computations and ADC time:
           static constexpr uint8_t interrupt_divisor = 60 / (1 + phase_correct_pwm);
158
           // The prescaler for the timer, affects PWM and control loop frequencies: static constexpr unsigned prescaler_fac = 1;
159
160
           // The prescaler for the ADC, affects speed of analog readings:
161
           static constexpr uint8_t adc_prescaler_fac = 64;
163
           \ensuremath{//} Turn off the motor after this many seconds of inactivity:
164
165
           static constexpr float timeout = 2;
166
           // EMA filter factor for fader position filters:
167
           static constexpr uint8 t adc ema K = 2:
168
           // SMA filter length for setpoint filters, improves tracking of ramps if the
169
           // setpoint changes in steps (e.g. when the DAW only updates the reference // every 20 ms). Powers of two are significantly faster (e.g. 32 works well):
170
171
           static constexpr uint8_t setpoint_sma_length = 0;
172
173
174
           // ----- Computed Quantities ----- //
175
           176
177
178
179
           // Frequency at which the interrupt fires:
           constexpr static float interrupt_freq =
                1. * F_CPU / prescaler_fac / 256 / (1 + phase_correct_pwm);
181
182
           // Clock speed of the ADC:
183
           constexpr static float adc_clock_freq = 1. * F_CPU / adc_prescaler_fac;
           // Pulse pin D13 if the control loop took too long:
184
185
           constexpr static bool enable_overrun_indicator =
                num_faders < 4 || fader_3_A2;</pre>
186
187
```

```
188
         static_assert(0 < num_faders && num_faders <= 4,</pre>
189
                        "At most four faders supported");
          static_assert(use_A6_A7 || !fader_3_A2,
190
                        "Cannot use A2 for motor driver "
191
                        "and analog input at the same time");
192
193
          static_assert(!WITH_MIDI || !serial_control,
194
                        "Cannot use MIDI and Serial control at the same time");
195
          static_assert(!WITH_MIDI || !print_controller_signals,
196
                        "Cannot use MIDI while printing controller signals");
197
198
     constexpr uint8_t Config::touch_masks[];
199
     constexpr float Ts = Config::Ts;
200
     // ----- ADC, Capacitive Touch State and Motors -----//
202
203
     ADCManager<Config> adc;
     TouchSense<Config> touch;
204
     Motors<Config> motors;
205
206
     // ----- Setpoints and References ----- //
207
208
     // Setpoints (target positions) for all faders:
209
210
     Reference<Config> references[Config::num_faders];
211
212
     // ------ Controllers -----//
213
214
     // The main PID controllers. Need tuning for your specific setup:
215
216
     PID controllers[] {
217
         // This is an example of a controller with very little overshoot
218
         {
219
                     // Kp: proportional gain
             2, // Ki: integral gain 0.035, // Kd: derivative gain Ts, // Ts: sampling time
220
221
222
             60, // fc: cutoff frequency of derivative filter (Hz), zero to disable
223
224
         // This one has more overshoot, but less ramp tracking error
225
226
227
                     // Kp: proportional gain
                    // Ki: integral gain
228
229
             0.028, // Kd: derivative gain
230
                     // Ts: sampling time
             Ts,
231
             40, // fc: cutoff frequency of derivative filter (Hz), zero to disable
232
         // This is a very aggressive controller
233
234
             8.55, // Kp: proportional gain
235
             440, // Ki: integral gain
0.043, // Kd: derivative gain
Ts, // Ts: sampling time
236
237
238
239
             70, // fc: cutoff frequency of derivative filter (Hz), zero to disable
240
241
         // Fourth controller
242
243
                    // Kp: proportional gain
// Ki: integral gain
244
             0.035, // Kd: derivative gain
245
                    // Ts: sampling time
246
247
              60, // fc: cutoff frequency of derivative filter (Hz), zero to disable
249
     };
250
     // ----- MTDT ----- //
251
252
     #if WITH_MIDI
253
     #include <Control Surface.h>
254
     USBDebugMIDI_Interface midi {Config::serial_baud_rate};
257
258
259
     HardwareSerialMIDI_Interface midi {Serial, Config::midi_baud_rate};
260
261
262
     template <uint8 t Idx>
     void sendMIDIMessages(bool touched) {
    // Don't send if the UART buffer is (almost) full
263
264
265
         if (Serial.availableForWrite() < 6) return;</pre>
266
         // Touch
         static bool prevTouched = false; // Whether the knob is being touched
267
268
         if (touched != prevTouched) {
269
              const MIDIAddress addr = MCU::FADER_TOUCH_1 + Idx;
270
              touched ? midi.sendNoteOn(addr, 127) : midi.sendNoteOff(addr, 127);
271
             prevTouched = touched;
272
         }
// Position
273
274
         static Hysteresis<6 - Config::adc_ema_K, uint16_t, uint16_t> hyst;
         if (prevTouched && hyst.update(adc.readFiltered(Idx))) {
275
              auto value = AH::increaseBitDepth<14, 10, uint16_t>(hyst.getValue());
276
277
             midi.sendPitchBend(MCU::VOLUME_1 + Idx, value);
278
         }
279
     }
280
     void updateMIDISetpoint(ChannelMessage msg) {
281
282
         auto type = msg.getMessageType();
```

```
283
          auto channel = msg.getChannel().getRaw();
284
          if (type == MIDIMessageType::PITCH_BEND && channel < Config::num_faders)</pre>
285
              references[channel].setMasterSetpoint(msg.getData14bit() >> 4);
286
     }
287
288
     void initMIDI() { midi.begin(); }
289
290
     void updateMIDI() {
         while (1) {
    auto evt = midi.read();
291
292
293
              if (evt == MIDIReadEvent::NO_MESSAGE)
294
                  break;
295
              else if (evt == MIDIReadEvent::CHANNEL_MESSAGE)
                  updateMIDISetpoint(midi.getChannelMessage());
296
297
298
     }
299
300
     #endif
301
     // ----- Printing all signals for serial plotter ----- //
302
303
304
      template <uint8_t Idx>
305
     void printControllerSignals(int16_t setpoint, int16_t adcval, int16_t control) {
          // Send (binary) controller signals over Serial to plot in Python
306
307
          if (Config::serial_control && references[Idx].experimentInProgress()) {
308
              const int16_t data[3] {setpoint, adcval, control};
309
              SLIPSender(Serial).writePacket(reinterpret_cast<const uint8_t *>(data),
                                              sizeof(data));
310
311
312
          // Print signals as text
313
         else if (Config::print_controller_signals &&
                   Idx == Config::controller_to_print) {
314
              Serial.print(setpoint);
316
              Serial.print('\t');
317
              Serial.print(adcval);
             Serial.print('\t');
Serial.print((control + 256) * 2);
318
319
320
              Serial.println();
321
322
     }
323
324
     // ------ Control logic -----//
325
326
      template <uint8_t Idx>
     void updateController(uint16 t setpoint, int16 t adcval, bool touched) {
327
328
         auto &controller = controllers[Idx];
329
          // Prevent the motor from being turned off after being touched
330
331
         if (touched) controller.resetActivityCounter();
332
333
          // Set the target position
334
          if (Config::setpoint_sma_length > 0) {
335
              static SMA<Config::setpoint_sma_length, uint16_t, uint32_t> sma;
336
              uint16_t filtsetpoint = sma(setpoint);
337
              controller.setSetpoint(filtsetpoint);
         } else {
338
             controller.setSetpoint(setpoint);
340
         }
341
342
          // Update the PID controller to get the control action
343
         int16_t control = controller.update(adcval);
344
345
          // Apply the control action to the motor
346
         if (Config::enable_controller) {
              if (touched) // Turn off motor if knob is touched
347
348
                  motors.setSpeed<Idx>(0);
349
              else
350
                  motors.setSpeed<Idx>(control);
351
         }
352
     #if WITH_MIDI
353
          sendMIDIMessages<Idx>(touched);
354
355
         printControllerSignals<Idx>(controller.getSetpoint(), adcval, control);
356
357
     #endif
358
     }
359
360
     template <uint8_t Idx>
     void readAndUpdateController() {
    // Read the ADC value for the given fader:
361
362
363
          int16_t adcval = adc.read(Idx);
364
          // If the ADC value was updated by the ADC interrupt, run the control loop:
365
          if (adcval >= 0) {
              // Check if the fader knob is touched
366
             bool touched = touch.read(Idx);
// Read the target position
367
368
369
              uint16_t setpoint = references[Idx].getNextSetpoint();
              // Run the control loop
370
              updateController<Idx>(setpoint, adcval, touched);
371
372
              // Write -1 so the controller doesn't run again until the next value is
373
              // available:
374
              adc.write(Idx, -1);
              if (Config::enable_overrun_indicator)
375
                  cbi(PORTB, 5); // Clear overrun indicator
376
377
         }
```

```
378
     }
379
                 ----- Setup & Loop -----//
380
381
382
     void onRequest();
383
     void onReceive(int);
384
     void updateSerialIn();
385
386
     void setup() -
          // Initialize some globals
387
388
          for (uint8_t i = 0; i < Config::num_faders; ++i) {</pre>
389
              // all fader positions for the control loop start of as -1 (no reading)
390
              adc.write(i, -1);
              // reset the filter to the current fader position to prevent transients
391
              adc.writeFiltered(i, analogRead(adc.channel_index_to_mux_address(i)));
392
              // after how many seconds of not touching the fader and not changing // the reference do we turn off the motor?
393
394
              controllers[i].setActivityTimeout(Config::timeout);
395
396
         }
397
          // Configure the hardware
398
          if (Config::enable_overrun_indicator) sbi(DDRB, 5); // Pin 13 output
399
400
401
     #if WITH_MIDI
402
         initMIDI();
403
     #else
404
          if (Config::print_frequencies || Config::print_controller_signals ||
405
              Config::serial control)
              Serial.begin(Config::serial_baud_rate);
406
407
     #endif
408
409
          adc.begin();
410
          touch.begin()
411
         motors.begin();
412
413
          // Print information to the serial monitor or legends to the serial plotter
          if (Config::print_frequencies) {
414
              Serial.println();
Serial.print(F("Interrupt frequency (Hz): "));
415
416
417
              Serial.println(Config::interrupt_freq);
              Serial.print(F("Controller sampling time (μs): "));
Serial.println(Config::Ts * 1e6);
418
419
420
              Serial.print(F("ADC clock rate (Hz): "));
              Serial.println(Config::adc_clock_freq);
Serial.print(F("ADC sampling rate (Sps): "));
421
422
423
              Serial.println(adc.adc_rate);
424
425
         if (Config::print_controller_signals) {
              Serial.println();
426
              Serial.println(F("Reference\tActual\tControl\t-"));
427
              Serial.println(F("0\t0\t0\t0\t0\t0\t0\t1024"));
428
429
430
431
          // Initalize I<sup>2</sup>C slave and attach callbacks
432
         if (Config::i2c_address) {
433
              Wire.begin(Config::i2c_address);
434
              Wire.onRequest(onRequest):
435
              Wire.onReceive(onReceive);
436
437
438
          // Enable Timer2 overflow interrupt, this starts reading the touch sensitive
439
          // knobs and sampling the ADC, which causes the controllers to run in the
440
          // main loop
441
          sbi(TIMSK2, T0IE2);
442
     }
443
     void loop() {
444
         if (Config::num_faders > 0) readAndUpdateController<0>();
445
          if (Config::num_faders > 1) readAndUpdateController<1>();
446
          if (Config::num_faders > 2) readAndUpdateController<2>();
447
         if (Config::num_faders > 3) readAndUpdateController<3>();
448
449
     #if WITH_MIDI
450
         updateMIDI();
     #else
451
         if (Config::serial_control) updateSerialIn();
452
     #endif
453
454
     }
455
456
     // ------ Interrupts ----- //
457
458
      // Fires at a constant rate of `interrupt_freq`.
459
     ISR(TIMER2_OVF_vect) {
460
          // We don't have to take all actions at each interupt, so keep a counter to
          // know when to take what actions.
461
462
          static uint8 t counter = 0:
463
464
         adc.update(counter);
465
         touch.update(counter);
466
467
          if (counter == Config::interrupt_divisor) counter = 0;
468
469
     }
470
     // Fires when the ADC measurement is complete. Stores the reading, both before
471
472
     // and after filtering (for the controller and for user input respectively).
```

```
473
    ISR(ADC_vect) { adc.complete(); }
474
475
      // ----- Wire ----- //
476
477
      \ensuremath{//} Send the touch status and filtered fader positions to the master.
478
     void onRequest() {
479
          uint8 t touched = 0;
          for (uint8_t i = 0; i < Config::num_faders; ++i)</pre>
480
              touched |= touch.touched[i] << i;</pre>
481
          Wire.write(touched);
482
          for (uint8_t i = 0; i < Config::num_faders; ++i) {</pre>
483
              uint16_t filt_read = adc.readFiltered14ISR(i);
484
485
              Wire.write(reinterpret_cast<const uint8_t *>(&filt_read), 2);
486
487
     }
488
     // Change the setpoint of the given fader based on the value in the message \,
489
490
     // received from the master.
     void onReceive(int count) {
491
         if (count < 2) return;
if (Wire.available() < 2) return;</pre>
492
493
          uint16_t data = Wire.read();
494
495
          data |= uint16_t(Wire.read()) << 8;</pre>
496
          uint8_t idx = data >> 12;
497
          data &= 0x03FF;
498
          if (idx < Config::num_faders) references[idx].setMasterSetpoint(data);</pre>
499
     }
500
     // ------ Serial ----- //
501
502
503
     // Read SLIP messages from the serial port that allow dynamically updating the
     // tuning of the controllers. This is used by the Python tuning script.
504
505
506
     // Message format: <command> <fader> <value>
     // Commands:
507
508
     //
          - p: proportional gain Kp
     //
509
          - i: integral gain Ki
          d: derivative gain Kdc: derivative filter cutoff frequency f_c (Hz)
510
511
          - m: maximum absolute control output
          - s: start an experiment, using getNextExperimentSetpoint
     // Fader index: up to four faders are addressed using the characters '0' - '3'.
     // Values: values are sent as 32-bit little Endian floating point numbers.
515
516
     // For example the message c0\x00\x00\x20\x42' sets the derivative filter
517
     // cutoff frequency of the first fader to 40.
518
519
520
     void updateSerialIn() {
         static SLIPParser parser;
static char cmd = '\0';
521
522
          static uint8_t fader_idx = 0;
523
524
          static uint8_t buf[4];
525
          static_assert(sizeof(buf) == sizeof(float), "");
526
          // This function is called if a new byte of the message arrives:
527
          auto on_char_receive = [&](char new_byte, size_t index_in_packet) {
528
              if (index_in_packet == 0)
  cmd = new_byte;
529
              else if (index_in_packet == 1)
530
                  fader_idx = new_byte - '0';
531
532
              else if (index_in_packet < 6)</pre>
                  buf[index_in_packet - 2] = new_byte;
534
         };
// Convert the 4-byte buffer to a float:
535
536
          auto as_f32 = [&] {
537
              float f:
              memcpy(&f, buf, sizeof(float));
538
539
              return f;
540
          // Read and parse incoming packets from Serial:
541
          while (Serial.available() > 0) {
542
              uint8_t c = Serial.read();
543
544
              auto msg_size = parser.parse(c, on_char_receive);
545
              // If a complete message of 6 bytes was received, and if it addresses
546
              // a valid fader:
              if (msg_size == 6 && fader_idx < Config::num_faders) {</pre>
547
                   // Execute the command:
548
549
                  switch (cmd) {
                      case 'p': controllers[fader_idx].setKp(as_f32()); break;
case 'i': controllers[fader_idx].setKi(as_f32()); break;
case 'd': controllers[fader_idx].setKd(as_f32()); break;
550
551
552
                       case 'c': controllers[fader_idx].setEMACutoff(as_f32()); break;
553
554
                       case 'm': controllers[fader_idx].setMaxOutput(as_f32()); break;
555
                       case 's':
556
                           references[fader_idx].startExperiment(as_f32());
                           controllers[fader_idx].resetIntegral();
557
558
                           break;
                       default: break;
                }
             }
561
562
         }
563
     }
```

ADC.hpp

```
#pragma once
    #include "Registers.hpp"
    #include <avr/interrupt.h>
    #include <util/atomic.h>
 5
 6
    #include <Arduino Helpers.h> // EMA.hpp
    #include <AH/Filters/EMA.hpp> // EMA filter
8
 9
    template <class Config>
10
    struct ADCManager {
12
         /\!/\!/ Evenly distribute the analog readings in the control loop period.
13
         constexpr static uint8_t adc_start_count =
14
             Config::interrupt_divisor / Config::num_faders;
         /// The rate at which we're sampling using the ADC
15
         constexpr static float add_rate = Config::interrupt_freq / adc_start_count;
// Check that this doesn't take more time than the 13 ADC clock cycles it
16
17
         // takes to actually do the conversion. Use 14 instead of 13 just to be safe.
18
19
         static_assert(adc_rate <= Config::adc_clock_freq / 14, "ADC too slow");</pre>
20
21
          // Enable the ADC with Vcc reference, with the given prescaler, auto
22
         /// trigger disabled, ADC interrupt enabled.
         \ensuremath{///} Called from main program, with interrupts enabled.
23
24
         void begin();
25
         \ensuremath{/\!/\!/} Start an ADC conversion on the given channel.
26
         /// Called inside an ISR.
27
         void startConversion(uint8_t channel);
28
29
30
         /// Start an ADC conversion at the right intervals.
31
         /// @param counter
32
         111
                      Counter that keeps track of how many times the timer interrupt
33
         111
                      fired, between 0 and Config::interrupt_divisor - 1.
         /// Called inside an ISR.
34
35
         void update(uint8_t counter);
36
37
         /// Read the latest ADC result.
         \ensuremath{///} Called inside an ISR.
38
39
         void complete();
40
41
         /// Get the latest ADC reading for the given index \,
42
         /// Called from main program, with interrupts enabled.
43
         int16_t read(uint8_t idx);
44
         /// \ensuremath{\mathsf{Get}} the latest filtered ADC reading for the given index.
         /// Called from main program, with interrupts enabled.
/// @return (16 - Config::adc_ema_K)-bit filtered ADC value.
45
46
47
         uint16_t readFiltered(uint8_t idx);
         /// Get the latest filtered ADC reading for the given index.
48
49
         /// Called from main program, with interrupts enabled.
         /// @return 14-bit filtered ADC value.
51
         uint16_t readFiltered14(uint8_t idx);
52
         /// Get the latest filtered ADC reading for the given index.
53
         /// Called inside an ISR.
54
         /// @return 14-bit filtered ADC value.
         uint16_t readFiltered14ISR(uint8_t idx);
55
56
57
         /// Write the ADC reading for the given index.
         /// Called from main program, with interrupts enabled.
58
         void write(uint8_t idx, int16_t val);
59
60
         /// Write the filtered ADC reading for the given index.
61
         \ensuremath{/\!/}\xspace Called only before ADC interrupts are enabled.
62
         /// @param val 10-bit ADC value.
         void writeFiltered(uint8_t idx, uint16_t val);
63
64
         /// Convert a 10-bit ADC reading to the largest possible range for the given
65
         /// value of Config::adc_ema_K.
66
67
         uint16_t shiftValue10(uint16_t val);
         /// Convert the given shifted filtered value to a 14-bit range.
68
69
         uint16_t unShiftValue14(uint16_t val);
70
71
         /// Convert the channel index between 0 and Config::num_faders - 1 to the
72
         /// actual ADC multiplexer address.
73
         constexpr static inline uint8 t
         channel_index_to_mux_address(uint8_t adc_mux_idx) {
74
75
             return Config::use_A6_A7
76
                         ? (adc_mux_idx < 2 ? adc_mux_idx : adc_mux_idx + 4)</pre>
77
                          : adc mux idx:
78
79
80
         /// Index of the ADC channel currently being read.
81
         uint8_t channel_index = Config::num_faders;
         /// Latest 10-bit ADC reading of each fader (updated in ADC ISR). Used for
82
         /// the control loop.
83
         volatile int16_t readings[Config::num_faders];
84
         /// Filters for ADC readings.
85
86
         EMA<Config::adc_ema_K, uint16_t> filters[Config::num_faders];
         /// Filtered (shifted) ADC readings. Used to output over MIDI etc. but not
87
88
         /// for the control loop.
89
         volatile uint16_t filtered_readings[Config::num_faders];
90
91
    template <class Config>
```

```
93
     inline void ADCManager<Config>::begin() {
          constexpr auto prescaler = factorToADCPrescaler(Config::adc_prescaler_fac);
 94
          static_assert(prescaler != ADCPrescaler::Invalid, "Invalid prescaler");
 95
 96
 97
          ATOMIC_BLOCK(ATOMIC_FORCEON) {
 98
              cbi(ADCSRA, ADEN); // Disable ADC
 99
              cbi(ADMUX, REFS1); // Vcc reference
sbi(ADMUX, REFS0); // Vcc reference
100
101
102
103
              cbi(ADMUX, ADLAR); // 8 least significant bits in ADCL
104
105
               setADCPrescaler(prescaler);
106
              cbi(ADCSRA, ADATE); // Auto trigger disable
sbi(ADCSRA, ADIE); // ADC Interrupt Enable
sbi(ADCSRA, ADEN); // Enable ADC
107
108
109
110
          }
     }
111
112
113
     template <class Config>
      inline void ADCManager<Config>::update(uint8_t counter) {
          if (Config::num_faders > 0 && counter == 0 * adc_start_count)
115
116
               startConversion(0);
117
          else if (Config::num_faders > 1 && counter == 1 * adc_start_count)
118
              startConversion(1);
119
          else if (Config::num_faders > 2 && counter == 2 * adc_start_count)
120
              startConversion(2);
          else if (Config::num_faders > 3 && counter == 3 * adc_start_count)
121
122
              startConversion(3);
123
124
125
      template <class Config>
     inline void ADCManager<Config>::startConversion(uint8_t channel) {
126
127
          channel_index = channel;
          ADMUX &= 0xF0;
ADMUX |= channel_index_to_mux_address(channel);
128
129
          sbi(ADCSRA, ADSC); // ADC Start Conversion
130
131
132
133
      template <class Config>
134
     inline void ADCManager<Config>::complete() {
135
          if (Config::enable_overrun_indicator && readings[channel_index] >= 0)
          sbi(PORTB, 5); // Set overrun indicator
uint16_t value = ADC; // Store ADC reading
136
137
138
          readings[channel_index] = value;
139
          // Filter the reading
auto &filter = filters[channel_index];
140
          filtered_readings[channel_index] = filter(shiftValue10(value));
141
142
143
144
      template <class Config>
145
      inline int16_t ADCManager<Config>::read(uint8_t idx) {
146
          int16_t val;
147
          ATOMIC_BLOCK(ATOMIC_FORCEON) { val = readings[idx]; }
148
          return val;
149
150
     template <class Config>
151
152
     inline void ADCManager<Config>::write(uint8_t idx, int16_t val) {
          ATOMIC_BLOCK(ATOMIC_FORCEON) { readings[idx] = val; }
153
154
155
156
      template <class Config>
     inline uint16_t ADCManager<Config>::shiftValue10(uint16_t val) {
157
158
          return val << (6 - Config::adc_ema_K);</pre>
159
160
      template <class Config>
     inline uint16_t ADCManager<Config>::unShiftValue14(uint16_t val) {
162
          const int shift = 6 - Config::adc_ema_K - 4;
163
          return shift >= 0 ? val >> shift : val << -shift;</pre>
164
165
166
     template <class Config>
167
     inline uint16 t ADCManager<Config>::readFiltered14ISR(uint8 t idx) {
168
          return unShiftValue14(filtered_readings[idx]);
169
170
171
      template <class Config>
172
173
     inline uint16_t ADCManager<Config>::readFiltered(uint8_t idx) {
174
          uint16_t val;
175
          ATOMIC_BLOCK(ATOMIC_FORCEON) { val = filtered_readings[idx]; }
176
          return val;
177
     }
178
179
     template <class Config>
     inline uint16_t ADCManager<Config>::readFiltered14(uint8_t idx) {
180
181
          return unShiftValue14(readFiltered(idx));
182
183
184
      template <class Config>
     inline void ADCManager<Config>::writeFiltered(uint8_t idx, uint16_t val) {
185
186
          filters[idx].reset(shiftValue10(val));
```

filtered_readings[idx] = shiftValue10(val);

188
}

Touch.hpp

```
#pragma once
     #include <avr/interrupt.h>
     #include <avr/io.h>
     #include <util/atomic.h>
 5
 6
     template <class Config>
     struct TouchSense {
 8
          /// The actual threshold as a number of interrupts instead of seconds:
 9
         static constexpr uint8_t touch_sense_thres =
   Config::interrupt_freq * Config::touch_rc_time_threshold;
10
12
          /// Ignore mains noise by making the "touched" status stick for longer than
13
          /// the mains period:
          static constexpr float period_50Hz = 1. / 50;
/// Keep the "touched" status active for this many periods (see below):
14
15
          static constexpr uint8_t touch_sense_stickiness =
   Config::interrupt_freq * period_50Hz * 4 / Config::interrupt_divisor;
16
17
          /// Check that the threshold is smaller than the control loop period:
18
          static_assert(touch_sense_thres < Config::interrupt_divisor,</pre>
19
20
                          "Touch sense threshold too high");
21
22
          /// The combined bit mask for all touch GPIO pins.
23
          static constexpr uint8_t gpio_mask =
24
              (Config::num_faders > 0 ? Config::touch_masks[0] : 0) |
               (Config::num_faders > 1 ? Config::touch_masks[1] : 0) |
(Config::num_faders > 2 ? Config::touch_masks[2] : 0) |
25
26
               (Config::num_faders > 3 ? Config::touch_masks[3] : 0);
27
29
          /// Initialize the GPIO pins for capacitive sensing.
30
          /// Called from main program, with interrupts enabled.
31
          void begin();
32
33
          /// Check which touch sensing knobs are being touched.
34
          /// @param counter
                        Counter that keeps track of how many times the timer interrupt
          111
35
          ///
                        fired, between 0 and Config::interrupt_divisor - 1.
36
37
          /// Called inside an ISR.
          void update(uint8_t counter);
38
39
40
          /// Get the touch status for the given index.
41
          /// Called from main program, with interrupts enabled.
42
          bool read(uint8_t idx);
43
44
          // Timers to take into account the stickiness.
         uint8_t touch_timers[Config::num_faders] {};
// Whether the knobs are being touched.
45
46
47
          volatile bool touched[Config::num_faders] {};
48
    };
49
     template <class Config>
51
     void TouchSense<Config>::begin() {
52
         ATOMIC_BLOCK(ATOMIC_FORCEON) {
53
              PORTB &= ~gpio_mask; // low
              DDRB |= gpio_mask; // output mode
54
55
         }
    }
56
    // 0. The pin mode is "output", the value is "low".
     // 1. Set the pin mode to "input", touch_timer = 0.
60
     // 2. The pin will start charging through the external pull-up resistor.
     // 3. After a fixed amount of time, check whether the pin became "high":
61
62
            if this is the case, the RC-time of the knob/pull-up resistor circuit
            was smaller than the given threshold. Since R is fixed, this can be used to infer C, the capacitance of the knob: if the capacitance is lower than
     //
63
    //
64
    // the threshold (i.e. RC-time is lower), this means the knob was not touched.
// 5. Set the pin mode to "output", to start discharging the pin to 0V again.
65
66
67
    // 6. Some time later, the pin has discharged, so switch to "input" mode and
            start charging again for the next RC-time measurement.
69
    // The "touched" status is sticky: it will remain set for at least
// touch_sense_stickiness ticks. If the pin never resulted in another "touched"
70
71
     \ensuremath{//} measurement during that period, the "touched" status for that pin is cleared.
72
73
74
     template <class Config>
     void TouchSense<Config>::update(uint8_t counter) {
75
76
         if (gpio_mask == 0)
77
78
          if (counter == 0) {
79
              DDRB &= ~gpio_mask; // input mode, start charging
80
          } else if (counter == touch_sense_thres) {
              uint8_t touched_bits = PINB;
81
              DDRB |= gpio_mask; // output mode, start discharging
82
              for (uint8_t i = 0; i < Config::num_faders; ++i) {
83
                   if (Config::touch_masks[i] == 0)
84
                   continue;
bool touch_i = (touched_bits & Config::touch_masks[i]) == 0;
85
87
                   if (touch_i) {
88
                        touch_timers[i] = touch_sense_stickiness;
89
                        touched[i] = true;
90
                   } else if (touch_timers[i] > 0) {
                        --touch_timers[i];
if (touch_timers[i] == 0) touched[i] = false;
91
92
```

```
93 }
94 }
95 }
96 }
97
98 template <class Config>
99 bool TouchSense<Config>::read(uint8_t idx) {
100 bool t;
101 ATOMIC_BLOCK(ATOMIC_FORCEON) { t = touched[idx]; }
102 return t;
103 }
```

Controller.hpp

```
#pragma once
     #include <stddef.h>
 3
     #include <stdint.h>
 5
 6
     /// @see @ref horner(float,float,const float(&)[N])
     constexpr inline float horner_impl(float xa, const float *p, size_t count,
                                             float t) {
 8
          return count == 0 ? p[count] + xa * t
 9
                              : horner_impl(xa, p, count - 1, p[count] + xa * t);
10
    }
11
12
13
     /// Evaluate a polynomial using
     ///~[Horner's~method] (https://en.wikipedia.org/wiki/Horner%27s\_method).\\
14
15
     template <size_t N>
    constexpr inline float horner(float x, float a, const float (&p)[N]) {
   return horner_impl(x - a, p, N - 2, p[N - 1]);
16
17
18
19
20
     /// Compute the weight factor of a exponential moving average filter
     /// with the given cutoff frequency.
     /// @see https://tttapa.github.io/Pages/Mathematics/Systems-and-Control-Theory/Digital-
22
     filters/Exponential%20Moving%20Average/Exponential-Moving-Average.html#cutoff-frequency
23
               for the formula.
     inline float calcAlphaEMA(float f_n) {
24
         // Taylor coefficients of
25
          // \alpha(f_n) = \cos(2\pi f_n) - 1 + \sqrt{(\cos(2\pi f_n)^2 - 4\cos(2\pi f_n) + 3)}
26
          // at f_n = 0.25
27
          constexpr static float coeff[] {
28
              +7.3205080756887730e-01, +9.7201214975728490e-01, -3.7988125051760377e+00, +9.5168450173968860e+00,
29
30
31
              -2.0829320344443730e+01, +3.0074306603814595e+01,
              -1.6446172139457754e+01, -8.0756002564633450e+01, +3.2420501524111750e+02, -6.5601870948443250e+02,
32
33
34
         };
35
          return horner(f_n, 0.25, coeff);
    }
36
37
38
     /// Standard PID (proportional, integral, derivative) controller. Derivative
39
     /// component is filtered using an exponential moving average filter.
40
     class PID {
41
       public:
42
         PID() = default;
43
          /// @param kp
          111
44
                       Proportional gain
45
         /// @param ki
46
         ///
                       Integral gain
47
          /// @param kd
48
                       Derivative gain
          /// @param Ts
49
50
          111
                       Sampling time (seconds)
         /// @param fc
51
                       Cutoff frequency of derivative EMA filter (Hertz),
52
         111
                       zero to disable the filter entirely
53
          111
         PID(float kp, float ki, float kd, float Ts, float f_c = 0,
54
              float maxOutput = 255)
55
              : Ts(Ts), maxOutput(maxOutput) {
56
57
              setKp(kp);
              setKi(ki);
58
59
              setKd(kd);
60
              setEMACutoff(f_c);
61
62
         /// Update the controller: given the current position, compute the control
63
64
          /// action.
         float update(uint16_t input) {
65
66
              // The error is the difference between the reference (setpoint) and the
              // actual position (input)
int16_t error = setpoint - input;
67
68
              // The integral or sum of current and previous errors
69
70
              int32_t newIntegral = integral + error;
              \ensuremath{//} Compute the difference between the current and the previous input,
71
              // but compute a weighted average using a factor \alpha \in (0,1]
72
              float diff = emaAlpha * (prevInput - input);
73
74
              // Update the average
75
              prevInput -= diff;
76
77
              // Check if we can turn off the motor
78
              if (activityCount >= activityThres && activityThres) {
                  float filtError = setpoint - prevInput;
if (filtError >= -errThres && filtError <= errThres) {
    errThres = 2; // hysteresis
    integral = newIntegral;</pre>
79
80
81
82
83
                       return 0:
                   } else {
84
85
                       errThres = 1;
86
87
              } else {
88
                  ++activityCount;
89
                   errThres = 1;
90
              }
91
```

```
92
              bool backward = false;
 93
              int32_t calcIntegral = backward ? newIntegral : integral;
 94
 95
              // Standard PID rule
 96
              float output = kp * error + ki_Ts * calcIntegral + kd_Ts * diff;
 97
 98
              // Clamp and anti-windup
 99
              if (output > maxOutput)
              output = maxOutput;
else if (output < -maxOutput)</pre>
100
101
                  output = -maxOutput;
102
103
104
                  integral = newIntegral;
105
106
              return output;
          }
107
108
          ///< Proportional gain
109
110
          void setKd(float kd) { this->kd_Ts = kd / this->Ts; } ///< Derivative gain
111
112
          float getKp() const { return kp; }
                                                       ///< Proportional gain
          float getKi() const { return ki_Ts / Ts; } ///< Integral gain float getKd() const { return kd_Ts * Ts; } ///< Derivative gain
114
115
116
117
          /// Set the cutoff frequency (-3 dB point) of the exponential moving average
          /// filter that is applied to the input before taking the difference for /// computing the derivative term.
118
119
          void setEMACutoff(float f_c) {
120
              float f_n = f_c * Ts; // normalized sampling frequency
121
122
              this->emaAlpha = f_c == 0 ? 1 : calcAlphaEMA(f_n);
123
124
          /// Set the reference/target/setpoint of the controller.
125
126
          void setSetpoint(uint16_t setpoint) {
127
              if (this->setpoint != setpoint) this->activityCount = 0;
128
              this->setpoint = setpoint;
129
          /// @see @ref setSetpoint(int16_t)
130
131
          uint16_t getSetpoint() const { return setpoint; }
133
          /// Set the maximum control output magnitude. Default is 255, which clamps
134
          /// the control output in [-255, +255].
135
          void setMaxOutput(float maxOutput) { this->maxOutput = maxOutput; }
          /// @see @ref setMaxOutput(float)
136
137
          float getMaxOutput() const { return maxOutput; }
138
139
          /// Reset the activity counter to prevent the motor from turning off.
          void resetActivityCounter() { this->activityCount = 0; }
140
          /// Set the number of seconds after which the motor is turned off, zero to
141
          /// keep it on indefinitely.
142
143
          void setActivityTimeout(float s) {
144
              if (s == 0)
145
                  activityThres = 0;
146
              else
147
                  activityThres = uint16_t(s / Ts) == 0 ? 1 : s / Ts;
148
149
          /// Reset the sum of the previous errors to zero.
150
151
          void resetIntegral() { integral = 0; }
152
153
          float Ts = 1;
154
                                       ///< Sampling time (seconds)
155
          float maxOutput = 255;
                                       ///< Maximum control output magnitude
156
          float kp = 1;
float ki_Ts = 0;
                                       ///< Proportional gain
                                       ///< Integral gain times Ts
157
          float kd_Ts = 0;
                                       ///< Derivative gain divided by Ts
158
          float emaAlpha = 1;
                                       ///< Weight factor of derivative EMA filter.
159
          float prevInput = 0;
                                       ///< (Filtered) previous input for derivative.
160
          uint16_t activityCount = 0; ///< How many ticks since last setpoint change.</pre>
161
          uint16_t activityThres = 0; ///< Threshold for turning off the output.</pre>
162
163
          uint8_t errThres = 1;
                                       ///< Threshold with hysteresis.
                                       ///< Sum of previous errors for integral. ///< Position reference.
164
          int32_t integral = 0;
165
          uint16_t setpoint = 0;
166 };
```

Motor.hpp

```
#pragma once
     #include "Registers.hpp"
     #include <avr/interrupt.h>
     #include <util/atomic.h>
 5
 6
     /// Configure Timer0 in either phase correct or fast PWM mode with the given
     /// prescaler, enable output compare B.
     inline void setupMotorTimer0(bool phase_correct, Timer0Prescaler prescaler) {
 8
         ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
 9
              setTimer0WGMode(phase_correct ? Timer0WGMode::PWM
10
                                                  TimerOWGMode::FastPWM);
              setTimerOPrescaler(prescaler);
12
              sbi(TCCR0A, COM0B1); // Table 14-6, 14-7 Compare Output Mode sbi(TCCR0A, COM0A1); // Table 14-6, 14-7 Compare Output Mode
13
14
15
    }
16
17
    /// Configure Timer2 in either phase correct or fast PWM mode with the given
18
19
     /// prescaler, enable output compare B.
20
     inline void setupMotorTimer2(bool phase_correct, Timer2Prescaler prescaler) {
         ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
    setTimer2WGMode(phase_correct ? Timer2WGMode::PWM
21
22
23
                                                : Timer2WGMode::FastPWM);
24
              setTimer2Prescaler(prescaler);
              sbi(TCCR2A, COM2B1); // Table 14-6, 14-7 Compare Output Mode sbi(TCCR2A, COM2A1); // Table 14-6, 14-7 Compare Output Mode
25
26
         }
27
28
    }
     /\!/\!/ Configure the timers for the PWM outputs.
31
     template <class Config>
32
     inline void setupMotorTimers() {
         constexpr auto prescaler0 = factorToTimer0Prescaler(Config::prescaler_fac);
static_assert(prescaler0 != Timer0Prescaler::Invalid, "Invalid prescaler");
33
34
         constexpr auto prescaler2 = factorToTimer2Prescaler(Config::prescaler_fac);
35
         static_assert(prescaler2 != Timer2Prescaler::Invalid, "Invalid prescaler");
36
37
38
         if (Config::num_faders > 0)
39
              setupMotorTimer2(Config::phase_correct_pwm, prescaler2);
40
         if (Config::num_faders > 2)
41
              setupMotorTimer0(Config::phase_correct_pwm, prescaler0);
42
    }
43
44
     /// Class for driving up to 4 DC motors using PWM.
     template <class Config>
struct Motors {
45
46
47
         void begin();
48
         template <uint8_t Idx>
49
         void setSpeed(int16_t speed);
51
         template <uint8_t Idx>
52
         void setupGPIO();
53
         template <uint8 t Idx>
54
         void forward(uint8 t speed);
55
         template <uint8 t Idx>
         void backward(uint8_t speed);
56
57
58
59
     template <class Config>
60
     inline void Motors<Config>::begin() {
61
         setupMotorTimers<Config>();
62
         if (Config::num_faders > 0) {
63
              sbi(DDRD, 2);
64
              sbi(DDRD, 3);
65
66
67
         if (Config::num_faders > 1) {
68
              sbi(DDRD, 7);
69
              sbi(DDRB, 3);
70
71
         if (Config::num_faders > 2) {
72
              sbi(DDRD, 4);
73
              sbi(DDRD, 5);
74
75
         if (Config::num_faders > 3) {
              if (Config::fader_3_A2)
76
77
                  sbi(DDRC, 2);
78
79
                  sbi(DDRB, 5);
80
              sbi(DDRD, 6);
81
         }
82
    }
83
84
    // Fast PWM (Table 14-6):
85
          Clear OCOB on Compare Match, set OCOB at BOTTOM (non-inverting mode).
     // Phase Correct PWM (Table 14-7):
87
88
          Clear OCOB on compare match when up-counting. Set OCOB on compare match
89
          when down-counting.
    template <class Config>
template <uint8_t Idx>
90
91
     inline void Motors<Config>::forward(uint8_t speed) {
```

```
93
          if (Idx >= Config::num_faders)
 94
               return;
          else if (Idx == 0)
 95
               ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
 96
 97
                   cbi(TCCR2A, COM2B0);
 98
                   cbi(PORTD, 2);
 99
                   OCR2B = speed;
100
              }
          else if (Idx == 1)
101
               ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
102
                   cbi(TCCR2A, COM2A0);
cbi(PORTD, 7);
103
104
105
                   OCR2A = speed;
106
          else if (Idx == 2)
    ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
107
108
109
                   cbi(TCCR0A, COM0B0);
                   cbi(PORTD, 4);
110
                   OCROB = speed;
111
              }
112
          else if (Idx == 3)
113
               ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
115
                   cbi(TCCR0A, COM0A0);
116
                   if (Config::fader_3_A2)
117
                        cbi(PORTC, 2);
118
                   else
                       cbi(PORTB, 5);
119
                   OCROA = speed;
120
               }
121
122
     }
123
     // Fast PWM (Table 14-6):
124
     // Set OCOB on Compare Match, clear OCOB at BOTTOM (inverting mode).
// Phase Correct PWM (Table 14-7):
126
127
           Set OCOB on compare match when up-counting. Clear OCOB on compare match
128
           when down-counting.
      template <class Config>
129
      template <uint8 t Idx>
130
      inline void Motors<Config>::backward(uint8_t speed) {
131
132
          if (Idx >= Config::num_faders)
133
              return;
134
          else if (Idx == 0)
135
               ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
                   sbi(TCCR2A, COM2B0);
sbi(PORTD, 2);
136
137
                   OCR2B = speed;
138
139
               }
140
          else if (Idx == 1)
               ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
141
                   sbi(TCCR2A, COM2A0);
sbi(PORTD, 7);
142
143
144
                   OCR2A = speed;
145
146
          else if (Idx == 2)
               ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
147
                   sbi(TCCR0A, COM0B0);
sbi(PORTD, 4);
148
149
150
                   OCROB = speed;
151
152
          else if (Idx == 3)
               ATOMIC_BLOCK(ATOMIC_RESTORESTATE) {
153
                   sbi(TCCR0A, COM0A0);
if (Config::fader_3_A2)
154
155
156
                        sbi(PORTC, 2);
                   else
157
                       sbi(PORTB, 5);
158
                   OCROA = speed;
159
160
               }
     }
163
      template <class Config>
164
      template <uint8_t Idx>
165
      inline void Motors<Config>::setSpeed(int16_t speed) {
          if (speed >= 0)
166
167
               forward<Idx>(speed);
168
          else
               backward<Idx>(-speed);
169
170
     }
```

Reference.hpp

```
#include <avr/pgmspace.h>
  #include <stddef.h>
  #include <stdint.h>
  #include <util/atomic.h>
5
6
  /// Reference signal for testing the controller.
  const uint8_t reference_signal[] PROGMEM = {
     // Ramp up
8
     0, 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, 19, 20,
     21, 22, 23, 24, 25, 26, 27, 28, 29, 30, 31, 32, 33, 34, 35, 36, 37, 38, 39,
     40, 41, 42, 43, 44, 45, 46, 47, 48, 49, 50, 51, 52, 53, 54, 55, 56, 57,
     59, 60, 61, 62, 63, 64, 65, 66, 67, 68, 69, 70, 71, 72, 73, 74, 75, 76,
12
13
     78, 79, 80, 81, 82, 83, 84, 85, 86, 87, 88, 89, 90, 91, 92, 93, 94, 95,
14
     97, 98, 99, 100, 101, 102, 103, 104, 105, 106, 107, 108, 109, 110, 111, 112
15
     113, 114, 115, 116, 117, 118, 119, 120, 121, 122, 123, 124, 125, 126, 127,
16
     128, 129, 130, 131, 132, 133, 134, 135, 136, 137, 138, 139, 140, 141, 142,
     143, 144, 145, 146, 147, 148, 149, 150, 151, 152, 153, 154, 155, 156, 157,
17
     158, 159, 160, 161, 162, 163, 164, 165, 166, 167, 168, 169, 170, 171, 172,
18
19
     173, 174, 175, 176, 177, 178, 179, 180, 181, 182, 183, 184, 185, 186, 187,
20
     188, 189, 190, 191, 192, 193, 194, 195, 196, 197, 198, 199, 200, 201, 202,
     203, 204, 205, 206, 207, 208, 209, 210, 211, 212, 213, 214, 215, 216, 217,
21
                                        229,
22
     218, 219, 220, 221, 222, 223, 224,
                           225, 226, 227, 228,
                                            230, 231,
                                                  232,
23
     233, 234, 235, 236, 237, 238, 239, 240, 241, 242, 243, 244, 245, 246, 247,
24
     248, 249, 250, 251, 252, 253, 254, 255,
25
     // Max
     26
27
     // Ramp down
28
     255, 254, 253, 252, 251, 250, 249, 248, 247, 246, 245, 244, 243, 242, 241,
29
     240, 239, 238, 237, 236, 235, 234, 233, 232, 231, 230, 229, 228, 227, 226,
     225, 224, 223, 222, 221, 220, 219, 218, 217, 216, 215, 214, 213, 212, 211,
30
31
     210, 209, 208, 207, 206, 205, 204, 203, 202, 201, 200, 199, 198, 197, 196,
     195, 194, 193, 192, 191, 190, 189, 188, 187, 186, 185, 184, 183, 182, 181,
32
33
     180, 179, 178, 177, 176, 175, 174,
                           173, 172, 171, 170, 169, 168, 167, 166,
34
     165, 164, 163, 162, 161, 160, 159, 158, 157, 156, 155, 154, 153, 152, 151,
     150, 149, 148, 147, 146, 145, 144, 143, 142, 141, 140, 139, 138, 137, 136,
35
     135, 134, 133, 132, 131, 130, 129, 128, 127,
36
     // Middle
37
     38
     39
40
41
     127, 127, 127,
42
43
     // Jump low
44
     45
46
47
     10, 10, 10, 10, 10, 10, 10,
48
     // Jump middle
49
     51
52
     53
     127, 127, 127, 127,
54
     // Jump high
     55
     56
     245, 245, 245, 245,
59
60
     // Jump middle
     61
62
     63
     64
     65
     66
67
     69
70
71
     // Ramp down
     127, 126, 125, 124, 123, 122, 121, 120, 119, 118, 117, 116, 115, 114, 113, 112, 111, 110, 109, 108, 107, 106, 105, 104, 103, 102, 101, 100, 99, 98, 97,
72
73
     96, 95, 94, 93, 92, 91, 90, 89, 88, 87, 86, 85, 84, 83, 82, 81, 80, 79, 78, 77, 76, 75, 74, 73, 72, 71, 70, 69, 68, 67, 66, 65, 64, 63, 62, 61, 60, 59,
74
                                                   78.
75
76
     58, 57, 56, 55, 54, 53, 52, 51, 50, 49, 48, 47, 46, 45, 44, 43, 42, 41, 40,
     39, 38, 37, 36, 35, 34, 33, 32, 31, 30, 29, 28, 27, 26, 25, 24, 23, 22, 21,
77
       19, 18, 17, 16, 15, 14, 13, 12, 11, 10, 9, 8, 7, 6, 5, 4, 3, 2, 1,
78
     20,
79
80
             81
     0.
         82
     Θ,
       83
     Θ.
         0, 0,
             0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0,
                                        Θ.
                                          Θ.
84
     0.0.
         85
             0, 0, 0, 0, 0, 0, 0, 0,
                            0.0.
                                0.
                                  0.0.
             87
             88
     0, 0, 0, 0,
89
             0, 0,
90
     0, 0, 0, 0, 0, 0};
91
  /// Get the number of elements in the given array.
```

```
93 | template <class T, size_t N>
     constexpr size_t len(const T (&)[N]) {
 94
 95
          return N;
 96
 97
 98
     /// Class that handles the three main references/setpoints:
     ///
 99
            1. Sequence programmed in PROGMEM
100
     111
            2. Test sequence for tuning experiments (activated over Serial)
      /// 3. Setpoint set by the I^2C master (for real-world use) template <class Config>
101
102
103
      class Reference {
        public:
104
105
          /// Called from ISR
          void setMasterSetpoint(uint16_t setpoint) {
106
107
               this->master_setpoint = setpoint;
108
109
          void startExperiment(float speed_div) {
110
               this->experiment_speed_div = speed_div;
111
               this->index = 0;
112
113
               this->seq_idx = 0;
          }
115
116
          bool experimentInProgress() const { return experiment_speed_div > 0; }
117
118
          uint16_t getNextProgmemSetpoint() {
119
               uint16_t setpoint = pgm_read_byte(reference_signal + index) * 4;
120
               ++seq idx;
               if (seq_idx >= Config::test_reference_speed_div) {
121
122
                   seq_idx = 0;
123
                   ++index;
124
                   if (index == len(reference_signal)) index = 0;
125
126
               return setpoint;
127
          }
128
          uint16_t getNextExperimentSetpoint() {
129
              constexpr uint16_t RAMPUP = 0xFFFF;
auto rampup = [](uint16_t idx, uint16_t duration) {
130
131
132
                   return uint32_t(1024) * idx / duration;
134
               constexpr uint16_t RAMPDOWN = 0xFFFE;
135
               auto rampdown = [&](uint16_t idx, uint16_t duration) {
136
                   return 1023 - rampup(idx, duration);
137
               struct TestSeq {
138
139
                   uint16_t setpoint;
                   uint16_t duration;
140
141
               // This array defines the test sequence
142
               constexpr static TestSeq seqs[] {
143
144
                    \{0,\ 256\}, \quad \{\text{RAMPUP},\ 128\},\ \{1023,\ 32\},\ \{0,\ 64\}, 
                                                                          {333, 32},
145
                   {666, 32}, {333, 32},
                                                {0, 32},
                                                             {512, 256},
146
147
148
              static uint8_t seq_index = 0;
static uint16_t index = 0;
149
150
               uint16_t duration = seqs[seq_index].duration * experiment_speed_div;
151
               uint16_t seq_setpoint = seqs[seq_index].setpoint;
152
               uint16_t setpoint;
153
               switch (seq_setpoint) {
                   case RAMPUP: setpoint = rampup(index, duration); break;
case RAMPDOWN: setpoint = rampdown(index, duration); break;
154
155
156
                   default: setpoint = seq_setpoint;
157
               ++index;
158
              if (index == duration) {
159
                   index = 0;
160
                   ++seq_index;
161
                   if (seq_index == len(seqs)) {
162
                        seq_index = 0;
163
164
                        experiment_speed_div = 0;
165
                   }
166
167
               return setpoint;
168
          }
169
170
          /// Called from main program with interrupts enabled
171
          uint16_t getNextSetpoint() {
               uint16_t setpoint;
172
173
               if (Config::serial_control && experiment_speed_div > 0)
174
                   // from the tuning experiment reference
175
                   setpoint = getNextExperimentSetpoint();
176
               else if (Config::test_reference)
                   \ensuremath{//} from the test reference
177
178
                   setpoint = getNextProgmemSetpoint();
179
180
                   // from the I<sup>2</sup>C master
                   ATOMIC_BLOCK(ATOMIC_FORCEON) { setpoint = master_setpoint; }
181
182
               return setpoint;
183
          }
184
        private:
185
          uint16 t index = 0:
186
187
          uint8_t seq_idx = 0;
```

```
float experiment_speed_div = 0;
volatile uint16_t master_setpoint = 0;

};
```

Registers.hpp

```
#pragma once
    #include <avr/io.h>
3
    #include <avr/sfr_defs.h>
    #include <util/delay.h> // F_CPU
5
6
    // ------ Utils ----- //
8
    #ifndef ARDUINO // Ensures that my IDE sees the correct frequency
    #undef F_CPU
10
    #define F_CPU 16000000UL
    #endif
12
13
14
    #ifndef sbi
    /// Set bit in register.
15
16
    template <class R>
    inline void sbi(R &reg, uint8_t bit) {
17
        reg |= (1u << bit);
18
19
20
    #define sbi sbi
    #endif
#ifndef cbi
21
22
23
    /// Clear bit in register.
    template <class R>
24
25
    inline void cbi(R &reg, uint8_t bit) {
        reg &= ~(1u << bit);
26
27
28
    #define cbi cbi
29
30
    /// Write bit in register.
31
    template <class R>
32
    inline void wbi(R &reg, uint8_t bit, bool value) {
33
        value ? sbi(reg, bit) : cbi(reg, bit);
34
35
    // ----- Timer0 ----- //
36
37
    /// Timer 0 clock select (Table 14-9).
38
    enum class TimerOPrescaler : uint8_t {
   None = 0b000,
39
40
        S1 = 0b001,

S8 = 0b010,
41
42
43
        S64 = 0b011
        S256 = 0b100
44
        S1024 = 0b101,
ExtFall = 0b110,
45
46
47
        ExtRise = 0b111,
48
        Invalid = 0xFF,
49
   };
51
    /// Timer 0 waveform generation mode (Table 14-8).
52
    enum class TimerOWGMode : uint8_t {
        Normal = 00000.
53
54
        PWM = 0b001,
        CTC = 0b010,
55
        FastPWM = 0b011,
56
        PWM_OCRA = 0b101
57
        FastPWM_OCRA = 0b111,
58
59
    };
60
61
    // Convert the prescaler factor to the correct bit pattern to write to the
62
    // TCCROB register (Table 14-9).
    constexpr inline TimerOPrescaler factorToTimerOPrescaler(uint16_t factor) {
63
       64
               : factor == 8
                                ? TimerOPrescaler::S8
65
               : factor == 64 ? TimerOPrescaler::S64
66
               : factor == 256 ? TimerOPrescaler::S256
67
               : factor == 1024 ? TimerOPrescaler::S1024
68
69
                                : Timer@Prescaler::Invalid;
70
    }
71
    /// Set the clock source/prescaler of Timer0 (Table 14-9).
72
    inline void setTimerOPrescaler(TimerOPrescaler ps) {
73
       if (ps == Timer0Prescaler::Invalid)
    return;
74
75
        wbi(TCCR0B, CS02, static_cast<uint8_t>(ps) & (1u << 2));</pre>
76
77
        wbi(TCCR0B, CS01, static_cast<uint8_t>(ps) & (1u << 1));
78
        wbi(TCCR0B, CS00, static_cast<uint8_t>(ps) & (1u << 0));
79
80
    /// Set the wavefrom generation mode of Timer0 (Table 14-8).
81
    inline void setTimerOWGMode(TimerOWGMode mode) {
82
        wbi(TCCR0B, WGM02, static_cast<uint8_t>(mode) & (1u << 2));
wbi(TCCR0A, WGM01, static_cast<uint8_t>(mode) & (1u << 1));</pre>
83
84
85
        wbi(TCCROA, WGMOO, static_cast<uint8_t>(mode) & (1u << 0));</pre>
86
87
88
    // ----- Timer2 ----- //
89
90
    /// Timer 0 clock select (Table 17-9).
91
    enum class Timer2Prescaler : uint8_t {
        None = 0b000,
92
```

```
93
          S1 = 0b001,
 94
          S8 = 0b010,
 95
          S32 = 0b011,
 96
          S64 = 0b100,
 97
          S128 = 0b101
 98
          S256 = 0b110
 99
          S1024 = 0b111
          Invalid = 0xFF,
100
101
     };
102
103
      /// Timer 0 waveform generation mode (Table 17-8).
      enum class Timer2WGMode : uint8_t {
104
          Normal = 0b000,
105
          PWM = 0b001,
106
107
          CTC = 0b010,
          FastPWM = 0\dot{b}011,
108
          PWM OCRA = 0b101
109
          FastPWM_OCRA = 0b111,
110
     };
111
112
      \ensuremath{//} Convert the prescaler factor to the correct bit pattern to write to the
113
      // TCCROB register (Table 17-9).
115
      constexpr inline Timer2Prescaler factorToTimer2Prescaler(uint16_t factor) {
116
          return factor == 1
                                   ? Timer2Prescaler::S1
                                    ? Timer2Prescaler::S8
? Timer2Prescaler::S32
117
                  : factor == 8
118
                  : factor == 32
                  : factor == 64 ? Timer2Prescaler::S64
: factor == 128 ? Timer2Prescaler::S128
119
120
                  : factor == 256 ? Timer2Prescaler::S256
121
122
                  : factor == 1024 ? Timer2Prescaler::S1024
123
                                    : Timer2Prescaler::Invalid;
124
      /// Set the clock source/prescaler of Timer2 (Table 17-9).
126
      inline void setTimer2Prescaler(Timer2Prescaler ps) {
127
          if (ps == Timer2Prescaler::Invalid)
128
129
              return;
          wbi(TCCR2B, CS22, static_cast<uint8_t>(ps) & (1u << 2));
wbi(TCCR2B, CS21, static_cast<uint8_t>(ps) & (1u << 1));</pre>
130
131
132
          wbi(TCCR2B, CS20, static_cast<uint8_t>(ps) & (1u << 0));
133
     }
134
135
      /// Set the wavefrom generation mode of Timer2 (Table 17-8).
      inline void setTimer2WGMode(Timer2WGMode mode) {
   wbi(TCCR2B, WGM22, static_cast<uint8_t>(mode) & (1u << 2));</pre>
136
137
          wbi(TCCR2A, WGM21, static_cast<uint8_t>(mode) & (1u << 1));</pre>
138
          wbi(TCCR2A, WGM20, static_cast<uint8_t>(mode) & (1u << 0));
139
140
     }
141
     // ------//
142
143
144
      /// ADC prescaler select (Table 23-5).
145
      enum class ADCPrescaler : uint8_t {
146
          S2 = 0b000
147
          S2 2 = 0b001
          S4 = 0b010
148
          S8 = 0b011,
149
150
          S16 = 0b100,
          S32 = 0b101,
151
152
          S64 = 0b110,
          S128 = 0b111
153
          Invalid = 0xFF,
154
155
     };
156
157
     // Convert the prescaler factor to the correct bit pattern to write to the
     // ADCSRA register (Table 23-5).
158
159
      constexpr inline ADCPrescaler factorToADCPrescaler(uint8_t factor) {
          return factor == 2
                                   ? ADCPrescaler::S2_2
160
                  : factor == 4
                                   ? ADCPrescaler::S4
161
                  : factor == 8 ? ADCPrescaler::S8
162
                  : factor == 16 ? ADCPrescaler::S16
163
164
                  : factor == 32 ? ADCPrescaler::S32
165
                  : factor == 64 ? ADCPrescaler::S64
                  : factor == 128 ? ADCPrescaler::S128
166
                                   : ADCPrescaler::Invalid:
167
168
     }
169
170
      /// Set the prescaler of the ADC (Table 23-5).
171
     inline void setADCPrescaler(ADCPrescaler ps) {
172
          if (ps == ADCPrescaler::Invalid)
173
              return;
174
          wbi(ADCSRA, ADPS2, static_cast<uint8_t>(ps) & (1u << 2));</pre>
175
          \label{local_static} wbi(ADCSRA, \ ADPS1, \ static\_cast < wint8\_t > (ps) \ \& \ (1u << 1));
176
          wbi(ADCSRA, ADPS0, static_cast<uint8_t>(ps) & (1u << 0));</pre>
     }
177
```

SerialSLIP.hpp

```
#include <Arduino.h>
 3
    namespace SLIP_Constants {
    const static uint8_t END = 0300;
const static uint8_t ESC = 0333;
 5
    const static uint8_t ESC_END = 0334;
 6
    const static wint8 t ESC ESC = 0335:
    } // namespace SLIP Constants
 8
 9
    /// Parses SLIP packets: https://datatracker.ietf.org/doc/html/rfc1055
10
11
    class SLIPParser {
12
      public:
13
         template <class Callback>
14
         size_t parse(uint8_t c, Callback callback);
15
         void reset() {
    size = 0;
16
17
             escape = false;
18
19
20
21
22
         size_t size = 0;
         bool escape = false;
23
24
25
     template <class Callback>
26
    size_t SLIPParser::parse(uint8_t c, Callback callback) {
27
28
         using namespace SLIP_Constants;
29
          * handle bytestuffing if necessary
30
31
32
         switch (c) {
             /* * if it's an END character then we're done with
33
34
              * the packet
35
36
37
             case END: {
                 /*
 * a minor optimization: if there is no
38
39
                   * data in the packet, ignore it. This is
40
                   * meant to avoid bothering IP with all
41
                   ^{\ast} the empty packets generated by the
42
                   * duplicate END characters which are in
43
                   * turn sent to try to detect line noise.
44
45
                  auto packetLen = size;
46
47
                  reset();
48
                  if (packetLen) return packetLen;
49
             } break;
51
              ^{\star} if it's the same code as an ESC character, wait
52
              * and get another character and then figure out
53
              * what to store in the packet based on that.
54
55
             case ESC: {
    escape = true;
56
57
58
             } break;
59
60
              ^{^{\prime}} ^{\star} here we fall into the default handler and let
61
              * it store the character for us
62
63
             default: {
64
65
                  if (escape) {
                      /*

* if "c" is not one of these two, then we
66
67
                       * have a protocol violation. The best bet
                       * seems to be to leave the byte alone and
69
                       * just stuff it into the packet
70
71
                      switch (c) {
   case ESC_END: c = END; break;
72
73
                           case ESC_ESC: c = ESC; break;
74
75
                           default: break; // LCOV_EXCL_LINE (protocol violation)
76
77
                      escape = false;
78
79
                  callback(c, size);
80
                  ++size;
             }
81
         }
82
83
         return 0:
    }
84
85
86
    /// Sends SLIP packets: https://datatracker.ietf.org/doc/html/rfc1055  
    class SLIPSender {
87
      public:
88
89
         SLIPSender(Stream &stream) : stream(stream) {}
90
         size_t beginPacket() { return stream.write(SLIP_Constants::END); }
size_t endPacket() { return stream.write(SLIP_Constants::END); }
91
92
```

```
93
           size_t write(const uint8_t *data, size_t len);
size_t writePacket(const uint8_t *data, size_t len) {
 94
 95
 96
                 size_t sent = \hat{0};
 97
                 sent += beginPacket();
 98
                 sent += write(data, len);
                 sent += endPacket();
 99
100
                return sent;
101
102
103
         private:
104
           Stream &stream;
105
106
      inline size_t SLIPSender::write(const uint8_t *data, size_t len) {
    // https://datatracker.ietf.org/doc/html/rfc1055
107
108
109
            using namespace SLIP_Constants;
110
            size_t sent = 0;
           /*

* for each byte in the packet, send the appropriate character
111
112
            * sequence
113
115
           while (len--) {
116
                switch (*data) {
                    /*

* if it's the same code as an END character, we send a
117
118
                      * special two character code so as not to make the 
* receiver think we sent an END
119
120
121
                     case END:
122
123
                          sent += stream.write(ESC);
124
                          sent += stream.write(ESC_END);
125
                          break;
126
127
                      * if it's the same code as an ESC character,

* we send a special two character code so as not
128
129
                      * to make the receiver think we sent an ESC
130
131
132
                     case ESC:
                          sent += stream.write(ESC);
134
                           sent += stream.write(ESC_ESC);
135
                          break;
136
                     /*
 * otherwise, we just send the character
 */
 * stream.write(*data);
137
138
139
140
141
142
                data++;
143
144
            return sent;
145
      }
```

SMA.hpp

```
#pragma once
    #include <Arduino_Helpers.h>
    #include <AH/Math/Divide.hpp>
5
    #include <AH/STL/algorithm> // std::fill
6
    #include <AH/STL/cstdint>
8
9
     * @brief Simple Moving Average filter.
10
12
       Returns the average of the N most recent input values.
13
14
       y[n] = \frac{1}{N} \sum_{i=0}^{N-1} x[n-i]
15
16
       @f]
17
                https://tttapa.github.io/Pages/Mathematics/Systems-and-Control-Theory/Digital-
       @see
18
    filters/Simple%20Moving%20Average/Simple-Moving-Average.html
19
20
                The number of samples to average.
21
       @tparam input_t
22
                The type of the input (and output) of the filter.
23
       @tparam sum t
24
                The type to use for the accumulator, must be large enough to fit
25
                N times the maximum input value.
26
27
28
    template <uint8_t N, class input_t = uint16_t, class sum_t = uint32_t>
29
    class SMA {
30
      public:
31
        /// Default constructor (initial state is initialized to all zeros).
32
33
        SMA() = default;
34
        /// Constructor (initial state is initialized to given value).
35
        ///
        /// @param initialValue
36
37
                    Determines the initial state of the filter:
        ///
                    @f$ x[-N] = \\ ldots = x[-2] = x[-1] = \\ text{initialValue} @f$
38
39
        SMA(input\_t\ initialValue)\ :\ sum(N\ *\ (sum\_t)initialValue)\ \{
40
            std::fill(std::begin(previousInputs), std::end(previousInputs),
41
42
                       initialValue);
43
        }
44
45
        /// Update the internal state with the new input (0, 1)^2 = 0
        /// new output @f$ y[n] @f$.
46
47
48
        /// @param input
49
                    The new input @f$ x[n] @f$.
        /// @return The new output @f$ y[n] @f$.
50
        input_t operator()(input_t input) {
51
52
            sum -= previousInputs[index];
            sum += input;
53
            previousInputs[index] = input;
54
            if (++index == N) index = 0;
55
            return AH::round_div<N>(sum);
57
58
59
      private:
60
        uint8_t index = 0;
61
        input_t previousInputs[N] {};
        sum t sum = 0:
62
    };
63
```