

Google Summer of Code 2019 Proposal: Digital Pre-Distortion (DPD) for GNU Radio

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1 Objective

Power amplifiers (PA) are a necessary element for signal broadcasting. However, device physics tells us that power amplifiers can never be perfectly linear. Power amplifiers and other non-linear components can introduce spectral leakage which is regulated by the FCC to avoid the leakage of energy onto adjacent channels. This is illustrated in Figure 1. Here, a signal is shown in the frequency domain as being the clean "main signal" rectangle. After being broadcast through the nonlinear PA, a skirt of leakage energy forms around the signal.

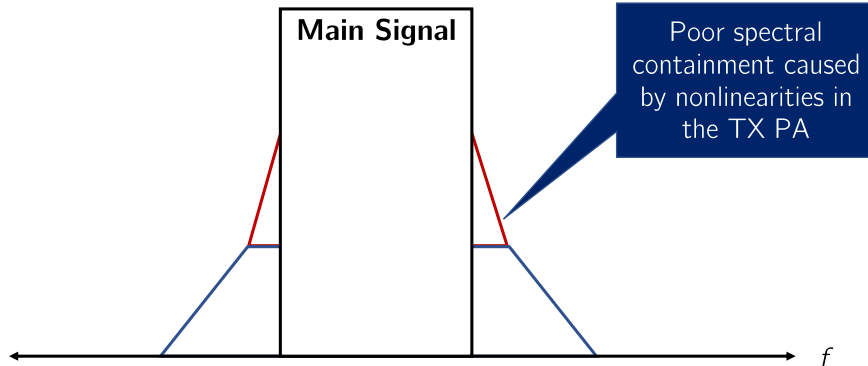


Figure 1: The effect of nonlinearities in the PA.

Typically PAs have a linear region towards the lower end of their power rating, however restricting input signals to this linear region would be expensive due to the significant loss in power efficiency and extra expense of higher power rating PA. The open source software suite GNU Radio currently lacks a core block to deal with this issue, which is what this project aims to fix via implementation of a generalized Digital Pre-Distortion (DPD) block. Specifically, I hope to implement the popular and highly cited method of predistorting with a memory polynomial via an indirect learning architecture using least-squares learning [2].

The remainder of this proposal is organized as follows. In Section 2, I provide background information for DPD including the common models, architectures, and learning algorithms. In Section 3, I provide information on the expected deliverables of the project followed by an approximate timeline for the project in Section 4. Section 5 discusses who I am, and why I am a good candidate for the project. Section 6 concludes the proposal.

2 Background

2.1 Digital Pre-Distortion

Power amplifiers are imperfect in the sense that scaling the input will not always linearly scale the output. As the input is increased the scaling factor will inevitably decay as the device saturates, and increasing this linearity through higher quality amplifiers is very expensive. Thus one must either limit the input signal to contain the signal to the linear region of the power amplifier, or live with the effects. **Digital Pre-Distortion** is the technique of dealing with these effects by distorting the input such that after going through the PA the output will be as if the original input was multiplied by a linear gain factor. However, what function the input should be distorted by is not known; thus any DPD technique must learn this function. The scheme I plan to implement starts with the concept of a memory polynomial.

2.2 Memory Polynomial

The predistorter which distorts the input signal accordingly is typically a nonlinear function that acts as an inverse of the PA's transfer function. However, this function is not known, thus a combination of memory modeling with polynomials is a common method to learn it. One of the most common configurations is the Parallel-Hammerstein model, also known as a memory polynomial. Below is a figure showing a single branch of such a memory polynomial with a memory depth of N , accounting for memory effects in the polynomial function which we are trying to model.

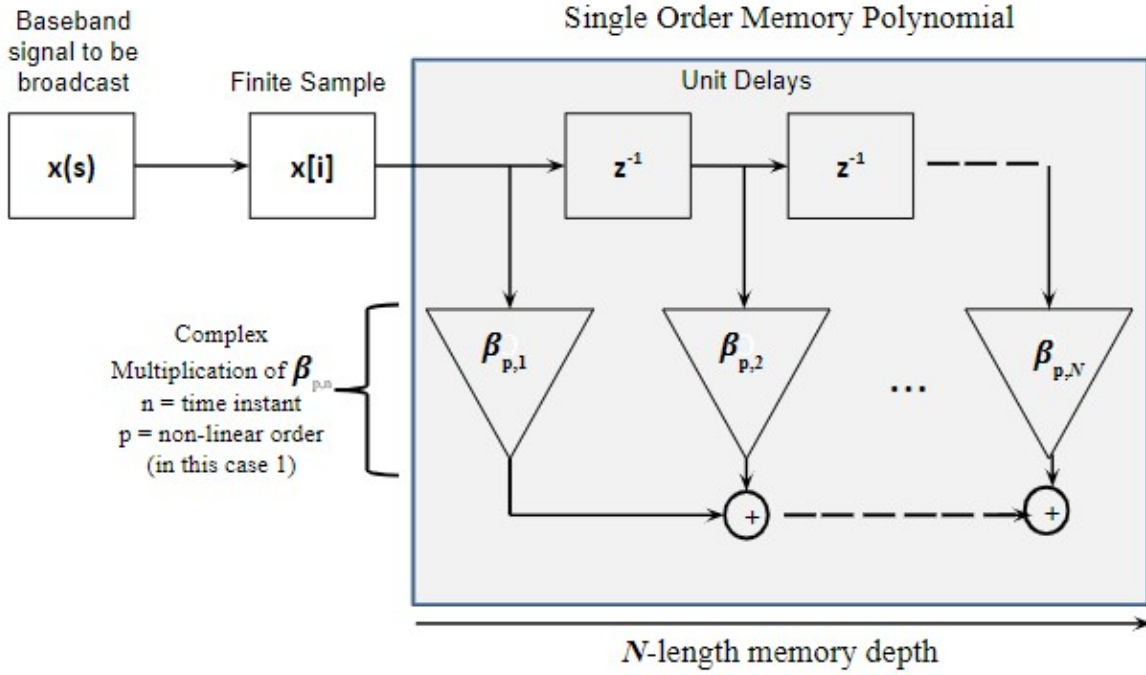


Figure 2: Single Polynomial branch of a Memory Polynomial

Then the "parallel" portion of the model can be thought of as having multiple polynomial "branches" followed by finite-impulse-response (FIR) filters to account for the multiple non-linear orders of polynomial being modelled.

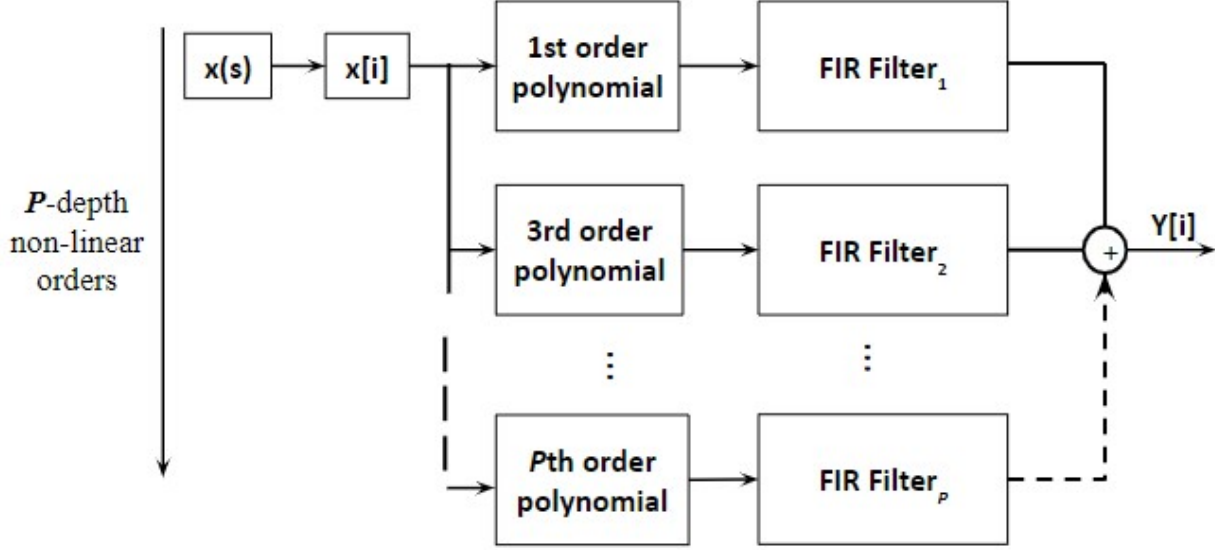


Figure 3: Parallel-Hammerstein Structure of a Memory Polynomial

This is also shown mathematically in equation 1. Here, $x(n)$ is a baseband signal to be broadcast, and $y(n)$ is the output of the memory polynomial based predistorter. P is the maximum polynomial degree considered, and M is the maximum memory depth considered. The symbols i and j index the polynomial degree and depth respectively. In most systems, only odd degrees are considered since the odd degrees contribute to in-band distortions, so i can be restricted to only index even numbers. The coefficient β is a complex scalar.

$$y(n) = \sum_{i=0}^{P-1} \sum_{j=0}^{M-1} \beta_{i,j} x(n-j) |x(n-j)|^i \quad (1)$$

2.3 Indirect Learning Architecture

Now with the memory polynomial technique of modelling a function to predistort by, there is still a fundamental problem with designing a predistorter where the ideal input to the PA is unknown. Hence, we can not create a DPD model from some sort of fit of input/output data. One popular way to overcome this is an Indirect Learning Architecture (ILA) shown in Figure 4.

In an ILA, the output of the PA, $y(n)$ is fed into a postdistorter that is equivalent to the predistorter. If the PA output is the ideal, linear output that we desire, then after we remove the gain, G , the input to the training distorter would be equal to the input of the predistorter, $x(n)$. The output of these blocks, $z(n)$ and $\hat{z}(n)$, should therefore be equal and the error would be zero.

This relationship can be leveraged for the learning of a predistorter. For the error to be zero, $\hat{z}(n) = z(n)$. For a use of the PA where we have the output, $y(n)$, this gives us an input/output relationship around the training distorter. We can use this to perform a fit to a DPD structure such as a memory polynomial through a technique such as least squares which will be discussed in the following section.

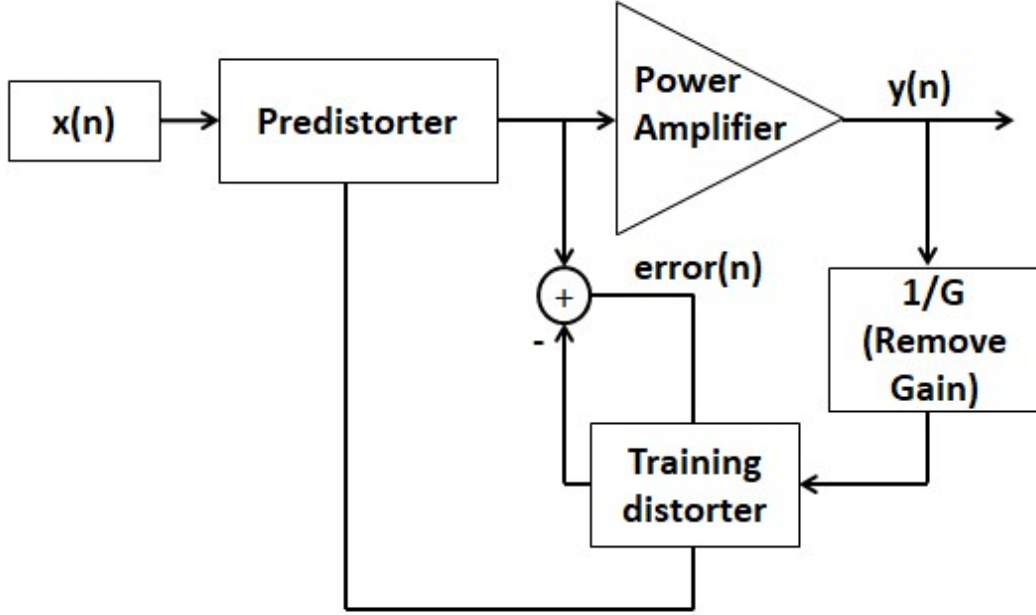


Figure 4: Indirect Learning Architecture Schematic

2.4 Least Squares Learning of Coefficients

Although the indirect learning architecture from the previous section provides an intuitive way to perform learning, the actual learning algorithm may be independent of the architecture. One common way to do this is by performing a least-squares fit.

One major advantage of the memory polynomial DPD structure is that the output is linear with respect to the coefficients, β . To illustrate this, let us consider the signal $x(n)$ to be a vector of length N . An equivalent representation of the memory polynomial would then be as follow where for brevity, we consider only up to polynomial degree $P = 3$ and memory depth $M = 2$.

$$\begin{bmatrix} y(0) \\ y(1) \\ \vdots \\ y(N-1) \end{bmatrix} = \begin{bmatrix} x(0) & 0 & x(0)|x(0)|^2 & 0 \\ x(1) & x(0) & x(1)|x(1)|^2 & x(0)|x(0)|^2 \\ \vdots & \vdots & \vdots & \vdots \\ x(N-1) & x(N-2) & x(N-1)|x(N-1)|^2 & x(N-2)|x(N-2)|^2 \end{bmatrix} \begin{bmatrix} \beta_{1,1} \\ \beta_{1,2} \\ \beta_{3,1} \\ \beta_{3,2} \end{bmatrix} \quad (2)$$

This is shown more consisely below in Equation 3, where the signal y is denoted as a vector, the basis functions for the memory polynomial are columns of the matrix X , and the coefficients are in the vector β .

$$y = X\beta \quad (3)$$

This system of equations is overdetermined as there are N equations for four unknowns, thus there is no exact solution. Instead, we choose the β that minimizes the sum of the square residuals. This is known as the least squares solution. In essence, we want to choose the β such that it is as close as possible to the desired output of the system, y . This is shown below.

$$\beta = \arg \min_{\beta} ||y - X\beta||_2 \quad (4)$$

This has the following solution.

$$\beta = (X^H X)^{-1} X^H y \quad (5)$$

We can therefore use this formula over a block of samples to compute DPD coefficients to use in a memory polynomial.

3 Deliverables

Digital Pre-Distortion is not yet a standard part of GNU Radio. This project aims to introduce a new, well documented, module allowing for basic DPD usage in any project. Throughout the development process I will have access equipment here at Rice including USRP B200, a Pluto SDR, and an IRIS SDR to test all the components of my code and make sure they are working as intended.

- **Implement Memory Polynomial Model:** The first element to be implemented is the memory polynomial which will be used to model the function which distorts the input signal. Initially I plan to start with making a single memory polynomial branch up to the third degree, before moving on to building out the complete Parallel-Hammerstein model.
- **Implement Indirect learning DPD system:** With the Memory Polynomial up and running I will then be able to start building the indirect learning architecture which leverages the memory polynomial to be able to model the function
- **Implement Least-Squares Learning Algorithm:** The final step will be to implement a least squares fit algorithm which fits the memory polynomial coefficients to minimize the difference between the expected and actual outputs over a large number of samples.
- **Debugging and Testing Tools:** Build tools to allow the user (and me during development) to understand what is going on within the completed DPD blocks. Allow the user to access the memory polynomial function to see what the signal is being distorted by. Also add tools to evaluate performance such as comparing distorted to un-distorted signals to show the improvement. Moreover, I'll create tools to monitor the convergence of DPD coefficients and view the AM/AM curve for a PA.
- **Documentation:** I will keep all code readable and well documented so that the final DPD code block for GNU Radio is easy to use and understand.

4 Timeline

I am a rising junior at Rice University studying Electrical Engineering and Computer Science. There is a group working on DPD here at Rice (see authors of [1]), who sparked my interest in this project - and will serve as great resources for me should this project be approved. Furthermore they will meet with me weekly to discuss the project and help me with any issues.

- Mid May: Further familiarity with GNU Radio structure, module development, and DPD.
- End of May: Initial single degree implementation of a PA model
- Beginning of June: Complete Parallel Hammerstein memory polynomial model
- First Phase Evaluation: For the first evaluation I plan to have a working memory polynomial block which outputs the coefficients of the memory polynomial representing an input signal. Around this time I also plan to be starting to build this memory polynomial into an indirect learning architecture.
- Beginning of July: Indirect Learning Architecture and Least Square fitting
- Second Phase Evaluation: In the second phase evaluation I hope to have a complete working prototype of the final DPD block. While not a finished product I hope to show off its core functionality.
- Beginning of August: Final bug fixes further improvements to the learning algorithm. Possibly implement other learning systems more advanced than a basic least squares fit, time permitting.
- Final Evaluation: submission of final code

5 About Me

I really want the world to know: Cyberspectrum is the best spectrum. Other than that, I have always been interested in computers since I was a kid who quickly outstripped my parent's technological abilities when I learned how to Ctrl-Alt-Delete when a program froze on the screen. One of my goals for my university education is to gain an electron to executable understanding of how computers operate. This project was brought to my attention by a group working on DPD here at Rice, which quickly gained my interest as a great opportunity to merge the skills of signal processing and programming that I have been learning in my two programs of study EE and CS respectively. While I have not worked extensively with GNU Radio before, I feel I have more than enough experience to complete this project. I have worked extensively with Python and to a lesser extent C through my coursework here at Rice, and I have also taken two introductory courses in Signal Processing as well as Linear Algebra. These courses together form the basis of theory behind this project, and getting to also use and improve my programming skills is another bonus. Thus I think working on this project this summer would be a great opportunity for me, and I am confident that I can deliver a great feature for GNU Radio.

6 Conclusion

A DPD module would be a great addition to an already extensive set of signal processing tools in the GNU Radio suite. It would allow users to correct the imperfections introduced to their systems by power amplifiers and other non-linear components that they may not have been aware of. The spectral leakage introduced can be very problematic and is regulated by the FCC, thus accounting for it is essential to any project. This project has room to grow beyond this summer. I hope to see the larger community expand on this project to implement other learning algorithms and realted algorithms such as crest factor reduction. Although this is a short summer-of-code project, the deliverables of this project could be used to help hobbyists and researchers throughout the GNURadio community.

References

- [1] M. Abdelaziz et al. "Low-Complexity Subband Digital Predistortion for Spurious Emission Suppression in Noncontiguous Spectrum Access". In: *IEEE Transactions on Microwave Theory and Techniques* 64.11 (Nov. 2016), pp. 3501–3517. ISSN: 0018-9480. DOI: 10.1109/TMTT.2016.2602208.
- [2] Lei Ding et al. "A robust digital baseband predistorter constructed using memory polynomials". In: *IEEE Transactions on Communications* 52.1 (Jan. 2004), pp. 159–165. ISSN: 0090-6778. DOI: 10.1109/TCOMM.2003.822188.