

# LV inner Breakout Board --> Power Conns--(All DCB slot power routes and a few pigtail power routes)

JPL2	
JD11_JPL2_1V5_Master	1
JD11_JPL2_1V5_Slave	2
JD11_10_JPL2_2V5	3
JD10_JPL2_1V5_Master	4
JD10_JPL2_1V5_Slave	5
JP8_JPL2_P4_LV_SOURCE	6
JP11_JPL2_P3_LV_SOURCE	7
JP11_JPL2_P4_LV_SOURCE	8
GND	9
GND	10
JD9_JPL2_1V5_Master	11
JD9_JPL2_1V5_Slave	12
JD9_8_JPL2_2V5	13
JD8_JPL2_1V5_Master	14
JD8_JPL2_1V5_Slave	15
GND	16
GND	17
GND	18
GND	19
GND	20
JP8_JPL2_P4_LV_RETURN	21
JP11_JPL2_P3_LV_RETURN	22
JP11_JPL2_P4_LV_RETURN	23
GND	24
GND	25
GND	26
GND	27
GND	28
GND	29
GND	30

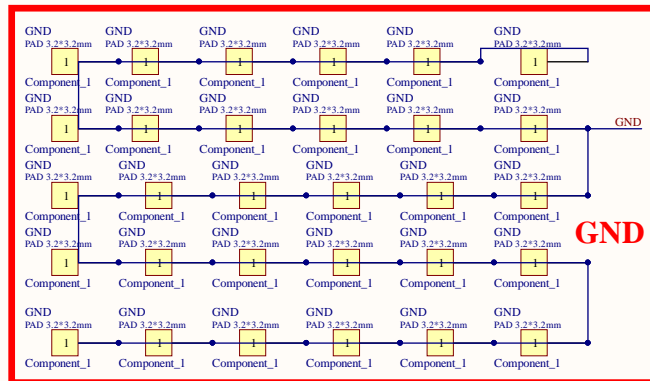
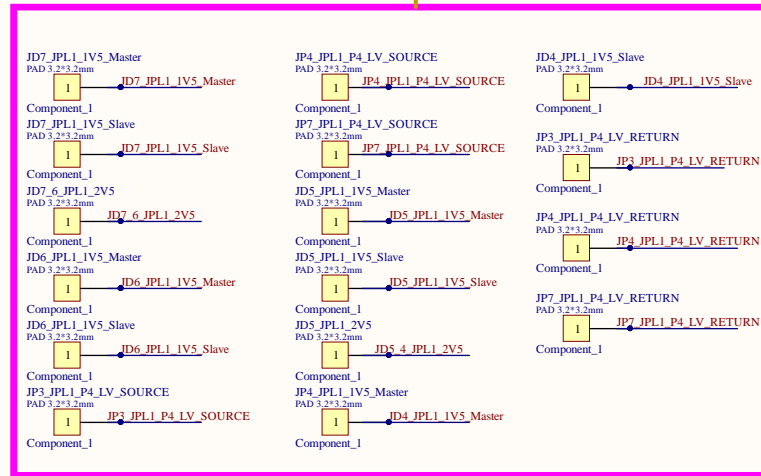
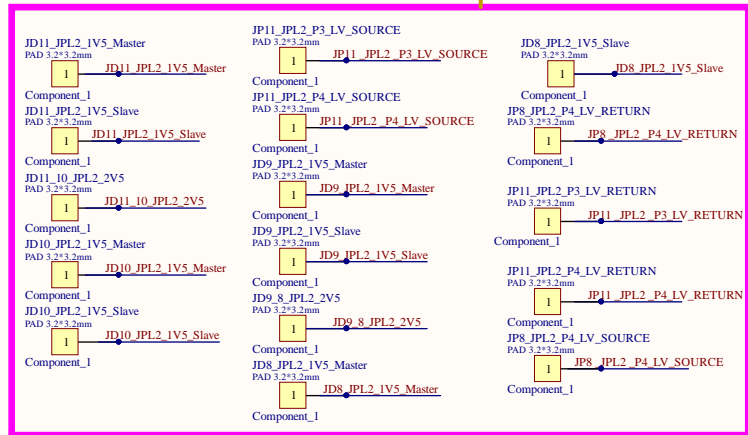
SOCKET PWR CONNECTOR

JPL1	
JD7_JPL1_1V5_Master	1
JD7_JPL1_1V5_Slave	2
JD7_6_JPL1_2V5	3
JD6_JPL1_1V5_Master	4
JD6_JPL1_1V5_Slave	5
JP3_JPL1_P4_LV_SOURCE	6
JP4_JPL1_P4_LV_SOURCE	7
JP7_JPL1_P4_LV_SOURCE	8
GND	9
GND	10
JD5_JPL1_1V5_Master	11
JD5_JPL1_1V5_Slave	12
JD5_4_JPL1_2V5	13
JD4_JPL1_1V5_Master	14
JD4_JPL1_1V5_Slave	15
GND	16
GND	17
GND	18
GND	19
GND	20
JP3_JPL1_P4_LV_RETURN	21
JP4_JPL1_P4_LV_RETURN	22
JP7_JPL1_P4_LV_RETURN	23
GND	24
GND	25
GND	26
GND	27
GND	28
GND	29
GND	30

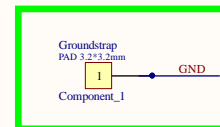
SOCKET PWR CONNECTOR

JPL0	
JD3_JPL0_1V5_Master	1
JD3_JPL0_1V5_Slave	2
JD3_2_JPL0_2V5	3
JD2_JPL0_1V5_Master	4
JD2_JPL0_1V5_Slave	5
JP0_JPL0_P4_LV_SOURCE	6
JP1_JPL0_P4_LV_SOURCE	7
JP2_JPL0_P4_LV_SOURCE	8
GND	9
GND	10
JD1_JPL0_1V5_Master	11
JD1_JPL0_1V5_Slave	12
JD1_0_JPL0_2V5	13
JD0_JPL0_1V5_Master	14
JD0_JPL0_1V5_Slave	15
GND	16
GND	17
GND	18
GND	19
GND	20
JP0_JPL0_P4_LV_RETURN	21
JP1_JPL0_P4_LV_RETURN	22
JP2_JPL0_P4_LV_RETURN	23
GND	24
GND	25
GND	26
GND	27
GND	28
GND	29
GND	30

SOCKET PWR CONNECTOR



Terminal for Ground Strap



Title			
LV_inner BREAKOUT BOARD, VERSION : FINAL			
Size	Number	Revision	
A3	MARIAPILAR PECO, MPIK	A	
Date:	12/8/2020	Sheet	of
File:	C:\Users\... \LV_PWRBreakoutBoard\2 Sch	Drawn By:	

