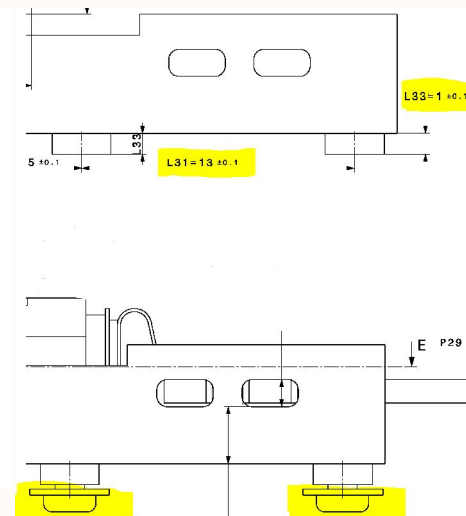
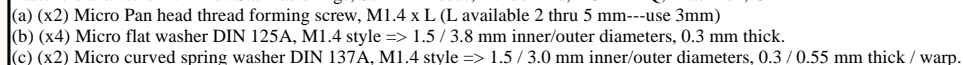
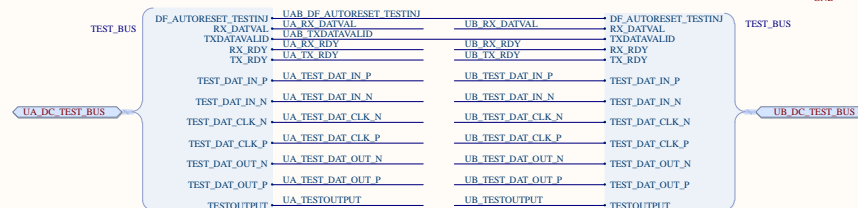


TestDatClk_P/N: Clock OUTPUTs of eport 32
TestDatOut_P/N: Data OUTPUTs of eport 32
DF_AUTO_RESET / TEST_INJ
TestOutput

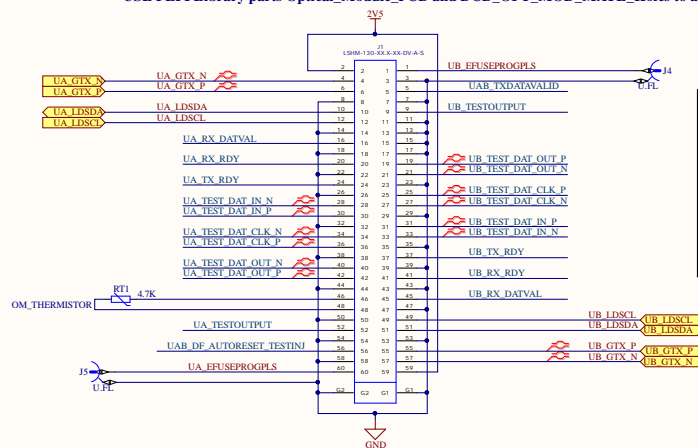
2.5V/100nF/L/47uF+0.1uF
L = 1uH to 4.7uH



TEST BUS* Label	MC VTTx Alt Signal Name
TESTOUTPUT	MC_RESETB
TEST_DAT_OUT_P	MC_I2C_SCL
TEST_DAT_OUT_N	MC_I2C_SDA
TEST_DAT_CLK_P	DC_RESETB
TEST_DAT_CLK_N	DC_I2C_SCL
TEST_DAT_IN_N	DC_I2C_SDA



USE PEPI Library parts Optical_Module_PCB and DCB_OPT_MOD_MATE. Holes to align hole features between assemblies.



Samtec Conn P/N:
LSHM-130-06.0-L-DV-A-S-K-TR

Title DCB Optical Mezzanine Board		
Size C	Number	Revision B
Date: 12/30/2019	Sheet 1 of	
File: Z:\windows\DCB OPT DB Schematic	Drawn By: Tom O'Bannon	