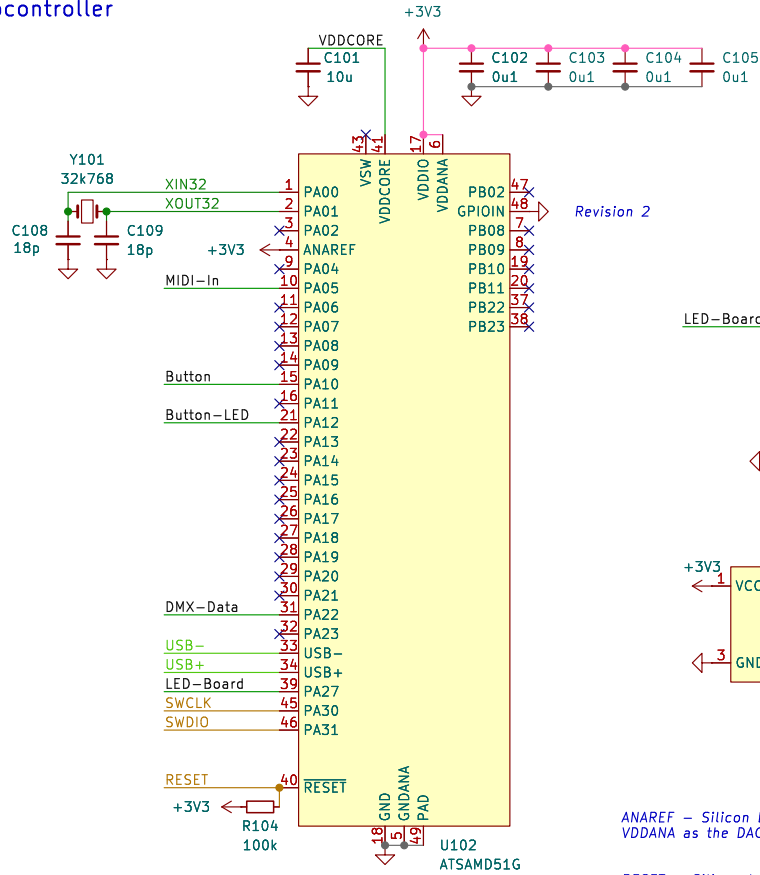
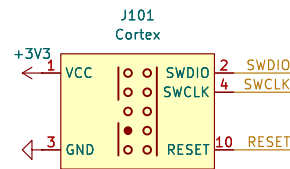
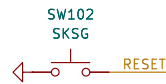


## Microcontroller



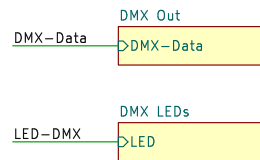
Revision 2



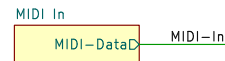
ANAREF - Silicon bug CHIP003-161  
VDDANA as the DAC Reference non-functional.

RESET - Silicon bug HIP003-159  
Internal pull-up non-functional.

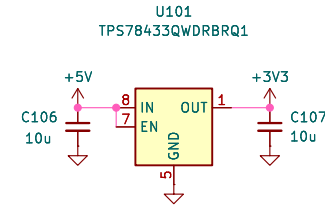
## DMX Out



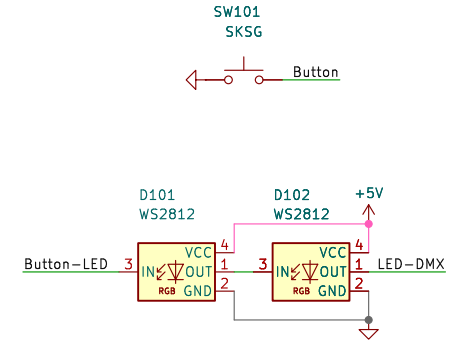
## MIDI In



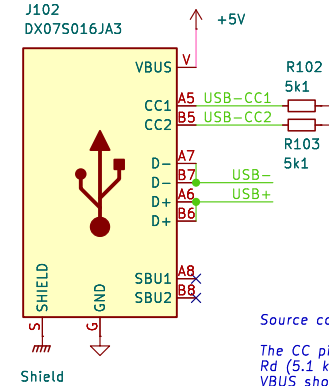
## Power +3V3



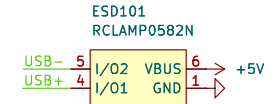
## Button



## USB



PWR\_FLAG  
PWR\_FLAG  
PWR\_FLAG



Source connected to a legacy device port.

The CC pins are pulled-down to GND  
Rd (5.1 kΩ ± 20%) to indicate that  
VBUS should be supplied by the host.

## Mechanical / Production

ICON102

V2

ICON104

dmx

INFO102

ORDER-NUMBER

SWB101

Button-bottom

INFO101

1 2 3 4

H101 MountingHole\_2mm  
H102 MountingHole\_2mm  
H103 MountingHole\_2mm  
H104 MountingHole\_2mm

ICON101

ICON103

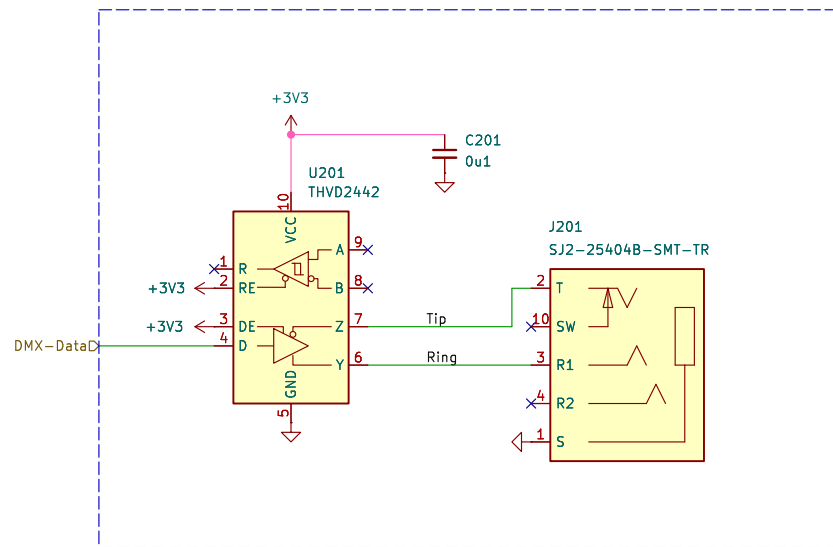
FID101

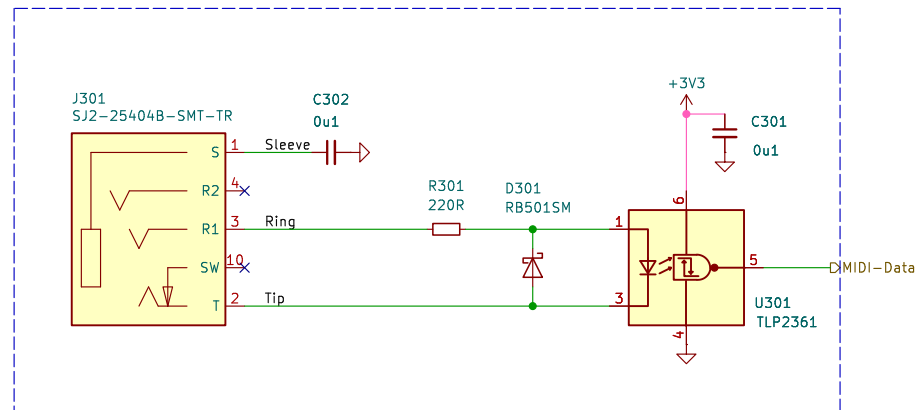
FID102

FID103

V2 dmx  
Versio Duo  
MIDI to DMX Bridge

Rev: 5  
Id: 1/4  
Sheet: Root  
Date: 2026-01-06





MIDI In

Rev: 3  
Id: 3/4  
Sheet: MIDI In  
Date: 2025-09-19



Rev: 1  
Id: 4/4  
Sheet: DMX LEDs  
Date: 2025-12-17