

FirstName Lastname
Institute name *****
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OBJECTIVE

To seek a Summer Internship under experienced guidance in an establishment of international recognition in the field of Electronics and Communication so as to hone my skills and attain excellent standards while meeting organizational needs.

EDUCATION

2002 - Present

Undergraduate Studies
Indian Institute of Technology Kharagpur, India

- Currently pursuing Master of Technology (Dual Degree) with **B.Tech. (Honours) in Electronics and Electrical Communication Engineering** and M.Tech. in Automation and Computer Vision, as a 3rd Year student from the Department of Electronics and Electrical Communication Engineering, IIT Kharagpur.
- Currently holding a **Cumulative Grade Point Average (CGPA)** of **9.46** on **10** after 5 Semesters
- Present **Institute Rank 2** among **200** Dual Degree students and placed within the **top 2%** of all students

2000 - 2002

Higher Secondary
D.G.Ruparel College of Science, Mumbai, Maharashtra, India

Scored 97.33% in Maths, Physics and Chemistry and 185/200 in Electronics (Vocational Course); and 91.00% overall in the Higher Secondary Certificate Examinations, March 2002.

1997 - 2000

Secondary School
Address

Scored **94.00%** aggregate in the Std.10 Indian State Certificate Examinations (ICSE), March 2000, in school among **168** students.

PROJECTS / SEMINARS

- May-July 2004 Successfully completed a **Computer Vision** and **Machine Learning** project entitled '**A Support Vector Machine and Wavelet-Based Approach to Detection of Alzheimer's Disease**' using **MR Brain Images**, under the guidance of **Prof. L. M. Patnaik**, Deptt. Of Computer Science & Automation, IISc Bangalore, under a **Summer Research Fellowship**; a Technical Research Paper of the same is currently under review at **Medical Image Analysis, Elsevier Publications**.
- December 2004 Selected by IIT Kharagpur to co-represent the Institute at the Third **Indo-German Winter Academy**, organized and attended by the University of Erlangen-Nuremberg, Germany, and IIT Kharagpur, IIT Kanpur and IIT Guwahati, in the '**Semiconductor Devices and Components**' course; delivered a researched lecture on '**Materials and Process Challenges for Processing of Multi-Layer Capacitors for Memory Applications**'.
- August-November 2004 Successfully completed a Term Project under the VLSI Engineering Course for '**Implementation of Fast Fourier Transform in Verilog HDL on the FPGA Platform**', under the guidance of **Prof. Swapna Banerjee**, Deptt. Of Electronics & Electrical Communication Engg.
- March-April 2004 Successfully completed the Design and Testing of **4-bit ALU on FPGA** using **Verilog HDL**, under the guidance of **Prof. Goutam Saha**, Deptt. Of Electronics & Electrical Communication Engg., as a Semi Term Project.

FELLOWSHIPS AND CERTIFICATES OF MERIT

- March 2004 Awarded the **Summer Research Fellowship 2004** by the Indian Academy of Sciences (IAS), Bangalore, India, to work at the **Indian Institute of Science**, Bangalore, India, from May-July 2004.
- 2003 - 2004 Won **3^d Place** in the **Annual Product Design** Competition of IIT Kharagpur; designed a Safety Mechanism for Car-drivers.
- 2001 - 2002
 - Awarded certificate of Merit for being among the **Top 1%** of 22571 students appearing in the **All-India Physics Olympiad** organized by the National Association of Physics Teachers, Kanpur.
 - Awarded certificate of Merit for being among the **Top 1%** of 20925 students appearing in the **All-India Chemistry Olympiad** organized by the National Association of Chemistry Teachers, Kanpur.
- 2000 - 2001 Awarded the **Lala Kailaspat Singhania Merit Scholarship** for securing the **1st Rank in School** in the Std.10 ICSE Examinations, with an aggregate of 94.00%.
- 1995 - 1996 Awarded **Silver Medal** for proficiency in the **Homi Bhabha Young Scientist Talent Search Competition** conducted by the Greater Bombay Science Teacher's Association.

SKILLS

- Experienced in Electronic-Simulation software such as **PSPICE** and **Circuit Maker**, and engineering software like **MATLAB** and **AutoCAD**.
- Competent in **C**, **C++** and **Verilog HDL** programming and exposed to Windows, Linux and Sun Solaris Operating Systems.
- Familiar with **Cadence Design Tools** for Circuit Analysis, **Xilinx Integrated Software Environment** and **FPGA Programming** using **Verilog HDL**.
- Assisted the Digital Electronics Labs, Deptt. Of Electronics & Electrical Comm., IIT Kharagpur, in **upgrading the Lab Manual**.
- Fluent in English and excellent communication skills.
- Foreign Language known: **German**

RELEVANT COURSES TAKEN

Engineering Courses:

Until Ongoing Semester

- Electronic Design Automation
- RF and Microwave Engineering
- Digital Signal Processing
- Control Systems Engineering
- VLSI Engineering
- Digital Communication
- Computer Architecture & Operating Systems
- Semiconductor Devices
- Analog Communication
- Digital Electronic Circuits
- Electromagnetic Engineering
- Analog Electronic Circuits
- Programming and Data Structures
- Signals and Networks
- Basic Electronic Circuits
- Electrical Technology

Mathematics and Science Courses:

- Computer Software (Algorithms)
- Probability and Statistics
- Numerical Solutions of Ordinary and Partial Differential Equations
- Vector and Complex Analysis
- Advanced Differential and Integral Calculus
- Physics
- Chemistry
- Operations Research (Management Breadth)

EXTRA-CURRICULAR ACTIVITIES

- **Head** of the **ROBOTIX Team** that organizes the **ROBOTIX Event** in Kshitij, the Annual Techno-Management Fest of IIT Kharagpur.
- **Magazine Secretary** of my Hall of Residence, 2003-04; responsible for publishing an annual Hall-Magazine and a fortnightly Wall-Magazine.
- Obtained the **A Certificate** in National Cadet Corps (**NCC**).
- Sports of Interest: **Table Tennis**

REFERENCES

Available upon request