

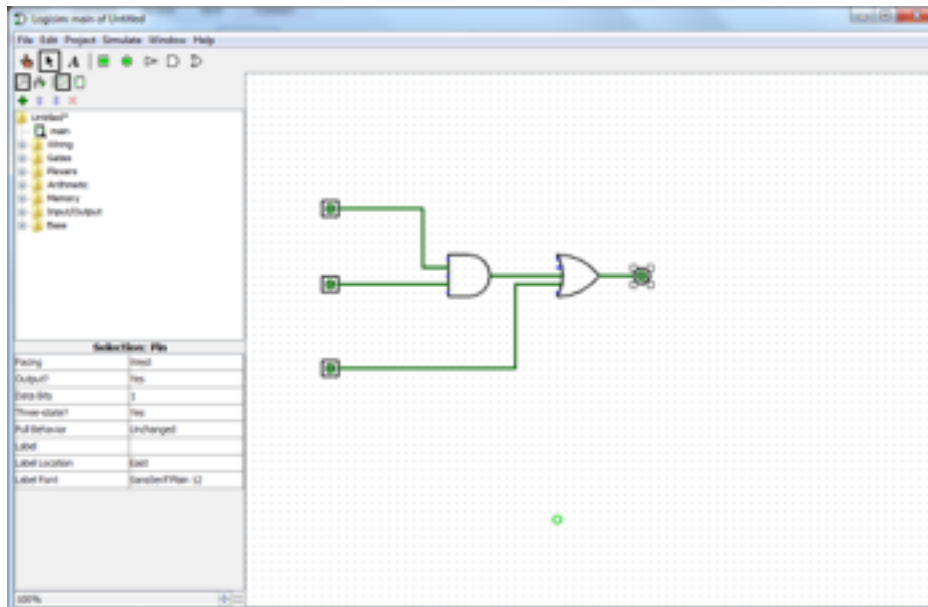
Willis Allstead

CPE 201

LAB 2

Part 1:

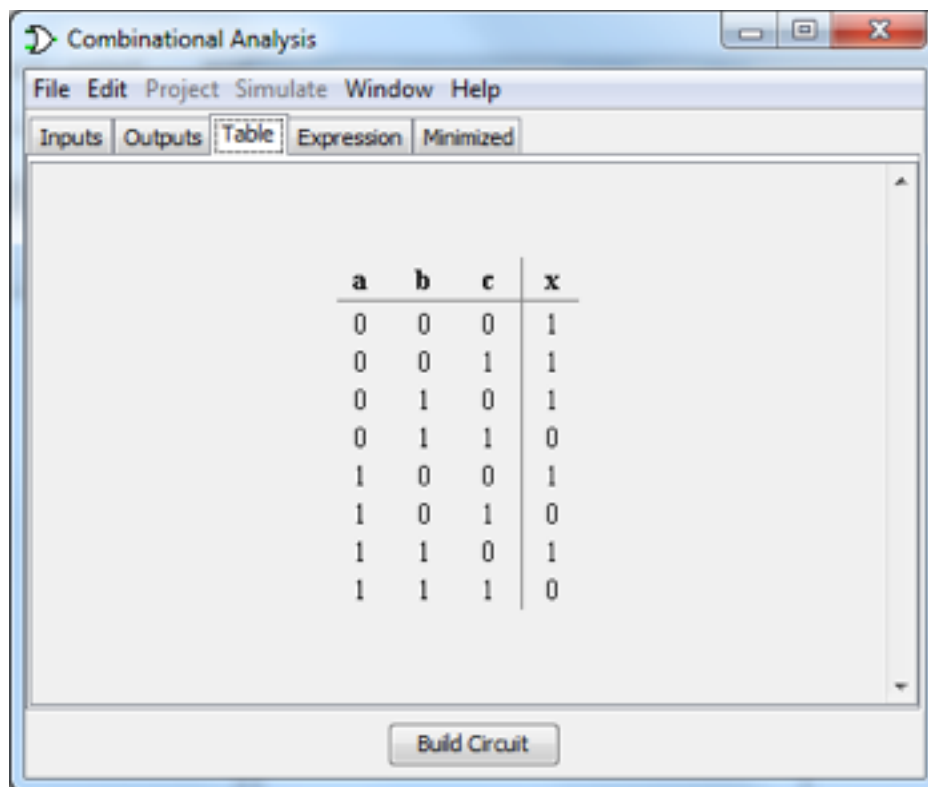
a)



The screenshot shows the 'Combinational Analysis' window. It has tabs for 'Inputs', 'Outputs', 'Table', 'Expression', and 'Minimized'. The 'Table' tab is selected, displaying a truth table for the circuit. The table has four columns: 'a', 'b', 'c', and 'x'. The rows represent all possible combinations of inputs a, b, and c, and the output x is calculated for each combination. A 'Build Circuit' button is located at the bottom of the window.

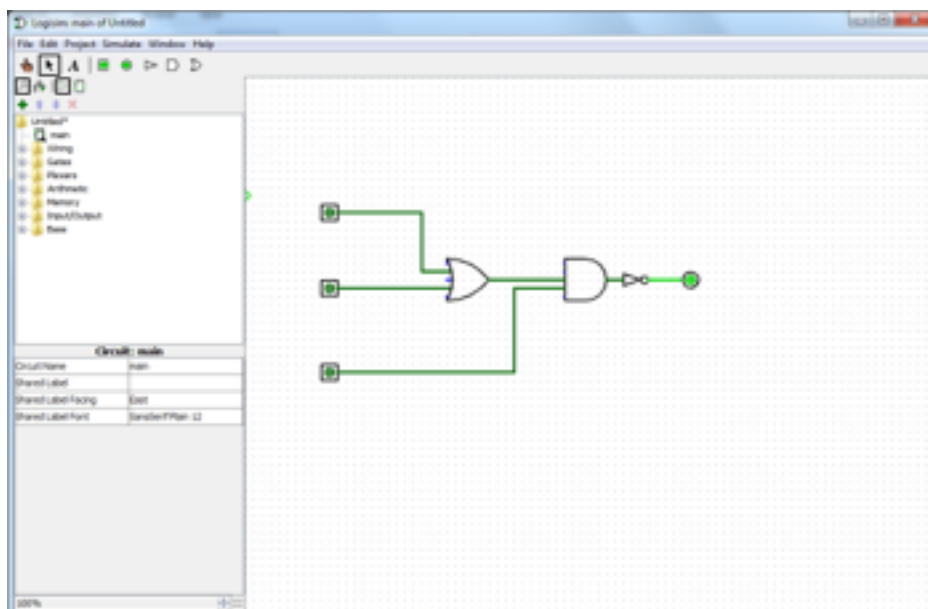
a	b	c	x
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

b)



The screenshot shows a software window titled "Combinational Analysis". It has a menu bar with "File", "Edit", "Project", "Simulate", "Window", and "Help". Below the menu bar are five tabs: "Inputs", "Outputs", "Table", "Expression", and "Minimized". The "Table" tab is selected, displaying a truth table with four columns: **a**, **b**, **c**, and **x**. The table contains eight rows of data. At the bottom of the window is a button labeled "Build Circuit".

a	b	c	x
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	0



c)

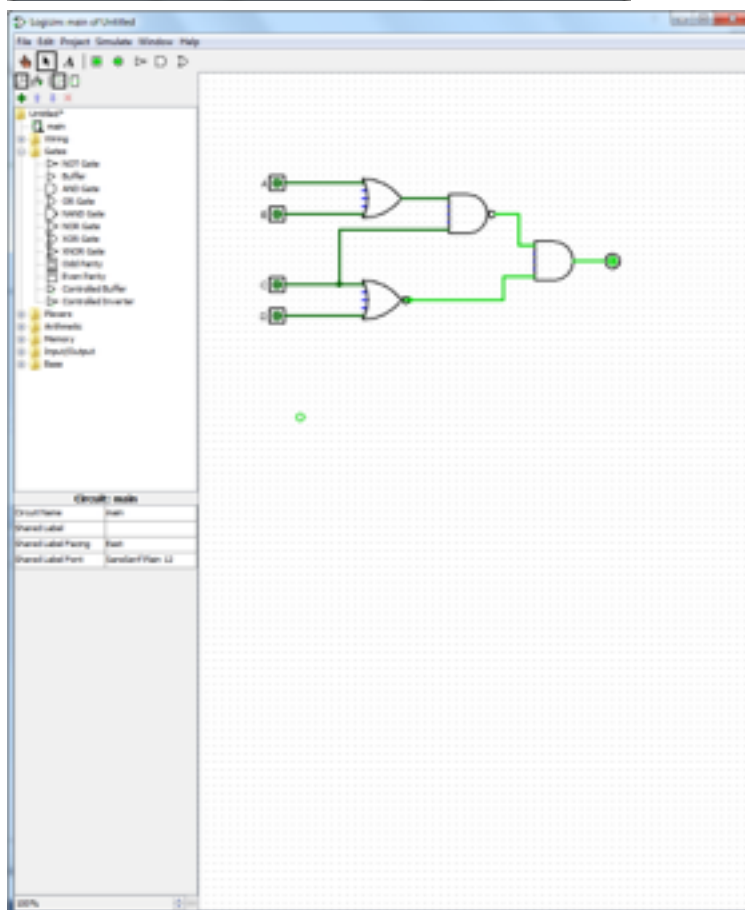
Combinational Analysis

File Edit Project Simulate Window Help

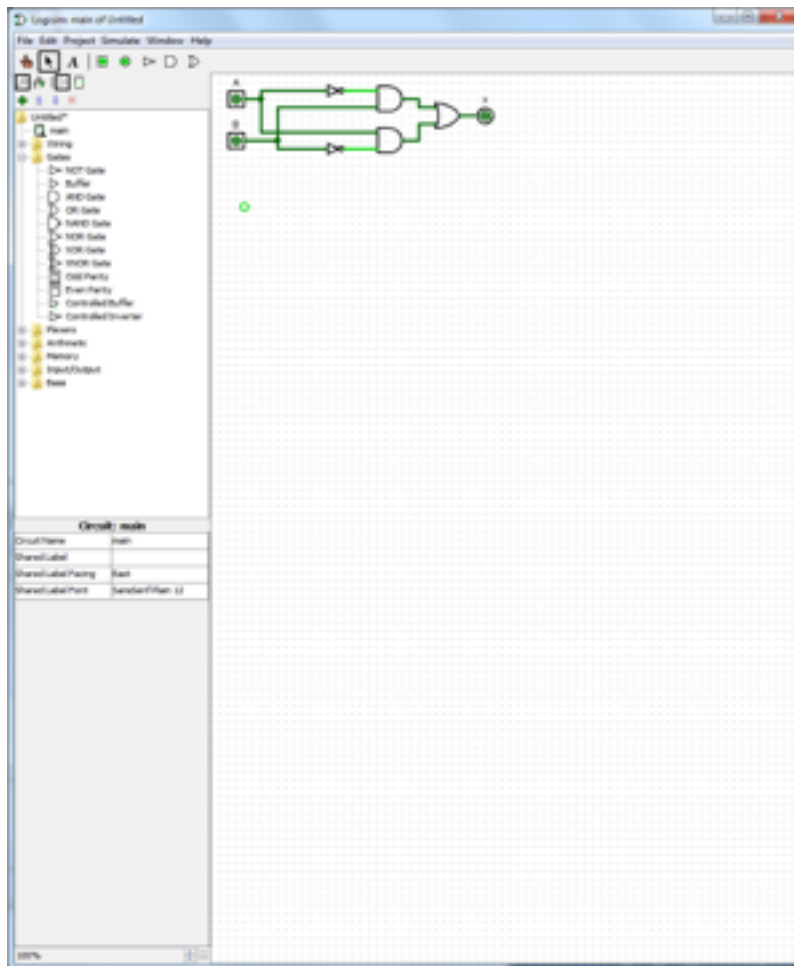
Inputs Outputs Table Expression Minimized

A	B	C	D	x
0	0	0	0	1
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	1
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	1
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	1
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

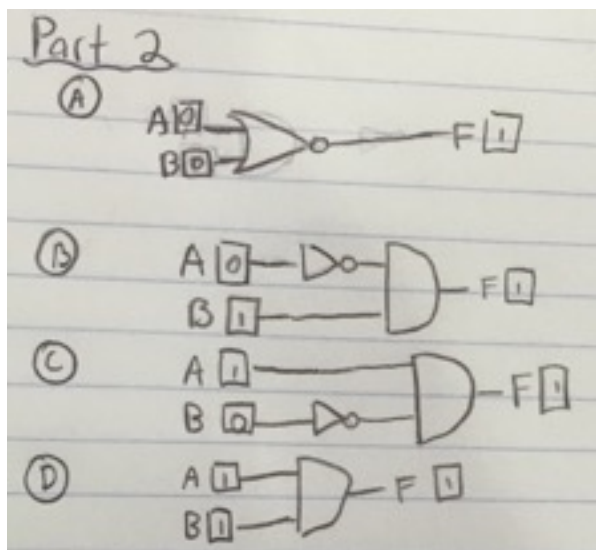
Build Circuit



d)



Part 2:



Part 3:

Part 3

(A)

(B)

1. 7486
2. 14 pins
3. 7 is GND
4. 4 XOR Gates
5. Pin 3

(C)

(D)

- 1.

