Willis Allstead

CPE 201L

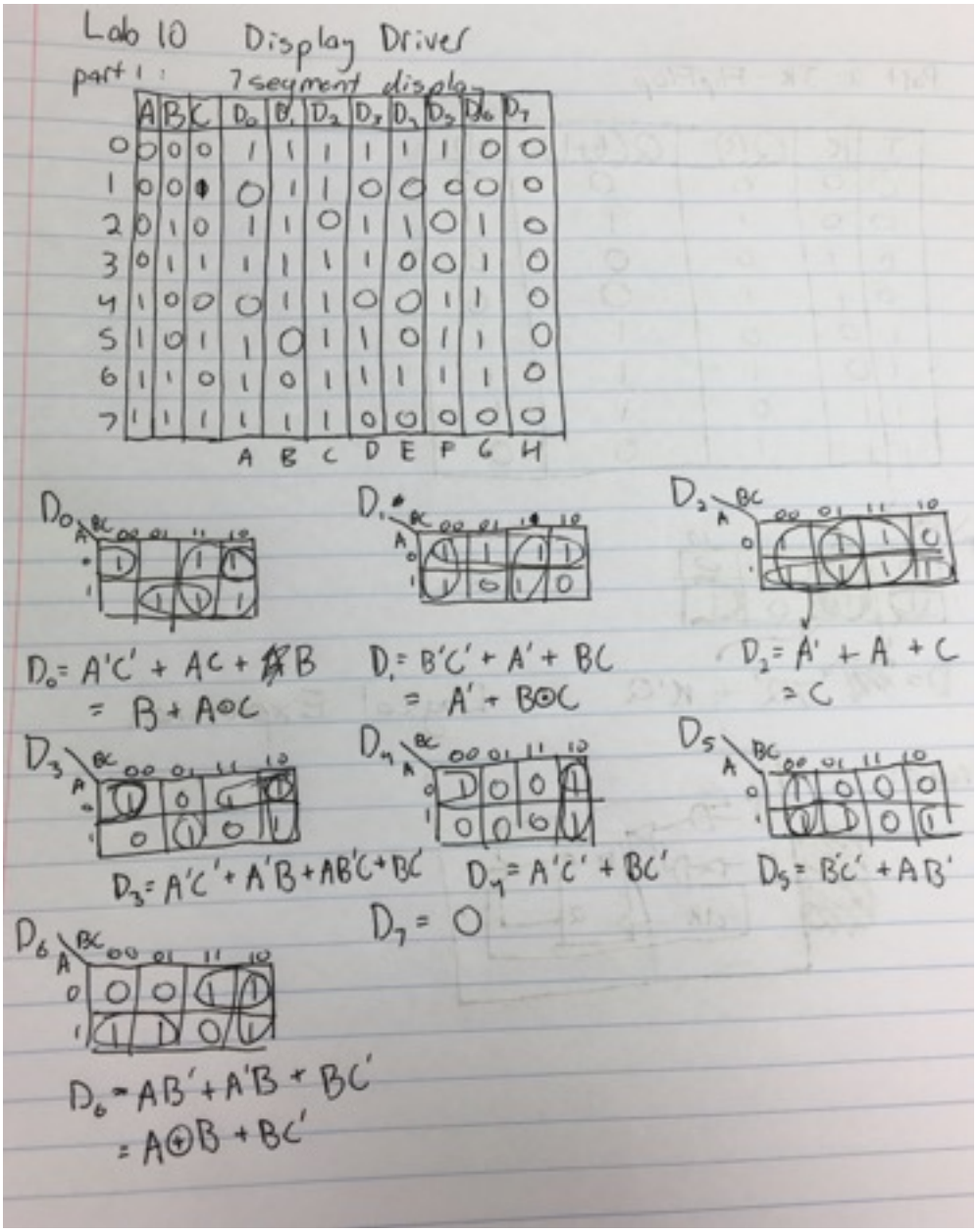
Section: 1102

Deepak Tosh

April 26, 2016

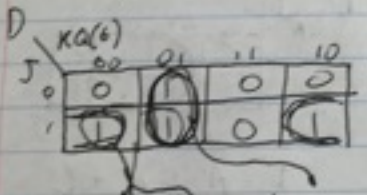
**LAB 10: Display Driver**

Work:



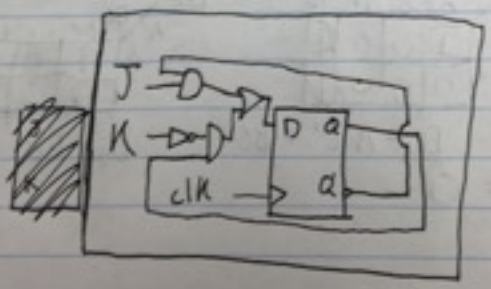
Part 2: JK - FlipFlop

| J | K | Q(t) | Q(t+1) | D |
|---|---|---|---|---|
| 0 | 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | 1 | 1 |
| 0 | 1 | 0 | 0 | 0 |
| 0 | 1 | 1 | 0 | 0 |
| 1 | 0 | 0 | 1 | 1 |
| 1 | 0 | 1 | 1 | 1 |
| 1 | 1 | 0 | 1 | 1 |
| 1 | 1 | 1 | 0 | 0 |



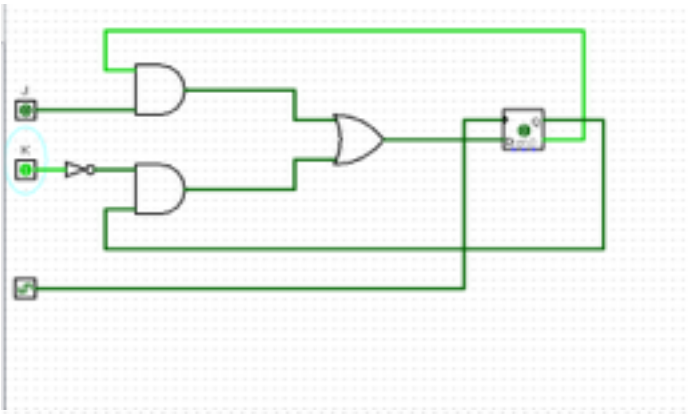

$D = JQ' + K'Q$ ← Logical Expression

Circuit:

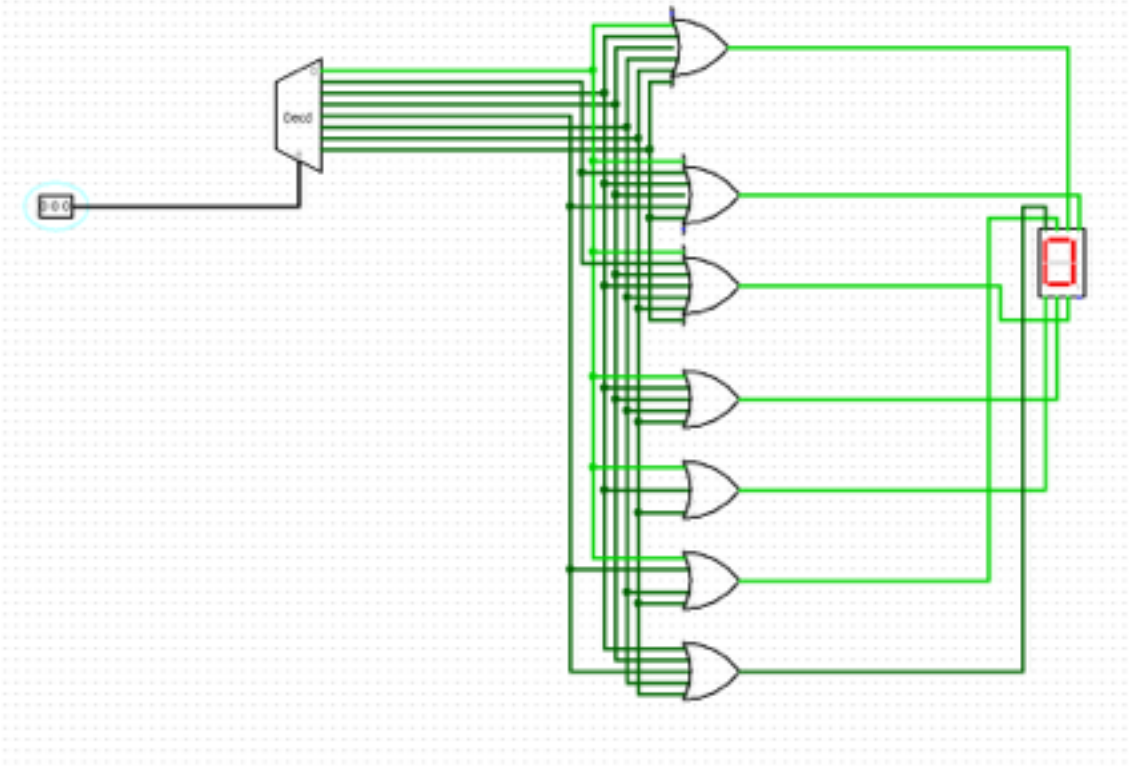


in Logism:

JK Flip-flop:



7-segment display:



On breadboard: