TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC9214AP, TC9214AF, TC9215AP, TC9215AF

HIGH VOLTAGE ANALOG SWITCH

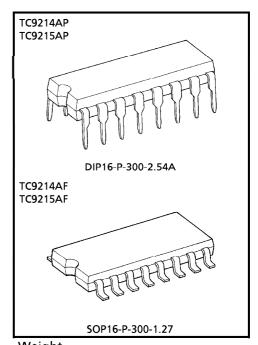
TC9214AP/AF, TC9215AP/AF are Analog Switch for high voltage audio application.

FEATURES

 Analog Switch Circuit Formation TC9214AP, TC9214AF: 5 circuits TC9215AP, TC9215AF: 6 circuits

- Dual Power Supply of (+) and (-) can be used.
- Including Level Shift Circuit, this IC can be operated by (+) power supply only under dual power supply operating.
- Setting Low Input-threshold-voltage in control signal input terminal. 5V CPU application can control this IC directly.

Package : DIP-16 PIN SOP-16 PIN

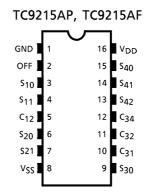


Weight

DIP16-P-300-2.54A : 1.0g (Typ.) SOP16-P-300-1.27 : 0.16g (Typ.)

PIN CONNECTION (TOP VIEW)

TC9214AP, TC9214AF					
		$\sqrt{}$			
GND	1	16	V_{DD}		
c ₁	2	15	C ₄		
s ₁₀	3	14	s ₄₀		
S ₁₁	4	13	S ₄₁		
s ₂₀	5	12	s ₃₀		
S ₂₁	6	11	\$ ₃₁		
c ₂	7	10	\$ ₃₂		
V _{SS}	8	9	C3		
			l		



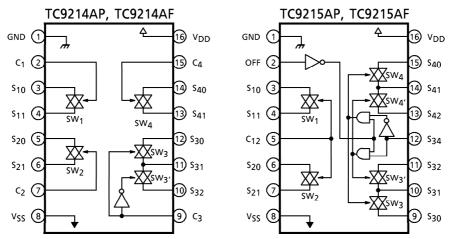
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BLOCK DIAGRAM



PIN FUNCTION
1. TC9214AP, TC9214AF

PIN No.	SYMBOL	PIN NAME	FUNCTION	NOTE	
1	GND	Ground Terminal	Dual power supplying : $+B \rightarrow V_{DD}$		
8	VSS	(-) Power Supply Terminal	0V →GND -B →V _{SS}	ı	
16	V _{DD}	(+) Power Supply Terminal	Single power supplying: $+B \rightarrow V_{DD}$ 0V $\rightarrow GND$, V_{SS}		
2	C ₁	Switch (1) Control Terminal	SWITCH CONNECTION		
3	S ₁₀	Switch (1) Input/	S ₁₀ S ₁₁ S ₁₁		
4	S ₁₁	Output Terminal	$c_1 \bigcirc $ sw_1		
5	S ₂₀	Switch (2) Input/	S ₂₀		
6	S ₂₁	Output Terminal	$c_2 \bigcirc $		
7	c ₂	Switch (2) Control Terminal	S ₄₀ S _{W4} S ₄₁		
9	c ₃	Switch (3) Control Terminal	S ₃₁ S ₃₀ S ₃₂ S ₃₂		
10	S ₃₂		C3 ()	_	
11	S ₃₁	Switch (3) Input / Output Terminal	TRUTH TABLE C ₁ , C ₂ , C ₄ SW ₁ , SW ₂ , SW ₃		
12	s ₃₀		H ON		
13	S ₄₁	Switch (4) Input/	L OFF		
14 S ₄₀	Output Terminal	C ₃ S ₃₀ -S ₃₁ S ₃₁ -S ₃₂			
15	C ₄	Switch (4) Control Terminal	H ON OFF L OFF ON		

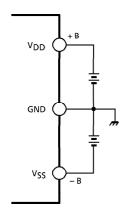
2. TC9215AP, TC9215AF

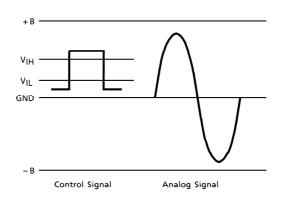
PIN No.	SYMBOL	PIN NAME	FUNCTION	NOTE
1	GND	Ground Terminal	Dual power supplying : +B →V _{DD}	
8	VSS	(-) Power Supply Terminal	0V →GND -B →VSS	_
16	V _{DD}	(+) Power Supply Terminal	Single power supplying: $+B \rightarrow V_{DD}$ $0V \rightarrow GND, V_{SS}$	
2	OFF	Switch (3), (4) OFF Input Terminal	SWITCH CONNECTION SW1 SW2	_
3	S ₁₀	Switch (1) Input/	s ₁₀	
4	S ₁₁	Output Terminal	$s_{11} \bigcirc \longrightarrow s_{21}$	
5	C ₁₂	Switch (1), (2) Control Terminal	C ₁₂ SW ₃ SW ₄ C	
6	S ₂₀	Switch (2) Input/	\$30	
7	s ₂₁	Output Terminal	\$31 \(\sigma \) \$341	
9	s ₃₀		C ₃₄ OFF	
10	S ₃₁	Switch (3) Input/ Output Terminal	TRUTH TABLE C ₁₂ SW ₁ , SW ₂	
11	S ₃₂		H ON	
12	C ₃₄	Switch (3), (4) Control Terminal	L OFF	
13	S ₄₂		OFF C ₃₄ S ₃₀ -S ₃₁ S ₃₁ -S ₃₂ S ₄₁ -S ₄₂	
14	S ₄₁	Switch (4) Input / Output Terminal	L ON OFF H OFF ON	
15	S ₄₀		H OFF OFF OFF	

NOTATION: POWER SUPPLY

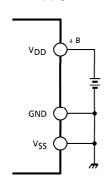
As the power supply is parted between analog switch unit and control unit, the analog switch unit operates in dual power supply of (+) and (-), in which case, the control unit operates in single power supply. Setting a low input-threshold voltage in control input terminal, 5V CPU application can control this IC directly.

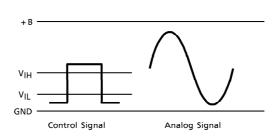
Dual power supply use





Single power supply use





(Note) In case of using single power supply in common with V_{SS} and GND terminal, half voltage of dual power supply must be supplied because of low operating voltage of a control circuit. (V_{DD} – GND \leqq 18V)

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Power Supply Voltage (1)	V _{DD} -V _{SS}	-0.3~36	V
Power Supply Voltage (2)	V _{DD} -GND	-0.3~20	٧
GND Input Voltage	V _{IN} (1)	-0.3~V _{DD} +0.3	٧
V _{SS} Input Voltage	V _{IN} (2)	$V_{SS} - 0.3 \sim V_{DD} + 0.3$	٧
Power Dissipation	PD	600 (300)	mW
Operating Temperature	T _{opr}	- 40~85	°C
Storage Temperature	T _{stg}	- 65∼150	°C

(): SOP-16 pin.

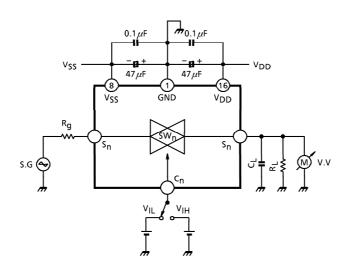
ELECTRICAL CHARACTERISTICS (Unless otherwise specified, $V_{DD} = 15V$, $V_{SS} = -15V$, GND = 0V, Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN.	TYP.	MAX.	UNIT
Operating Supply Voltage (1)		V _{DD} -V _{SS}		Dual power supplying		9.0	?	34	V
Operating Supply Voltage (2)		V _{DD} -GND	Single power supplying		4.5	?	18	v	
Operating Su	upply Current	I _{DD}	_	No load, No sign	al	_	0.1	0.5	mA
Input	"H" Level	VIH		Control input terminal		4.0	~	V_{DD}	
Voltage	"L" Level	VIL	_	$V_{DD} = 4.5 \sim 18V$		GND	~	1.0	
Input	"H" Level	lΗ		Control input	V _{IH} = 15V	- 0.1	_	0.1	
Current	"L" Level	ΊL	_	- I	V _{IL} = 0V	- 0.1	_	0.1	μΑ
Analog Switch ON Resistance		RON	-	$V_{DD} = 5.0V, V_{SS} =$	= - 5.0V	_	200	300	
				$V_{DD} = 9.0V, V_{SS} = -9.0V$		_	80	100	Ω
				$V_{DD} = 15V, V_{SS} = -15V$		_	60	80	
Analog Switch OFF Leak		lOFF	_	$V_{IN} = V_{DD} \sim V_{SS}$		_	± 0.1	± 100	nA
Total Harmonic Distortion		THD		f_{IN} = 1kHz, V_{IN} = 1 V_{rms} R_g = 600 Ω , R_L = 10k Ω BW = 20Hz~20kHz		_	0.01	0.05	%
Cross Talk		c _T	1			80	90	_	dB
Output Noise Voltage		٧N	1		2	_	2.0	_	μ V _{rms}
Maximum Control			1)/ 0\/ \/	,		100		kHz
Frequency		.		$V_{IL} = 0V$, $V_{IH} = 5V$		50	100	_	
Maximum Transfer		fMAX		$R_L = 10k\Omega$, $C_L = 15pF$ (*1)			5		MHz
Frequency						_	3	_	101
Field Through		FS		$R_L = 10k\Omega$, $C_L = 1$	5pF (*2)	_	300	_	kHz

^(*1) To supply the $V_{IN} = 1.0V_{rms}$ sign wave. f_{MAX} means 3dB down frequency from $f_{IN} = 1kHz$.

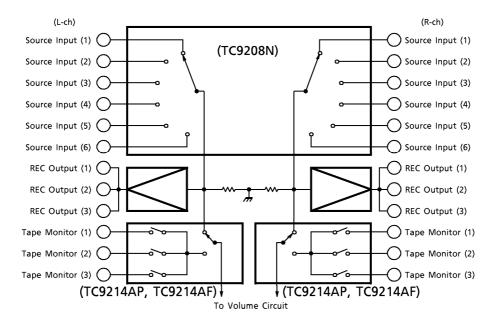
(*2) To supply the $V_{\mbox{IN}}$ = 1.0 $V_{\mbox{rms}}$ sign wave. F_S means frequency for cross-talk 50dB.

TEST CIRCUIT 1



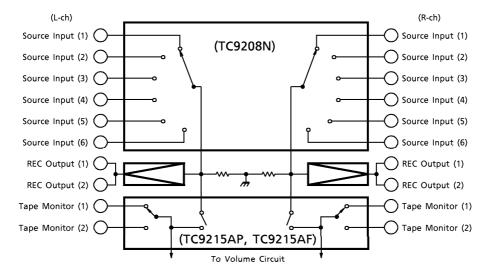
APPLICATION CIRCUIT

- 1. TC9208N + TC9214AP, TC9214AF x 2
 - Monitor switching for 6 source input circuits and 3 tape-recorder.



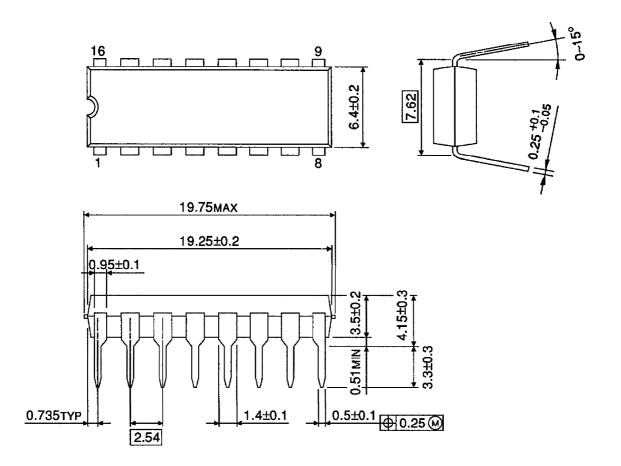
2. TC9208N + TC9215AP, TC9215AF

• Monitor switching for 6 source input circuits and 2 tape-recoder.



OUTLINE DRAWING DIP16-P-300-2.54A

Unit: mm



Weight: 1.0g (Typ.)

OUTLINE DRAWING SOP16-P-300-1.27 Unit:mm 16 9 9 9 10.705TYP 10.8MAX 10.3±0.2 10.8±0.2

Weight: 0.16g (Typ.)