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Course: Digital System Design
Project: Finite State Machine
Input and output signals:
Input signal: KEY, CNT_OF
Moore's outputs: FSM_CNT_CE, FSM_MX_MEM, FSM_MX_LCD
Mealy's outputs: FSM_LCD_WR, FSM_LCD_CLR
Codes: code1 = 986297109, code2 = 9301359922

