

High then float during reset
RD WR DEN

Float during reset
DT/R A(D)[19-0]

Pull-up during reset
RD UCS LCS

Low during reset
ALE HLDA (means active low on the address latches OE)

High during reset
BEN (GAL)

Atsuko Ito

Sheet: /GLUE/

File: glue.kicad_sch

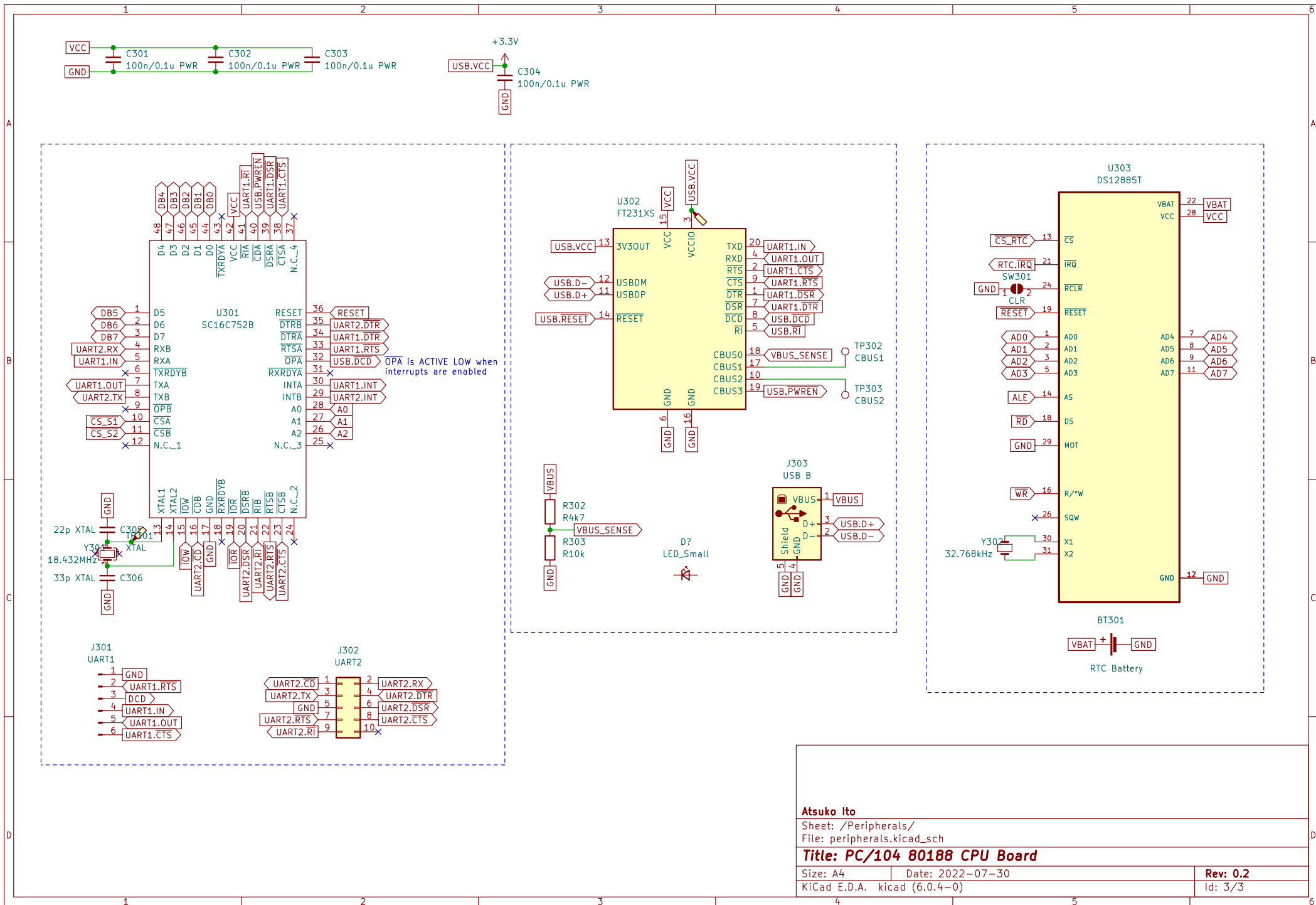
Title: PC/104 80188 CPU Board

Size: A4 Date: 2022-07-30

KiCad E.D.A. kicad (6.0.4-0)

Rev: 0.2

Id: 2/3



Atsuko Ito

Sheet: /Peripherals/

File: peripherals.kicad_sch

Title: PC/104 80188 CPU Board

Size: A4 Date: 2022-07-30

KiCad E.D.A. kicad (6.0.4-0)

Rev: 0.2

Id: 3/3