

True - Estimated length (bias)

terminal

internal

0.5
0.0
-0.5
-1.0

Homogeneous

Loc

Sp

Sp
Loc

Sp
Loc
Sp/Loc

Sp
Loc
Sp/Loc
highILS

Homogeneous

Loc

Sp

Sp
Loc

Sp
Loc
Sp/Loc

Sp
Loc
Sp/Loc
highILS



CASTLES



ERaBLE



Naive



Patristic(AVG)+FastME



Patristic(MIN)+FastME