

	RTL Design	Latency [Cyc]	Max Freq. [MHz]	LUTs [#]	FFs [#]	LoC [#]	Key Support [#]	Sbox BRAM Use
1	DFiant_200	13	293	6070	3931	334	3	Yes
2	DFiant_300	25	311	3956	8196	334	3	Yes
3	DFiant_450	38	442	3782	11437	334	3	Yes
1	Vivado_200	20	231	2658	3811	161	1	Yes
2	Vivado_300	30	308	2624	1777	161	1	Yes
3	Vivado_450	39	350	2567	3957	161	1	Yes
1	Das Core	32	451	10369	11927	340	1	Yes
2	Hsing Core	21	454	11103	5386	922	3	No
3	Salah Core	41	410	9204	9687	618	1	No