Broadband measurements of the surface impedance in ferromagnetic wires as a boundary condition for scattering problems

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Abstract

In the cm-wave range, the skin depth is comparable to the micron radius of ferromagnetic wires and thus their magnetic and conductive properties will influence the scattered electromagnetic field. Solving the scattering problem by considering the complicated internal magnetic structure of the wire becomes unpractical. There is a need to develop hybrid methods when the external problem of scattering on an inclusion is solved together with the experimental impedance conditions on its surface. Herein, we propose an accurate broadband impedance measurement technique operating up to 15 GHz. The PCB measurement and calibration cells designed in this work allow flexibility in choosing external stimuli such as magnetic field, tensile stress, and heat. The experimental impedance can be then used as the boundary condition in electromagnetic simulations making it possible to model, predict and design wire-based composites using numerical solvers such as CST Studio Suite.

- Keywords: Impedance boundary condition; Ferromagnetic microwires; Network calibration;
- 15 De-embedding; Microwave scattering; PCB cells.

1. Introduction

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Dynamic magnetic behaviour through magnetoimpedance (MI) effect [1] is an essential property to characterize the microwave response of different materials such as ferromagnetic microwires [2-6], ribbons [7], and thin films [8-10]. Apart from fundamental research interest, in the technological context materials exhibiting a large MI effect arise as field/stress-sensitive sensors that can be integrated into a wide range of devices [11-16]. Special emphasis has been placed on Co-based and Fe-based glass-coated microwires. The thin glass shell (2 – 15 microns) along with the magnetostriction (positive or negative) of the core alloy (diameter 5-30microns) determine their unique magnetic anisotropic and mechanical properties. The technology of continuous casting of such wires makes it possible to produce highly-flexible threads of kilometre length [17]. These features allow them to be used as functional fillers in composite materials to introduce electromagnetic functionalities in the microwave range (ultrahigh frequency UHF and super high frequency SHF bands), such as field/stress tunability, cloaking, absorption and shielding [18-24]. Such plethora of functional materials and their increasing trend towards operating at broader bandwidth stimulate the development of accurate measurement techniques to characterize their MI and microwave behaviour. The most commonly used methods are based on shorted coaxial transmission lines [25-27], microstrip transmission lines (MTLs) and coplanar waveguides (CPWs) etched on printed circuit boards (PCB) [28-32] aided by vector network analysers (VNA). In the shorted coaxial lines, one end of the microwire is connected to the inner conductor, while the other end is connected to the ground of the coaxial cap. The alignment of the sample along the axis will determine the measurement accuracy. Except for testing under magnetic field, measurements with sample stretching or heating are hardly possible in such coaxial cells. MTLs consist of a metal-backed substrate of relative permittivity ε_r on top of which is a metal strip with its width determined by the characteristic impedance of the line [33]. The sample is usually mounted on the surface of a PCB in the gap between two aligned microstrips and connected to them by soldering or conducting painting/glue. This method works for elongated samples of various geometries (wire, film, or ribbon), which can be considered as a continuation of a microstrip waveguide, although with a different characteristic impedance. Mounting the sample on the surface of PCB allows the application of external stimuli, including magnetic field, heat, and tensile stress. Meanwhile, the CPW PCB cells have an additional ground plane on the side of the microstrips that improves signal integrity at higher frequencies. This plane is also convenient for adding shunt surface elements, such as SHORT and LOAD terminations, which can be used for calibration of the microstrip network. The CPW cells are suitable for another measurement

scheme where a thin and sufficiently wide sample (film or ribbon) is placed over the continuous strip, covering only its central segment. Direct electrical contact with the strip is excluded by introducing a subtle gap or isolating layer. The parameter of interest is no longer the impedance of the sample itself, but its magnetic and dielectric properties, determined indirectly through the measurement of S-parameters of the loaded waveguide segment [28].

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VNA, coaxial cables, non-coaxial strips on PCB, and a non-lumped sample constitute a cascaded network that requires a multi-stage calibration procedure. The ultimate goal is to extend the reference plane directly to the sample. VNA and the cables can be calibrated using the standard 12-term error correction model with the coaxial SHORT-OPEN-LOAD-THRU (SOLT) terminations. The strip waveguides must be de-embedded based on different methods [28,30,31,34]. In turn, an elongated sample over a continuous ground (or segment of a loaded strip) constitutes a structure which waveguide properties must be considered when calculating material parameters from the S-parameter measurements. The principles of multi-stage calibration have already been implemented for thin film samples, where ferromagnetic resonance (FMR) and permeability dispersion were of primary interest [28,31]. The study of Bilzer et al. [28] is closely related to the method developed in our present work since the waveguide properties of the loaded CPW segment were explicitly revealed there. The measurements were carried out up to 14 GHz, which is of interest for microwave scattering applications. However, this method requires an additional reference measurement when the film sample is removed leaving a continuous strip, and thus cannot be applied to a wire sample. In another method proposed by Gonzalez et al. [31], a thin film sample was electrically attached to microstrips on the PCB surface. It was assumed that the per-unit-length resistance and inductance (used as indicative parameters characterising FMR), could be derived from the measured characteristic impedance and propagation exponent. However, it remained unclear how the shunt capacitance and conductance (per unit length) were estimated, which are required to solve the corresponding equations. The maximum measurement frequency was up to only 3 GHz, which, given the sample length used (11 mm), does not allow to judge the accuracy of the phase distortion compensation along the sample. A radical decrease in sample length enables considering the sample as a lumped element without a phase incursion even at higher GHz. Such measurements have been performed with short wires connected at the end of an open coaxial connector between the core and the ground shell [35,36]. However, in very short samples, the magnetic domain structure at the ends might significantly change both the magnetostatic and high-frequency properties. Moreover, due to open-end parasitic capacitance and radiation losses (which are not included in the calibration), the accuracy of impedance measurements will deteriorate.

In this work, we develop a broadband method for measuring accurately the field and stress dependencies of the surface impedance in ferromagnetic wires. The measurements in the frequency range up to 15 GHz were performed on MTL cells. In contrast to Bilzer and Gonzalez [28,31], the part of the network formed by the microstrip and the coaxial connector on PCB was not assumed to be ideal. Its 3-term model was created using a calibration cell where three reflection coefficients were measured from identical microstrips with surface mount SOL terminations. Touchstone S1P files containing the reflection coefficients were then created for such microstrips and uploaded to the VNA for automatic de-embedding. The phase distortion along the sample was compensated numerically using a phase unwrapping technique. The measured impedance dispersions were fitted with multi-pole dispersion models available in CST Studio Suite and followed the Kramers-Kronig relations. Recently, it has been shown that conventional impedance models [37,38] cannot accurately reproduce impedance response in the transition from MHz to GHz [39]. Thus, these models have limited practical value in modelling the microwave response in free space. However, with our reliable impedance measurement technique, it would be possible to realise a hybrid approach [40], where the experimental surface impedance is used to numerically solve the scattering problem of an individual wire inclusion without considering its complex magnetic domain structure [41]. Such approach will enable designing wire-filled composites with desirable microwave properties and predict their microwave response under external stress/temperature/field stimuli.

2. PCB measurement and calibration cells

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We have explored two types of PCB measurement cells, straight and dogbone, shown in Fig. 1. A cell consists of two microstrips on one side of the board, and a continuous copper ground plane on the opposite side (Fig. 1a). The microstrips are soldered to SMA connectors at the edge of the PCB. A straight wire sample is placed in the gap between the microstrips and connected to them with conductive silver paint, which solidifies without additional heating. Rogers's composite materials [42] are widely used for high-frequency PCB designs due to their stable and isotropic dielectric properties with negligible temperature drift and low loss. Our boards were made of RO4003-C composite with a thickness of 0.812 mm and dielectric constant $D_k = 3.55$ over a broad frequency range and low dielectric losses. To calculate the microstrip width, we used the online microwave calculator available on Rogers' website. For the chosen D_k and thickness, the width of the microstrips was calculated as 1.742 mm,

providing a characteristic impedance close to 50 Ω over the whole frequency range. Further rules for designing waveguide structures can be found in [33].

A simpler straight PCB cell allows the application of external stimuli such as magnetic field and heating (Fig. 1b). Heating can be realized by local blowing of samples with hot air, while cooling other parts of the cell, including SMA connectors. The dogbone cell (Fig. 1c) has protrusions for gripping by the clamps of the stress machine that allows the transfer of tensile stress to the sample through the elastic deformation of the PCB. After the sample was connected to the microstrips, it can be additionally glued to the board for more efficient stress-transfer. Simultaneously with stretching, the sample can be exposed to magnetic field and heating. Each cell type has a fixed microstrip length, and the gap between microstrips can be varied to accommodate samples of varying lengths (10 and 20 mm). The length of the sample should be much longer than the end segments involved in the connection to the microstrips (1-2 mm each from each side). Measurement of shorter wire samples would solve the problem of phase incursion along them [35,36]. However, it may introduce significant distortions due to the magnetic domains at the wire ends, as well as increasing uncertainty in the length outside the contact pads.

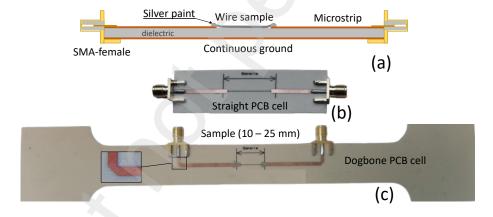


Fig. 1. PCB cells used for the impedance measurements (a) schematic diagram of the PCB cell; (b) straight cell; (c) dogbone cell

Phase and amplitude distortions, including multiple reflections introduced by the microstrips, will be compensated by de-embedding based on the 3-term error correction model. This method requires three reflection coefficient (S_{11}) measurements from the microstrip with SHORT, OPEN, and LOAD (50 Ω) surface mount terminations (SOL). Further details are discussed in the following section. To provide such measurements, calibration cells were developed for the straight (Fig. 2a) and dogbone (Fig. 2b) geometries. The microstrips on the

calibration cells were reproduced exactly as those on the measurement cells. OPEN termination is just the open end of the microstrip. Up to several GHz, such termination will behave as an ideal reflector with $S_{110} = 1$ (linear magnitude). However, in the vicinity of 6 GHz, a resonance was observed, indicating the need for a more accurate model. Apparently, already starting from this frequency, it is necessary to consider the terminal parasitic capacitance, as well as radiation losses from the open end which result in an effective impedance in series with the capacitance. SHORT is a 0.3 mm via (copper plated hole) connecting the microstrip end with the continuous ground on the opposite side of the board. This termination will behave like a true reflector with $S_{11S} = -1$ (linear magnitude) well beyond 6 GHz. Further improvement of the model will require considering the parasitic inductance of the via and its skin effect.

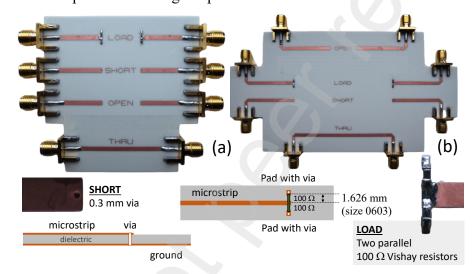


Fig. 2. Two types of PCB calibration cells used for the 3-term error correction model. (a) straight cell; (b) dogbone cell.

LOAD is two 100 Ω Vishay RF resistors (case size 0603) soldered in parallel between the microstrip and two via-grounded pads, thus having 50 Ω . Such configuration is used to reduce the parasitic inductance associated with them. Up to 15 GHz, S_{11L} of such resistor termination may be as low as -20 dB. Therefore, in the 3-term error correction model, we assume that $S_{11L} = 0$ (linear magnitude). For RF resistors, Vishay provides LCR equivalent circuits that could be used to model more accurately their S_{11L} as the terminations. However, they still contain contact pads and vias for which the LCR parameters should also be considered.

Coplanar waveguide PCB cells could also be explored. Their calibration cells are more convenient because SHORT and LOAD surface-mount terminations can be connected between the microstrip and the adjacent ground planes on the same side. This plane is usually connected

to the main ground on the opposite side by a row of vias along the edge. Such arrangements should minimise the parasitic parameters.

3. Multi-stage calibration for measuring the impedance of ferromagnetic wires on the PCB cells

The implemented calibration procedure, shown in Fig. 3, includes three stages: (i) full 2-port calibration of VNA and the cables attached to its ports using SOLT terminations (T stands for THRU), (ii) de-embedding the microstrips using the 3-term error correction model, and (iii) compensating the delay time along the sample using the unwrapped phase. The 2-port SOLT calibration, also named TOSM in Rohde&Schwarz VNAs (M stands for MATCH), is based on the 12-term error correction model which most fully describes the VNA error model [43]. This type of calibration is implemented in all VNAs. All that is needed is a mechanical calibration kit with SOLT coaxial standards and their analytical LCR models provided by the VNA manufacturer.

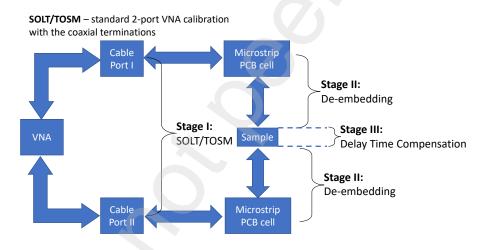


Fig. 3. Multi-stage calibration for impedance measurements.

3.1. Stage I: SOLT calibration of VNA and cables

Once SOLT calibration is completed and activated, it will be automatically applied to all measurements in real-time. At the Stage I, the reference plane has already been extended to the end of the cables. Now all S-parameters of microstrips can be recovered using the 3-term error model shown in Fig. 4. The model operates with only three error parameters: E_{DF} (directivity forward), E_{RF} (reflection tracking forward), and E_{SF} (source mismatch forward). The actual reflection S_{11A} produced by a termination connected to the microstrip end is entangled into S_{11M} measured from the coaxial connection soldered to the PCB:

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$$S_{11M} = \frac{b_1}{a_1} = E_{DF} + \frac{E_{RF}S_{11A}}{1 - S_{11A}E_{SF}}$$
 (1),

where a_1 and b_1 represent input and output signals, respectively. By measuring three reflections on the PCB calibration cell, where the microstrip is terminated with non-coaxial SOL, it becomes possible to find E_{DF} , E_{RF} , and E_{SF} , and then recover all S-parameters of the microstrip. Note that during these measurements the calibration cell is connected to the VNA and the cables which have been already calibrated at Stage 1.

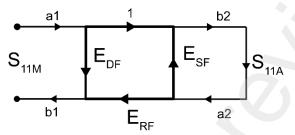


Fig. 4. 3-term error correction model. $a_{1,2}$ and $b_{1,2}$ represent input and output signals, respectively.

In Eq. (1), we have three unknowns E_{DF} , E_{RF} , and E_{SF} which can be found from S_{11M} measured from the three known termination standards SHORT ($S_{11A} \rightarrow S_{11S}$), OPEN ($S_{11A} \rightarrow S_{11D}$), and LOAD ($S_{11A} \rightarrow S_{11D}$):

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$$\begin{cases}
S_{11MS} = E_{DF} + \frac{E_{RF}S_{11S}}{1 - S_{11S}E_{SF}} \\
S_{11MO} = E_{DF} + \frac{E_{RF}S_{11O}}{1 - S_{11O}E_{SF}} \\
S_{11ML} = E_{DF} + \frac{E_{RF}S_{11L}}{1 - S_{11L}E_{SF}}
\end{cases}$$
(2).

After getting rid of the denominators, the system of Eq. (2) becomes linear with respect to three

variables: $x = E_{DF}$, $y = E_{SF}$, and $z = (E_{RF} - E_{DF}E_{SF})$:

$$\begin{cases} E_{DF} + S_{11MS}S_{11S}E_{SF} + S_{11S}(E_{RF} - E_{DF}E_{SF}) = S_{11MS} \\ E_{DF} + S_{11MO}S_{11O}E_{SF} + S_{11O}(E_{RF} - E_{DF}E_{SF}) = S_{11MO} \\ E_{DF} + S_{11ML}S_{11L}E_{SF} + S_{11L}(E_{RF} - E_{DF}E_{SF}) = S_{11ML} \end{cases}$$
(3).

For our terminations on the PCB calibration cells, we assumed $S_{11S} \equiv -1$, $S_{110} \equiv 1$, and $S_{11L} \equiv 0$ for the whole frequency range. If more accurate LCR models are available for the terminations, the interpolated values can be used. Solving Eq. (3) numerically for each frequency point within the sweep range set at SOLT of the VNA, all S-parameters of the microstrip can be found for the whole frequency range: $S_{11} = x$, $S_{22} = y$, and $S_{21} = S_{12} = \pm \sqrt{z + xy}$. The proper sign at the square root is chosen from the condition that the unwrapped

phase $\varphi = \pm arg (\sqrt{z + xy})$ must have a negative slope. If the left and right microstrips are not symmetrical, the measurements and calculations described above must be performed for each microstrip.

3.2. Stage II: De-embedding microstrips

Modern VNAs allow embedding or de-embedding networks beyond the reference plane by an already established calibration. The microstrips can be considered as the networks connected beyond the reference plane at the cable ends. Thus, de-embedding them will extend the reference plane to the sample interface. A diagram explaining the principles of 2-port deembedding is shown in Fig. 5. We follow the convention that S_{11} of a network faces the port to which it is connected.

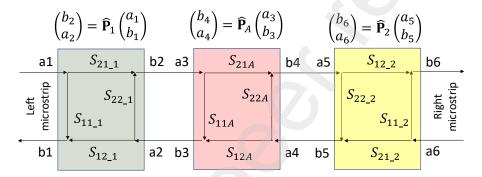


Fig. 5. Principles of 2-port de-embedding.

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S-matrices do not allow performing calculations with cascaded networks, *i.e.* the product of the S-matrices of two cascaded networks is not equal to the S-matrix of the combined network. To carry out matrix calculations with cascading networks, we must operate with the propagation P-matrices, which can be introduced as follows:

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$$\binom{b_2}{a_2} = \hat{\mathbf{P}} \binom{a_1}{b_1}$$
 (4).

It relates the input and output at the left with the input and output at the right. P-parameters can be expressed through S-parameters:

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$$\hat{\mathbf{P}} = \begin{pmatrix} \frac{S_{12}S_{21} - S_{22}S_{11}}{S_{12}} & \frac{S_{22}}{S_{12}} \\ -\frac{S_{11}}{S_{12}} & \frac{1}{S_{12}} \end{pmatrix} = \frac{1}{S_{12}} \begin{pmatrix} S_{12}S_{21} - S_{22}S_{11} & S_{22} \\ -S_{11} & 1 \end{pmatrix}$$
 (5).

In turn, S-parameters can be expressed through P-parameters:

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$$\hat{\mathbf{S}} = \begin{pmatrix} -\frac{P_{21}}{P_{22}} & \frac{1}{P_{22}} \\ P_{11} - \frac{P_{12}P_{21}}{P_{22}} & \frac{P_{12}}{P_{22}} \end{pmatrix}$$
 (6).

The sample stacked between two microstrips constitutes a combined network with the propagation matrix $\hat{\mathbf{P}}_2\hat{\mathbf{P}}_A\hat{\mathbf{P}}_1$ (see Fig. 5):

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$$\binom{b_6}{a_6} = \hat{\mathbf{P}}_2 \hat{\mathbf{P}}_A \hat{\mathbf{P}}_1 \binom{a_1}{b_1} = \hat{\mathbf{P}}_M \binom{a_1}{b_1}$$
 (7).

Here, $\hat{\mathbf{P}}_1$ and $\hat{\mathbf{P}}_2$ are the known propagation matrices of the left and right microstrips respectively, obtained using the PCB calibration cell, 3-term model, and Eq. (5). $\hat{\mathbf{P}}_A$ is the unknown propagation matrix of the sample. On the other hand, $\hat{\mathbf{P}}_M = \hat{\mathbf{P}}_2 \hat{\mathbf{P}}_A \hat{\mathbf{P}}_1$ of the combined network can be obtained by measuring its S-parameters and using Eq. (5). Therefore, $\hat{\mathbf{P}}_A = \hat{\mathbf{P}}_2^{-1}$ $\hat{\mathbf{P}}_M \hat{\mathbf{P}}_1^{-1}$, where all three matrices at the right are known. Then, $\hat{\mathbf{P}}_A$ can be recalculated to $\hat{\mathbf{S}}_A$ using Eq. (6). Note that for the 2-port de-embedding, the whole matrix $\hat{\mathbf{S}}_M$ of the combined network must be measured. Whereas, for the 1-port de-embedding, S_{11A} can be recovered from S_{11M} (see Eq. (1)):

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$$S_{11A} = \frac{S_{11M} - E_{DF}}{E_{RF} + E_{SF}(S_{11M} - E_{DF})}$$
 (8).

In our measurement setup, we use Rohde&Schwarz (R&S) ZNB20 2-port VNA, 100 kHz – 20 GHz. In this VNA, the de-embedding can be performed automatically after uploading the S2P Touchstone text file (.s2p) for each network. If the networks are symmetrical as in our case, the same file can be uploaded for both ports. The S2P file includes the header # Hz S RI R 50 and consists of nine tab-delimited columns: frequency [Hz], Re[S_{11}], Im[S_{11}], Re[S_{21}], Im[S_{21}], Re[S_{21}], Im[S_{21}], Re[S_{22}], Im[S_{22}]. We have developed a Python program code, available at a public repository on GitHub [44], which creates S2P files of the microstrips from $S_{11/22MSOL}$ (see Eq. (3)) measured from the PCB calibration cell. The surface-mount terminations can be considered ideal or have dispersion behaviour saved in $S_{11/22SOL}$. Also, the algorithm can perform the 1-port or 2-port de-embedding if this option is not available in an old VNA model. Further details can be found in the manual on GitHub.

After activating SOLT calibration and de-embedding (Stages I and II), S_{11A} and S_{21A} of the sample can be correctly measured. For the 1-port and 2-port measurements, the sample impedance is calculated as follows:

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$$Z_A = 50 \times \frac{1 + S_{11A}}{1 - S_{11A}} [\Omega]$$
 (9),

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$$Z_A = 100 \times \frac{(1 - S_{21A})}{S_{21A}} [\Omega]$$
 (10).

However, these equations assume that the sample is lumped, *i.e.* it has zero electrical length, or at least much shorter than the wavelength. This condition is not met in our case. A piece of wire above a continuous ground is a waveguide structure resulting in a phase incursion. Compensating for this phase incursion constitutes Stage III in our calibration procedure (Fig. 3), which will be explained next.

3.3. Stage III: Phase unwrapping and delay time compensation along the wire sample

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Our measurement cells are 2-port. Typical phase dispersions of S_{21A} measured in ferromagnetic wire samples on the straight and dogbone PCB cells are shown in Figs. 6(a, b), respectively. Piecewise negative phase slopes separated by device induced jumps (phase can be measured only within $[-\pi,\pi]$) are caused by the delay time along the wire. They may be distorted due to parasitic resonances and deterioration in the de-embedding accuracy at higher GHz frequencies. For any analysis and further processing, the phase must be free from 2π jumps and returned to a continuous form with a negative slope. By fitting this slope in a certain frequency range with a straight trend line $\varphi(f) = af + b$, where f is the frequency, the delay time can be calculated as $\Delta t = |a|/(2\pi)$. Although phase unwrapping is available in modern VNAs, performing it directly on the device may be difficult. When allocating a certain frequency interval for unwrapping, VNA may show point spikes caused by numerical uncertainties during de-embedding. These spikes will not allow the phase to be unwrapped correctly. Therefore, it is desirable to save the raw phase dispersion over the entire range in a csv/text file, truncate the frequency interval as proposed in Fig. 6, and then unwrap the phase numerically. The corresponding Python algorithm is available at our public GitHub repository in the folder Delay time [44]. To calculate the impedance using Eq. (10), S_{21A} must be replaced by the corrected $\tilde{S}_{21A}(\omega) = S_{21A}(\omega) \times \exp(i\omega\Delta t)$. This completes the Stage III of calibration.

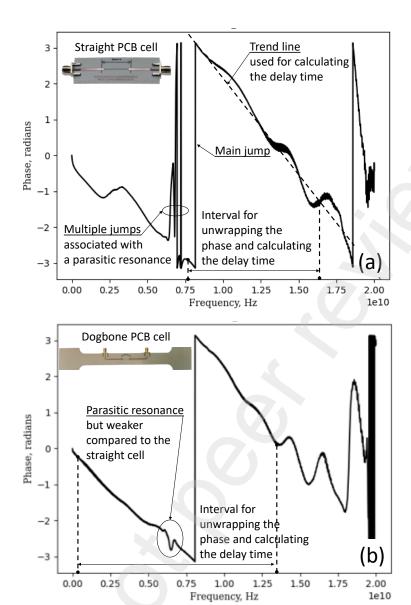


Fig. 6. Phase dispersions of S_{21A} measured in ferromagnetic wire samples on the straight (a) and dogbone (b) PCB cells.

3.4. Impedance measurement setup

To measure the impedance, we designed the setup shown in Fig. 7. It includes three programmable R&S devices: ZNB20 VNA (2-port, 100 kHz – 20 GHz), Function Generator HMF2550, and HMC 8012 Digital Multimeter (DMM). The only analog device is Accel Instruments Waveform Amplifier TS250-2 used for feeding a Helmholtz coil. All devices are mounted on a trolley, which allows the setup to be parked next to various installations, such as a stress machine or a bench with the coil. Impedance measurements require three programmable devices to operate in sync according to the block diagram shown in Fig. 8. The device control was implemented in LabVIEW 2015 through NI VISA driver. SOLT calibration

(Stage I) and de-embedding (Stage II) are already activated on VNA before starting measurements. The program iterates the DC offset voltage at the output of the function generator from its minimum $-V_g$ to maximum $+V_g$ with the specified number of points N. The DC offset output is applied to the power amplifier to feed the Helmholtz coil. After applying a voltage, the operation is paused for the time duration of $20 \times L/R$, where L and R are the coil inductance and resistance, to achieve the saturation of the coil current. The VNA sweep is then activated to measure $S_{21/11}$ at discrete frequency points (N_f) . During this sweep, the field is kept constant. The actual voltage applied to the coil after the power amplification is measured by the DMM. Its value is passed to the program to be recalculated into the field using the coil calibration coefficient (Oe/V). The real and imaginary parts of the measured Sparameter are transferred as an array to the PC, where they are corrected to compensate for the sample delay time (Stage III), and then converted into impedance using Eq. (9) or (10). The measurement process is repeated in a loop for the next field point. To reproduce possible magnetic hysteresis properties, the DC offset is iterated in both directions: from negative to positive values (loop forward) and then in the inverse direction (loopback). Stress and temperature measurements are currently not integrated into the control program. These parameters are changed manually after a loop over the field points is completed.

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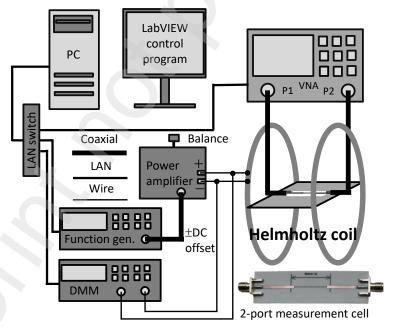


Fig. 7. Automated setup for measuring the field dependence of the impedance.

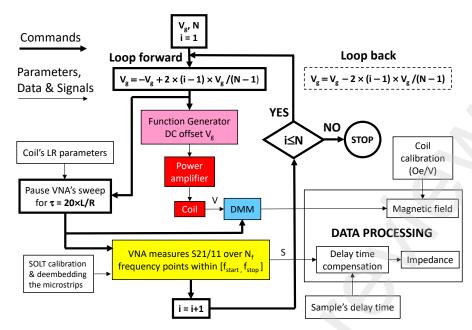


Fig. 8. Block diagram of the LabVIEW control program that synchronizes the operation of three R&S devices for measuring the field dependence of the impedance.

4. Broadband impedance measurement under in-situ magnetic field and stress

As an illustration of the proposed measurement technique, Fig. 9 shows a broadband dispersion of the impedance in a glass-coated ferromagnetic wire. The continuous wire reel, kindly provided by MFTI Ltd (Republic of Moldova), was fabricated by the Taylor-Ulitovskiy method [17] from a Co-based alloy of the composition $Fe_4Co_{68.7}Ni_1B_{13}Si_{11}Mo_{2.3}$ (wt.%) The total wire diameter, including the glass shell, was 23.2 μ m with a metal core of 19.1 μ m. A piece of wire 20 mm long was fixed on a dogbone PCB cell. The impedance was recalculated from \tilde{S}_{21A} using Eq. (10) over the frequency range 1 MHz – 15 GHz in the presence of longitudinal DC bias fields. As will be shown in the next section, the uncertainty in determining the delay time along the sample used for the correction of S_{21A} (Stage III) does not exceed \pm 3 ps or less than \pm 0.5 mm in terms of the sample length.

In ferromagnetic wires, the magnetic AC permeability arises as a result of the magnetization precession induced by the circular magnetic field when passing a high-frequency current. Conductivity and effective AC permeability together determine the skin-depth for the high-frequency current. In Co-based glass-coated wires having negative magnetostriction, the equilibrium magnetisation has a circular direction which can be deflected by applying a moderate longitudinal DC magnetic field. This leads to a field dependence of the AC permeability and, as a result, the impedance [1]. The most noticeable effect in this type of wire is a broad FMR, which gradually develops when a longitudinal magnetic field is applied, as shown in Fig. 9. The impedance amplitude reaches its maximum value around 1 GHz when the

magnetisation is saturated along the wire axis. As FMR is not the subject of study in this work, we mention it only because in its vicinity the impedance will demonstrate maximum sensitivity to external stimuli, which is important for applications where tunable scattering properties are required.

In Fig. 10, the stress dependence of the impedance was measured in the vicinity of FMR at 900 MHz at different longitudinal bias fields. The dogbone PCB cell was stretched between the grippers of a desktop Instron machine. The strain was measured by an optical camera method [39]. On one hand, stress sensitivity requires the magnetisation to be deflected from the circular direction. Without external magnetic field, the stress sensitivity of impedance is negligible (yellow trace). On the other hand, magnetisation saturated along the wire axis by a strong longitudinal field will require a larger tensile stress to rotate it back into the circular direction. This tension may be outside the elastic regime. Thus, there should be an optimal longitudinal external field that provides the largest stress sensitivity of the impedance within the elastic regime. For the sample in Fig. 10, the optimal magnetic field which produces largest stress sensitivity of impedance is about 5.36 Oe.

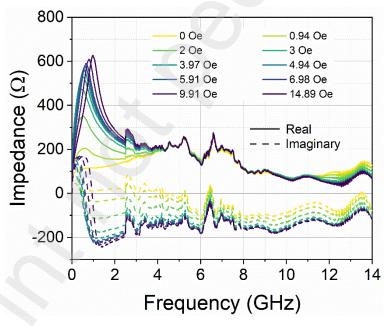


Fig. 9. Broadband dispersion of the impedance recalculated from the corrected \tilde{S}_{21A} at different longitudinal bias fields. Three-stage calibration has been applied.

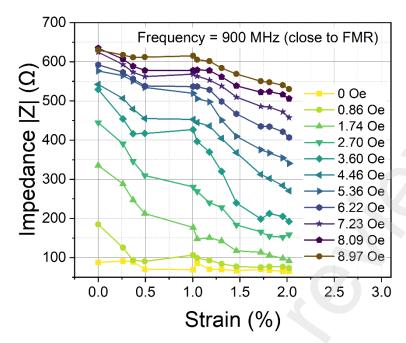


Fig. 10. Stress dependence of the wire impedance measured at a fixed frequency in the vicinity of FMR at different longitudinal bias fields.

5. Surface impedance of ferromagnetic wires as a boundary condition in scattering simulations

In the cm-wave range, the skin depth is comparable to the micron radius of the wire. The entire volume of the wire with its conductive and magnetic properties will be involved in the formation of the scattered electromagnetic field. Widely used models [37,38] cannot accurately reproduce MI properties in the frequency range of interest for scattering applications (transition from MHz to GHz). This prompts us to develop a method for measuring surface impedance as a boundary condition for scattering problems. The definition of the surface impedance tensor $\hat{\varsigma}$ given in CST coincides with that in Ref. [45]:

$$\overline{\mathbf{E}}_t = \hat{\mathbf{\varsigma}}(\overline{\mathbf{H}}_t \times \mathbf{n}) \tag{11},$$

where the tangent vector of the total electric field $\overline{\mathbf{E}}_t$ at the wire surface is expressed through the vector product of the unit normal vector \mathbf{n} directed inside the conductor and the tangent magnetic field $\overline{\mathbf{H}}_t$ at the wire surface. In the cylindrical coordinate system (x,φ,r) associated with the wire, the impedance boundary condition can be written in the coordinate form:

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$$\begin{cases} \overline{E}_{x} = \varsigma_{xx} \overline{H}_{\varphi} - \varsigma_{x\varphi} \overline{H}_{x} \\ \overline{E}_{\varphi} = \varsigma_{\varphi x} \overline{H}_{\varphi} - \varsigma_{\varphi \varphi} \overline{H}_{x} \end{cases}$$
 (12).

In a nonmagnetic conductor, $\zeta_{x\phi/\phi x} \equiv 0$. The presence of the off-diagonal components is a distinctive feature of ferromagnetic samples with a certain type of magnetic anisotropy. The use of these components for sensor applications in the MHz range has already been explored previously [46]. A thin wire only allows one wave polarization that is essential for the formation of a scattered field: with an electric vector along the wire. Therefore, $\zeta_{x\phi}\overline{H}_x$ in the first equation in Eq. (12) can be neglected. The second equation in Eq. (12) defines the induced circular current, which will introduce a negligibly smaller radiation additive compared to the longitudinal induced current. Therefore, this equation can be neglected. Only one equation remains essential for scattering problems: $\overline{E}_x = \zeta_{xx}\overline{H}_{\varphi}$.

CST allows experimental impedance boundary conditions for solving external electrodynamic problems. The impedance can be provided in a tabular form: frequency, real part, and imaginary part. These values are then interpolated using a predefined template that includes first and second-order resonant terms. When calculating the wire impedance Z from the corrected \tilde{S}_{21A} , the delay time along the sample is fully compensated, thus reducing its effective electrical length to zero. The total current I along such an ideal impedance element does not have a longitudinal distribution, as well as \overline{E}_x . The circular magnetic field on the surface of the wire is $\overline{H}_{\varphi}^{SI} = I/2\pi a = V/2\pi aZ = l\overline{E}_x/2\pi aZ$ (SI units) or $\overline{H}_{\varphi}^{cgs} = 2l/ac = 2V/acZ = 2l\overline{E}_x/acZ$ (cgs units). Here, a and a are the wire radius and length respectively, and a0 cm/s is the speed of light (cgs). Using these equations, for the longitudinal surface impedance a1 cm/s we obtain:

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$$\zeta_{xx}^{SI}[\Omega/\text{sq}] = Z[\Omega] \times \frac{2\pi a}{l}$$
 (13),

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$$\zeta_{\chi\chi}^{\text{cgs}}[(\text{s/cm})/\text{sq}] = Z[\Omega] \times \frac{10^9 a}{2lc}$$
 (14).

In Eq. (14), the normalization factor is chosen so that the impedance is measured in Ohms. The notations Ω /sq and (s/cm)/sq adopted here refers to the surface (sq). The wire parameters a and b can be measured in any units because they enter the equation as a ratio.

The CST Studio Suite (release version 2022.04) offers a multi-resonance physical model along with numerical optimization to fit the experimental surface impedance dispersion:

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$$\zeta(\omega) = \zeta_{\infty} + i\omega L + \sum_{n=1}^{N} \frac{\beta_{0,n}}{\alpha_{0,n} + i\omega} + \sum_{n=1}^{M} \frac{\gamma_{0,n} + i\omega\gamma_{1,n}}{\delta_{0,n} + i\omega\delta_{1,n} - \omega^2}$$
 (15).

The fitting parameters have the following physical interpretations. The 1st order resonance

terms $(n = \overline{1,N})$: frequency of the pole $f_{p1,n} = \alpha_{0,n}/2\pi$ [Hz], static gain contribution $g_{1,n} =$

 $\beta_{0,n}/\alpha_{0,n}$ [Ω /sq]. The 2nd order resonance terms $(n = \overline{1,M})$: resonance frequency $f_{p2,n} =$

 $\sqrt{\delta_{0,n}}/2\pi$ [Hz], quality factor $Q_{2,n} = \sqrt{\delta_{0,n}}/\delta_{1,n}$, frequency of the zero $f_{z2,n} = \gamma_{0,n}/2\pi\gamma_{1,n}$ [Hz], static gain contribution $g_{2,n} = \gamma_{0,n}/\delta_{0,n}$ [Ω /sq]. The maximum number of resonant terms used for a fitting cannot exceed 10, including the order of the poles. For example, two resonances of 1st order and four resonances of 2nd order.

Fig. 11 illustrates the principles of impedance fitting used in CST. The dispersion curves for the real and imaginary parts of ς_{xx} in the presence of an external longitudinal bias field (3.97 Oe) were fitted with $\varsigma_{\infty}=0.00647476~\Omega/\mathrm{sq},~L=1.07214\times10^{-12}~\mathrm{H},~\mathrm{two}$ resonance terms of 1st order, and three resonance terms of 2nd order. The physical parameters for the resonance terms are shown in Table 1. The total fitting order 8 was set in the program interface, and the choice of resonant terms between the 1st and 2nd orders was made automatically by the CST optimization algorithm. The fitting was able to identify and accurately describe the LC resonances $f_{p2,n}$ ($n=\overline{1,3}$), including the resonance at $\sim 6.37~\mathrm{GHz}$ ($f_{p2,2}$) caused by the measurement cell. The 1st order model is responsible for magnetic resonance properties, revealing two poles with $f_{p1,1} \approx 200~\mathrm{MHz}$ and $f_{p1,2} \approx 1~\mathrm{GHz}$. The sum of these two terms decays as $1/\omega$ at $\omega\to\infty$ and proportional to ω at $\omega\to0$ due to $g_{1,1}$ and $g_{1,2}$ having close absolute values and opposite signs. The gains $g_{2,n}$ ($n=\overline{1,3}$) are significantly smaller, and hence the 2^{nd} order model contributes much less within the frequency range [$f_{p1,1}$, $f_{p1,2}$].

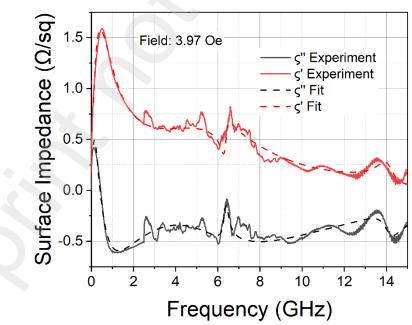


Fig. 11. The dispersion curves for the real and imaginary parts of ζ_{xx} in the presence of an external longitudinal bias field 3.97 Oe fitted with the model template in Eq. (15).

Table 1. Physical parameters of the 1st and 2nd order resonance models used to fit the impedance dispersion in Fig. 11.

N	1	2	3
f_{p1} [GHz]	0.200253	1.03141	
$g_1 \left[\Omega/\mathrm{sq}\right]$	- 1.54142	1.76759	
f_{p2} [GHz]	14.1188	6.36786	6.76186
Q_2	14.6469	20.9207	0.965668
f _{z2} [GHz]	12.8863	15.2074	4.53192
$g_2 \left[\Omega/\mathrm{sq}\right]$	0.00964765	- 0.014956	0.340934

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When recalculating the corrected \tilde{S}_{21A} into the impedance, even a slight error in the delay time (determined from the unwrapped phase), may violate physical conditions imposed on the impedance dispersion. Thus, further adjustment is needed to ensure that the real part of the impedance remains positive and tends to zero at higher GHz frequencies. As for the imaginary part of the impedance, it is not physically forbidden to go into the positive region. However, if a premature transition is exhibited caused by an error in the delay time, this will cause the real part to grow again, which will be noticeable. Ignoring these physical conditions makes impedance measurements practically useless [30]. An adjustment algorithm for the delay time is used here and available at GitHub repository in the folder Impedance dispersion [44]. As the frequency increases, both the real and imaginary curves are asymptotically approaching the horizontal axis, thereby reducing the tolerated uncertainty in the delay time. Our PCB cells allow de-embedding up to 15 GHz. We have found that for this frequency range, the delay time uncertainty cannot exceed ± 3 ps. This corresponds to ± 0.5 mm uncertainty in the sample length, given that the speed factor of wave propagation over PCB is ~ 0.6 . In Fig. 12, the experimental solid curves of the real and imaginary parts of the impedance were reconstructed from \tilde{S}_{21A} with the calculated and adjusted delay time. The shadows accompanying these central curves correspond to the impedance tolerance caused by the delay time uncertainty ± 3 ps, which does not violate the physical conditions, at least up to 15 GHz. The central curves (0 ps) and curves corresponding to the tolerance limits (+3 ps) and -3 pswere fitted with the CST model template in Eq. (15). It can be seen that the fitting curves for 0 ps are better centred with respect to their parent experimental curves. Thus, fitting with the theoretical model can serve as an additional criterion for minimizing the error in the delay time. Compared with our measurements, the measurement proposed before was limited to 3 GHz [31], which do not allow any conclusions to be drawn about the accuracy of the phase

compensation along the sample. Also, there was no analysis carried out on the correspondence of measurements to impedance models that would guarantee the fulfilment of the Kramers-Kronig relations.

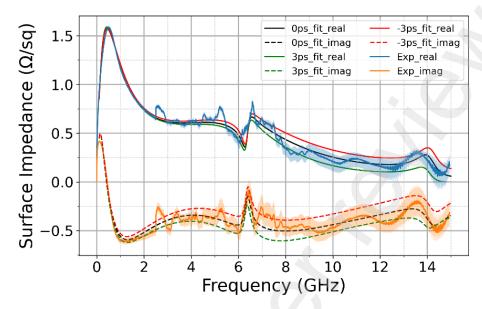


Fig. 12. The impedance tolerance caused by the uncertainty of the delay time.

It has been shown that for wires with a circular anisotropy, the impedance dependence on the external longitudinal field at fixed frequencies cannot be described by existing models in the MHz to GHz transition [39,47]. A typical impedance transformation scenario observed in the experiment is shown in Fig. 13. Starting around 700 MHz, when the change in impedance with field reaches its maximum, there is a bending point in the curve producing a double slope (800 MHz). As the frequency increases, the bending point remains fixed relative to the field axis, while the maximum shifts toward higher fields. The impedance value at the bending point is gradually decreasing. For frequencies above 5 GHz, the maxima shift so far that the impedance curve looks like a horizontal line. In theory, however, such phenomena are not predicted. There is a gradual rise in the wings of the curve after the maxima with increasing frequency, while these maxima remain fixed relative to the field axis. The discrepancy between theory and experiment occurs in the MHz to GHz frequency range which is pivotal for scattering applications.

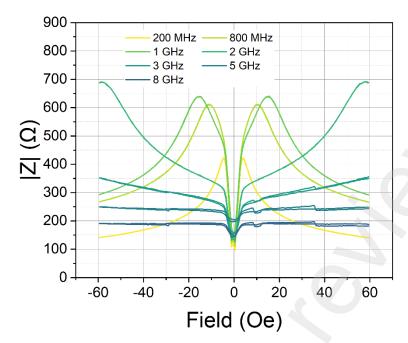


Fig. 13. The magneto-impedance transformation scenario when transitioning from the MHz to GHz ranges.

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The question of how to construct a rigorous scattering theory for wire inclusions remains open. The solution to the internal problem is impossible due to the complex magnetic domain structure. Further progress will be made through the development of hybrid methods in which the external scattering problem is solved numerically along with experimental impedance boundary conditions. The single-particle scattering problem is the first step in this direction. The approach using the antenna integro-differential equation for the wire current does not require the mesh as in more universal finite-element methods [40]. However, the solution of many such equations interlocking through a complex operator of electromagnetic interaction between inclusions would be numerically costly. It would also be worthwhile to explore the extent to which CST can be used for such simulations. In a single-particle problem, we calculated the distribution of the surface circular magnetic field \overline{H}_{ω} along a wire 50 mm long and 19.1 µm in diameter induced by a plane wave (Fig. 14). The 1.5 GHz wave was linearly polarized with an electric field (1 V/m) along the wire. The experimental surface impedance was used as the boundary condition. The field \overline{H}_{φ} is a measure of the total effective current $I = 2\pi a \overline{H}_{\omega}$ induced by the wave, which takes zero values at the ends of the wire fulfilling the boundary conditions. A sufficiently dense mesh is required to reduce the simulation error on the surface of the wire. We tried using "the cells per max model box edge" (simulation option in CST) equals 10 or 100. The number 10 provides a non-physical periodical

angular dependence of \overline{H}_{φ} on the wire surface, while it should be constant. The simulations shown in Fig. 14 used 100 cells resulting in a much smaller ripple of \overline{H}_{φ} over the azimuthal angle φ . The field values were obtained by averaging \overline{H}_{φ} over φ for each longitudinal position with a 5 mm step. The simulation has been done on a DELL XPS 8930 computer (i7-9700 CPU@3.00 GHz, RAM 32 GB, Windows 10 Pro, 64-bit operating system). The computation time ranges from seconds to a few minutes depending on the mesh numbers. Using a detailed mesh to model such a thin wire compared to its length and wavelength does not look quite reasonable. This issue should be further addressed.

As the computation power increases, the preferences of researchers will certainly shift toward universal solvers. Therefore, we see broad prospects for using CST for modelling electromagnetic properties of composite structures with wire inclusions. The hybrid approach removes one of the significant obstacles which is finding the solution of the complicated internal problem. The rest is purely an electrodynamic task that dispenses with complex material properties.

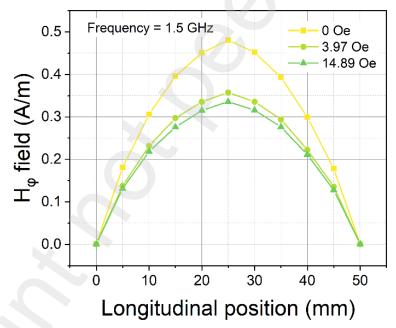


Fig. 14. Distribution of the surface circular magnetic field along a 50 mm wire dipole induced by the plane wave with the electrical field along the wire. The experimental surface impedance was used as the boundary condition at three values of the bias field.

6. Conclusion

We have developed a broadband measurement technique for the surface impedance of ferromagnetic wires up to 15 GHz. It includes a three-stage calibration procedure: SOLT calibration of VNA and coaxial cables, de-embedding of microstrips, and compensation of the delay time (phase incursion) along the wire sample. The impedance is measured on PCB cells to allow external magnetic field, tensile stress, and heating stimuli. De-embedding of microstrips is implemented using the specially designed calibration PCB cells with the surface mount SOL terminations. By measuring three reflection coefficients on the calibration cells and using the 3-term model, the S-parameters of the microstrips can be determined. Compensation of the delay time along the sample was implemented using the phase unwrapping technique. The ability to carry out de-embedding up to 15 GHz made it possible to reduce the uncertainty in determining the delay time to ± 3 ps. The accuracy of the impedance measurements has been verified by fitting the dispersion model provided by the CST Studio Suite, which ensures meeting the Kramers-Kronig relations. Use of experimental impedance boundary conditions for scattering problems was tested in CST to calculate the current distribution in a wire dipole induced by an electromagnetic wave. The hybrid approach combining experimental material parameters and electrodynamic simulations provide solid ground for designing the microwave response of wire-filled composite materials.

Declaration of Competing Interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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