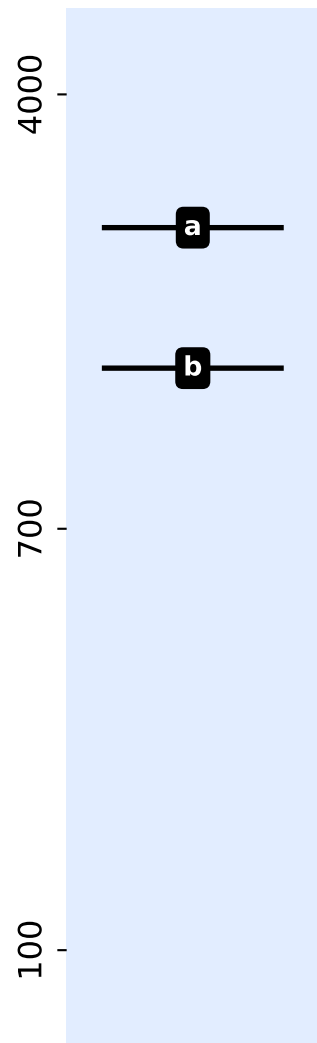
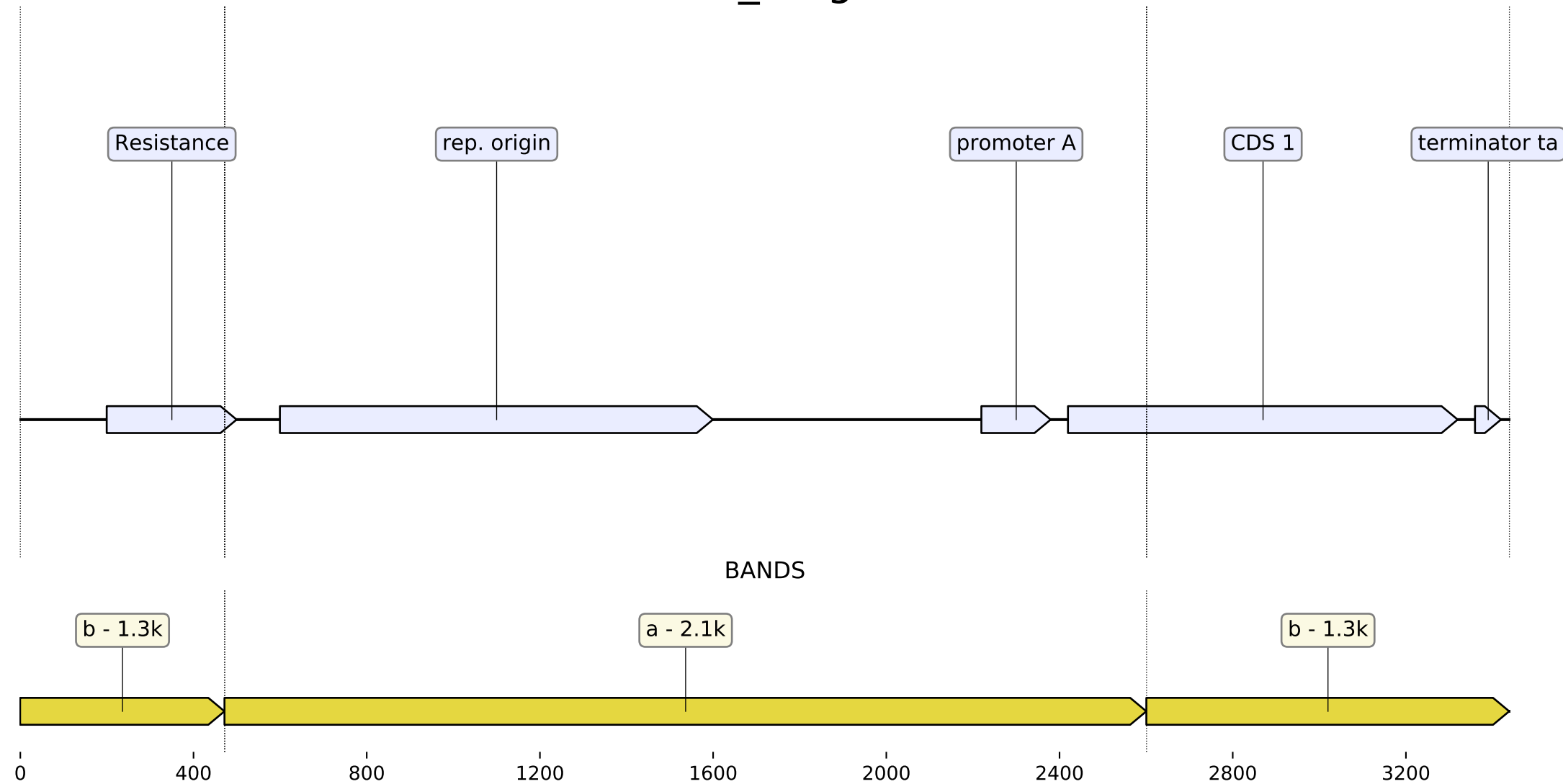


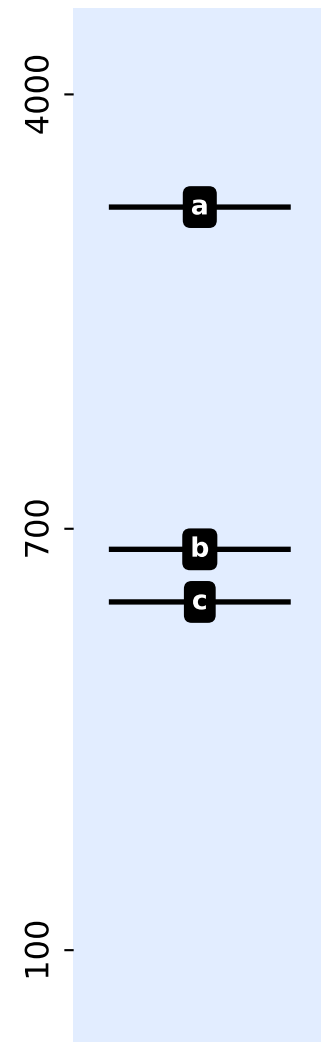
**BamHI + NcoI**



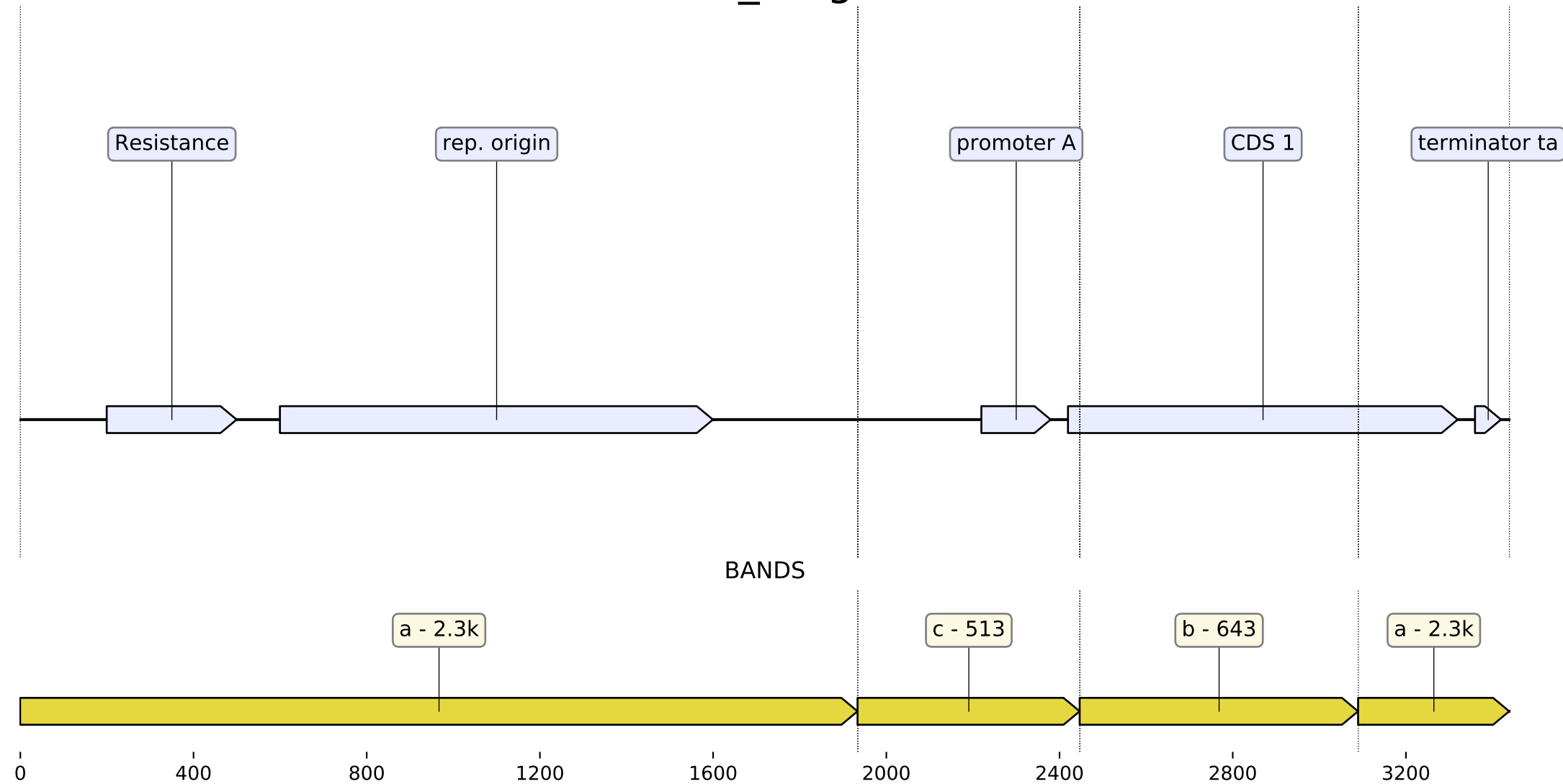
asm\_00.gb



**Bsal + XbaI**



asm\_00.gb



styl

4000

700

100

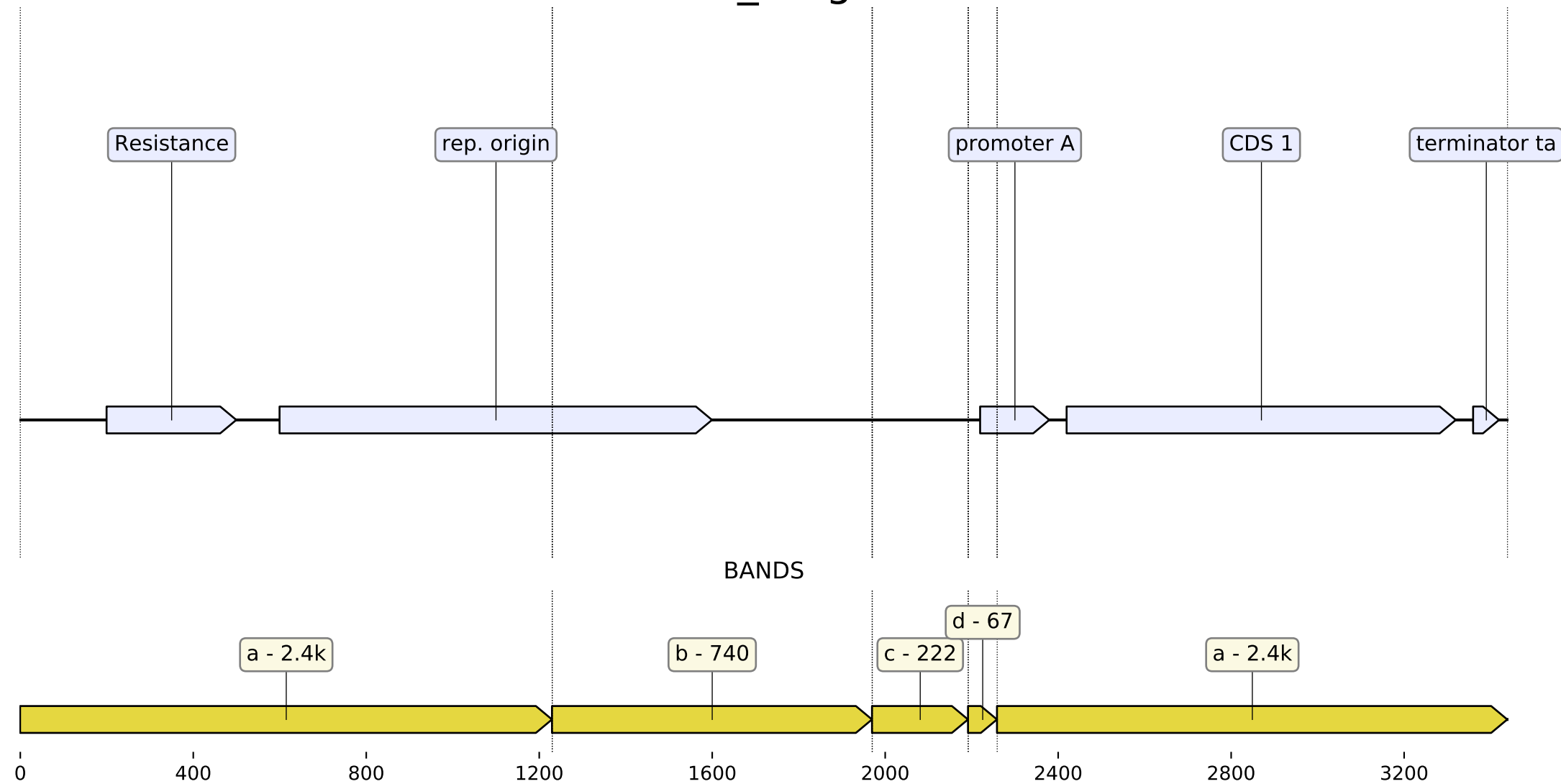
a

b

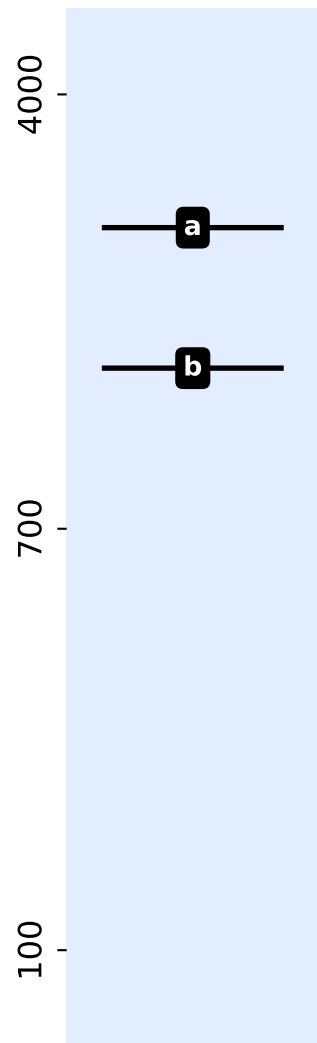
c

d

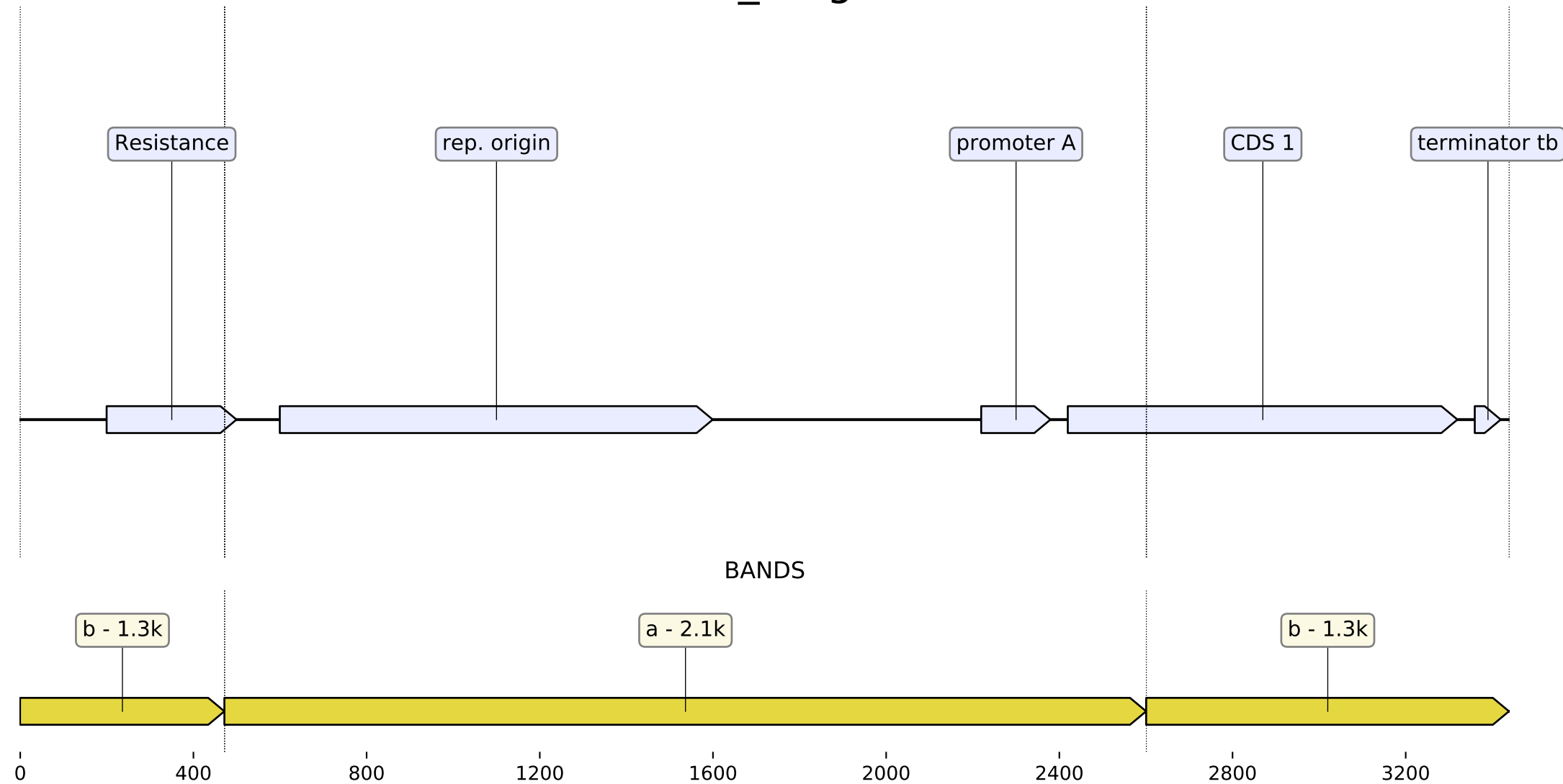
asm\_00.gb



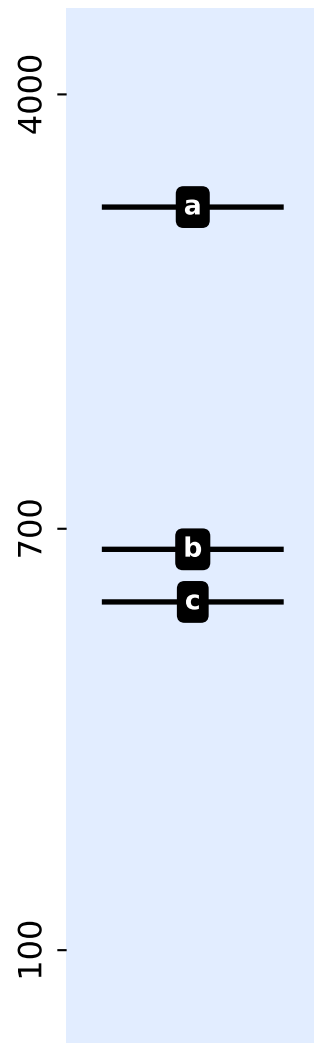
**BamHI + NcoI**



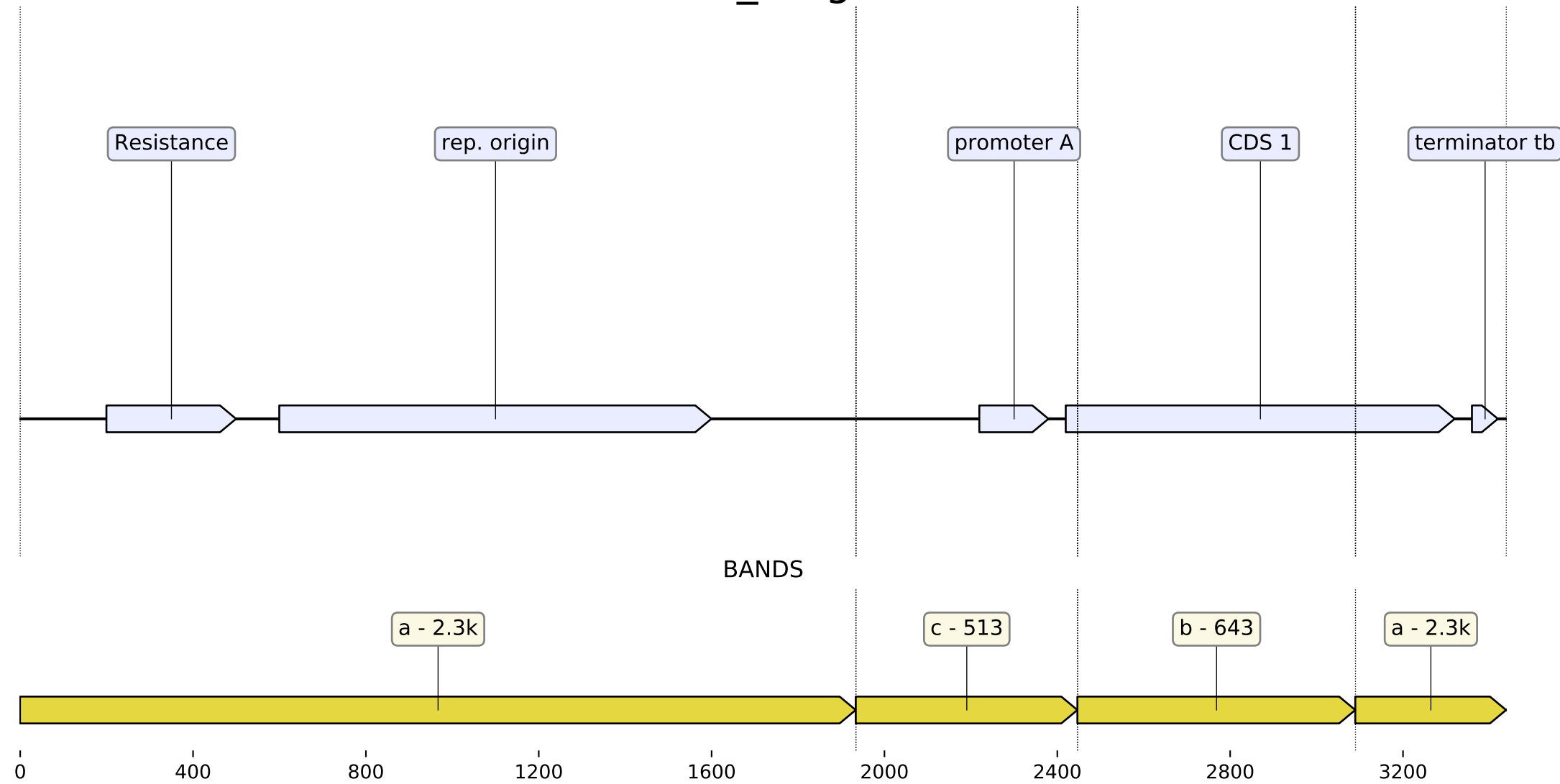
asm\_01.gb



**Bsal + XbaI**



asm\_01.gb



styl

4000

700

100

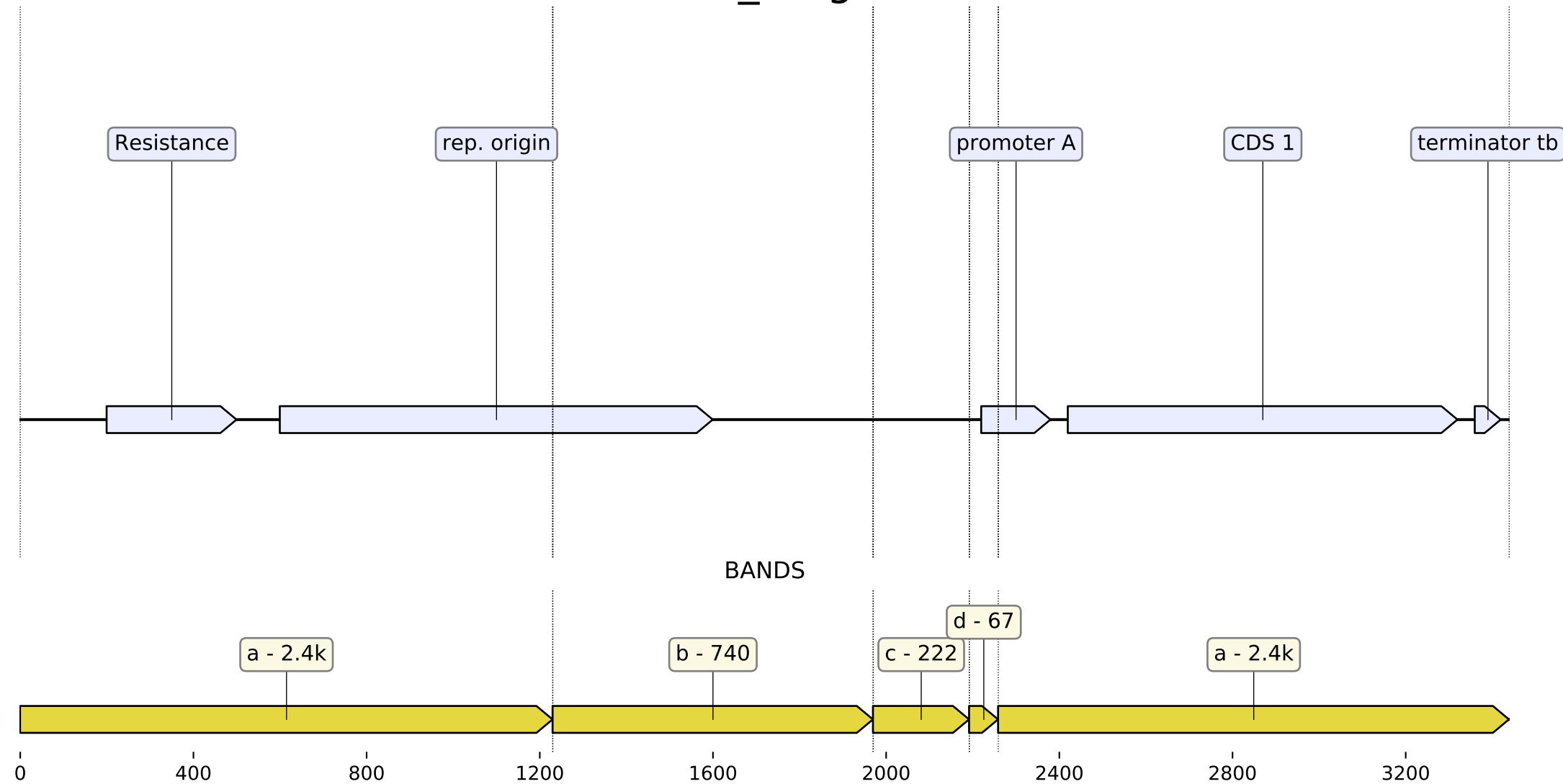
a

b

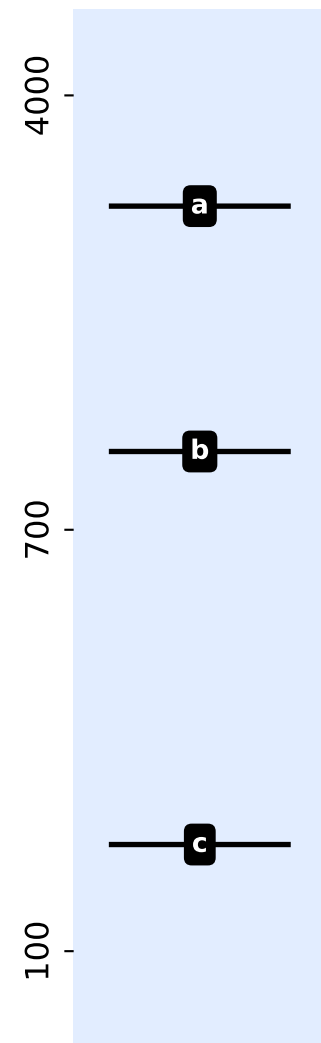
c

d

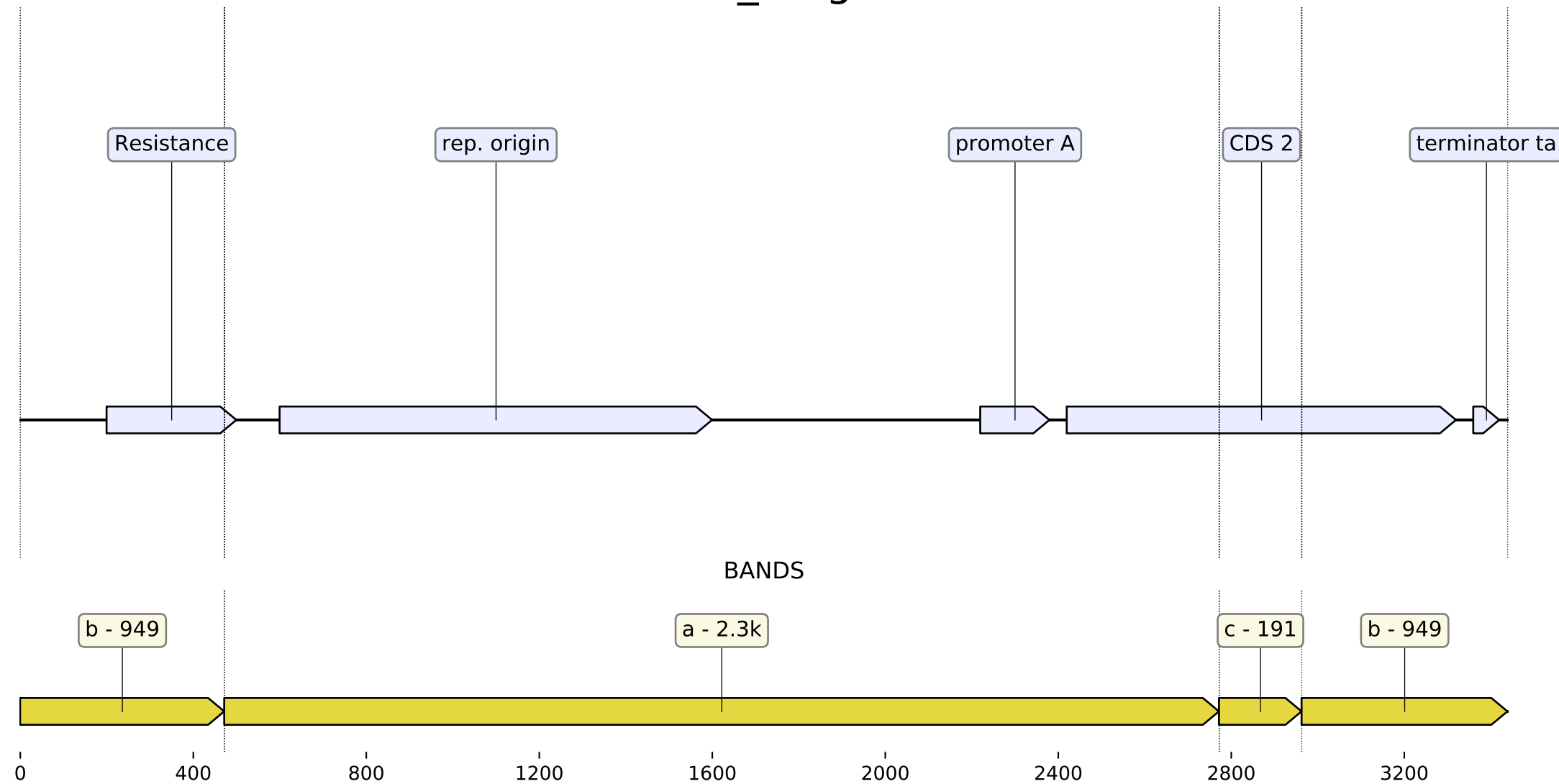
asm\_01.gb



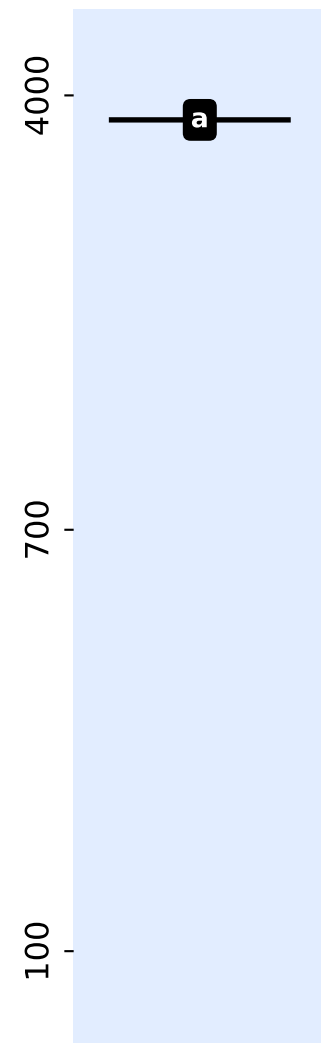
# BamHI + NcoI



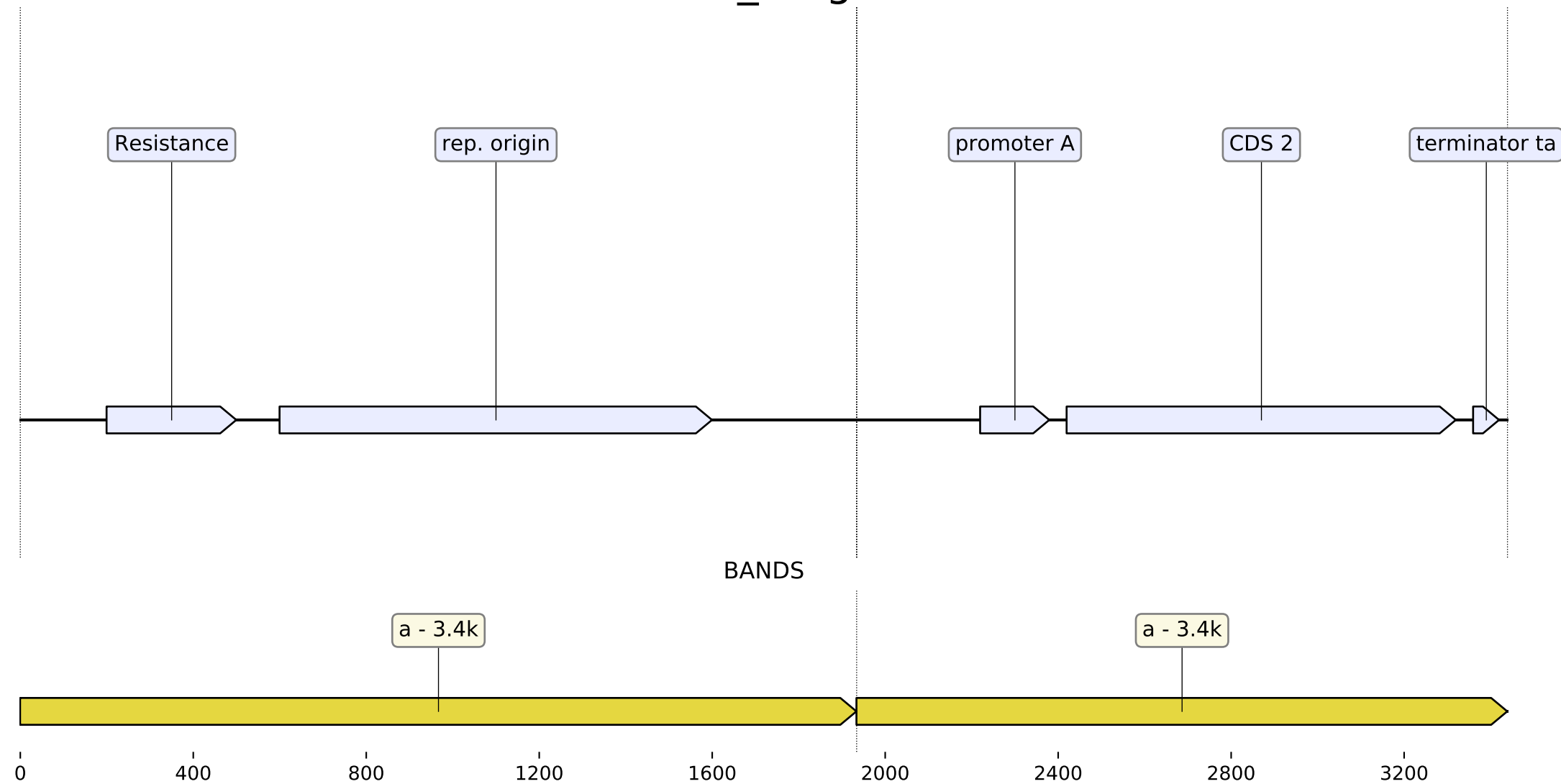
asm\_02.gb



**Bsal + XbaI**



asm\_02.gb





styl

4000

700

100

a

c

d

e

asm\_02.gb

Resistance

rep. origin

promoter A

CDS 2

terminator ta

BANDS

e - 67

c - 704

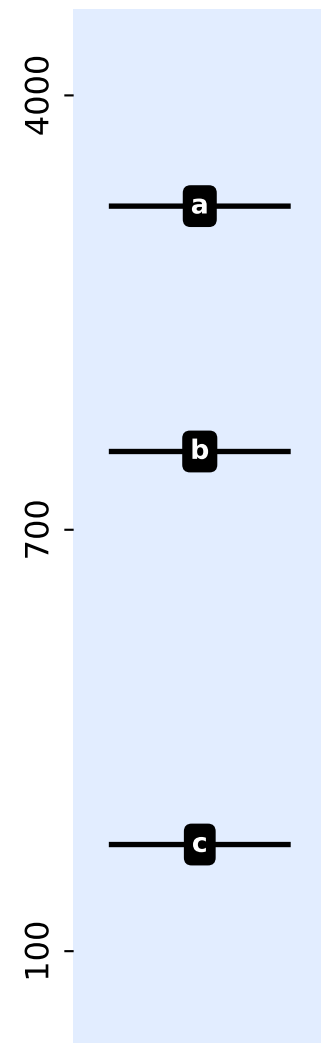
d - 222

b - 740

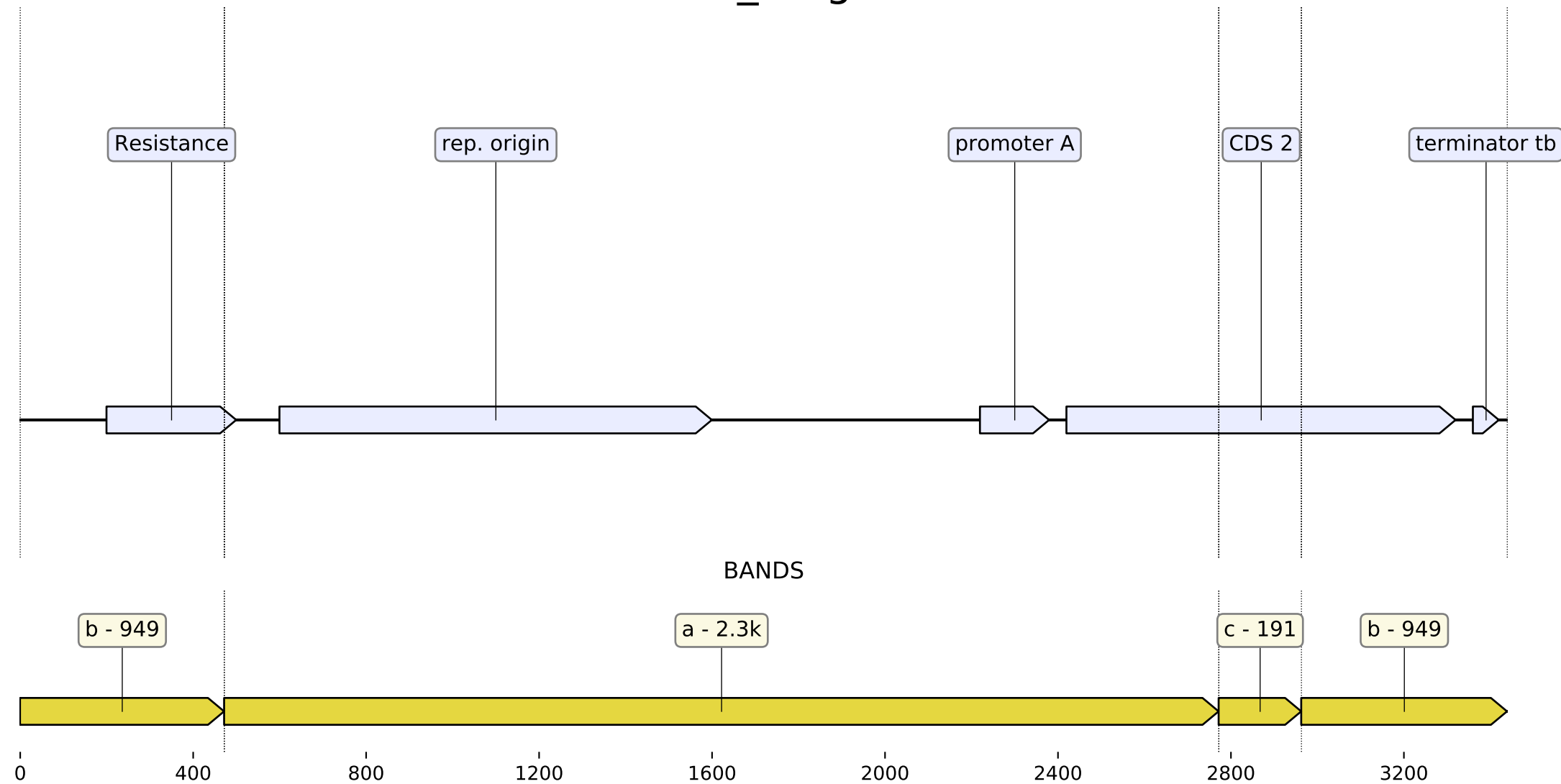
a - 1.7k

a - 1.7k

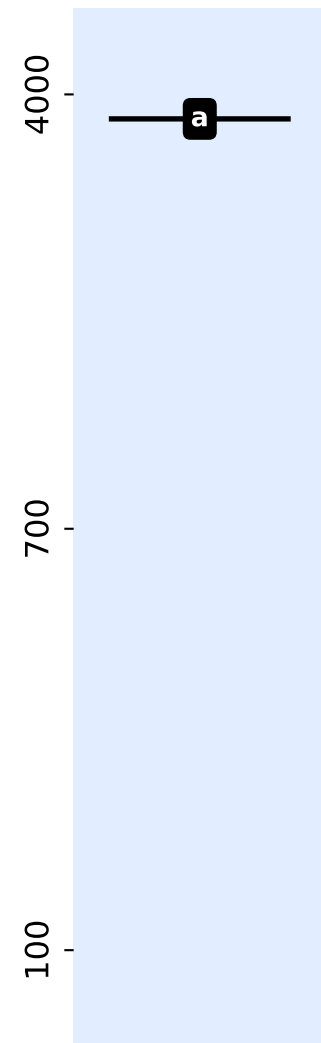
# BamHI + NcoI



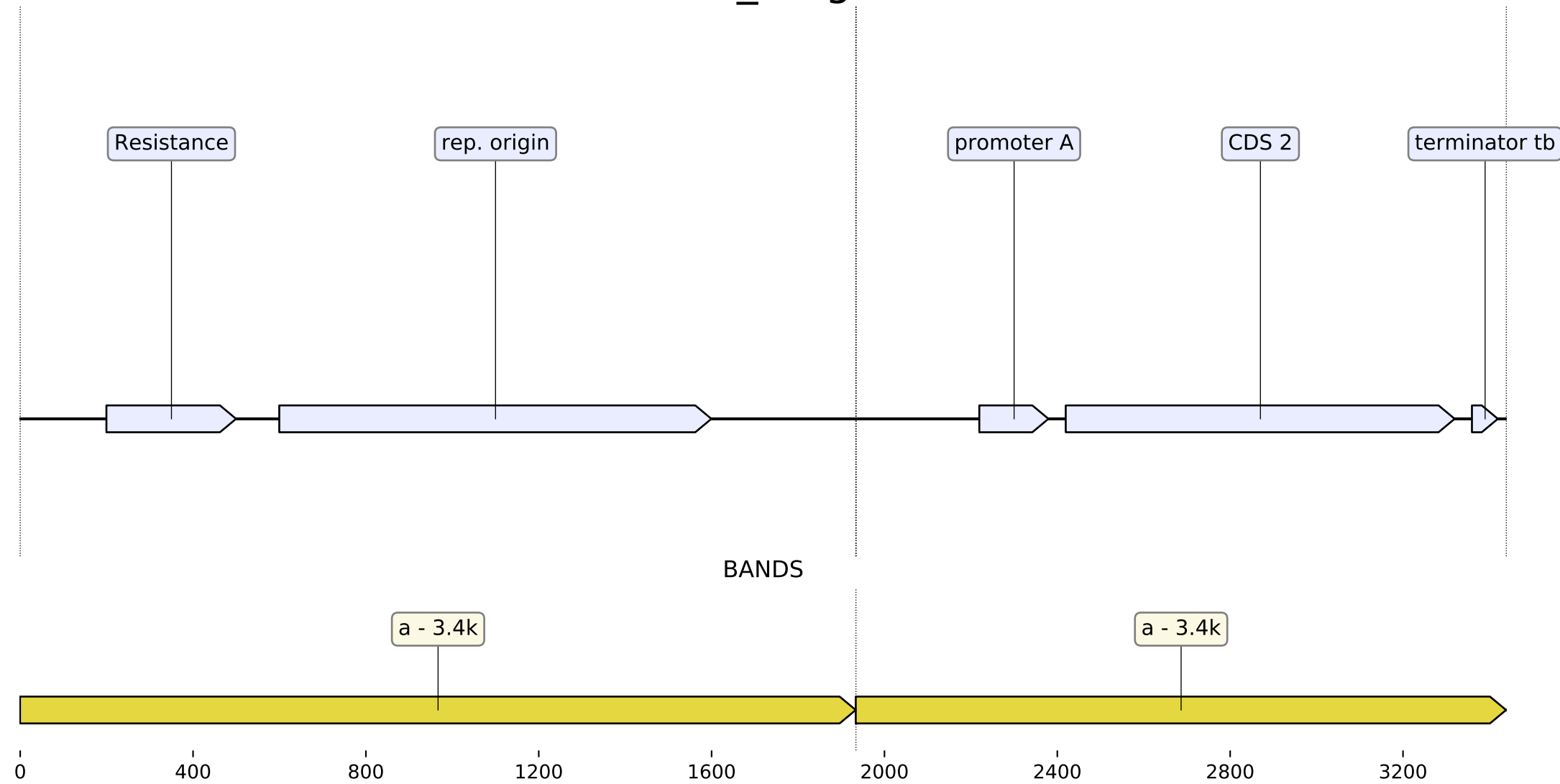
asm\_03.gb



**Bsal + XbaI**



asm\_03.gb



styl

4000

700

100

a

c

d

e

asm\_03.gb

Resistance

rep. origin

promoter A

CDS 2

terminator tb

BANDS

e - 67

d - 222

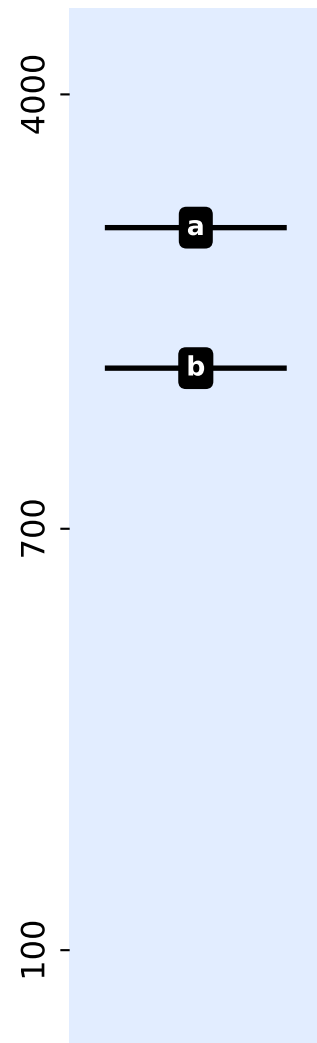
c - 704

a - 1.7k

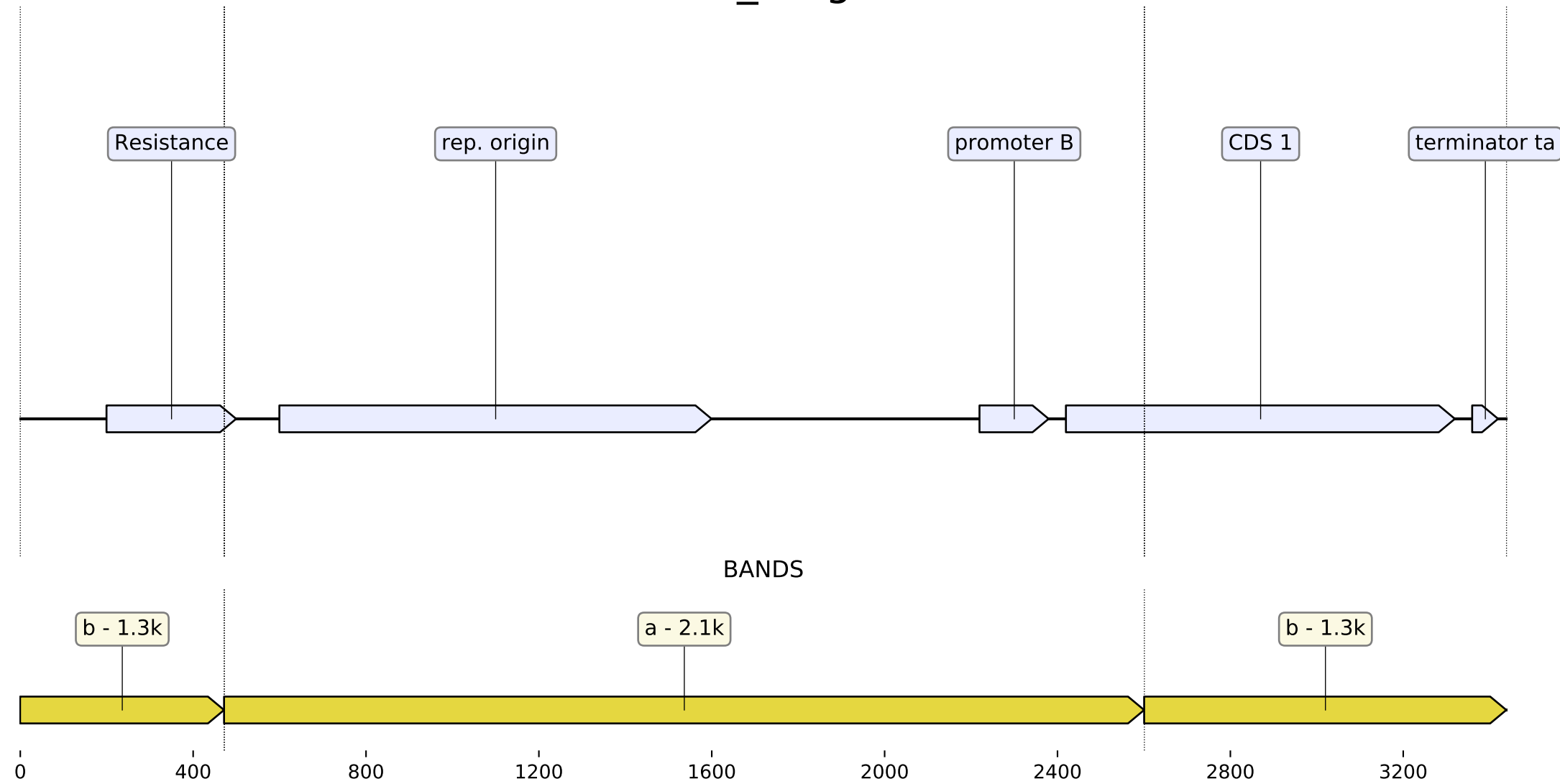
b - 740

a - 1.7k

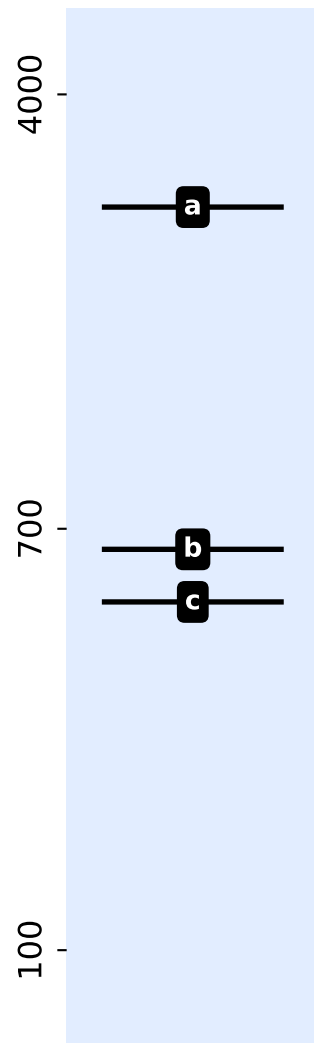
**BamHI + NcoI**



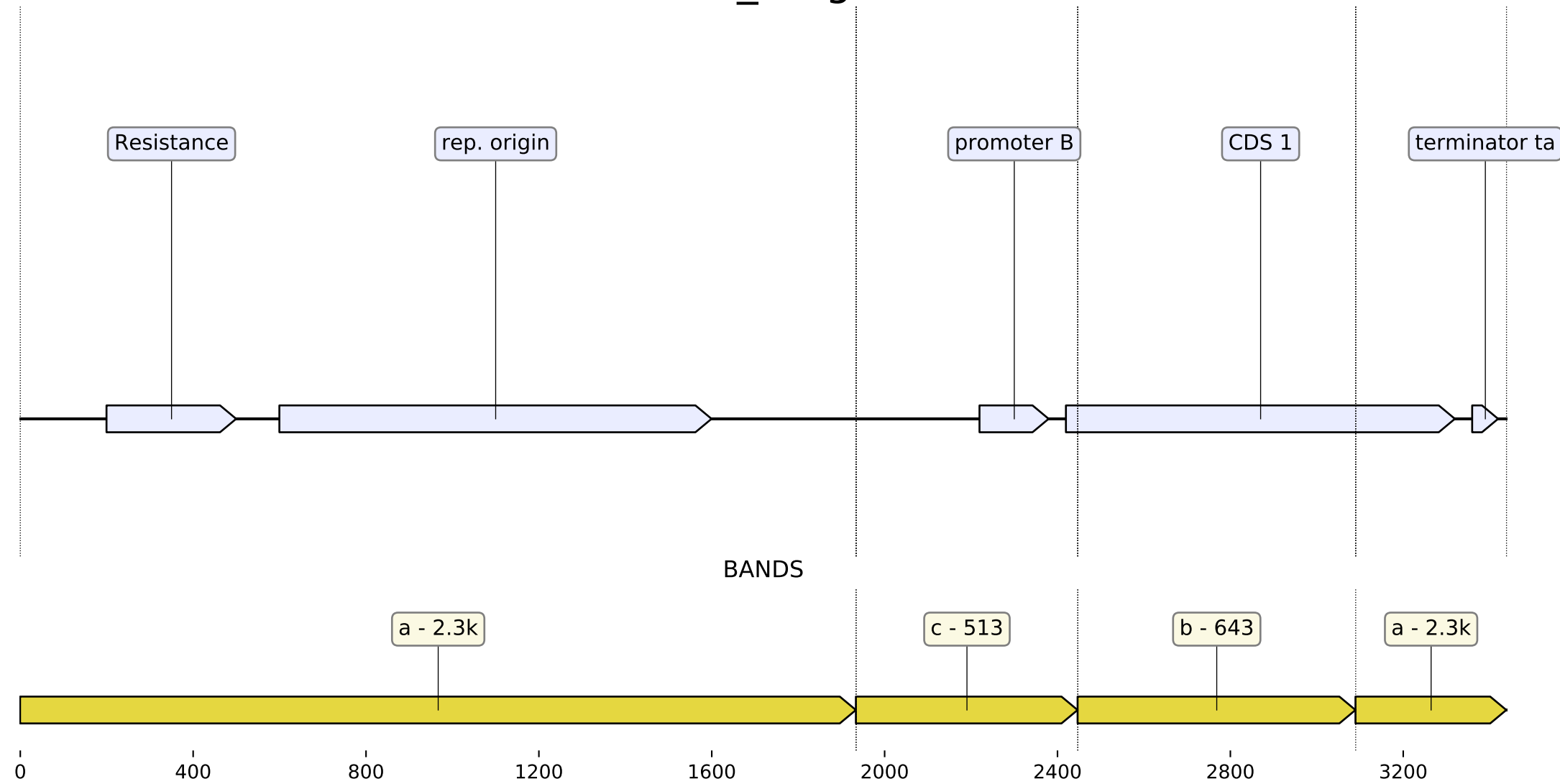
asm\_04.gb



**Bsal + XbaI**



asm\_04.gb



styl

4000

700

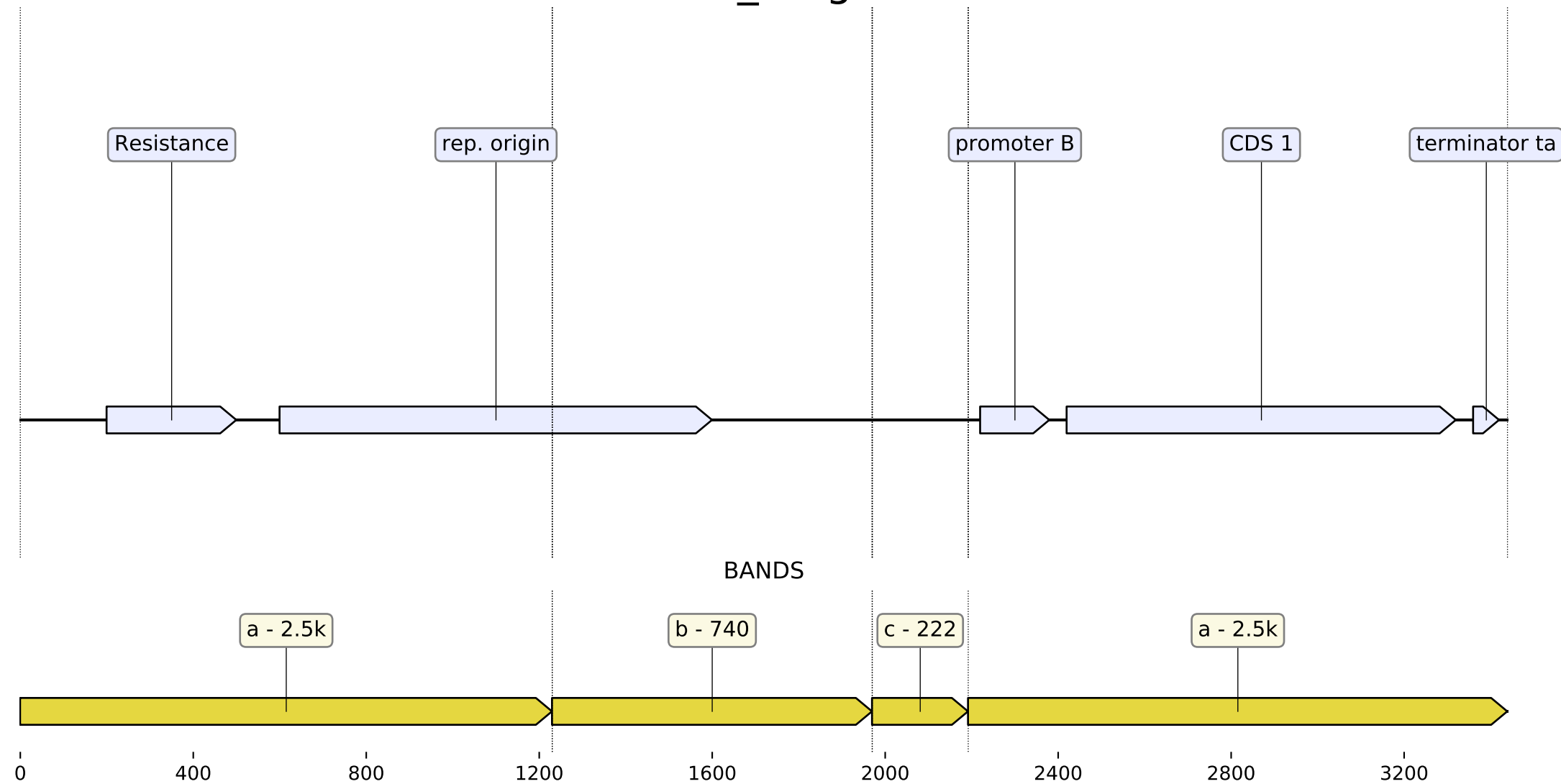
100

a

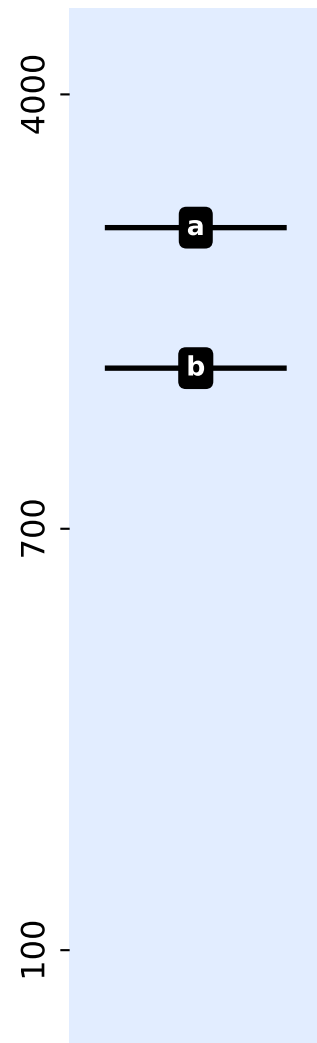
b

c

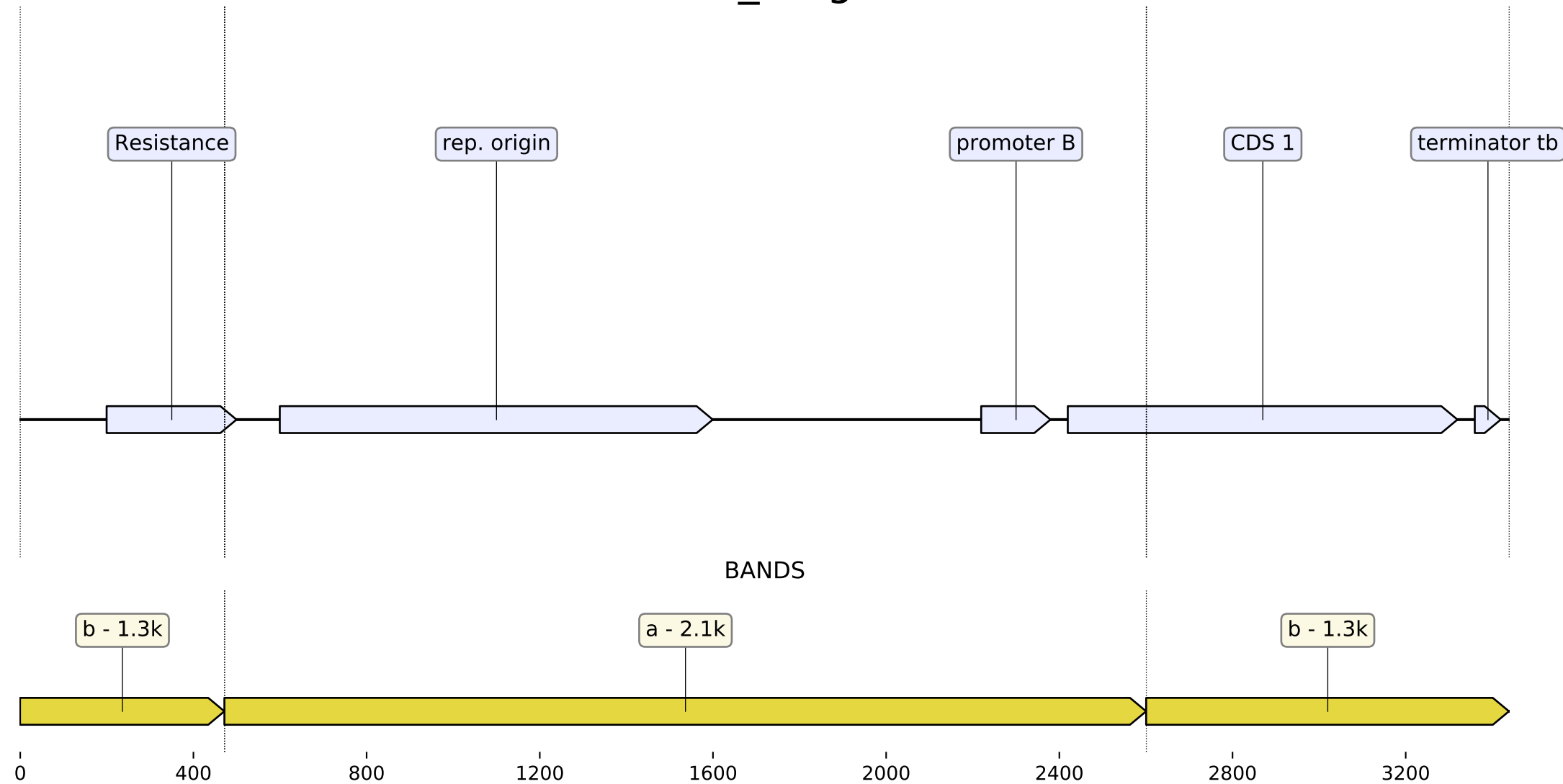
asm\_04.gb



**BamHI + NcoI**

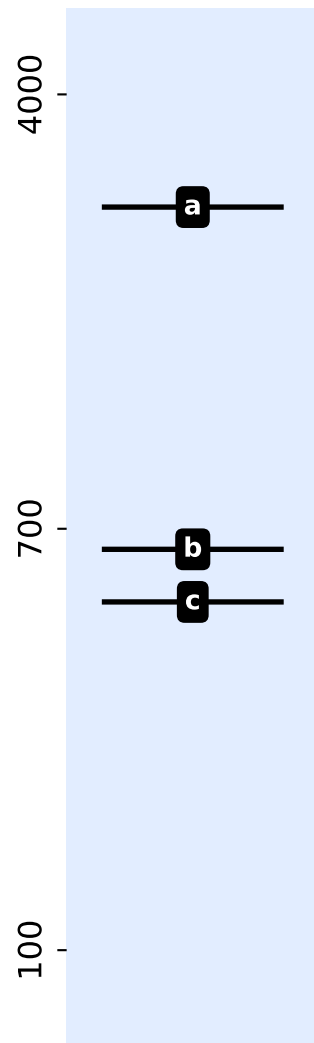


asm\_05.gb

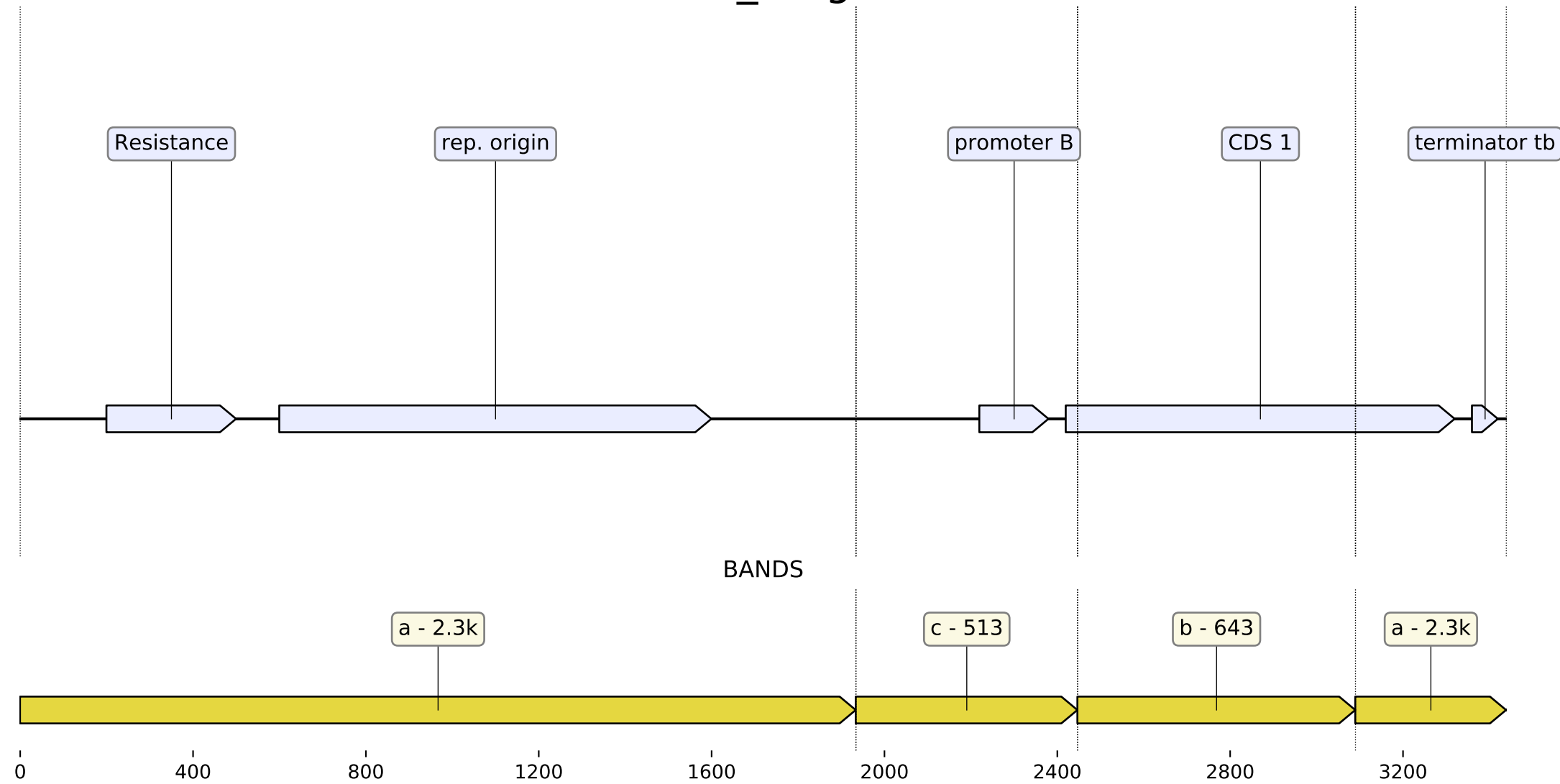




**Bsal + XbaI**



asm\_05.gb



styl

4000

700

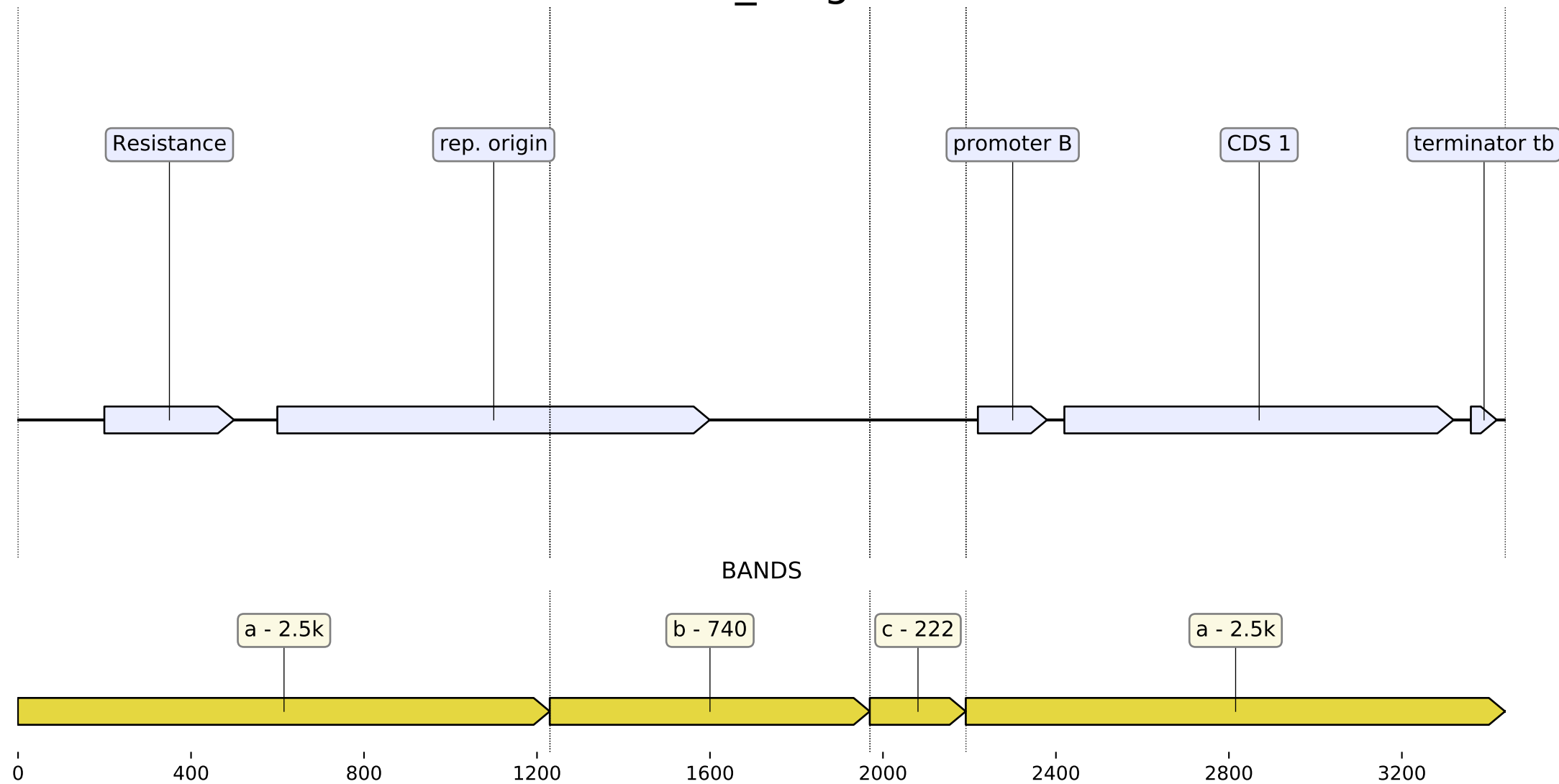
100

a

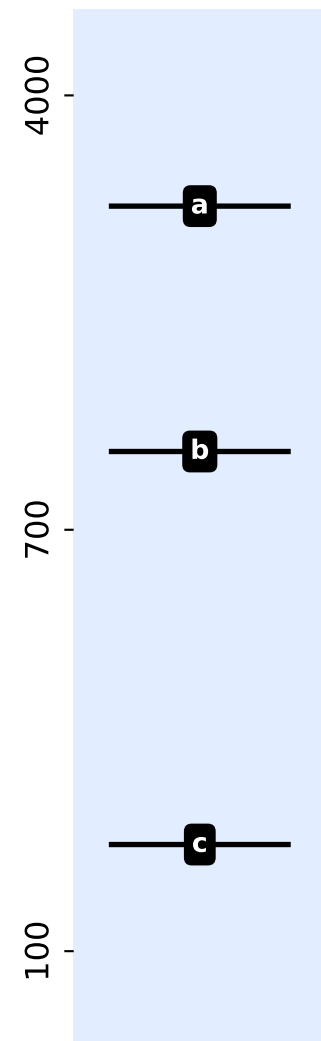
b

c

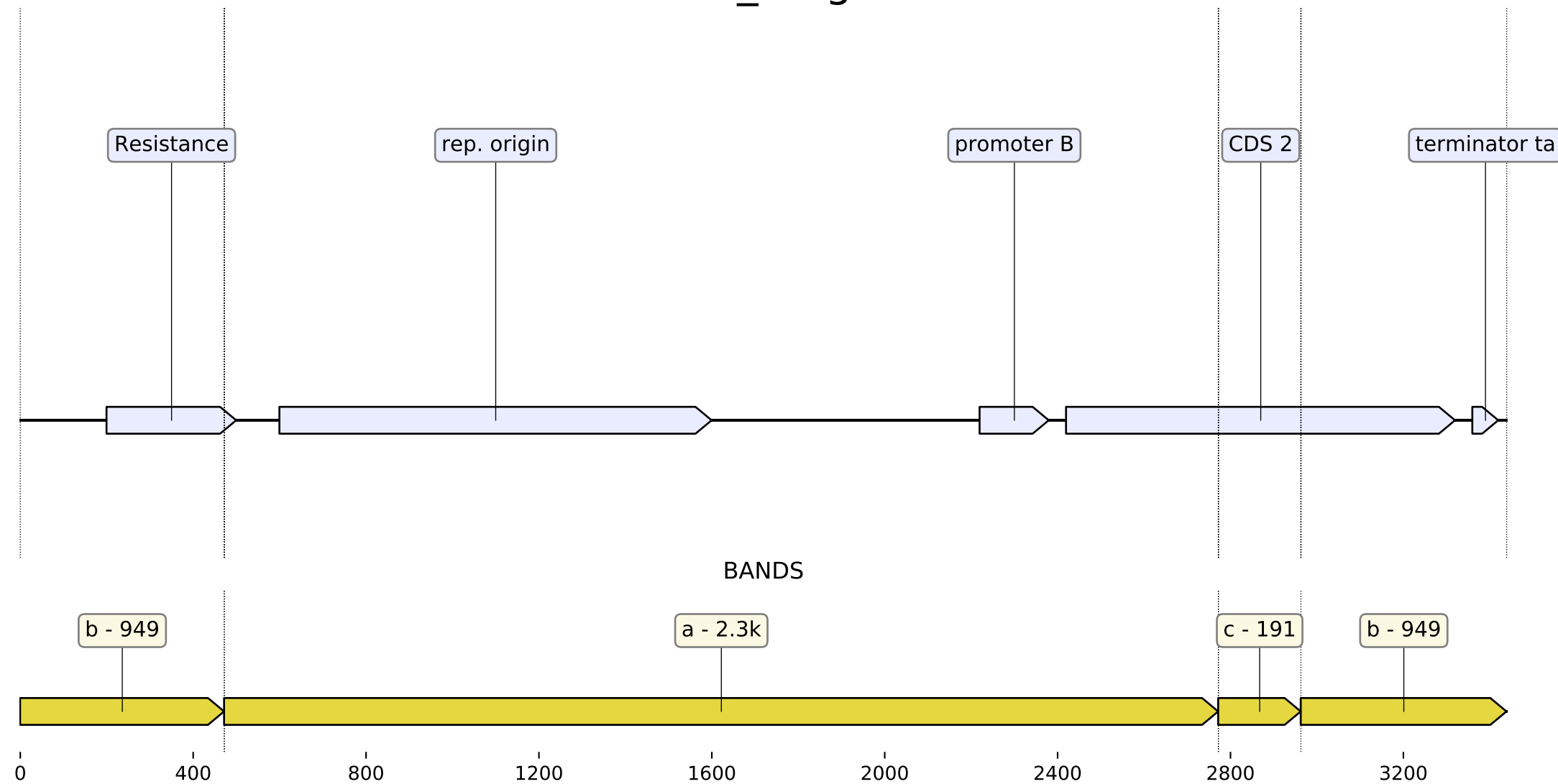
asm\_05.gb



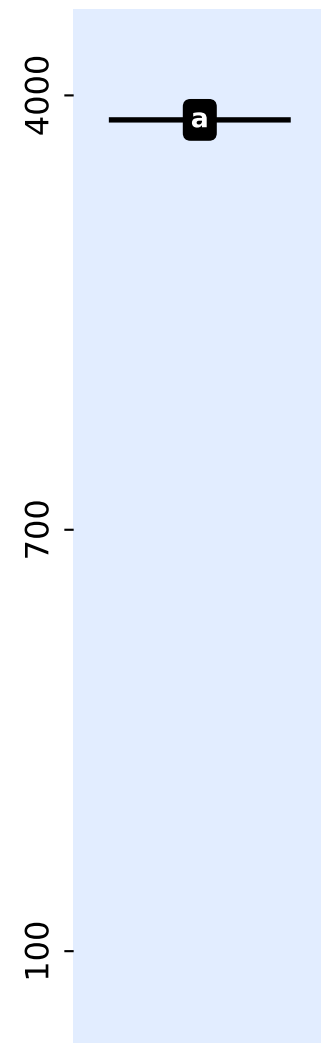
# BamHI + NcoI



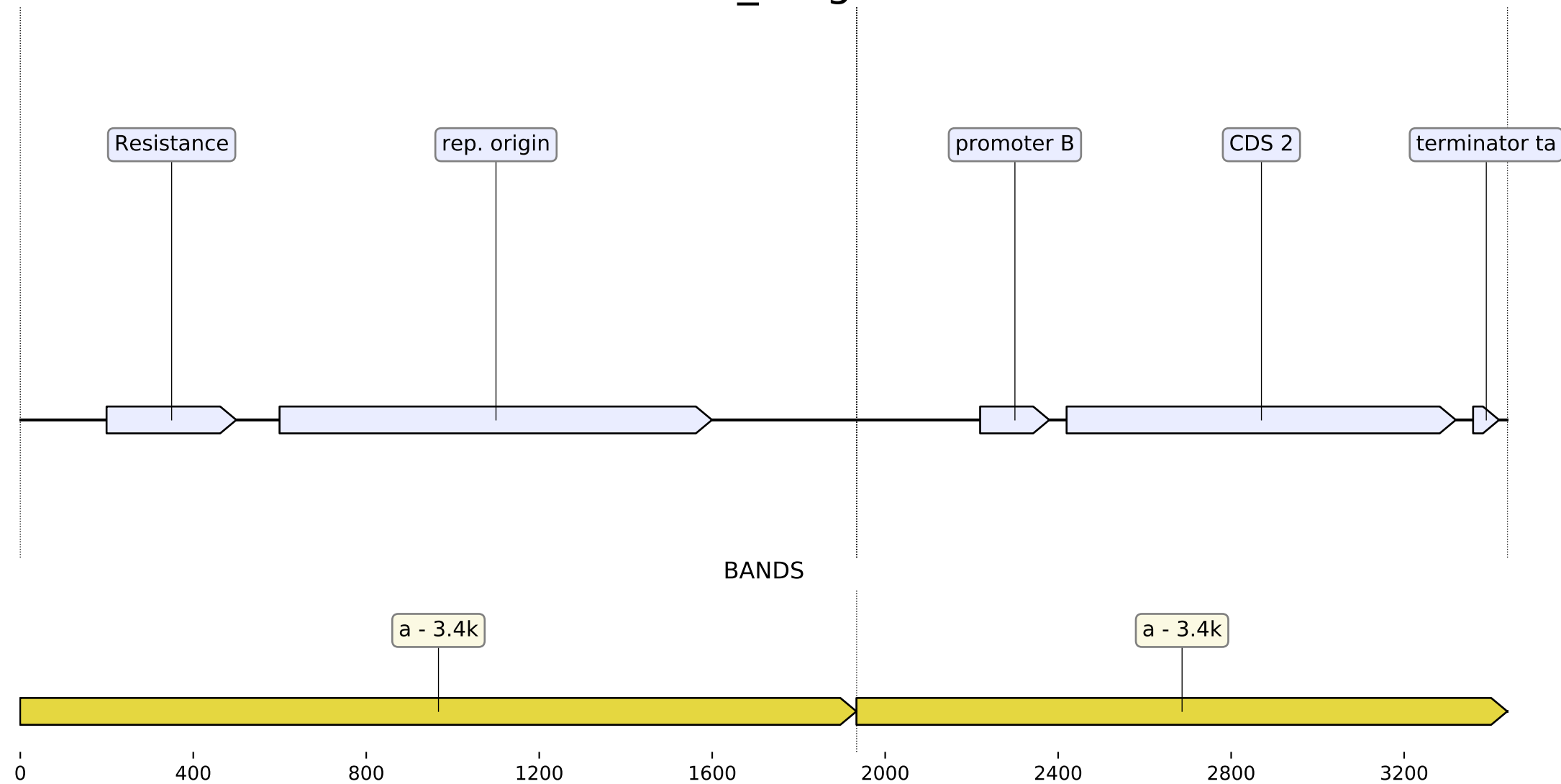
asm\_06.gb



**Bsal + XbaI**



asm\_06.gb



styl

4000

700

100

a

c

d

asm\_06.gb

Resistance

rep. origin

promoter B

CDS 2

terminator ta

BANDS

a - 1.7k

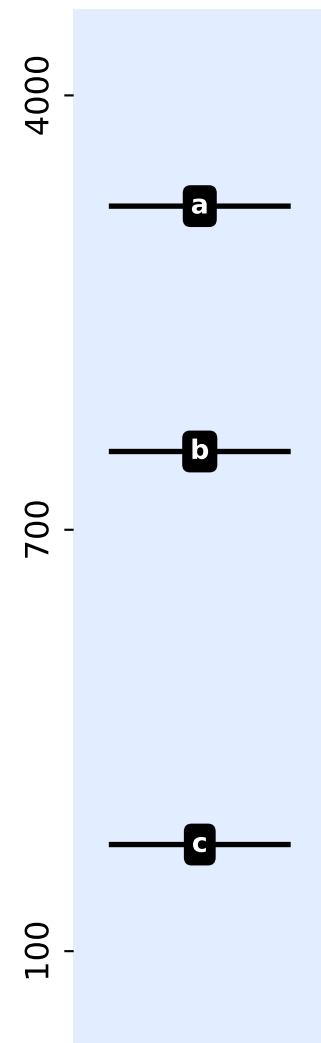
c - 740

d - 222

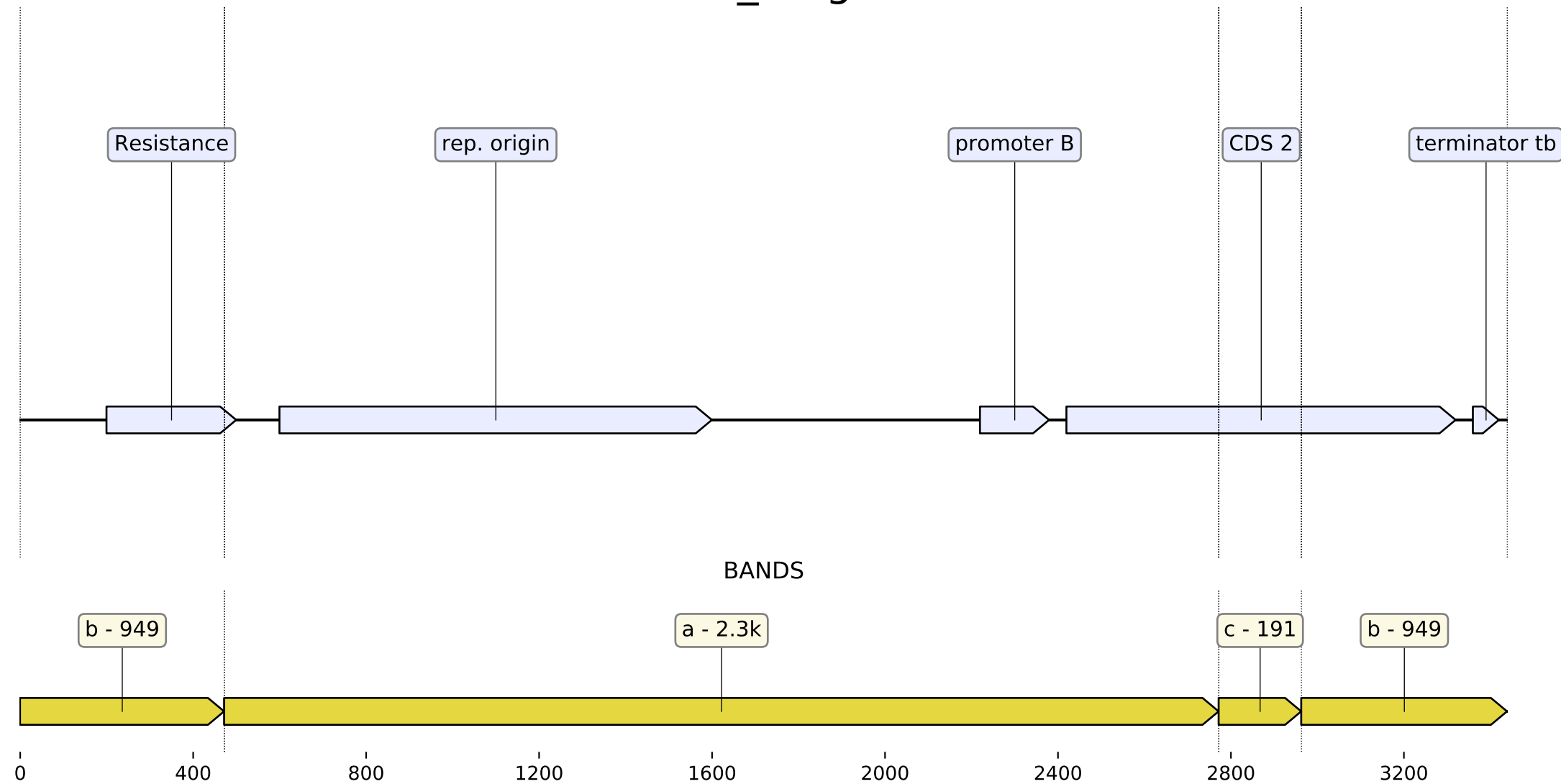
b - 771

a - 1.7k

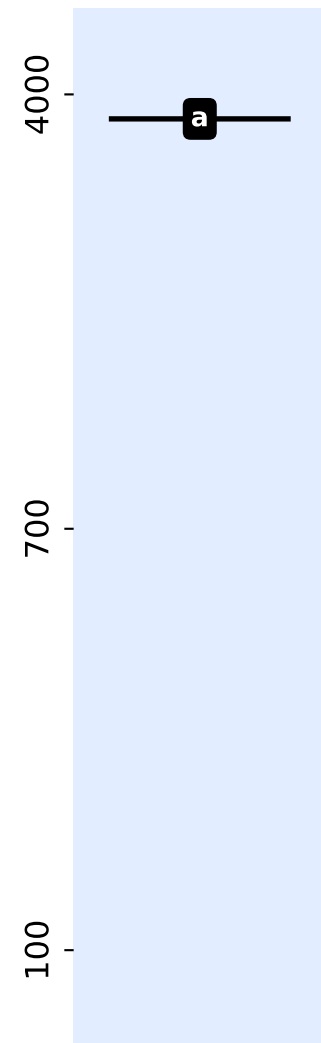
# BamHI + NcoI



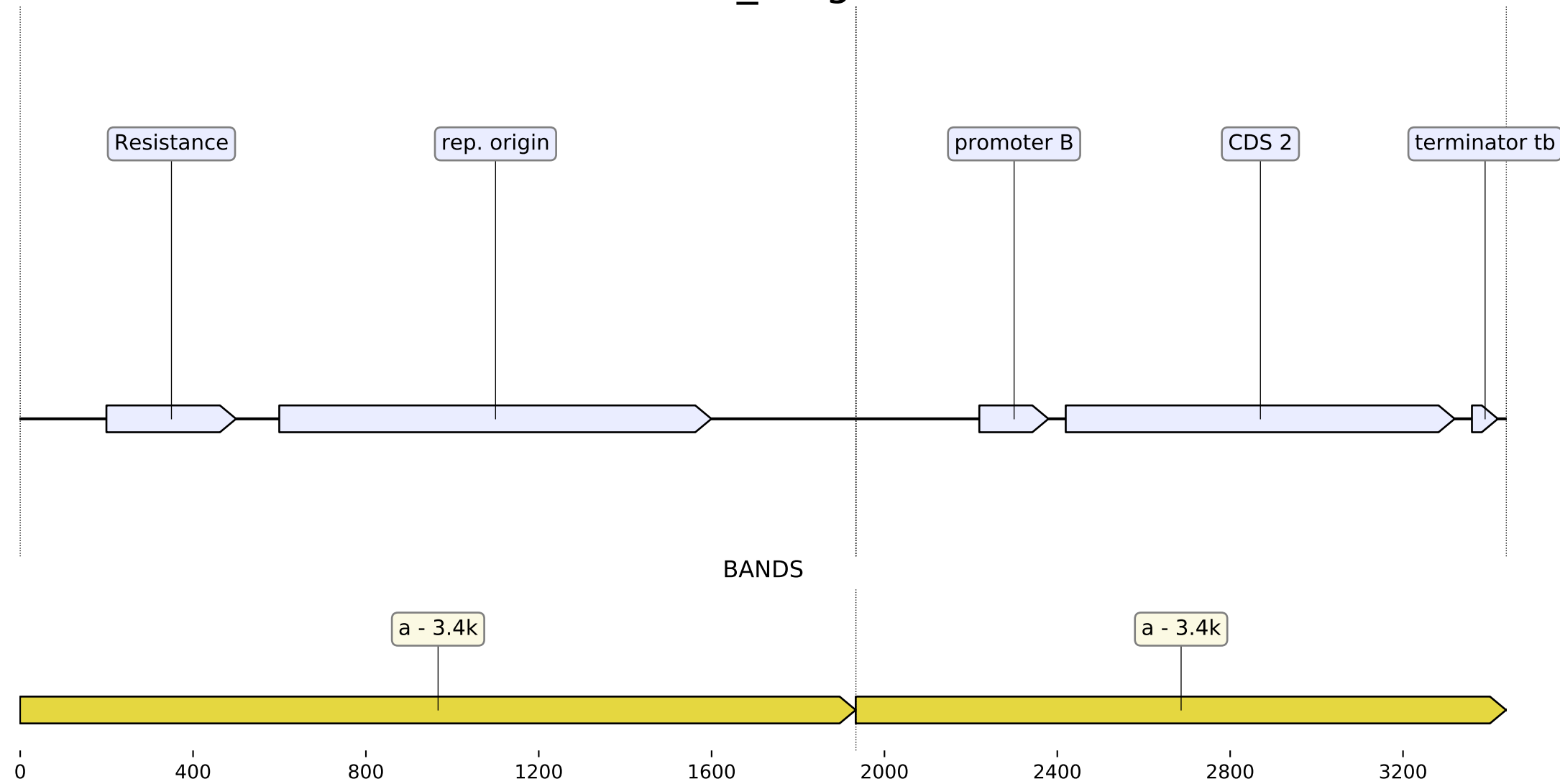
asm\_07.gb



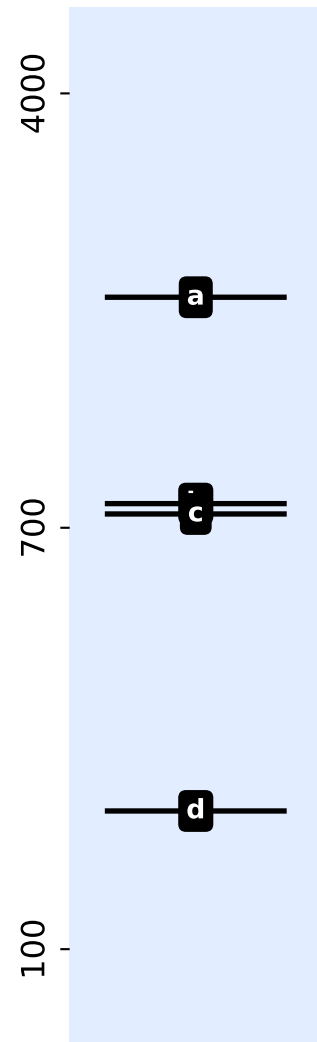
**Bsal + XbaI**



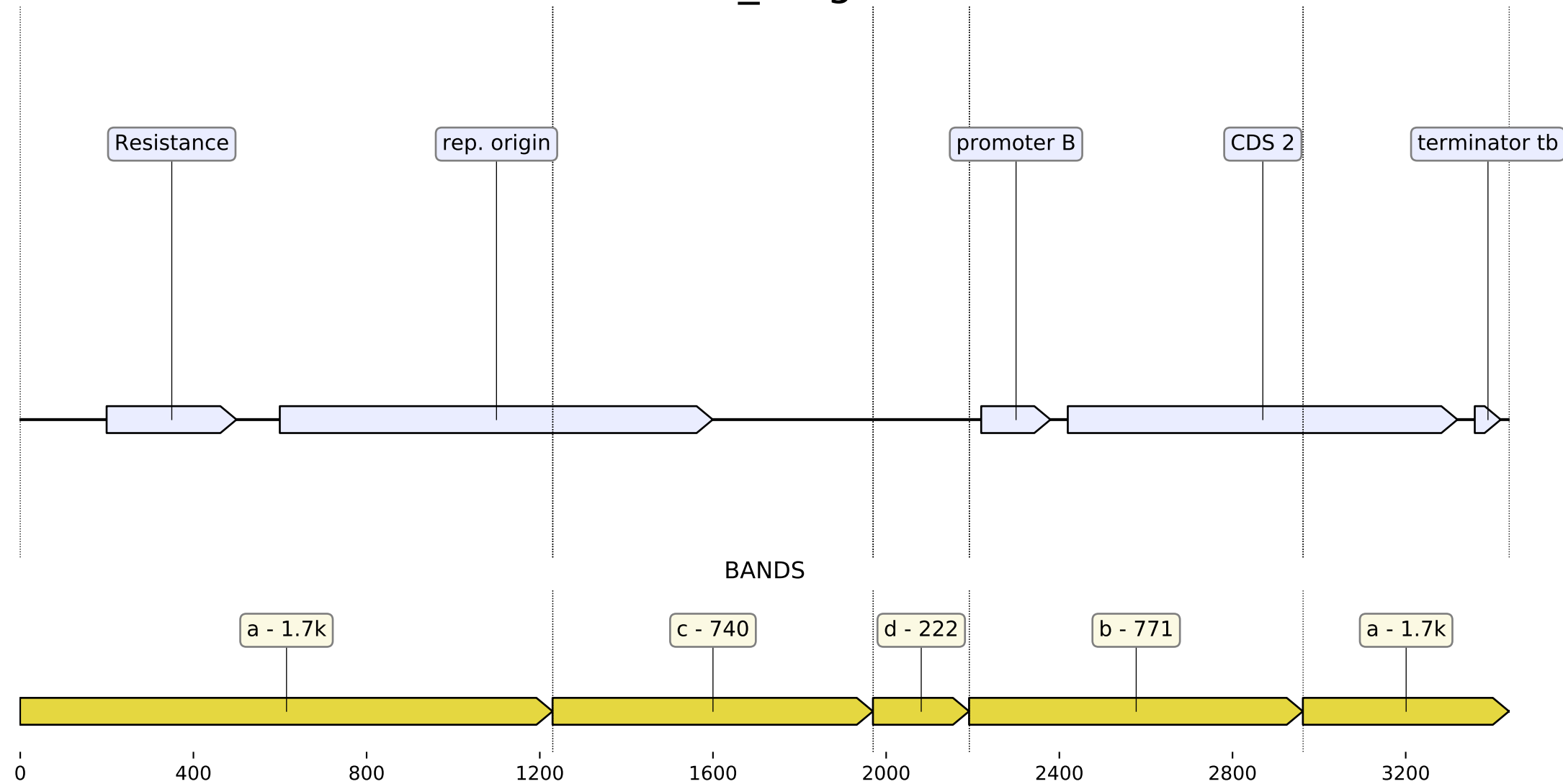
asm\_07.gb



styl

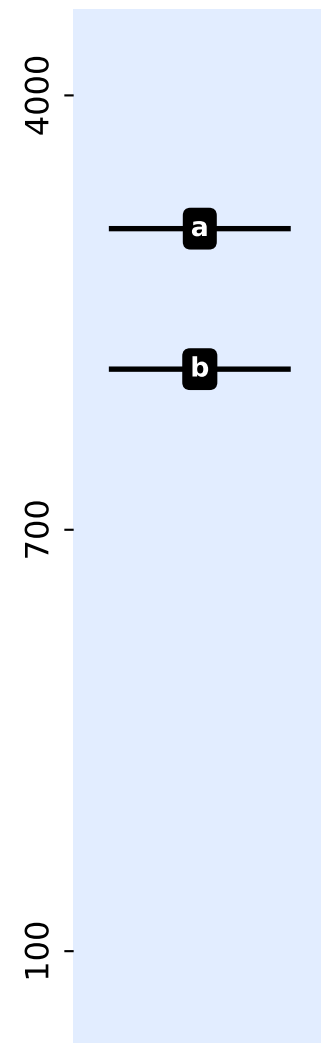


asm\_07.gb

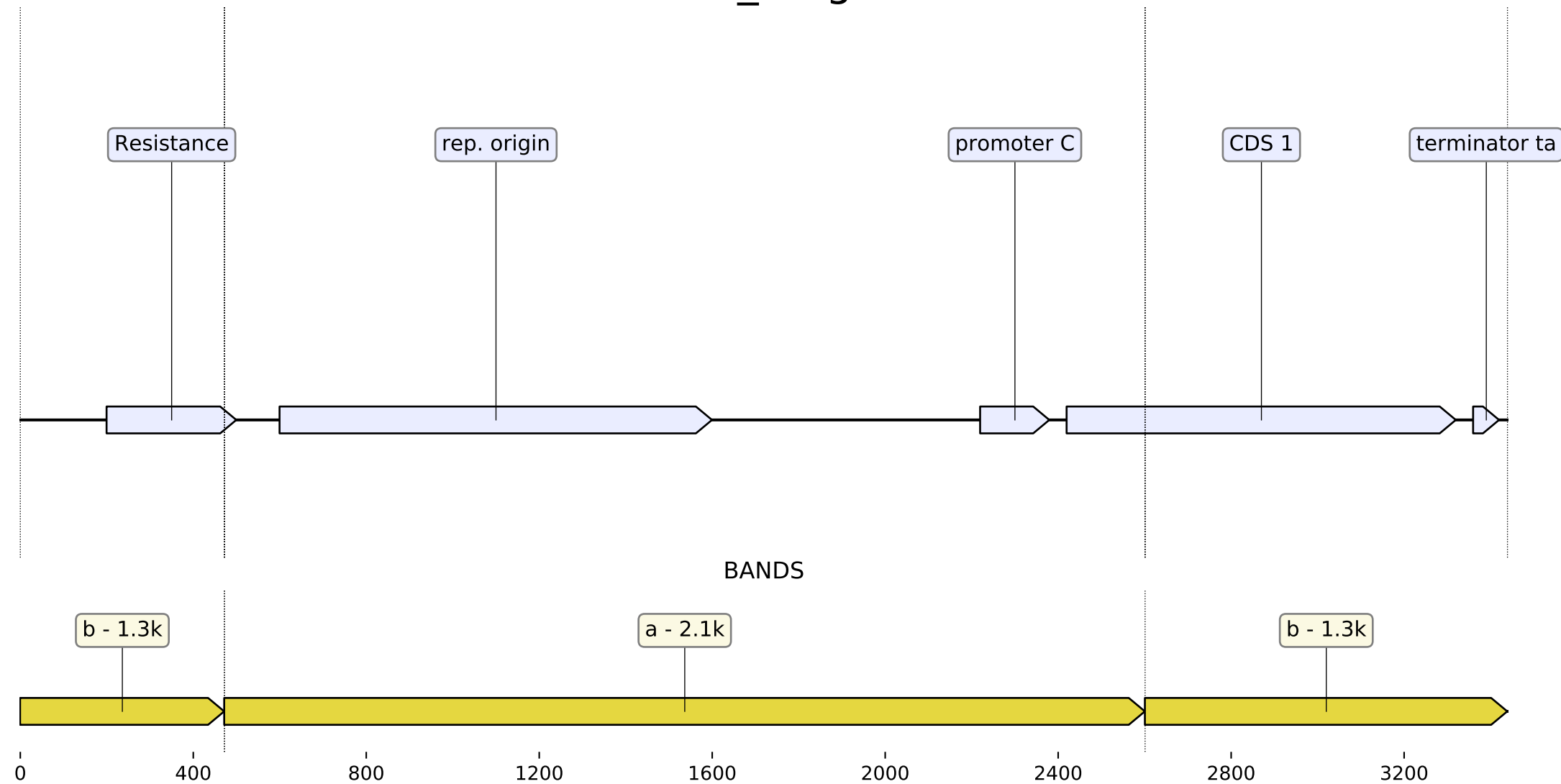




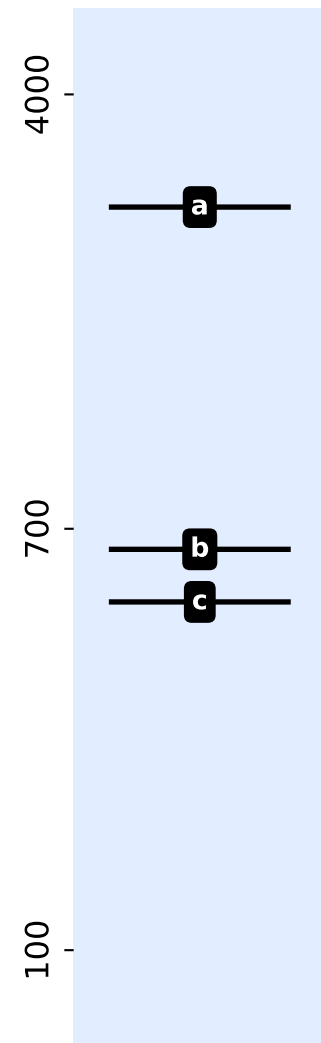
**BamHI + NcoI**



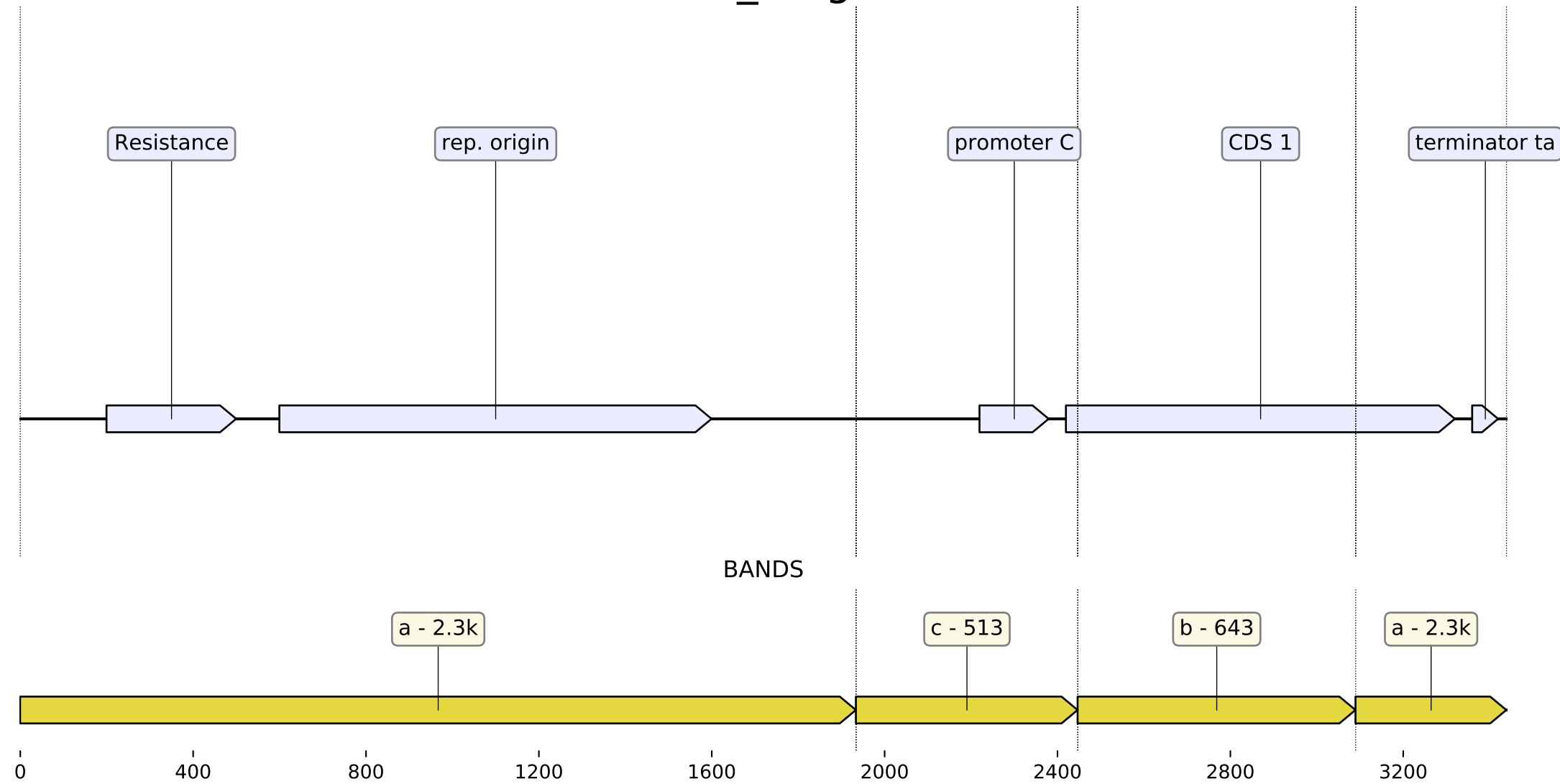
asm\_08.gb



**Bsal + XbaI**



asm\_08.gb



styl

4000

700

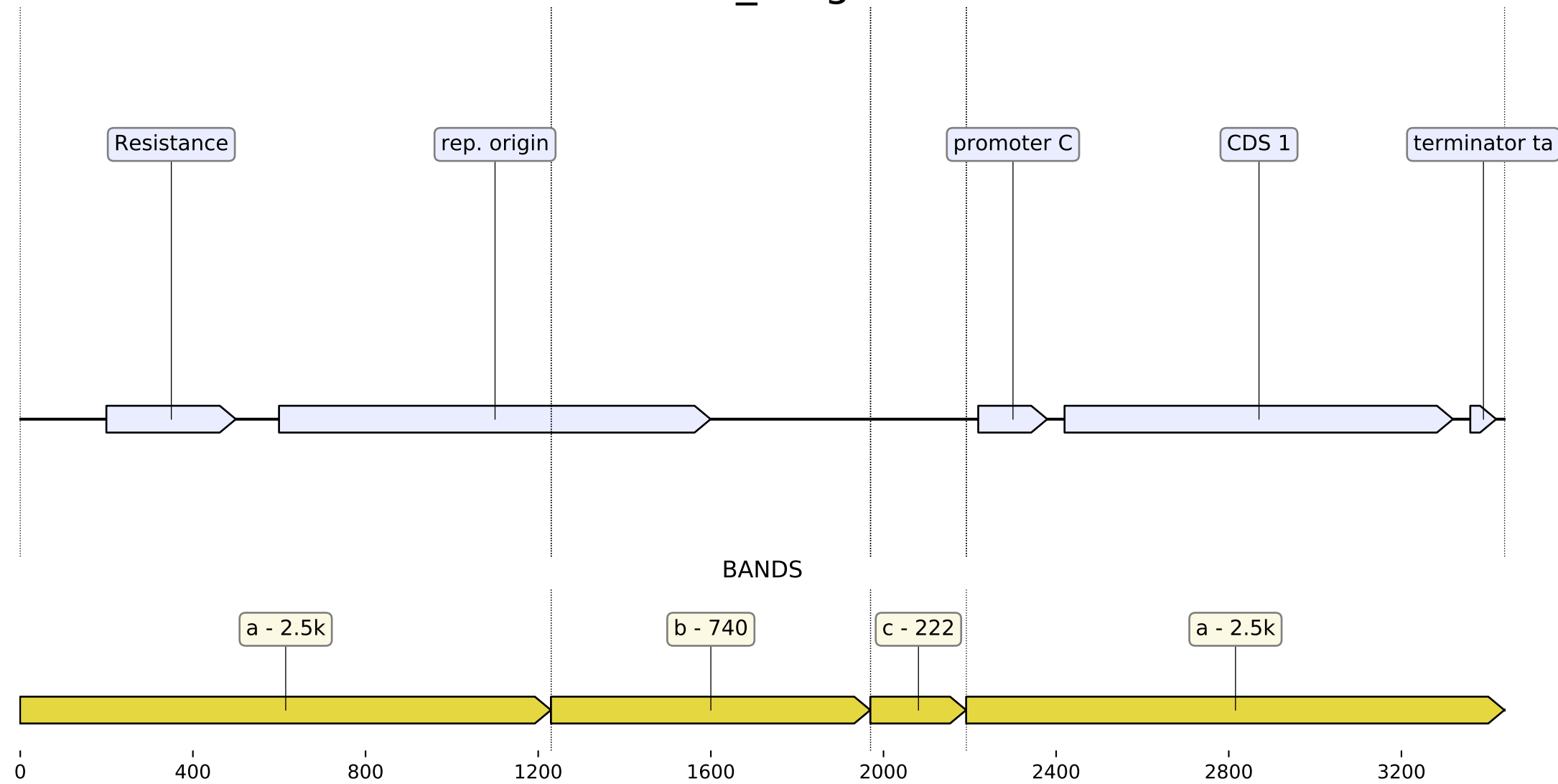
100

a

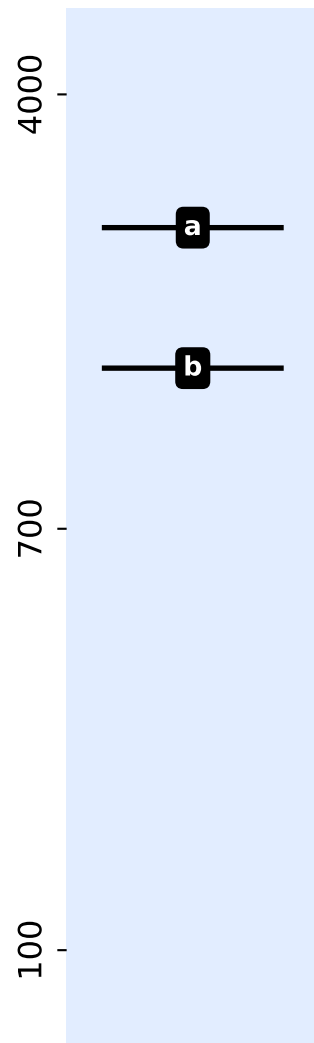
b

c

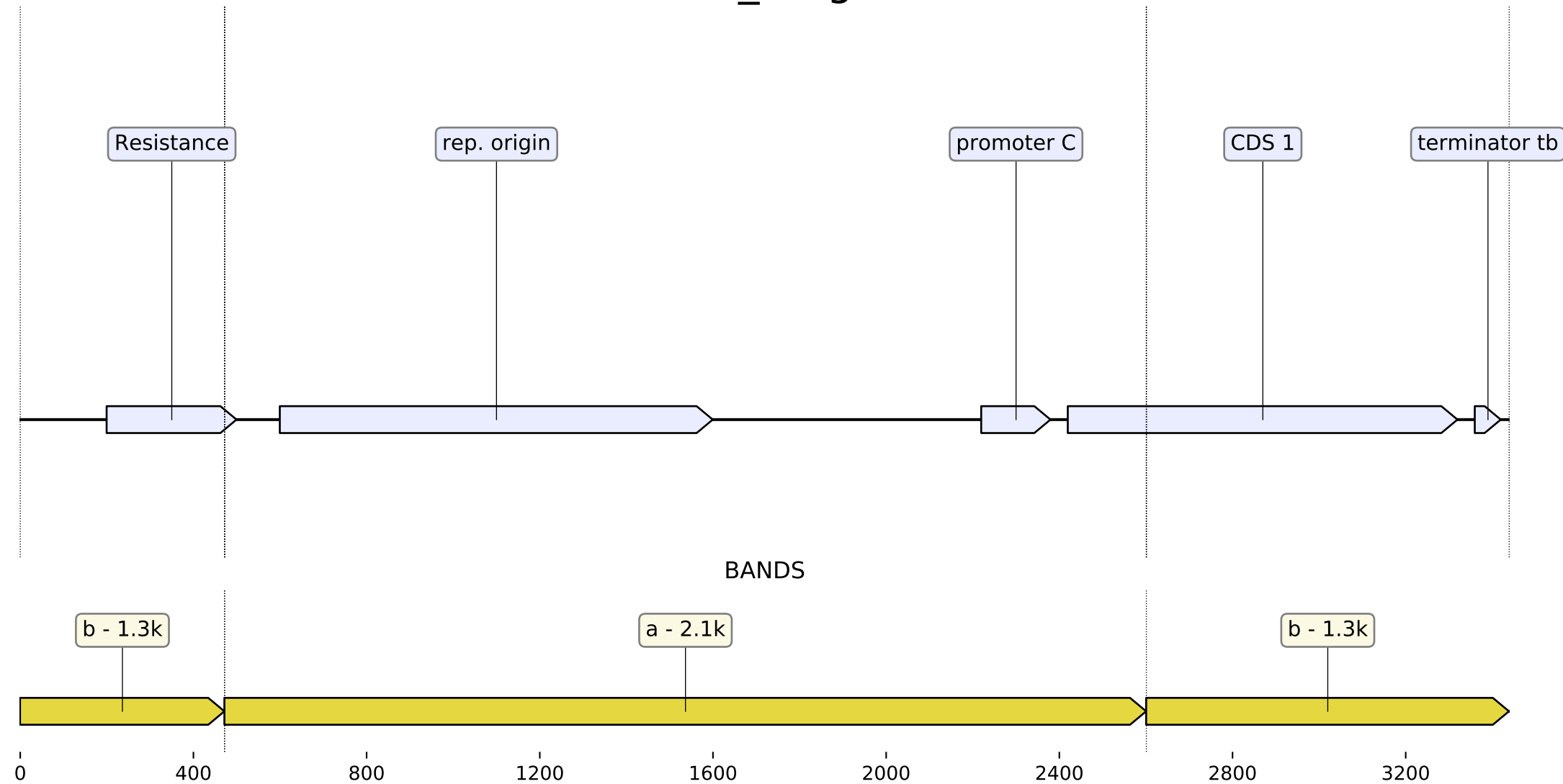
asm\_08.gb



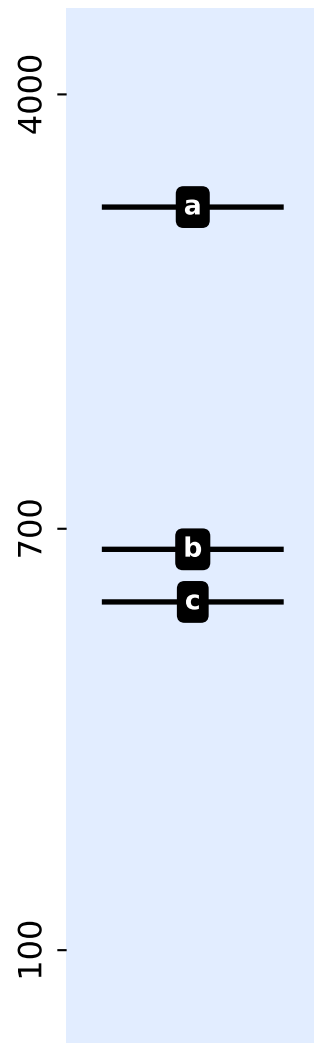
**BamHI + NcoI**



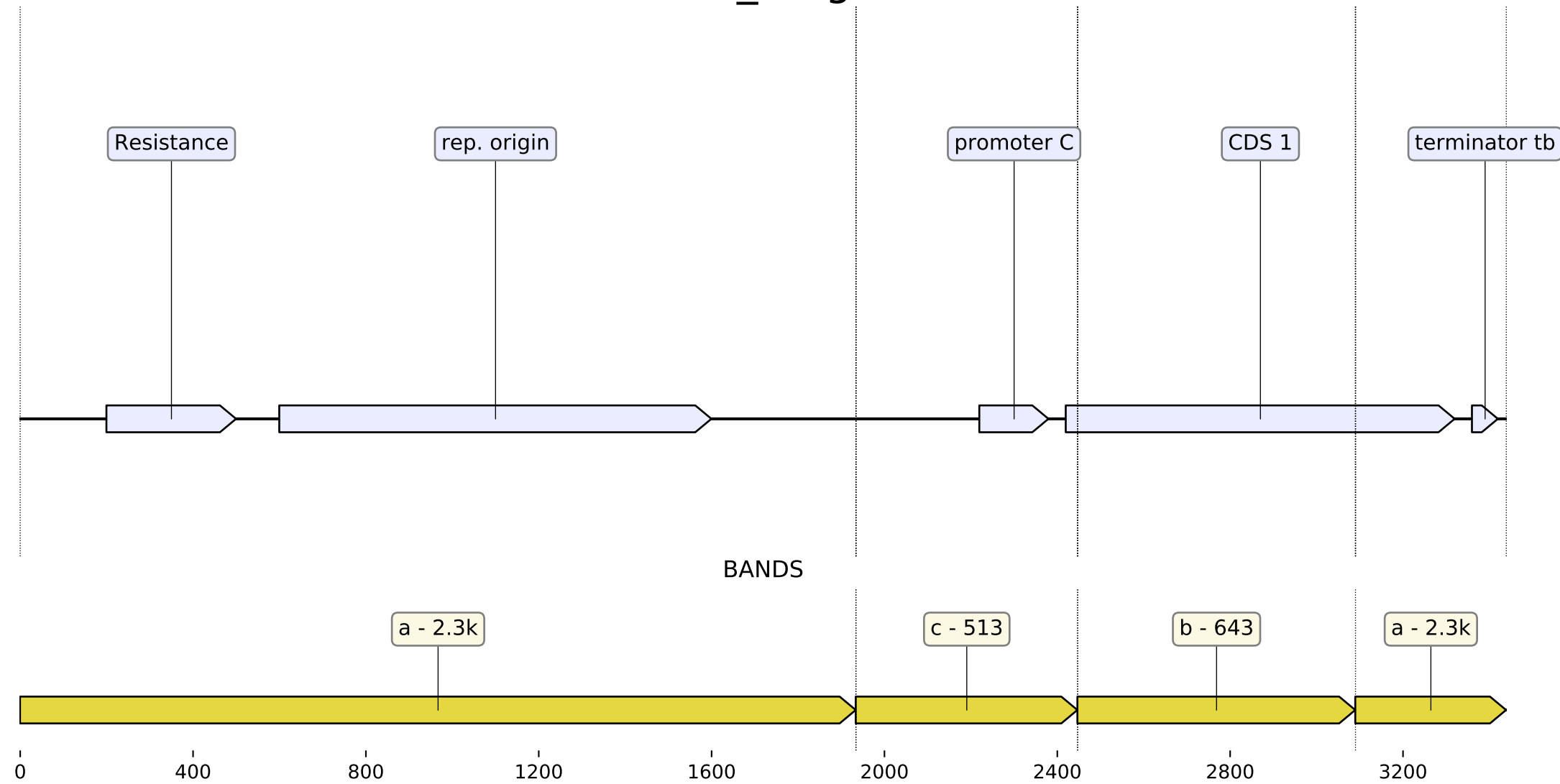
asm\_09.gb



**Bsal + XbaI**



asm\_09.gb



styl

4000

700

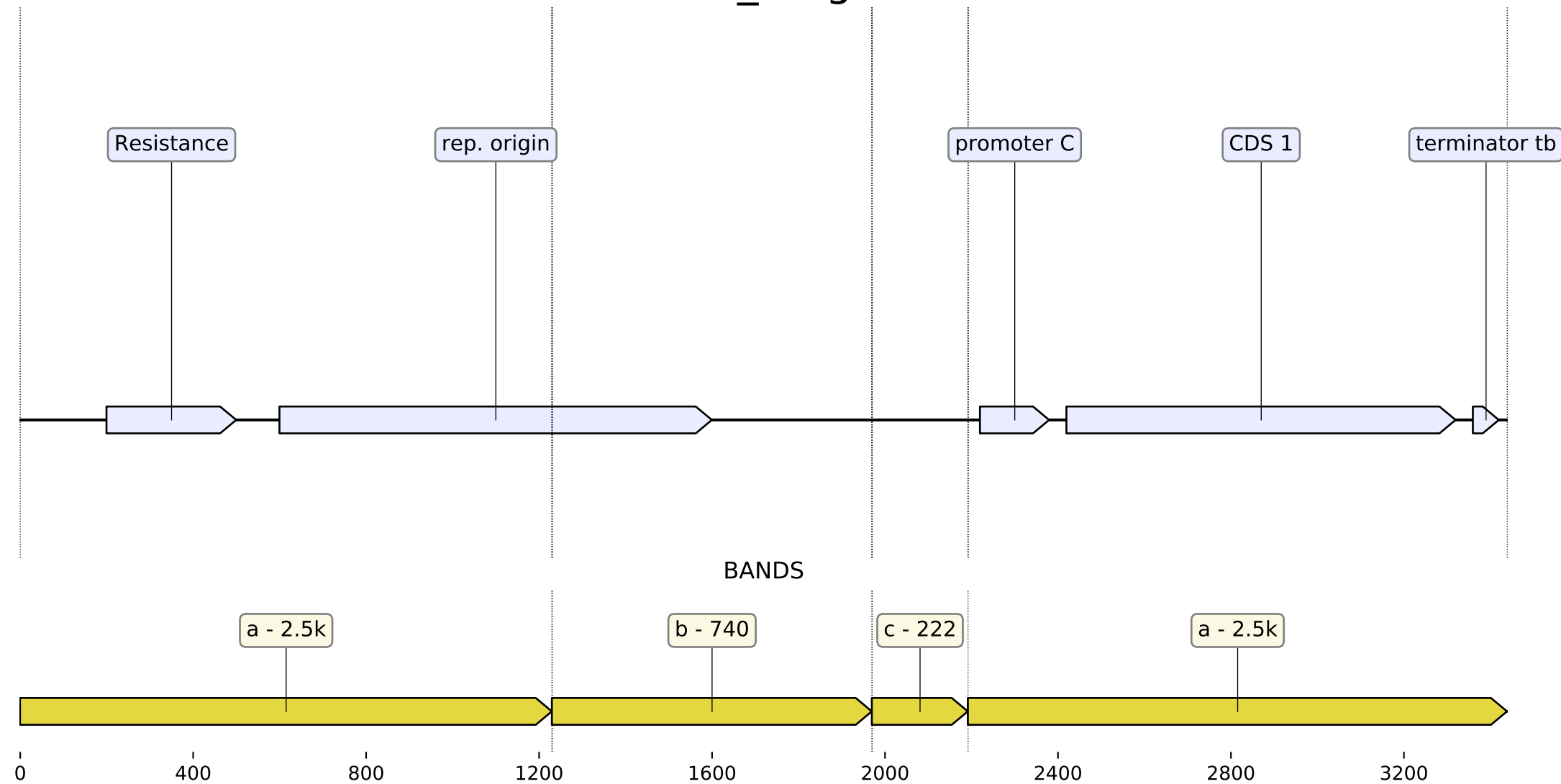
100

a

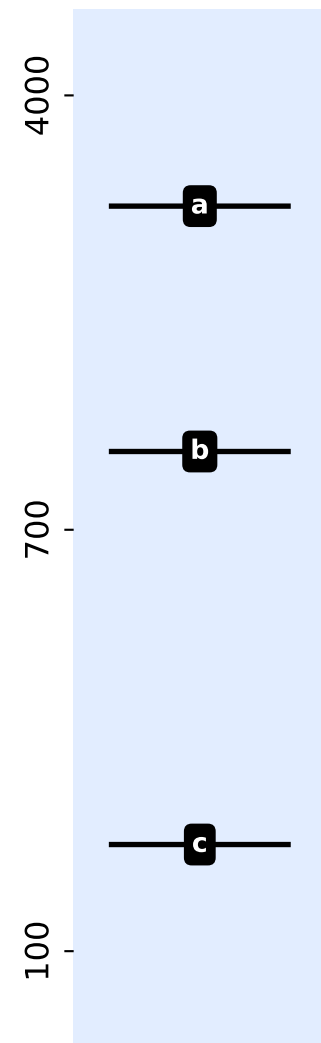
b

c

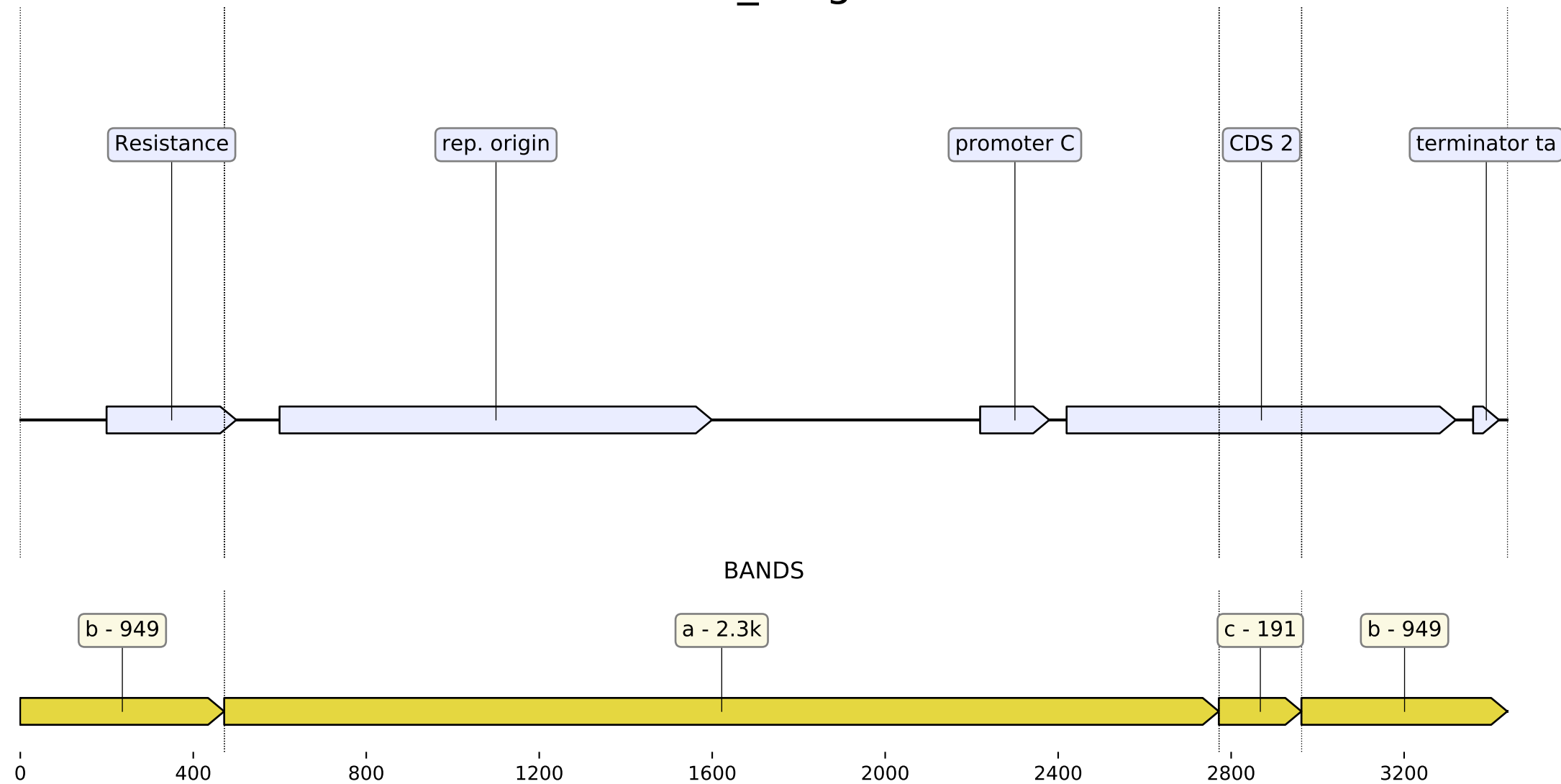
asm\_09.gb



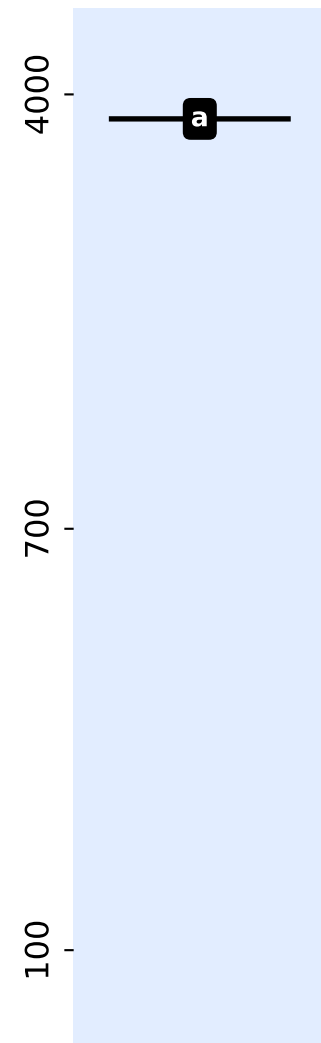
# BamHI + NcoI



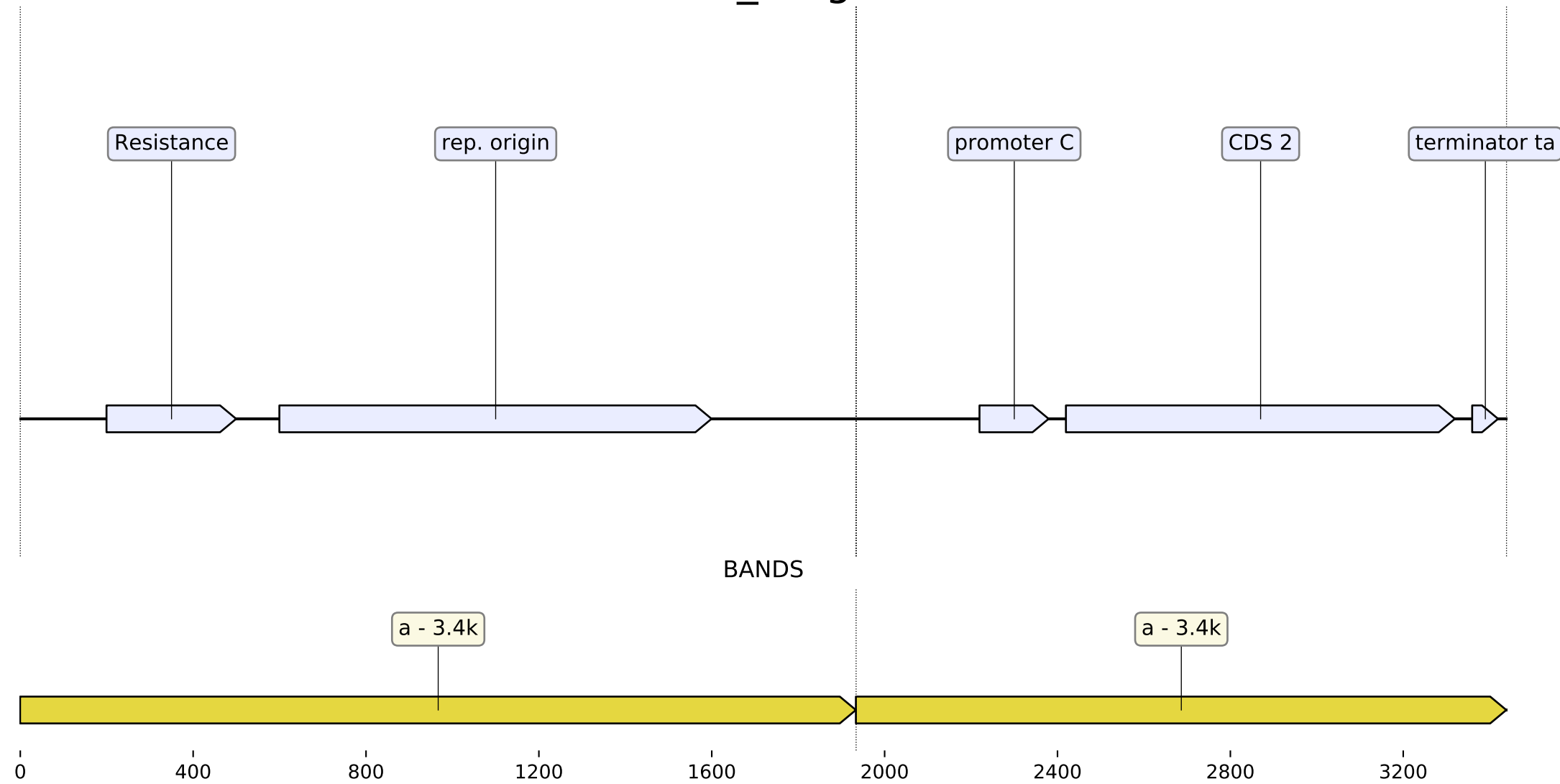
asm\_10.gb



**Bsal + XbaI**



asm\_10.gb





Styl

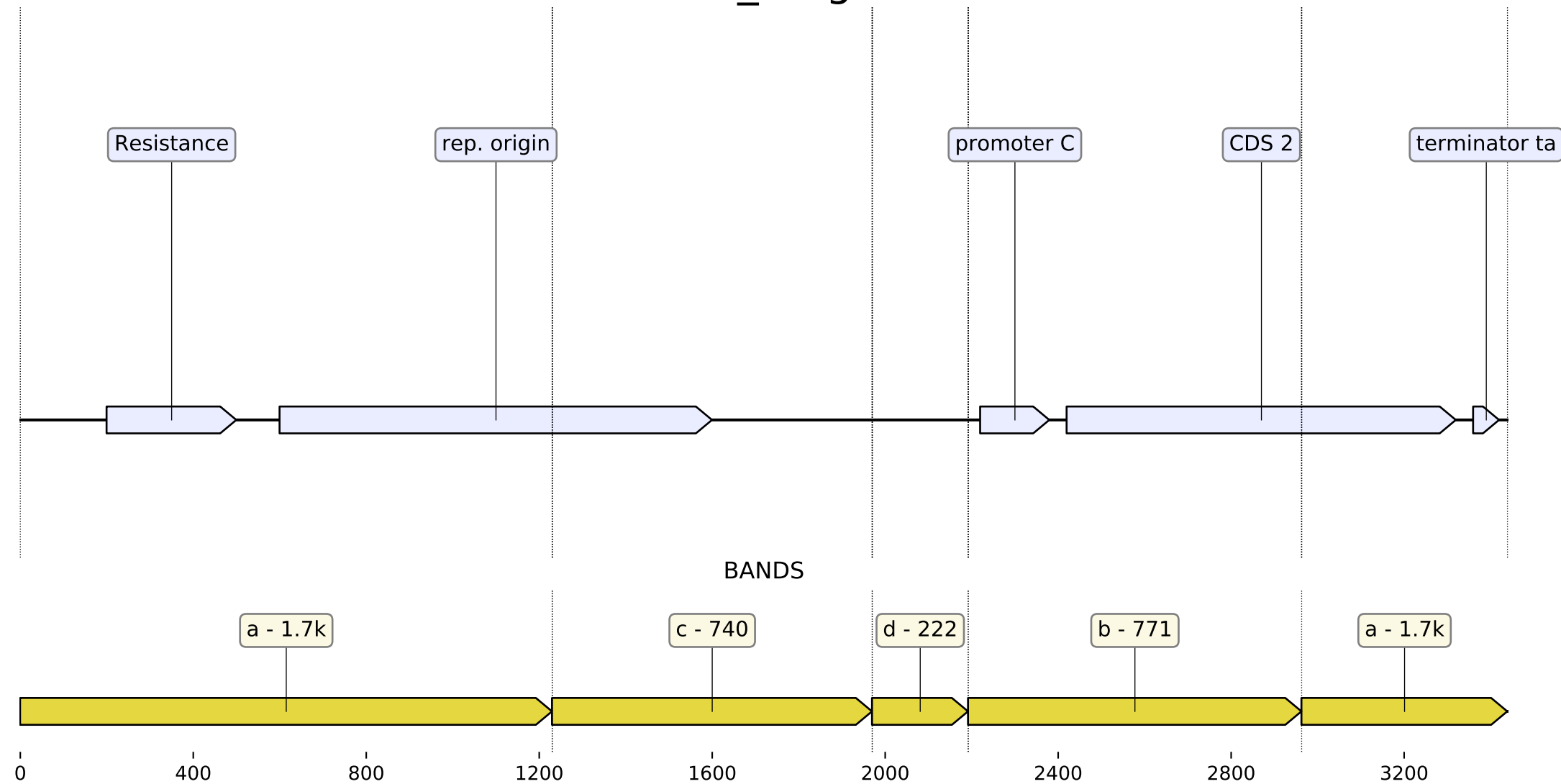
4000  
700  
100

a

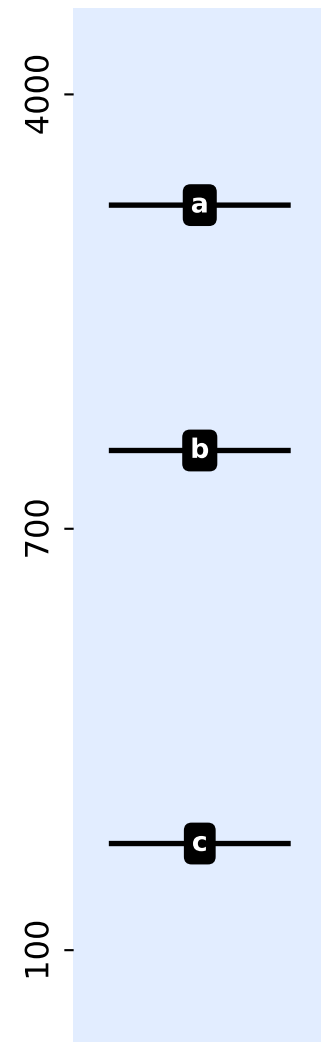
c

d

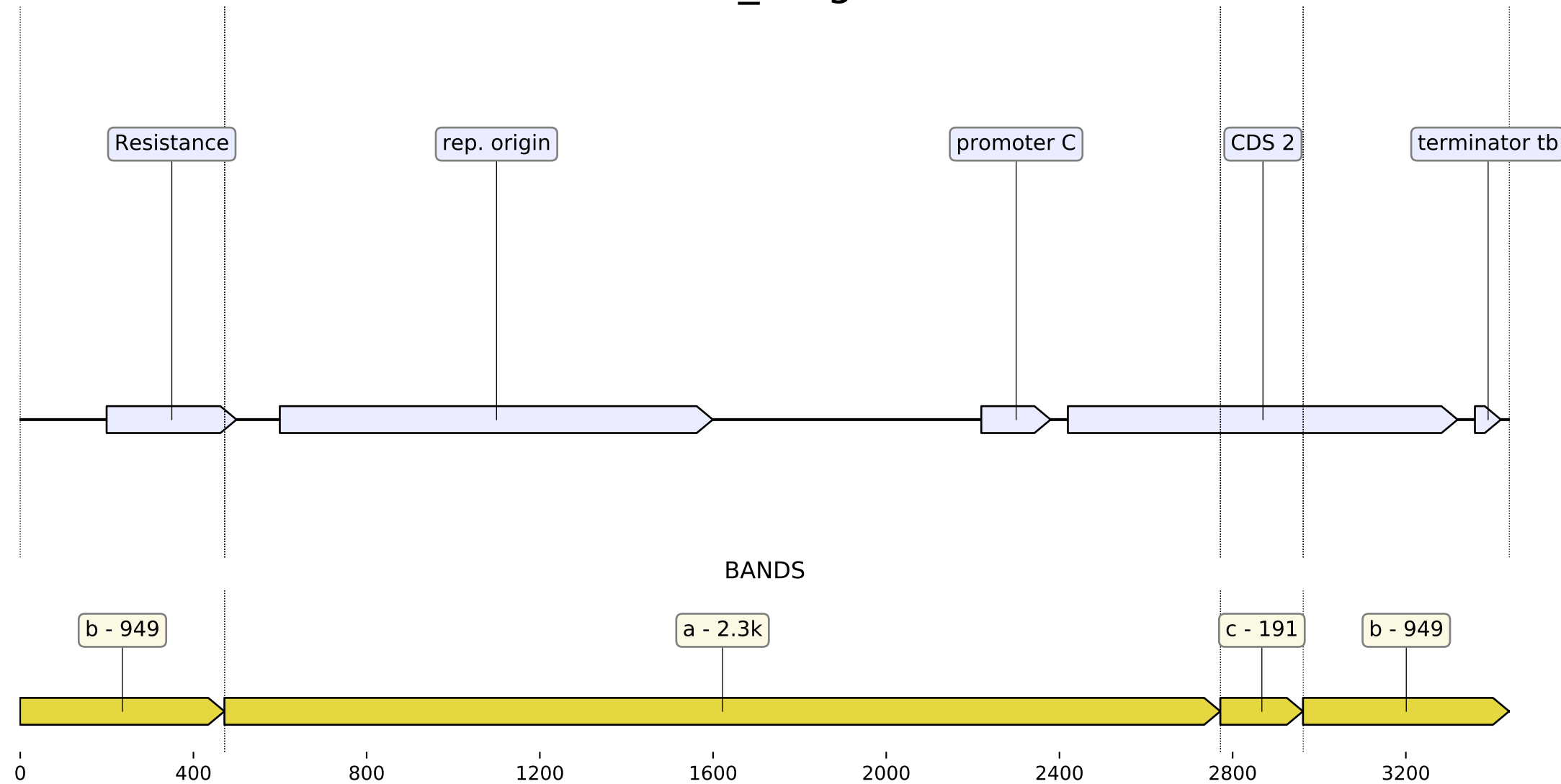
asm\_10.gb



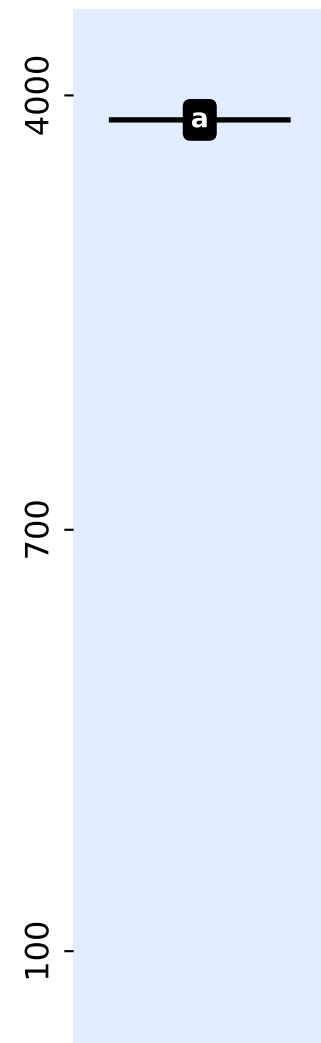
# BamHI + NcoI



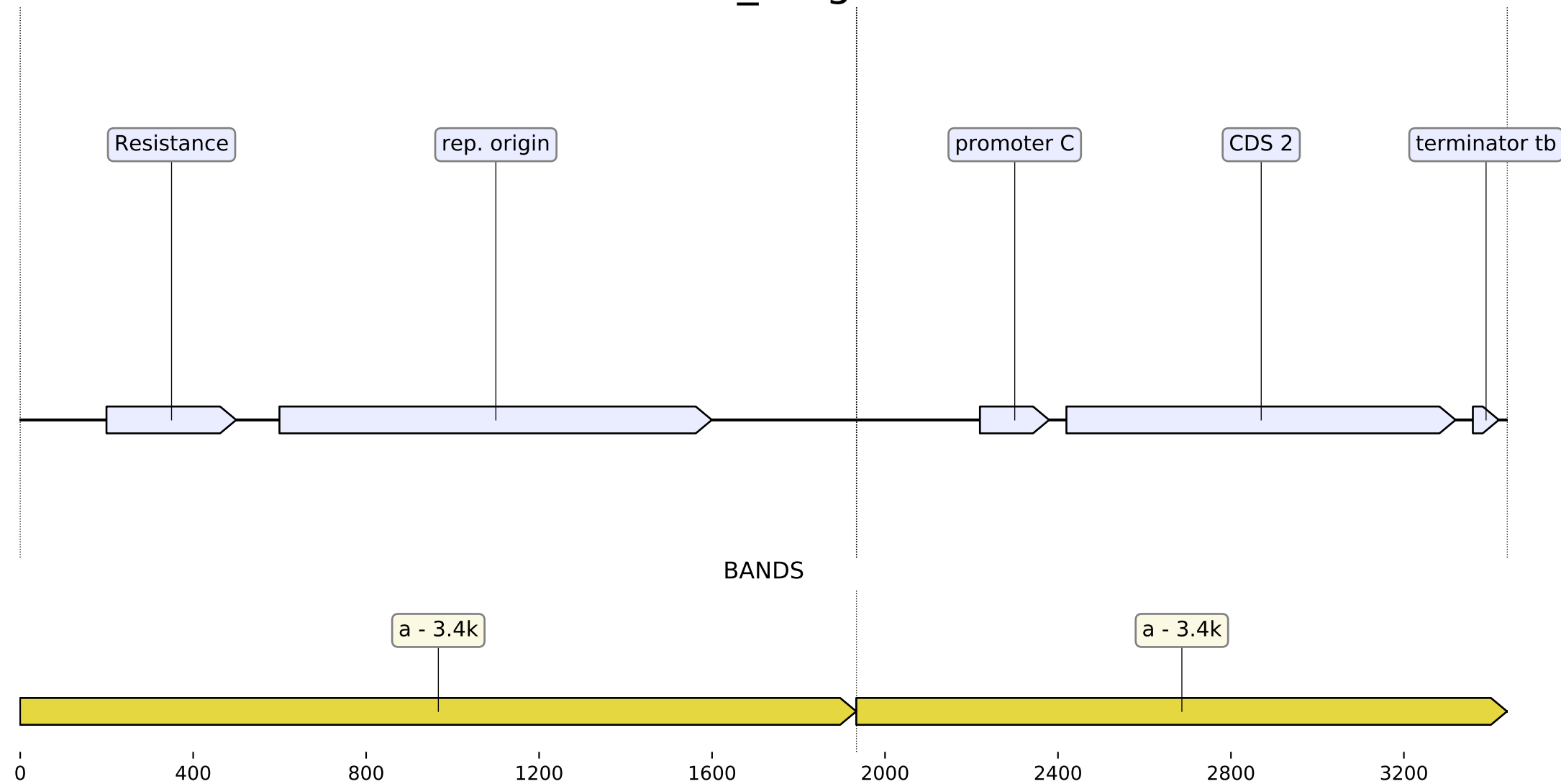
asm\_11.gb



**Bsal + XbaI**



asm\_11.gb



styl

4000

700

100

a

c

d

asm\_11.gb

Resistance

rep. origin

promoter C

CDS 2

terminator tb

BANDS

a - 1.7k

c - 740

d - 222

b - 771

a - 1.7k