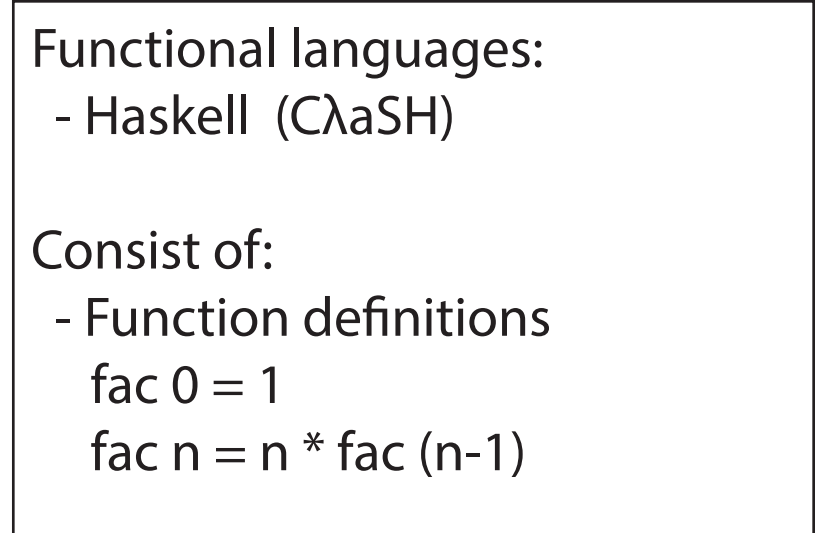


compile

Sequence of instructions

execute (CPU)

Result



compile (CλaSH)

Hardware Description Language

synthesis  
deploy (FPGA)  
run

Result