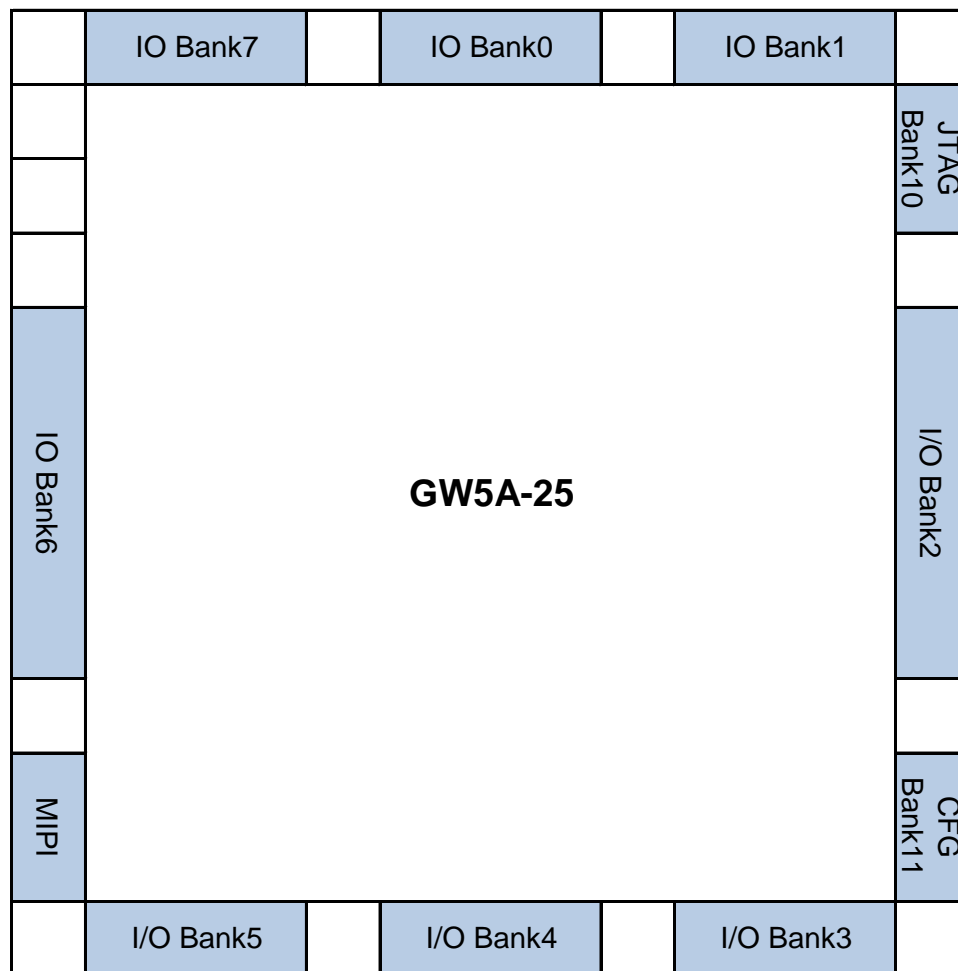| Date	Version	Description
04/20/2023	1.0E	Initial version published. MG121N, UG324S, UG256C, PG256C, and UG324 packages supported.
05/25/2023	1.1E	The descriptions of MI0, MI1, MI2, and MI3 pins in Pin Definitions sheet updated. The PLL configuration info. of IOB58A, IOL3A, IOL14A, IOR24A, and IOR31A pins for all packages updated. MG196S and UG225S packages added. The info. of MODE for MG121N package in Pin List 1 sheet updated. The notes in Pin Definitions sheet updated.
06/30/2023	1.2E	LQ100 and PG256S packages added. The descriptions of DIO pins in Pin Definitions sheet updated.
08/10/2023	1.2.1E	The description of VCC_EXT for UG324 package in Power sheet optimized.

Pin Name	I/O	Description
User I/O		
IO[End][Row/Column Number][A/B]	I/O/LVDS	[End] indicates the pin location, including L(left), R(right), B(bottom), and T(top).
		[Row/Column Number] indicates the pin row/column number. If [End] is T(top) or B(bottom), the pin indicates the column number of the corresponding CFU. If [End] is L(left) or R(right), the pin indicates the row number of the corresponding CFU.
		[A/B] indicates differential signal pair information.
		LVDS indicates that the pin only supports LVDS output.
[pin]_[End][Row/Column Number][A/B]	I/O	[pin] indicates pin name. Other information is as above.
		Shorted IOs with pins are ranked by number. The first IO uses the pin name. Other shorted IOs use the pin name and the location information removed the character "IO". For example, IOR1A/IOB14A are shorted, its pin name is C1, then IOR1A corresponds to C1, IOB14A corresponds to C1_B14A.
Multi-Function Pins		
IO[End][Row/Column Number][A/B]/MMM		/MMM represents one or more of the other functions in addition to being general purpose user I/O. When these functions are not in use, these pins can be used as user I/O.
D00	I/O	CPU Mode: Data input/output (bidirectional) pin D00 MSPI Mode: Serial data input in X1 mode; In X2 and X4 modes, the input pin of parallel data bit 1 that connects to DQ1/Q/SO/IO1 pins of external Flash device
MI0		MSPI Mode: Serial instruction and address output, in X2 and X4 modes, the input pin of parallel data bit 0 that connects to DQ0/D/SI/IO0 pins of external Flash device
MI1		MSPI Mode: Serial data input in X1 mode; In X2 and X4 modes, the input pin of parallel data bit 1 that connects to DQ1/Q/SO/IO1 pins of external Flash device
MI2	I/O	MSPI Mode: In X4 mode, the input pin of parallel data bit 2 that connects to pin DQ2/W#/WP#/IO2 of external Flash device respectively
MI3		MSPI Mode: In X4 mode, the input pin of parallel data bit 3 that connects to pin DQ3/HOLD#/IO3 of external Flash device respectively
D01	I/O	CPU Mode: Data input/output (bidirectional) pin D01 MSPI Mode: In X4 mode, the input pin of parallel data bit 2 that connects to pin DQ2/W#/WP#/IO2 of external Flash device respectively
D02	I/O	CPU Mode: Data input/output (bidirectional) pin D02 MSPI Mode: In X4 mode, the input pin of parallel data bit 3 that connects to pin DQ3/HOLD#/IO3 of external Flash device respectively
D03	I/O	CPU Mode: Data input/output (bidirectional) pin D03
D04~D07	I/O	CPU Mode: Data input/output port D04~D07
D08~D15	I	CPU Mode: Data input port D08~D15

Pin Name	I/O	Description
CCLK	I/O	Configuration Clock Slave Mode: CCLK is input and need connect to external clock source Master Mode: CCLK is output
EMCCLK	I	External Input Clock Signal Master Mode: EMCCLK is used as the clock source of FPGA configuration logic and output CCLK Slave Mode: EMCCLK is not associated with slave mode
MCKTEST	I/O	MCLK CIB output test pin
MOSI	I/O	MSPI Mode: Serial instruction and address output, in X2 and X4 modes, the input pin of parallel data bit 0 that connects to DQ0/D/SI/IO0 pins of external Flash device
MISO	I/O	MSPI Mode: Serial data input in X1 mode; In X2 and X4 modes, the input pin of parallel data bit 1 that connects to DQ1/Q/SO/IO1 pins of external Flash device
SDA	I/O	I2C serial data line External pull-up is required when GowinCONFIG configuration mode supports I2C
SCL	I	I2C serial clock line External pull-up is required when GowinCONFIG configuration mode supports I2C
SO	O	SO in SSPI mode
SI	I/O	SI in SSPI mode
CSI_B	I	CPU Mode: Chip select signal, active-low Master CPU Mode: The chip select signal can connect to external configuration controller and can be grounded directly or grounded in series with a 1KΩ resistor Slave CPU Mode: External configuration controller can select FPGA through controlling CSI_B signal In Master and Slave modes, CSI_B signal is sent from external controller; In other modes, CSI_B signal is not associated with external controller
CSO_B	O	FPGA cascade configuration mode (Daisy Chain): Connect the next level device SERIAL mode: Output the configuration data of the next level device Master SPI mode: output the configuration data of the next level device CPU mode: output the chip select signal of the next level device
PUDC_B	I	Active-low, weak pull-up selection signal pin during configuration： Enable internal weak pull-up resistor during configuration after FPGA power up PUDC_B low: all GPIOs except PUDC_B weak pull-up PUDC_B high: all GPIOs are in high impedance PUDC_B are not allowed to be left floating during configuration

Pin Name	I/O	Description
RDWR_B	I	CPU Mode: Data write/read controlling signal When RDWR is high, FPGA outputs data, while RDWR is low, external controller writes data into FPGA Master CPU Mode: Connect external controller RDWR signal; Connect directly or in series with resistor ≤ 1KΩ to GND Slave CPU Mode: External controller RDWR signal In CPU mode, low 8-bit dedicated IO will be affected by RDWR status after wakeup; low 8-bit dedicated IO will not be affected by RDWR status if it is set as multiplexed IO
SSPI_CLK	I/O	SSPI/QSSPI Configuration Mode: Clock input pin
SSPI_WPN	I/O	QSSPI Configuration Mode: Data input pin
GCLKC_[x]	I	Differential input pin of GCLKT_[x], C(Comp), [x]: Clock No.
GCLKT_[x]	I	Global clock input pin, T(True), [x]: Clock No.
GCLKT/C_[x]A	I	Default dedicated pin of GCLKT/C_[x], [x]: Clock No.
GCLKT/C_[x]B	I	Dedicated pin of GCLKT/C_[x] When the corresponding A is not used as a dedicated pin for GCLK, the function of GCLK pin is implemented by configuration, [x]: Clock No.
CLKHOLD_N	I, internal weak pull-down	In SSPI mode, active high In CPU mode, active low
DOUT	O	SERIAL Mode: Data output
DIN	I, internal weak pull-down	SERIAL Mode: Data input
TMS	I, internal weak pull-up	JTAG Mode: Serial mode input
TCK	I	JTAG Mode: Serial clock input
TDO	O	JTAG Mode: Serial data output
TDI	I, internal weak pull-up	JTAG mode: Serial data input
RECONFIG_N	I, internal weak pull-up	Global reset GowinCONFIG logic signal, active low
DONE[1]	O	High, the programming configuration has been completed successfully; Low, the programming configuration has not been completed or failed.
	I	When the DONE signal is low, delay the chip to activate. Activate the chip until the DONE signal is high.
READY[1]	O	High, the device can be programmed and configured currently; Low, the device cannot be programmed and configured currently.
MCS_N	O	MSPI Mode: Enable signal MCS_N, active-low
SSPI_CS_N	I/O	SSPI Mode: Enable signal SSPI_CS_N, active-low, and internal weak pull-up
LPLL_C_FB/RPLL_C_FB/ TPLL_C_FB/BPLL_C_FB	I	Left/Right/Up/Down PLL feedback input pin, C(Comp)

Pin Name	I/O	Description
LPLL_T_FB/RPLL_T_FB	I	Left/Right/Top/Bottom PLL feedback input pin, T(True)
LPLL_C_IN/RPLL_C_IN	I	Left/Right/Top/Bottom PLL clock input pin, C(Comp)
LPLL_T_IN/RPLL_T_IN/TPLL_T_IN/BPLL_T_IN	I	Left/Right/Top/Bottom PLL clock input pin, T(True)
MODE2	I, internal weak pull-down	GowinCONFIG mode selection pin: if this pin is marked as "VCCIO", it's internally powered; if this pin is marked as "GND", it's internally grounded.
MODE1	I, internal weak pull-down	GowinCONFIG mode selection pin: if this pin is marked as "VCCIO", it's internally powered; if this pin is marked as "GND", it's internally grounded.
MODE0	I, internal weak pull-down	GowinCONFIG mode selection pin: if this pin is marked as "VCCIO", it's internally powered; if this pin is marked as "GND", it's internally grounded.
Other Pins		
VSS	NA	Ground pin
VCC	NA	Power supply pin of core voltage
VCCIO#	NA	Power supply pin of I/O voltage for I/O BANK#
VCCC	NA	Power supply pin of clock tree voltage
VCCX	NA	Power supply pin of auxiliary voltage
VCC_REG	NA	Power supply pin of Regulator voltage
VQPS[2]	NA	Power supply pin of voltage required for eFuse writing
VCC_EXT[3]	NA	Power supply pin of VCC/VCCC Regulator voltage and MIPI LP voltage
M*_VDD*	NA	Power supply pin of MIPI voltage
NC	NA	Reserved
ADCVN	DIO	SENSOR differential analog signal input pin
ADCVP	DIO	SENSOR differential analog signal input pin
M0_CKN	DIO	The clock channel differential pin of MIPI_DPHY
M0_CKP	DIO	The clock channel differential pin of MIPI_DPHY
M0_D0N	DIO	The data channel 0 differential pin of MIPI_DPHY
M0_D0P	DIO	The data channel 0 differential pin of MIPI_DPHY
M0_D1N	DIO	The data channel 1 differential pin of MIPI_DPHY
M0_D1P	DIO	The data channel 1 differential pin of MIPI_DPHY
M0_D2N	DIO	The data channel 2 differential pin of MIPI_DPHY

Pin Name	I/O	Description
M0_D2P	DIO	The data channel 2 differential pin of MIPI_DPHY
M0_D3N	DIO	The data channel 3 differential pin of MIPI_DPHY
M0_D3P	DIO	The data channel 3 differential pin of MIPI_DPHY

Note!

[1] The default state of READY/DONE is open-drain output, internal weak pull-up. DONE outputs 0 during configuration.

[2] VCC_REG generates 0.9V passing through the internal LDO to power the PLL.

[3] VQPS is the power supply of voltage required for eFuse writing. This power supply can be floating or for GND when eFuse is not needed to be written.

Note!

[1] Each Bank has independent reference voltage (VREF).

[2] You can choose to use the embedded VREF source of IOB (0.6V, 0.75V, 0.9V, 1.25V, 1.5V, and VCCIO-based scaled voltages (36%,50%,64%)).

[3] You can also select to use external VREF input (use any IO pins as external VREF input).

Note!
[1] You can ignore the short-circuited pins.
[2] The pin is internally grounded.
[3] The pin is internal-circuited to VCCIO.

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG121N
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	B1_B10A[1]
IOB10B/D04/SI/SSI0	I/O	5	none	D04/SI/SSI0	Comp_of_IOB10A	True	none	A3_B10B[1]
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	E2
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	E1
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	C1_B14A[1]
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	D1
IOB1A/RECONFIGN	I/O	5	none	RECONFIGN		none	none	D8
IOB24A	I/O	5	none		True_of_IOB24B	True	x16	A1
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	F2
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	F1
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	E8
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	E6
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	B2
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	C2
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	GND[2]
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	D5
IOB54B/GCLKC_11B/D02/MI3/BPLL_C_FB1	I/O	4	none	GCLKC_11B/D02/MI3/BPLL_C_FB1	Comp_of_IOB54A	True	none	E4
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	B3
IOB56B	I/O	4	none		Comp_of_IOB56A	True	none	C3
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	E5
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	D6
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	E3
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	E7
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	VCCIO[3]

Note!
[1] You can ignore the short-circuited pins.
[2] The pin is internally grounded.
[3] The pin is internal-circuited to VCCIO.

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG121N
IOB64A/DONE	I/O	4	none	DONE		none	none	D7
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	G4
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	H4
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	A2_B8A[1]
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	H1
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	H2
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	B11
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	B10
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	A11
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	A10
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	E11
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	E10
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	C11
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	C10
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	D11
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	D10
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	L2
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	L1
IOR1A/TCK	I/O	10	none	TCK		none	none	C1
IOR1B/TDI	I/O	10	none	TDI		none	none	A3
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	K1
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	K2
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	J4
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	K4
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	G2
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	G1

Note!
[1] You can ignore the short-circuited pins.
[2] The pin is internally grounded.
[3] The pin is internal-circuited to VCCIO.

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG121N
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	L4
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	L3
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	J1
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	J2
IOR3A/TMS	I/O	10	none	TMS		none	none	B1
IOR3B/TDO	I/O	10	none	TDO		none	none	A2
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	K11
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	L11
IOT15A	I/O	7	none		True_of_IOT15B	True	x16	L10
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	K10
IOT19A	I/O	7	none		True_of_IOT19B	True	x16	L7
IOT19B	I/O	7	none		Comp_of_IOT19A	True	none	L8
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	J11
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	J10
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	K7
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	J7
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	L6
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	K6
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	L9
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	K9
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	H11
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	H10
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	J8
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	K8
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	F7
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	F6
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	H5
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	J5

Note!
[1] You can ignore the short-circuited pins.
[2] The pin is internally grounded.
[3] The pin is internal-circuited to VCCIO.

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG121N
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	L5
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	K5
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	H8
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	H7
IOT68A	I/O	1	DQ0		True_of_IOT68B	True	x16	G7
IOT68B	I/O	1	DQ0		Comp_of_IOT68A	True	none	G8
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	F5
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	G5
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	G11
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	G10
M0_CKN	DIO	MIPI	none			none	none	B7
M0_CKP	DIO	MIPI	none			none	none	A7
M0_D0N	DIO	MIPI	none			none	none	B9
M0_D0P	DIO	MIPI	none			none	none	A9
M0_D1N	DIO	MIPI	none			none	none	B8
M0_D1P	DIO	MIPI	none			none	none	A8
M0_D2N	DIO	MIPI	none			none	none	B6
M0_D2P	DIO	MIPI	none			none	none	A6
M0_D3N	DIO	MIPI	none			none	none	B5
M0_D3P	DIO	MIPI	none			none	none	A5
M0_VDD_12/VCC_EXT	Power	N/A						K3
M0_VDDA	Power	N/A						C8
M0_VDDD	Power	N/A						D9
M0_VDDX/VCCX	Power	N/A						H3
M0_VDDX/VCCX	Power	N/A						G6
M0_VDDX/VCCX	Power	N/A						G3
M0_VDDX/VCCX	Power	N/A						G9
NC	N/A	N/A						C4

Note!
[1] You can ignore the short-circuited pins.
[2] The pin is internally grounded.
[3] The pin is internal-circuited to VCCIO.

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG121N
NC	N/A	N/A						C6
VCC/VCCC	Power	N/A						F8
VCC/VCCC	Power	N/A						F4
VCC/VCCC	Power	N/A						E9
VCC_REG	Power	N/A						J6
VCCIO4	Power	N/A						D4
VCCIO0/VCCIO1	Power	N/A						J9
VCCIO10/VCCIO5	Power	N/A						D2
VCCIO2/VCCIO3	Power	N/A						A4
VCCIO6/VCCIO7	Power	N/A						F10
VQPS	Power	N/A						D3
VSS	Ground	N/A						B4
VSS	Ground	N/A						C5
VSS	Ground	N/A						C7
VSS	Ground	N/A						C9
VSS	Ground	N/A						F11
VSS	Ground	N/A						F3
VSS	Ground	N/A						F9
VSS	Ground	N/A						H6
VSS	Ground	N/A						H9
VSS	Ground	N/A						J3

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOB10B/D04/SI/SSI0	I/O	5	none	D04/SI/SSI0	Comp_of_IOB10A	True	none	V5
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	R5
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	T5
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	N6
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	P7
IOB16A	I/O	5	none		True_of_IOB16B	True	x16	T6
IOB16B	I/O	5	none		Comp_of_IOB16A	True	none	V6
IOB18A	I/O	5	none		True_of_IOB18B	True	x16	N7
IOB18B	I/O	5	none		Comp_of_IOB18A	True	none	P8
IOB1A/RECONFIGN	I/O	5	none	RECONFIGN		none	none	V2
IOB20A	I/O	5	none		True_of_IOB20B	True	x16	U7
IOB20B	I/O	5	none		Comp_of_IOB20A	True	none	V7
IOB22A	I/O	5	none		True_of_IOB22B	True	x16	U8
IOB22B	I/O	5	none		Comp_of_IOB22A	True	none	V8
IOB24A	I/O	5	none		True_of_IOB24B	True	x16	M8
IOB24B	I/O	5	none		Comp_of_IOB24A	True	none	N8
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	M10
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	N9
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	T9
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	V9
IOB2A	I/O	5	none		True_of_IOB2B	True	x16	T4
IOB2B	I/O	5	none		Comp_of_IOB2A	True	none	V4
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	R8
IOB31B/GCLKC_10A/D15/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D15/BPLL_C_FB0	Comp_of_IOB31A	True	none	T8
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	U10
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	V10
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	R10

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	T10
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	U3
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	V3
IOB39A	I/O	4	none		True_of_IOB39B	True	x16	U11
IOB39B	I/O	4	none		Comp_of_IOB39A	True	none	V11
IOB41A	I/O	4	none		True_of_IOB41B	True	x16	N10
IOB41B	I/O	4	none		Comp_of_IOB41A	True	none	P11
IOB43A	I/O	4	none		True_of_IOB43B	True	x16	T12
IOB43B	I/O	4	none		Comp_of_IOB43A	True	none	V12
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	R11
IOB45B	I/O	4	none		Comp_of_IOB45A	True	none	T11
IOB48A	I/O	4	none		True_of_IOB48B	True	x16	M11
IOB48B	I/O	4	none		Comp_of_IOB48A	True	none	N11
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	N5
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	P6
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	U13
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	V13
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	N12
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	P12
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	T14
IOB54B/GCLKC_11B/D02/MI3/BPLL_C_FB1	I/O	4	none	GCLKC_11B/D02/MI3/BPLL_C_FB1	Comp_of_IOB54A	True	none	V14
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	U15
IOB56B	I/O	4	none		Comp_of_IOB56A	True	none	V15
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	R13
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	T13
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	U16
IOB60B	I/O	4	none		Comp_of_IOB60A	True	none	V16
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	R15
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	T15
IOB64A/DONE	I/O	4	none	DONE		none	none	V17

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	P15
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	P16
IOB67A	I/O	3	none		True_of_IOB67B	True	x16	L14
IOB67B	I/O	3	none		Comp_of_IOB67A	True	none	M13
IOB69A	I/O	3	DQ5		True_of_IOB69B	True	x16	M14
IOB69B	I/O	3	DQ5		Comp_of_IOB69A	True	none	N14
IOB6A	I/O	5	none		True_of_IOB6B	True	x16	R7
IOB6B	I/O	5	none		Comp_of_IOB6A	True	none	T7
IOB71A	I/O	3	DQ5		True_of_IOB71B	True	x16	U17
IOB71B	I/O	3	DQ5		Comp_of_IOB71A	True	none	U18
IOB73A	I/O	3	DQ5		True_of_IOB73B	True	x16	T17
IOB73B	I/O	3	DQ5		Comp_of_IOB73A	True	none	T18
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	N15
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	N16
IOB77A	I/O	3	DQ5		True_of_IOB77B	True	x16	P17
IOB77B	I/O	3	DQ5		Comp_of_IOB77A	True	none	P18
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	N17
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	N18
IOB81A	I/O	3	DQ4		True_of_IOB81B	True	x16	M16
IOB81B	I/O	3	DQ4		Comp_of_IOB81A	True	none	M18
IOB83A	I/O	3	DQ4		True_of_IOB83B	True	x16	L17
IOB83B	I/O	3	DQ4		Comp_of_IOB83A	True	none	L18
IOB85A	I/O	3	DQS4/DQ4		True_of_IOB85B	True	x16	K17
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	K18
IOB87A	I/O	3	DQ4		True_of_IOB87B	True	x16	J16
IOB87B	I/O	3	DQ4		Comp_of_IOB87A	True	none	J18
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	H17
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	H18
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	R3
IOB8B/D06	I/O	5	none	D06	Comp_of_IOB8A	True	none	T3
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	L15

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	L16
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	K2
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	K1
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	L2
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	L1
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	M3
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	M1
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	N2
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	N1
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	P2
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	P1
IOL23A	I/O	6	DQ6		True_of_IOL23B	True	x16	T2
IOL23B	I/O	6	DQ6		Comp_of_IOL23A	True	none	T1
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	U2
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	U1
IOL27A	I/O	6	DQ6		True_of_IOL27B	True	x16	L6
IOL27B	I/O	6	DQ6		Comp_of_IOL27A	True	none	M5
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	N4
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	N3
IOL31A	I/O	6	none		True_of_IOL31B	True	x16	P4
IOL31B	I/O	6	none		Comp_of_IOL31A	True	none	P3
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	H2
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	H1
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	K4
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	K3
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	J3
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	J1
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	L4

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	L3
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	D17
IOR12B/ADCINCLK	I/O	2	DQS2/DQ2	ADCINCLK	Comp_of_IOR12A	True	none	D18
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	F15
IOR14B/ADCOTEST	I/O	2	DQ2	ADCOTEST	Comp_of_IOR14A	True	none	F16
IOR16A	I/O	2	DQ2		True_of_IOR16B	True	x16	E16
IOR16B	I/O	2	DQ2		Comp_of_IOR16A	True	none	E18
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	H13
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	H14
IOR1A/TCK	I/O	10	none	TCK		none	none	A17
IOR1B/TDI	I/O	10	none	TDI		none	none	D15
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	F17
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	F18
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	G16
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	G18
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	H15
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	H16
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	J13
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	K14
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	K12
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	K13
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	L12
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	L13
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	K15
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	K16
IOR35A	I/O	11	none			none	none	R16
IOR3A/TMS	I/O	10	none	TMS		none	none	B18
IOR3B/TDO	I/O	10	none	TDO		none	none	D16
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	H12
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	G13
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	F14

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	G14
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	C17
IOR9B/MCKTEST	I/O	2	DQ2	MCKTEST	Comp_of_IOR9A	True	none	C18
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	F2
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	F1
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	J7
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	J6
IOT15A	I/O	7	none		True_of_IOT15B	True	x16	E3
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	E1
IOT17A	I/O	7	none		True_of_IOT17B	True	x16	F4
IOT17B	I/O	7	none		Comp_of_IOT17A	True	none	F3
IOT19A	I/O	7	none		True_of_IOT19B	True	x16	D2
IOT19B	I/O	7	none		Comp_of_IOT19A	True	none	D1
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	H4
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	H3
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	L7
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	K6
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	E4
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	D3
IOT25A	I/O	7	none		True_of_IOT25B	True	x16	F6
IOT25B	I/O	7	none		Comp_of_IOT25A	True	none	F5
IOT27A	I/O	7	none		True_of_IOT27B	True	x16	C2
IOT27B	I/O	7	none		Comp_of_IOT27A	True	none	C1
IOT29A/PUDC_B	I/O	0	none	PUDC_B	True_of_IOT29B	True	x16	D4
IOT29B	I/O	0	none		Comp_of_IOT29A	True	none	C4
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	B2
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	A2
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	D6
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	C6
IOT35A	I/O	0	none		True_of_IOT35B	True	x16	B3
IOT35B	I/O	0	none		Comp_of_IOT35A	True	none	A3

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	B4
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	A4
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	C5
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	A5
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	L5
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	K5
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	F7
IOT41B	I/O	0	none		Comp_of_IOT41A	True	none	E6
IOT43A	I/O	0	none		True_of_IOT43B	True	x16	B6
IOT43B	I/O	0	none		Comp_of_IOT43A	True	none	A6
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	E7
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	E8
IOT48A	I/O	0	none		True_of_IOT48B	True	x16	C7
IOT48B	I/O	0	none		Comp_of_IOT48A	True	none	A7
IOT50A	I/O	0	none		True_of_IOT50B	True	x16	D8
IOT50B	I/O	0	none		Comp_of_IOT50A	True	none	C8
IOT52A	I/O	0	none		True_of_IOT52B	True	x16	G8
IOT52B	I/O	0	none		Comp_of_IOT52A	True	none	F8
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	B8
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	A8
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	D9
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	C9
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	B9
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	A9
IOT5A	I/O	7	none		True_of_IOT5B	True	x16	H6
IOT5B	I/O	7	none		Comp_of_IOT5A	True	none	H5
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	D11
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	C11
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	C10
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	A10
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	G9

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	F9
IOT68A	I/O	1	DQ0		True_of_IOT68B	True	x16	B11
IOT68B	I/O	1	DQ0		Comp_of_IOT68A	True	none	A11
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	G11
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	F10
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	B12
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	A12
IOT74A	I/O	1	DQ1/DQS_01		True_of_IOT74B	True	x16	F11
IOT74B	I/O	1	DQ1/DQS_01		Comp_of_IOT74A	True	none	E11
IOT76A	I/O	1	DQ1		True_of_IOT76B	True	x16	D12
IOT76B	I/O	1	DQ1		Comp_of_IOT76A	True	none	C12
IOT78A	I/O	1	DQ1		True_of_IOT78B	True	x16	C13
IOT78B	I/O	1	DQ1		Comp_of_IOT78A	True	none	A13
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	G3
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	G1
IOT80A	I/O	1	DQS1/DQ1		True_of_IOT80B	True	x16	F12
IOT80B	I/O	1	DQS1/DQ1		Comp_of_IOT80A	True	none	E12
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	B14
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	A14
IOT85A	I/O	1	DQ1		True_of_IOT85B	True	x16	F13
IOT85B	I/O	1	DQ1		Comp_of_IOT85A	True	none	E13
IOT87A	I/O	1	none		True_of_IOT87B	True	x16	C15
IOT87B	I/O	1	none		Comp_of_IOT87A	True	none	A15
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	D14
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	C14
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	B16
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	A16
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	H7
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	G6
VCC_EXT	Power	N/A						J8
VCC_EXT	Power	N/A						H9

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
VCC_EXT	Power	N/A						K11
VCC_EXT	Power	N/A						H11
VCC_EXT	Power	N/A						K9
VCC_EXT	Power	N/A						L10
VCC_EXT	Power	N/A						M7
VCC_EXT	Power	N/A						L8
VCC_EXT	Power	N/A						M12
VCC_EXT	Power	N/A						G7
VCCIO10/VCCX	Power	N/A						P5
VCCIO10/VCCX	Power	N/A						G10
VCCIO10/VCCX	Power	N/A						K7
VCCIO10/VCCX	Power	N/A						M9
VCCIO10/VCCX	Power	N/A						E14
VCCIO10/VCCX	Power	N/A						B1
VCCIO10/VCCX	Power	N/A						E9
VCCIO10/VCCX	Power	N/A						P14
VCCIO10/VCCX	Power	N/A						B17
VCCIO10/VCCX	Power	N/A						J12
VCCIO10/VCCX	Power	N/A						P10
VCCIO10/VCCX	Power	N/A						E5
NC	N/A	N/A						P13
VCC_REG	Power	N/A						J10
VCCIO10	Power	N/A						P5
VCCIO0/VCCIO1	Power	N/A						B15
VCCIO0/VCCIO1	Power	N/A						E10
VCCIO0/VCCIO1	Power	N/A						D13
VCCIO0/VCCIO1	Power	N/A						D7
VCCIO0/VCCIO1	Power	N/A						B5
VCCIO0/VCCIO1	Power	N/A						B10
VCCIO2/VCCIO3	Power	N/A						G15
VCCIO2/VCCIO3	Power	N/A						R17

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
VCCIO2/VCCIO3	Power	N/A						J14
VCCIO2/VCCIO3	Power	N/A						J17
VCCIO2/VCCIO3	Power	N/A						M15
VCCIO2/VCCIO3	Power	N/A						E17
VCCIO4/VCCIO5	Power	N/A						P9
VCCIO4/VCCIO5	Power	N/A						U4
VCCIO4/VCCIO5	Power	N/A						R6
VCCIO4/VCCIO5	Power	N/A						R12
VCCIO4/VCCIO5	Power	N/A						U9
VCCIO4/VCCIO5	Power	N/A						U14
VCCIO6/VCCIO7	Power	N/A						J5
VCCIO6/VCCIO7	Power	N/A						E2
VCCIO6/VCCIO7	Power	N/A						G4
VCCIO6/VCCIO7	Power	N/A						M4
VCCIO6/VCCIO7	Power	N/A						J2
VCCIO6/VCCIO7	Power	N/A						R2
VQPS	Power	N/A						L11
VSS	Ground	N/A						A1
VSS	Ground	N/A						A18
VSS	Ground	N/A						B13
VSS	Ground	N/A						B7
VSS	Ground	N/A						C16
VSS	Ground	N/A						C3
VSS	Ground	N/A						D10
VSS	Ground	N/A						D5
VSS	Ground	N/A						E15
VSS	Ground	N/A						G12
VSS	Ground	N/A						G17
VSS	Ground	N/A						G2
VSS	Ground	N/A						G5
VSS	Ground	N/A						H10

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
VSS	Ground	N/A						H8
VSS	Ground	N/A						J11
VSS	Ground	N/A						J15
VSS	Ground	N/A						J4
VSS	Ground	N/A						J9
VSS	Ground	N/A						K10
VSS	Ground	N/A						K8
VSS	Ground	N/A						L9
VSS	Ground	N/A						M17
VSS	Ground	N/A						M2
VSS	Ground	N/A						M6
VSS	Ground	N/A						N13
VSS	Ground	N/A						R1
VSS	Ground	N/A						R14
VSS	Ground	N/A						R18
VSS	Ground	N/A						R4
VSS	Ground	N/A						R9
VSS	Ground	N/A						T16
VSS	Ground	N/A						U12
VSS	Ground	N/A						U6
VSS	Ground	N/A						V1
VSS	Ground	N/A						V18

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOB10B/D04/SI/SSI0	I/O	5	none	D04/SI/SSI0	Comp_of_IOB10A	True	none	V5
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	R5
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	T5
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	N6
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	P7
IOB16A	I/O	5	none		True_of_IOB16B	True	x16	T6
IOB16B	I/O	5	none		Comp_of_IOB16A	True	none	V6
IOB18B	I/O	5	none		Comp_of_IOB18A	True	none	P8
IOB1A/RECONFIGN	I/O	5	none	RECONFIGN		none	none	V2
IOB20A	I/O	5	none		True_of_IOB20B	True	x16	U7
IOB20B	I/O	5	none		Comp_of_IOB20A	True	none	V7
IOB22A	I/O	5	none		True_of_IOB22B	True	x16	U8
IOB22B	I/O	5	none		Comp_of_IOB22A	True	none	V8
IOB24A	I/O	5	none		True_of_IOB24B	True	x16	M8
IOB24B	I/O	5	none		Comp_of_IOB24A	True	none	N8
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	M10
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	N9
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	T9
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	V9
IOB2A	I/O	5	none		True_of_IOB2B	True	x16	T4
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	R8
IOB31B/GCLKC_10A/D15/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D15/BPLL_C_FB0	Comp_of_IOB31A	True	none	T8
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	U10
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	V10
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	R10
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	T10
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	U3

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	V3
IOB39A	I/O	4	none		True_of_IOB39B	True	x16	U11
IOB39B	I/O	4	none		Comp_of_IOB39A	True	none	V11
IOB41A	I/O	4	none		True_of_IOB41B	True	x16	N10
IOB41B	I/O	4	none		Comp_of_IOB41A	True	none	P11
IOB43A	I/O	4	none		True_of_IOB43B	True	x16	T12
IOB43B	I/O	4	none		Comp_of_IOB43A	True	none	V12
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	R11
IOB45B	I/O	4	none		Comp_of_IOB45A	True	none	T11
IOB48A	I/O	4	none		True_of_IOB48B	True	x16	M11
IOB48B	I/O	4	none		Comp_of_IOB48A	True	none	N11
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	N5
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	P6
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	U13
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	V13
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	N12
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	P12
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	T14
IOB54B/GCLKC_11B/D02/MI3/BPLL_C_FB1	I/O	4	none	GCLKC_11B/D02/MI3/BPLL_C_FB1	Comp_of_IOB54A	True	none	V14
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	U15
IOB56B	I/O	4	none		Comp_of_IOB56A	True	none	V15
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	R13
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	T13
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	U16
IOB60B	I/O	4	none		Comp_of_IOB60A	True	none	V16
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	R15
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	T15
IOB64A/DONE	I/O	4	none	DONE		none	none	V17
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	P15

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	P16
IOB67A	I/O	3	none		True_of_IOB67B	True	x16	L14
IOB67B	I/O	3	none		Comp_of_IOB67A	True	none	M13
IOB69A	I/O	3	DQ5		True_of_IOB69B	True	x16	M14
IOB69B	I/O	3	DQ5		Comp_of_IOB69A	True	none	N14
IOB6A	I/O	5	none		True_of_IOB6B	True	x16	R7
IOB6B	I/O	5	none		Comp_of_IOB6A	True	none	T7
IOB71A	I/O	3	DQ5		True_of_IOB71B	True	x16	U17
IOB71B	I/O	3	DQ5		Comp_of_IOB71A	True	none	U18
IOB73A	I/O	3	DQ5		True_of_IOB73B	True	x16	T17
IOB73B	I/O	3	DQ5		Comp_of_IOB73A	True	none	T18
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	N15
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	N16
IOB77A	I/O	3	DQ5		True_of_IOB77B	True	x16	P17
IOB77B	I/O	3	DQ5		Comp_of_IOB77A	True	none	P18
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	N17
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	N18
IOB81A	I/O	3	DQ4		True_of_IOB81B	True	x16	M16
IOB81B	I/O	3	DQ4		Comp_of_IOB81A	True	none	M18
IOB83A	I/O	3	DQ4		True_of_IOB83B	True	x16	L17
IOB83B	I/O	3	DQ4		Comp_of_IOB83A	True	none	L18
IOB85A	I/O	3	DQS4/DQ4		True_of_IOB85B	True	x16	K17
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	K18
IOB87A	I/O	3	DQ4		True_of_IOB87B	True	x16	J16
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	H17
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	H18
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	R3
IOB8B/D06	I/O	5	none	D06	Comp_of_IOB8A	True	none	T3
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	L15
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	L16
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	K2

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	K1
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	L2
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	L1
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	M3
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	M1
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	N2
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	N1
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	P2
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	P1
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	H2
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	H1
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	K4
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	K3
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	J1
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	L4
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	L3
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	D17
IOR12B/ADCINCLK	I/O	2	DQS2/DQ2	ADCINCLK	Comp_of_IOR12A	True	none	D18
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	F15
IOR14B/ADCOTEST	I/O	2	DQ2	ADCOTEST	Comp_of_IOR14A	True	none	F16
IOR16A	I/O	2	DQ2		True_of_IOR16B	True	x16	E16
IOR16B	I/O	2	DQ2		Comp_of_IOR16A	True	none	E18
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	H13
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	H14
IOR1A/TCK	I/O	10	none	TCK		none	none	A17
IOR1B/TDI	I/O	10	none	TDI		none	none	D15
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	F17
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	F18
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	G16
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	G18

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	H15
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	J13
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	K14
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	K12
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	K13
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	L12
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	L13
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	K15
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	K16
IOR35A	I/O	11	none			none	none	R16
IOR3A/TMS	I/O	10	none	TMS		none	none	B18
IOR3B/TDO	I/O	10	none	TDO		none	none	D16
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	H12
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	G13
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	F14
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	G14
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	C17
IOR9B/MCKTEST	I/O	2	DQ2	MCKTEST	Comp_of_IOR9A	True	none	C18
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	F2
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	F1
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	J7
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	J6
IOT15A	I/O	7	none		True_of_IOT15B	True	x16	E3
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	E1
IOT17A	I/O	7	none		True_of_IOT17B	True	x16	F4
IOT17B	I/O	7	none		Comp_of_IOT17A	True	none	F3
IOT19A	I/O	7	none		True_of_IOT19B	True	x16	D2
IOT19B	I/O	7	none		Comp_of_IOT19A	True	none	D1
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	H4
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	H3
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	L7

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	K6
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	E4
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	D3
IOT25A	I/O	7	none		True_of_IOT25B	True	x16	F6
IOT25B	I/O	7	none		Comp_of_IOT25A	True	none	F5
IOT27A	I/O	7	none		True_of_IOT27B	True	x16	C2
IOT27B	I/O	7	none		Comp_of_IOT27A	True	none	C1
IOT29A/PUDC_B	I/O	0	none	PUDC_B	True_of_IOT29B	True	x16	D4
IOT29B	I/O	0	none		Comp_of_IOT29A	True	none	C4
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	B2
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	A2
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	D6
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	C6
IOT35A	I/O	0	none		True_of_IOT35B	True	x16	B3
IOT35B	I/O	0	none		Comp_of_IOT35A	True	none	A3
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	B4
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	A4
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	C5
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	A5
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	L5
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	K5
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	F7
IOT41B	I/O	0	none		Comp_of_IOT41A	True	none	E6
IOT43A	I/O	0	none		True_of_IOT43B	True	x16	B6
IOT43B	I/O	0	none		Comp_of_IOT43A	True	none	A6
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	E7
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	E8
IOT48A	I/O	0	none		True_of_IOT48B	True	x16	C7
IOT48B	I/O	0	none		Comp_of_IOT48A	True	none	A7
IOT50A	I/O	0	none		True_of_IOT50B	True	x16	D8
IOT50B	I/O	0	none		Comp_of_IOT50A	True	none	C8

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOT52A	I/O	0	none		True_of_IOT52B	True	x16	G8
IOT52B	I/O	0	none		Comp_of_IOT52A	True	none	F8
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	B8
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	A8
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	D9
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	C9
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	B9
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	A9
IOT5B	I/O	7	none		Comp_of_IOT5A	True	none	H5
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	D11
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	C11
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	C10
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	A10
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	G9
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	F9
IOT68A	I/O	1	DQ0		True_of_IOT68B	True	x16	B11
IOT68B	I/O	1	DQ0		Comp_of_IOT68A	True	none	A11
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	G11
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	F10
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	A12
IOT74A	I/O	1	DQ1/DQS_01		True_of_IOT74B	True	x16	F11
IOT74B	I/O	1	DQ1/DQS_01		Comp_of_IOT74A	True	none	E11
IOT76A	I/O	1	DQ1		True_of_IOT76B	True	x16	D12
IOT76B	I/O	1	DQ1		Comp_of_IOT76A	True	none	C12
IOT78A	I/O	1	DQ1		True_of_IOT78B	True	x16	C13
IOT78B	I/O	1	DQ1		Comp_of_IOT78A	True	none	A13
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	G3
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	G1
IOT80A	I/O	1	DQS1/DQ1		True_of_IOT80B	True	x16	F12
IOT80B	I/O	1	DQS1/DQ1		Comp_of_IOT80A	True	none	E12
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	B14

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	A14
IOT85A	I/O	1	DQ1		True_of_IOT85B	True	x16	F13
IOT85B	I/O	1	DQ1		Comp_of_IOT85A	True	none	E13
IOT87A	I/O	1	none		True_of_IOT87B	True	x16	C15
IOT87B	I/O	1	none		Comp_of_IOT87A	True	none	A15
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	D14
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	C14
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	B16
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	A16
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	H7
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	G6
M0_CKN	DIO	MIPI	none			none	none	M5
M0_CKP	DIO	MIPI	none			none	none	L6
M0_D0N	DIO	MIPI	none			none	none	T1
M0_D0P	DIO	MIPI	none			none	none	T2
M0_D1N	DIO	MIPI	none			none	none	U1
M0_D1P	DIO	MIPI	none			none	none	U2
M0_D2N	DIO	MIPI	none			none	none	N3
M0_D2P	DIO	MIPI	none			none	none	N4
M0_D3N	DIO	MIPI	none			none	none	P3
M0_D3P	DIO	MIPI	none			none	none	P4
M0_VDD_12	Power	N/A						V4
VCC_EXT	Power	N/A						H6
VCC_EXT	Power	N/A						J3
VCC_EXT	Power	N/A						J18
VCC_EXT	Power	N/A						B12
VCC_EXT	Power	N/A						H16
VCC_EXT	Power	N/A						N7
M0_VDDA/M0_VDDD/VCC/VCCC	Power	N/A						H9
M0_VDDA/M0_VDDD/VCC/VCCC	Power	N/A						G7
M0_VDDA/M0_VDDD/VCC/VCCC	Power	N/A						H11

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
M0_VDDA/M0_VDDD/VCC/VCCC	Power	N/A						J8
M0_VDDA/M0_VDDD/VCC/VCCC	Power	N/A						L8
M0_VDDA/M0_VDDD/VCC/VCCC	Power	N/A						M7
M0_VDDA/M0_VDDD/VCC/VCCC	Power	N/A						M12
M0_VDDA/M0_VDDD/VCC/VCCC	Power	N/A						L10
M0_VDDA/M0_VDDD/VCC/VCCC	Power	N/A						K9
M0_VDDA/M0_VDDD/VCC/VCCC	Power	N/A						K11
M0_VDDX/VCCIO10/VCCX	Power	N/A						P5
M0_VDDX/VCCIO10/VCCX	Power	N/A						M9
M0_VDDX/VCCIO10/VCCX	Power	N/A						P10
M0_VDDX/VCCIO10/VCCX	Power	N/A						E9
M0_VDDX/VCCIO10/VCCX	Power	N/A						B1
M0_VDDX/VCCIO10/VCCX	Power	N/A						E5
M0_VDDX/VCCIO10/VCCX	Power	N/A						P14
M0_VDDX/VCCIO10/VCCX	Power	N/A						B17
M0_VDDX/VCCIO10/VCCX	Power	N/A						E14
M0_VDDX/VCCIO10/VCCX	Power	N/A						J12
M0_VDDX/VCCIO10/VCCX	Power	N/A						K7
M0_VDDX/VCCIO10/VCCX	Power	N/A						G10
NC	N/A	N/A						P13
VCC_REG	Power	N/A						J10
VCCIO0/VCCIO1	Power	N/A						B15
VCCIO0/VCCIO1	Power	N/A						B10
VCCIO0/VCCIO1	Power	N/A						E10
VCCIO0/VCCIO1	Power	N/A						B5
VCCIO0/VCCIO1	Power	N/A						D7
VCCIO0/VCCIO1	Power	N/A						D13
VCCIO2/VCCIO3	Power	N/A						J17
VCCIO2/VCCIO3	Power	N/A						R17
VCCIO2/VCCIO3	Power	N/A						E17
VCCIO2/VCCIO3	Power	N/A						M15

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
VCCIO2/VCCIO3	Power	N/A						G15
VCCIO2/VCCIO3	Power	N/A						J14
VCCIO4/VCCIO5	Power	N/A						U14
VCCIO4/VCCIO5	Power	N/A						P9
VCCIO4/VCCIO5	Power	N/A						U9
VCCIO4/VCCIO5	Power	N/A						U4
VCCIO4/VCCIO5	Power	N/A						R12
VCCIO4/VCCIO5	Power	N/A						R6
VCCIO6/VCCIO7	Power	N/A						R2
VCCIO6/VCCIO7	Power	N/A						E2
VCCIO6/VCCIO7	Power	N/A						J2
VCCIO6/VCCIO7	Power	N/A						J5
VCCIO6/VCCIO7	Power	N/A						G4
VCCIO6/VCCIO7	Power	N/A						M4
VQPS	Power	N/A						L11
VSS	Ground	N/A						A18
VSS	Ground	N/A						B13
VSS	Ground	N/A						B7
VSS	Ground	N/A						C16
VSS	Ground	N/A						C3
VSS	Ground	N/A						D10
VSS	Ground	N/A						D5
VSS	Ground	N/A						E15
VSS	Ground	N/A						G12
VSS	Ground	N/A						G17
VSS	Ground	N/A						G2
VSS	Ground	N/A						G5
VSS	Ground	N/A						H10
VSS	Ground	N/A						H8
VSS	Ground	N/A						J11
VSS	Ground	N/A						J15

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	U5
VSS	Ground	N/A						J4
VSS	Ground	N/A						J9
VSS	Ground	N/A						K10
VSS	Ground	N/A						K8
VSS	Ground	N/A						A1
VSS	Ground	N/A						L9
VSS	Ground	N/A						M17
VSS	Ground	N/A						M2
VSS	Ground	N/A						M6
VSS	Ground	N/A						N13
VSS	Ground	N/A						R1
VSS	Ground	N/A						R14
VSS	Ground	N/A						R18
VSS	Ground	N/A						R4
VSS	Ground	N/A						R9
VSS	Ground	N/A						T16
VSS	Ground	N/A						U12
VSS	Ground	N/A						U6
VSS	Ground	N/A						V1
VSS	Ground	N/A						V18

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	N5	N5
IOB10B/D04/SI/SSI0	I/O	5	none	D04/SI/SSI0	Comp_of_IOB10A	True	none	P5	P5
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	N6	N6
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	P6	P6
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	N4	N4
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	P4	P4
IOB1A/RECONFIGN	I/O	5	none	RECONFIGN		none	none	N1	N1
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	N7	N7
IOB31B/GCLKC_10A/D15/BPLL_C__FB0	I/O	4	none	GCLKC_10A/D15/BPLL_C_FB0	Comp_of_IOB31A	True	none	P7	P7
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	N8	N8
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	P8	P8
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	N2	N2
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	P2	P2
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	N3	N3
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	P3	P3
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	L8	L8
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	M8	M8
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	N9	N9
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	P9	P9
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	N10	N10
IOB54B/GCLKC_11B/D02/MI3/BPLL_C_FB1	I/O	4	none	GCLKC_11B/D02/MI3/BPLL_C_FB1	Comp_of_IOB54A	True	none	P10	P10
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	N12	N12
IOB56B	I/O	4	none		Comp_of_IOB56A	True	none	P12	P12
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	N11	N11

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	P11	P11
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	M12	M12
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	N13	N13
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	P13	P13
IOB64A/DONE	I/O	4	none	DONE		none	none	N14	N14
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	M13	M13
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	M14	M14
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	L13	L13
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	L14	L14
IOB85A	I/O	3	DQS4/DQ4		True_of_IOB85B	True	x16	K13	K13
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	K14	K14
IOB87A	I/O	3	DQ4		True_of_IOB87B	True	x16	J13	J13
IOB87B	I/O	3	DQ4		Comp_of_IOB87A	True	none	J14	J14
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	H11	H11
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	H12	H12
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	L4	L4
IOB8B/D06	I/O	5	none	D06	Comp_of_IOB8A	True	none	M4	M4
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	H13	H13
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	H14	H14
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	J4	J4
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	J3	J3
IOL27A	I/O	6	DQ6		True_of_IOL27B	True	x16	K2	K2
IOL27B	I/O	6	DQ6		Comp_of_IOL27A	True	none	K1	K1
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	L2	L2
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	L1	L1
IOL31A	I/O	6	none		True_of_IOL31B	True	x16	M2	M2
IOL31B	I/O	6	none		Comp_of_IOL31A	True	none	M1	M1
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	H2	H2
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	H1	H1
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	J2	J2

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	J1	J1
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	F4	F4
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	F3	F3
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	F11	F11
IOR12B/ADCINCLK	I/O	2	DQS2/DQ2	ADCINCLK	Comp_of_IOR12A	True	none	F12	F12
IOR1A/TCK	I/O	10	none	TCK		none	none	B13	B13
IOR1B/TDI	I/O	10	none	TDI		none	none	A13	A13
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	J11	J11
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	J12	J12
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	F13	F13
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	F14	F14
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	G13	G13
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	G14	G14
IOR35A	I/O	11	none			none	none	L12	L12
IOR3A/TMS	I/O	10	none	TMS		none	none	B14	B14
IOR3B/TDO	I/O	10	none	TDO		none	none	C14	C14
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	C12	C12
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	C13	C13
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	D13	D13
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	D14	D14
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	E13	E13
IOR9B/MCKTEST	I/O	2	DQ2	MCKTEST	Comp_of_IOR9A	True	none	E14	E14
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	D4	D4
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	D3	D3
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	F2	F2
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	F1	F1
IOT29A/PUDC_B	I/O	0	none	PUDC_B	True_of_IOT29B	True	x16	B2	B2
IOT29B	I/O	0	none		Comp_of_IOT29A	True	none	A2	A2
IOT35A	I/O	0	none		True_of_IOT35B	True	x16	B3	B3
IOT35B	I/O	0	none		Comp_of_IOT35A	True	none	A3	A3

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	B4	B4
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	A4	A4
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	G2	G2
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	G1	G1
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	B5	B5
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	A5	A5
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	B6	B6
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	A6	A6
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	B7	B7
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	A7	A7
IOT5A	I/O	7	none		True_of_IOT5B	True	x16	E2	E2
IOT5B	I/O	7	none		Comp_of_IOT5A	True	none	E1	E1
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	D8	D8
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	C8	C8
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	B8	B8
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	A8	A8
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	B9	B9
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	A9	A9
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	B10	B10
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	A10	A10
IOT78A	I/O	1	DQ1		True_of_IOT78B	True	x16	B11	B11
IOT78B	I/O	1	DQ1		Comp_of_IOT78A	True	none	A11	A11
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	D2	D2
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	D1	D1
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	D11	D11
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	C11	C11
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	B12	B12
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	A12	A12
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	C1	C1
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	B1	B1

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
VCC_EXT	Power	N/A						E9	
VCC_EXT	Power	N/A						K5	
VCC_EXT	Power	N/A						K6	
VCC_EXT	Power	N/A						E5	
VCC_EXT	Power	N/A						F6	
VCC_EXT	Power	N/A						K9	
VCC_EXT	Power	N/A						E10	
VCC_EXT	Power	N/A						K10	
VCC_EXT	Power	N/A						F5	
VCC_EXT	Power	N/A						F9	
VCC_EXT	Power	N/A						F10	
VCC_EXT	Power	N/A						J5	
VCC_EXT	Power	N/A						E6	
VCC_EXT	Power	N/A						J9	
VCCIO10/VCCX	Power	N/A						E7	E7
VCCIO10/VCCX	Power	N/A						L7	L7
VCCIO10/VCCX	Power	N/A						H5	H5
VCCIO10/VCCX	Power	N/A						G10	G10
VCCIO10/VCCX	Power	N/A						K7	K7
VCCIO10/VCCX	Power	N/A						D7	D7
VCCIO10/VCCX	Power	N/A						G9	G9
VCCIO10/VCCX	Power	N/A						H6	H6
VCC/VCCC	Power	N/A							E9
VCC/VCCC	Power	N/A							K5
VCC/VCCC	Power	N/A							K6
VCC/VCCC	Power	N/A							E5
VCC/VCCC	Power	N/A							F6
VCC/VCCC	Power	N/A							K9
VCC/VCCC	Power	N/A							E10
VCC/VCCC	Power	N/A							K10
VCC/VCCC	Power	N/A							F5

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
VCC/VCCC	Power	N/A							F9
VCC/VCCC	Power	N/A							F10
VCC/VCCC	Power	N/A							J5
VCC/VCCC	Power	N/A							E6
VCC/VCCC	Power	N/A							J9
VCC_REG	Power	N/A						J6	J6
VCCIO0	Power	N/A						C4	C4
VCCIO0	Power	N/A						C5	C5
VCCIO1	Power	N/A						C10	C10
VCCIO1	Power	N/A						C9	C9
VCCIO2	Power	N/A						E12	E12
VCCIO2	Power	N/A						G11	G11
VCCIO2	Power	N/A						D12	D12
VCCIO3	Power	N/A						G12	G12
VCCIO3	Power	N/A						K12	K12
VCCIO3	Power	N/A						K11	K11
VCCIO4	Power	N/A						M9	M9
VCCIO4	Power	N/A						M10	M10
VCCIO5	Power	N/A						M6	M6
VCCIO5	Power	N/A						M5	M5
VCCIO6	Power	N/A						G3	G3
VCCIO6	Power	N/A						E3	E3
VCCIO6	Power	N/A						E4	E4
VCCIO7	Power	N/A						K3	K3
VCCIO7	Power	N/A						H3	H3
VCCIO7	Power	N/A						K4	K4
VQPS	Power	N/A						L11	L11
VSS	Ground	N/A						A1	A1
VSS	Ground	N/A						A14	A14
VSS	Ground	N/A						C2	C2
VSS	Ground	N/A						C3	C3

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
VSS	Ground	N/A						C6	C6
VSS	Ground	N/A						C7	C7
VSS	Ground	N/A						D10	D10
VSS	Ground	N/A						D5	D5
VSS	Ground	N/A						D6	D6
VSS	Ground	N/A						D9	D9
VSS	Ground	N/A						E11	E11
VSS	Ground	N/A						E8	E8
VSS	Ground	N/A						F7	F7
VSS	Ground	N/A						F8	F8
VSS	Ground	N/A						G5	G5
VSS	Ground	N/A						G6	G6
VSS	Ground	N/A						G7	G7
VSS	Ground	N/A						G8	G8
VSS	Ground	N/A						H10	H10
VSS	Ground	N/A						H4	H4
VSS	Ground	N/A						H7	H7
VSS	Ground	N/A						H8	H8
VSS	Ground	N/A						H9	H9
VSS	Ground	N/A						J10	J10
VSS	Ground	N/A						J7	J7
VSS	Ground	N/A						J8	J8
VSS	Ground	N/A						K8	K8
VSS	Ground	N/A						L10	L10
VSS	Ground	N/A						L3	L3
VSS	Ground	N/A						L5	L5
VSS	Ground	N/A						L6	L6
VSS	Ground	N/A						L9	L9
VSS	Ground	N/A						M11	M11
VSS	Ground	N/A						M3	M3
VSS	Ground	N/A						M7	M7

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
VSS	Ground	N/A						P1	P1
VSS	Ground	N/A						P14	P14
VSS	Ground	N/A						G4	G4

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	P5	P5
IOB10B/D04/SI/SSI0	I/O	5	none	D04/SI/SSI0	Comp_of_IOB10A	True	none	R5	R5
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	N6	N6
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	R6	R6
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	N4	N4
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	TRUE	none	R4	R4
IOB1A/RECONFIGN	I/O	5	none	RECONFIGN		none	none	R2	R2
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	L7	L7
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	M6	M6
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	P7	P7
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	R7	R7
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	K8	K8
IOB31B/GCLKC_10A/D15/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D15/BPLL_C_FB0	Comp_of_IOB31A	True	none	L8	L8
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	M8	M8
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	N7	N7
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	N8	N8
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	R8	R8
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	P3	P3
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	R3	R3
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	P9	P9
IOB45B	I/O	4	none		Comp_of_IOB45A	True	none	R9	R9
IOB48A	I/O	4	none		True_of_IOB48B	True	x16	M9	M9
IOB48B	I/O	4	none		Comp_of_IOB48A	True	none	N9	N9
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	M5	M5
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	N5	N5
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	L9	L9
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	M10	M10

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	N10	N10
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	R10	R10
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	M11	M11
IOB54B/GCLKC_11B/D02/MI3/BPLL_C_FB1	I/O	4	none	GCLKC_11B/D02/MI3/BPLL_C_FB1	Comp_of_IOB54A	True	none	N11	N11
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	P13	P13
IOB56B	I/O	4	none		Comp_of_IOB56A	True	none	R13	R13
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	P11	P11
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	R11	R11
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	L10	L10
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	N12	N12
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	R12	R12
IOB64A/DONE	I/O	4	none	DONE		none	none	R14	R14
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	P14	P14
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	P15	P15
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	K12	K12
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	L12	L12
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	K10	K10
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	K11	K11
IOB81A	I/O	3	DQ4		True_of_IOB81B	True	x16	N14	N14
IOB81B	I/O	3	DQ4		Comp_of_IOB81A	True	none	N15	N15
IOB83A	I/O	3	DQ4		True_of_IOB83B	True	x16	M13	M13
IOB83B	I/O	3	DQ4		Comp_of_IOB83A	True	none	M15	M15
IOB85A	I/O	3	DQS4/DQ4		True_of_IOB85B	True	x16	L14	L14
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	L15	L15
IOB87A	I/O	3	DQ4		True_of_IOB87B	True	x16	K13	K13
IOB87B	I/O	3	DQ4		Comp_of_IOB87A	True	none	K15	K15
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	J14	J14
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	J15	J15

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	L6	L6
IOB8B/D06	I/O	5	none	D06	Comp_of_IOB8A	True	none	L5	L5
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	M3	M3
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	M1	M1
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	N2	N2
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	N1	N1
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	G5	G5
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	G3	G3
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	H6	H6
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	H5	H5
IOL23A	I/O	6	DQ6		True_of_IOL23B	True	x16	K5	K5
IOL23B	I/O	6	DQ6		Comp_of_IOL23A	True	none	J4	J4
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	J5	J5
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	H4	H4
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	M4	M4
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	L3	L3
IOL31A	I/O	6	none		True_of_IOL31B	True	x16	P2	P2
IOL31B	I/O	6	none		Comp_of_IOL31A	True	none	P1	P1
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	J2	J2
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	J1	J1
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	K4	K4
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	J3	J3
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	K3	K3
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	K1	K1
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	L2	L2
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	L1	L1
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	D13	D13
IOR12B/ADCINCLK	I/O	2	DQS2/DQ2	ADCINCLK	Comp_of_IOR12A	True	none	D15	D15
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	E14	E14
IOR14B/ADCOTEST	I/O	2	DQ2	ADCOTEST	Comp_of_IOR14A	True	none	E15	E15

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
IOR16A	I/O	2	DQ2		True_of_IOR16B	True	x16	F13	F13
IOR16B	I/O	2	DQ2		Comp_of_IOR16A	True	none	F15	F15
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	G11	G11
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	G12	G12
IOR1A/TCK	I/O	10	none	TCK		none	none	A14	A14
IOR1B/TDI	I/O	10	none	TDI		none	none	E10	E10
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	H12	H12
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	G13	G13
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	H10	H10
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	H11	H11
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	J11	J11
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	J13	J13
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	G14	G14
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	G15	G15
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	H13	H13
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	H15	H15
IOR35A	I/O	11	none			none	none	L13	L13
IOR3A/TMS	I/O	10	none	TMS		none	none	E13	E13
IOR3B/TDO	I/O	10	none	TDO		none	none	D12	D12
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	F11	F11
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	F12	F12
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	B14	B14
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	B15	B15
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	C14	C14
IOR9B/MCKTEST	I/O	2	DQ2	MCKTEST	Comp_of_IOR9A	True	none	C15	C15
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	D3	D3
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	D1	D1
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	C2	C2

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	C1	C1
IOT15A	I/O	7	none		True_of_IOT15B	True	x16	D4	D4
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	E3	E3
IOT17A	I/O	7	none		True_of_IOT17B	True	x16	E5	E5
IOT17B	I/O	7	none		Comp_of_IOT17A	True	none	E4	E4
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	G2	G2
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	G1	G1
IOT29A/PUDC_B	I/O	0	none	PUDC_B	True_of_IOT29B	True	x16	B2	B2
IOT29B	I/O	0	none		Comp_of_IOT29A	True	none	A2	A2
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	B3	B3
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	A3	A3
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	C4	C4
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	A4	A4
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	H3	H3
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	H1	H1
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	D5	D5
IOT41B	I/O	0	none		Comp_of_IOT41A	True	none	C5	C5
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	E6	E6
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	D6	D6
IOT48A	I/O	0	none		True_of_IOT48B	True	x16	B5	B5
IOT48B	I/O	0	none		Comp_of_IOT48A	True	none	A5	A5
IOT50A	I/O	0	none		True_of_IOT50B	True	x16	C6	C6
IOT50B	I/O	0	none		Comp_of_IOT50A	True	none	A6	A6
IOT52A	I/O	0	none		True_of_IOT52B	True	x16	D7	D7
IOT52B	I/O	0	none		Comp_of_IOT52A	True	none	C7	C7
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	F8	F8
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	E8	E8
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	E7	E7
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	D8	D8
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	B7	B7
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	A7	A7

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
IOT5A	I/O	7	none		True_of_IOT5B	True	x16	F5	F5
IOT5B	I/O	7	none		Comp_of_IOT5A	True	none	F4	F4
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	C8	C8
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	A8	A8
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	B9	B9
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	A9	A9
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	F10	F10
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	E9	E9
IOT68A	I/O	1	DQ0		True_of_IOT68B	True	x16	C10	C10
IOT68B	I/O	1	DQ0		Comp_of_IOT68A	True	none	A10	A10
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	D10	D10
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	C9	C9
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	B11	B11
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	A11	A11
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	F3	F3
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	F1	F1
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	D11	D11
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	C11	C11
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	C12	C12
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	A12	A12
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	B13	B13
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	A13	A13
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	E2	E2
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	E1	E1
VCC_EXT	Power	N/A						K7	
VCC_EXT	Power	N/A						G8	
VCC_EXT	Power	N/A						H9	
VCC_EXT	Power	N/A						J8	
VCC_EXT	Power	N/A						F9	
VCC_EXT	Power	N/A						J10	
VCCIO10/VCCX	Power	N/A						F7	F7

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
VCCIO10/VCCX	Power	N/A						M12	M12
VCCIO10/VCCX	Power	N/A						E12	E12
VCCIO10/VCCX	Power	N/A						J6	J6
VCCIO10/VCCX	Power	N/A						B1	B1
VCCIO10/VCCX	Power	N/A						K9	K9
VCCIO10/VCCX	Power	N/A						G10	G10
VCCIO10/VCCX	Power	N/A						L4	L4
NC	N/A	N/A						G6	G6
VCC/VCCC	Power	N/A							K7
VCC/VCCC	Power	N/A							G8
VCC/VCCC	Power	N/A							H9
VCC/VCCC	Power	N/A							J8
VCC/VCCC	Power	N/A							F9
VCC/VCCC	Power	N/A							J10
VCC_REG	Power	N/A						H7	H7
VCCIO0	Power	N/A						B8	B8
VCCIO0	Power	N/A						B4	B4
VCCIO1	Power	N/A						D9	D9
VCCIO1	Power	N/A						B12	B12
VCCIO2	Power	N/A						H14	H14
VCCIO2	Power	N/A						D14	D14
VCCIO3	Power	N/A						M14	M14
VCCIO3	Power	N/A						J12	J12
VCCIO4	Power	N/A						P8	P8
VCCIO4	Power	N/A						P12	P12
VCCIO5	Power	N/A						M7	M7
VCCIO5	Power	N/A						P4	P4
VCCIO6	Power	N/A						M2	M2
VCCIO6	Power	N/A						H2	H2
VCCIO7	Power	N/A						G4	G4
VCCIO7	Power	N/A						D2	D2

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
VQPS	Power	N/A						L11	L11
VSS	Ground	N/A						A1	A1
VSS	Ground	N/A						A15	A15
VSS	Ground	N/A						B10	B10
VSS	Ground	N/A						B6	B6
VSS	Ground	N/A						C13	C13
VSS	Ground	N/A						C3	C3
VSS	Ground	N/A						E11	E11
VSS	Ground	N/A						F14	F14
VSS	Ground	N/A						F2	F2
VSS	Ground	N/A						F6	F6
VSS	Ground	N/A						G7	G7
VSS	Ground	N/A						G9	G9
VSS	Ground	N/A						H8	H8
VSS	Ground	N/A						J7	J7
VSS	Ground	N/A						J9	J9
VSS	Ground	N/A						K14	K14
VSS	Ground	N/A						K2	K2
VSS	Ground	N/A						K6	K6
VSS	Ground	N/A						N13	N13
VSS	Ground	N/A						N3	N3
VSS	Ground	N/A						P10	P10
VSS	Ground	N/A						P6	P6
VSS	Ground	N/A						R1	R1
VSS	Ground	N/A						R15	R15

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	LQ100[1]
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	52
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	51
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	54
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	53
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	57
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	58
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	59
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	60
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	61
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	62
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	63
IOB54B/GCLKC_11B/D02/MI3/BPLL_C_FB1	I/O	4	none	GCLKC_11B/D02/MI3/BPLL_C_FB1	Comp_of_IOB54A	True	none	64
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	65
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	66
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	67
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	68
IOB64A/DONE	I/O	4	none	DONE		none	none	69
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	70
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	71
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	74
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	75
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	35
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	36
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	37
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	38

Note!								
[1] EV version								
Pin Name	**Function**	**BANK**	**DQS**	**Configuration Function**	**Differential Pair**	**LVDS**	**X16**	**LQ100[1]**
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	39
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	40
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	41
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	42
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	43
IOL27B	I/O	6	DQ6		Comp_of_IOL27A	True	none	47
IOL31A	I/O	6	none		True_of_IOL31B	True	x16	48
IOL31B	I/O	6	none		Comp_of_IOL31A	True	none	49
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	28
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	27
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	30
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	29
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	31
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	34
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	32
IOR1A/TCK	I/O	10	none	TCK		none	none	98
IOR1B/TDI	I/O	10	none	TDI		none	none	99
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	91
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	90
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	89
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	88
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	87
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	86
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	85
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	84
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	83
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	82
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	81

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	LQ100[1]
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	78
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	77
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	76
IOR3A/TMS	I/O	10	none	TMS		none	none	96
IOR3B/TDO	I/O	10	none	TDO		none	none	97
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	95
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	94
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	21
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	20
IOT15A	I/O	7	none		True_of_IOT15B	True	x16	19
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	18
IOT19A	I/O	7	none		True_of_IOT19B	True	x16	17
IOT19B	I/O	7	none		Comp_of_IOT19A	True	none	16
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	25
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	24
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	15
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	14
IOT27A	I/O	7	none		True_of_IOT27B	True	x16	13
IOT27B	I/O	7	none		Comp_of_IOT27A	True	none	12
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	10
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	9
IOT74A	I/O	1	DQ1/DQS_01		True_of_IOT74B	True	x16	8
IOT74B	I/O	1	DQ1/DQS_01		Comp_of_IOT74A	True	none	7
IOT80A	I/O	1	DQS1/DQ1		True_of_IOT80B	True	x16	4
IOT80B	I/O	1	DQS1/DQ1		Comp_of_IOT80A	True	none	3
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	2
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	1
VCC_EXT/VCC_REG/VCCIO0	Power	N/A						93
VCC_EXT/VCC_REG/VCCIO0	Power	N/A						50

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	LQ100[1]
VCC_EXT/VCC_REG/VCCIO0	Power	N/A						11
VCCIO10/VCCX	Power	N/A						26
VCCIO10/VCCX	Power	N/A						100
NC	N/A	N/A						45
VCCIO1	Power	N/A						5
VCCIO2	Power	N/A						80
VCCIO6	Power	N/A						46
VCCIO7	Power	N/A						23
VCCIO3/VCCIO4/VCCIO5	Power	N/A						73
VCCIO3/VCCIO4/VCCIO5	Power	N/A						55
VSS	Ground	N/A						6
VSS	Ground	N/A						22
VSS	Ground	N/A						33
VSS	Ground	N/A						44
VSS	Ground	N/A						56
VSS	Ground	N/A						72
VSS	Ground	N/A						79
VSS	Ground	N/A						92

Note!
[1] EV version

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[1]
IOB10A/D03/SSPI_CN	I/O	5	none	D03/SSPI_CS_N	True_of_IOB10B	True	x16	N5
IOB10B/D04/SI/SSI0	I/O	5	none	D04/SI/SSI0	Comp_of_IOB10A	True	none	P5
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	R5
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	T5
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	P6
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	T6
IOB1A/RECONFIGN	I/O	5	none	RECONFIGN		none	none	T2
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	R7
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	T7
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	M9
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	N8
IOB2A	I/O	5	none		True_of_IOB2B	True	x16	P4
IOB2B	I/O	5	none		Comp_of_IOB2A	True	none	T4
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	P7
IOB31B/GCLKC_10A/D15/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D15/BPLL_C_FB0	Comp_of_IOB31A	True	none	M7
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	P8
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	T8
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	R9
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	T9
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	R3
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	T3
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	L10
IOB45B	I/O	4	none		Comp_of_IOB45A	True	none	M10
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	M6
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	N6
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	N9

Note!								
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Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[1]
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	P9
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	N11
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	P11
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	N12
IOB54B/GCLKC_11B/D02/MI3/BPLL_C_FB1	I/O	4	none	GCLKC_11B/D02/MI3/BPLL_C_FB1	Comp_of_IOB54A	True	none	P12
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	L11
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	P10
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	T10
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	M12
IOB60B	I/O	4	none		Comp_of_IOB60A	True	none	M11
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	R11
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	T11
IOB64A/DONE	I/O	4	none	DONE		none	none	P13
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	M13
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	M14
IOB69A	I/O	3	DQ5		True_of_IOB69B	True	x16	L12
IOB69B	I/O	3	DQ5		Comp_of_IOB69A	True	none	L13
IOB71A	I/O	3	DQ5		True_of_IOB71B	True	x16	R12
IOB71B	I/O	3	DQ5		Comp_of_IOB71A	True	none	T12
IOB73A	I/O	3	DQ5		True_of_IOB73B	True	x16	T14
IOB73B	I/O	3	DQ5		Comp_of_IOB73A	True	none	T13
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	R14
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	T15
IOB77A	I/O	3	DQ5		True_of_IOB77B	True	x16	R15
IOB77B	I/O	3	DQ5		Comp_of_IOB77A	True	none	R16
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	P15
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	P16
IOB81A	I/O	3	DQ4		True_of_IOB81B	True	x16	L14
IOB81B	I/O	3	DQ4		Comp_of_IOB81A	True	none	L16

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[1]
IOB83A	I/O	3	DQ4		True_of_IOB83B	True	x16	M15
IOB83B	I/O	3	DQ4		Comp_of_IOB83A	True	none	M16
IOB85A	I/O	3	DQS4/DQ4		True_of_IOB85B	True	x16	N14
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	N16
IOB87A	I/O	3	DQ4		True_of_IOB87B	True	x16	K15
IOB87B	I/O	3	DQ4		Comp_of_IOB87A	True	none	K16
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	J14
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	J16
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	L8
IOB8B/D06	I/O	5	none	D06	Comp_of_IOB8A	True	none	L7
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	K12
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	K11
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	J3
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	J1
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	K2
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	K1
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	L3
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	L1
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	M2
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	M1
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	N3
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	N1
IOL23A	I/O	6	DQ6		True_of_IOL23B	True	x16	P2
IOL23B	I/O	6	DQ6		Comp_of_IOL23A	True	none	P1
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	R2
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	R1
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	M4
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	M3
IOL31A	I/O	6	none		True_of_IOL31B	True	x16	M5
IOL31B	I/O	6	none		Comp_of_IOL31A	True	none	N4

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[1]
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	K3
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	J4
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	F2
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	F1
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	G3
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	G1
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	H2
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	H1
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	D14
IOR12B/ADCINCLK	I/O	2	DQS2/DQ2	ADCINCLK	Comp_of_IOR12A	True	none	D16
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	F13
IOR14B/ADCOTEST	I/O	2	DQ2	ADCOTEST	Comp_of_IOR14A	True	none	F14
IOR16A	I/O	2	DQ2		True_of_IOR16B	True	x16	C15
IOR16B	I/O	2	DQ2		Comp_of_IOR16A	True	none	C16
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	E15
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	E16
IOR1A/TCK	I/O	10	none	TCK		none	none	C14
IOR1B/TDI	I/O	10	none	TDI		none	none	C12
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	F15
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	F16
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	G14
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	G16
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	H15
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	H16
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	G12
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	H11
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	H13
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	H14
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	J11

Note!

[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[1]
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	J12
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	J13
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	K14
IOR35A	I/O	11	none			none	none	P14
IOR3A/TMS	I/O	10	none	TMS		none	none	A15
IOR3B/TDO	I/O	10	none	TDO		none	none	E14
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	E13
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	E12
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	B15
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	B16
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	F12
IOR9B/MCKTEST	I/O	2	DQ2	MCKTEST	Comp_of_IOR9A	True	none	G11
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	F4
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	F3
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	E2
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	E1
IOT15A	I/O	7	none		True_of_IOT15B	True	x16	F6
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	F5
IOT17A	I/O	7	none		True_of_IOT17B	True	x16	E4
IOT17B	I/O	7	none		Comp_of_IOT17A	True	none	E3
IOT19A	I/O	7	none		True_of_IOT19B	True	x16	D3
IOT19B	I/O	7	none		Comp_of_IOT19A	True	none	D1
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	H4
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	H3
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	C1
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	B1
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	C3
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	C2
IOT25A	I/O	7	none		True_of_IOT25B	True	x16	B2
IOT25B	I/O	7	none		Comp_of_IOT25A	True	none	A2

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[1]
IOT27A	I/O	7	none		True_of_IOT27B	True	x16	B3
IOT27B	I/O	7	none		Comp_of_IOT27A	True	none	A3
IOT29A/PUDC_B	I/O	0	none	PUDC_B	True_of_IOT29B	True	x16	C4
IOT29B	I/O	0	none		Comp_of_IOT29A	True	none	A4
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	B5
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	A5
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	D5
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	C5
IOT35A	I/O	0	none		True_of_IOT35B	True	x16	B6
IOT35B	I/O	0	none		Comp_of_IOT35A	True	none	A6
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	F7
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	E6
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	C7
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	A7
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	J6
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	H5
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	D6
IOT41B	I/O	0	none		Comp_of_IOT41A	True	none	C6
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	B8
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	A8
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	C9
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	A9
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	B10
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	A10
IOT5A	I/O	7	none		True_of_IOT5B	True	x16	L4
IOT5B	I/O	7	none		Comp_of_IOT5A	True	none	L5
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	E7
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	E8
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	E10
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	C10

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[1]
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	D8
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	C8
IOT68A	I/O	1	DQ0		True_of_IOT68B	True	x16	C11
IOT68B	I/O	1	DQ0		Comp_of_IOT68A	True	none	A11
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	F9
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	D9
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	K5
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	K6
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	B12
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	A12
IOT85A	I/O	1	DQ1		True_of_IOT85B	True	x16	C13
IOT85B	I/O	1	DQ1		Comp_of_IOT85A	True	none	A13
IOT87A	I/O	1	none		True_of_IOT87B	True	x16	F10
IOT87B	I/O	1	none		Comp_of_IOT87A	True	none	E11
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	B14
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	A14
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	D11
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	D12
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	G6
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	G5
VCC_EXT	Power	N/A						J9
VCC_EXT	Power	N/A						J7
VCC_EXT	Power	N/A						G9
VCC_EXT	Power	N/A						H10
VCC_EXT	Power	N/A						G7
VCC_EXT	Power	N/A						K8
VCC_EXT	Power	N/A						K10
VCCIO10/VCCX	Power	N/A						G10
VCCIO10/VCCX	Power	N/A						F11
VCCIO10/VCCX	Power	N/A						J10

Note!								
[1] EV version								
Pin Name	**Function**	**BANK**	**DQS**	**Configuration Function**	**Differential Pair**	**LVDS**	**X16**	**PG256S[1]**
VCCIO10/VCCX	Power	N/A						H6
VCCIO10/VCCX	Power	N/A						L6
VCCIO10/VCCX	Power	N/A						F8
VCCIO10/VCCX	Power	N/A						L9
VCCIO10/VCCX	Power	N/A						E5
VCC_REG	Power	N/A						H8
VCCIO0	Power	N/A						D7
VCCIO0	Power	N/A						B4
VCCIO1	Power	N/A						B13
VCCIO1	Power	N/A						D10
VCCIO1	Power	N/A						B9
VCCIO2	Power	N/A						J15
VCCIO2	Power	N/A						D15
VCCIO2	Power	N/A						G13
VCCIO3	Power	N/A						K13
VCCIO3	Power	N/A						N15
VCCIO3	Power	N/A						R13
VCCIO4	Power	N/A						N10
VCCIO4	Power	N/A						R8
VCCIO5	Power	N/A						N7
VCCIO5	Power	N/A						R4
VCCIO6	Power	N/A						N2
VCCIO6	Power	N/A						K4
VCCIO6	Power	N/A						J2
VCCIO7	Power	N/A						D2
VCCIO7	Power	N/A						G4
VQPS	Power	N/A						H12
VSS	Ground	N/A						A1
VSS	Ground	N/A						A16
VSS	Ground	N/A						B11

Note!								
[1] EV version								
Pin Name	**Function**	**BANK**	**DQS**	**Configuration Function**	**Differential Pair**	**LVDS**	**X16**	**PG256S**[1]
VSS	Ground	N/A						B7
VSS	Ground	N/A						D13
VSS	Ground	N/A						D4
VSS	Ground	N/A						E9
VSS	Ground	N/A						G15
VSS	Ground	N/A						G2
VSS	Ground	N/A						G8
VSS	Ground	N/A						H7
VSS	Ground	N/A						H9
VSS	Ground	N/A						J5
VSS	Ground	N/A						J8
VSS	Ground	N/A						K7
VSS	Ground	N/A						K9
VSS	Ground	N/A						L15
VSS	Ground	N/A						L2
VSS	Ground	N/A						M8
VSS	Ground	N/A						N13
VSS	Ground	N/A						P3
VSS	Ground	N/A						R10
VSS	Ground	N/A						R6
VSS	Ground	N/A						T1
VSS	Ground	N/A						T16

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
IOB10A/D06/SSPI_CS_N	I/O	5	none	D06/SSPI_CS_N	True_of_IOB10B	True	x16	E6	E6
IOB10B/D05/SI/SSI0	I/O	5	none	D05/SI/SSI0	Comp_of_IOB10A	True	none	E7	E7
IOB12A/GCLKT_10B/D15/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D15/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	C6	C6
IOB12B/GCLKC_10B/D07/DOUT/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/D07/DOUT/LPLL1_C_IN1	Comp_of_IOB12A	True	none	A5	A5
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	B6	B6
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	A6	A6
IOB16A	I/O	5	none		True_of_IOB16B	True	x16	D6	D6
IOB16B	I/O	5	none		Comp_of_IOB16A	True	none	D5	D5
IOB18A	I/O	5	none		True_of_IOB18B	True	x16	B4	B4
IOB18B	I/O	5	none		Comp_of_IOB18A	True	none	A4	A4
IOB20A	I/O	5	none		True_of_IOB20B	True	x16	F7	F7
IOB20B	I/O	5	none		Comp_of_IOB20A	True	none	F6	F6
IOB22A	I/O	5	none		True_of_IOB22B	True	x16	B5	B5
IOB22B	I/O	5	none		Comp_of_IOB22A	True	none	A2	A2
IOB24A	I/O	5	none		True_of_IOB24B	True	x16	D3	D3
IOB24B	I/O	5	none		Comp_of_IOB24A	True	none	C3	C3
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	B3	B3
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	A3	A3
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	B1	B1
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	D4	D4
IOB2A	I/O	5	none		True_of_IOB2B	True	x16	B8	B8
IOB2B	I/O	5	none		Comp_of_IOB2A	True	none	A8	A8
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	C2	C2
IOB31B/GCLKC_10A/D01/MOSI/MI0/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D01/MOSI/MI0/BPLL_C_FB0	Comp_of_IOB31A	True	none	C1	C1
IOB33A/GCLKT_9/D13/MCS_N/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/MCS_N/BPLL_T_IN1	True_of_IOB33B	True	x16	D2	D2
IOB33B/GCLKC_9	I/O	4	none	GCLKC_9	Comp_of_IOB33A	True	none	D1	D1
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	E5	E5

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	E1	E1
IOB37A/D08	I/O	4	none	D08	True_of_IOB37B	True	x16	G5	G5
IOB37B	I/O	4	none		Comp_of_IOB37A	True	none	F5	F5
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	F3	F3
IOB4A/D04/SDA	I/O	5	none	D04/SDA	True_of_IOB4B	True	x16	B7	B7
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	A7	A7
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	F2	F2
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	F1	F1
IOB52A/RECONFIG_N	I/O	4	none	RECONFIG_N	True_of_IOB52B	True	x16	H5	H5
IOB54A/GCLKT_11B/D10/MI2	I/O	4	none	GCLKT_11B/D10/MI2	True_of_IOB54B	True	x16	G2	G2
IOB54B/GCLKC_11B/MI3/BPLL_C_FB1	I/O	4	none	GCLKC_11B/MI3/BPLL_C_FB1	Comp_of_IOB54A	True	none	G1	G1
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	H2	H2
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	H1	H1
IOB62B/CSI_B	I/O	4	none	CSI_B	Comp_of_IOB62A	True	none	J3	J3
IOB64A/READY	I/O	4	none	READY		none	none	F4	F4
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	J6	J6
IOB65B	I/O	3	none		Comp_of_IOB65A	True	none	L3	L3
IOB69A	I/O	3	DQ5		True_of_IOB69B	True	x16	J2	J2
IOB69B	I/O	3	DQ5		Comp_of_IOB69A	True	none	J1	J1
IOB6A	I/O	5	none		True_of_IOB6B	True	x16	C8	C8
IOB6B	I/O	5	none		Comp_of_IOB6A	True	none	D8	D8
IOB71A	I/O	3	DQ5		True_of_IOB71B	True	x16	K2	K2
IOB71B	I/O	3	DQ5		Comp_of_IOB71A	True	none	K1	K1
IOB73A	I/O	3	DQ5		True_of_IOB73B	True	x16	K5	K5
IOB73B	I/O	3	DQ5		Comp_of_IOB73A	True	none	L4	L4
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	L2	L2
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	L1	L1
IOB77A	I/O	3	DQ5		True_of_IOB77B	True	x16	N2	N2
IOB77B	I/O	3	DQ5		Comp_of_IOB77A	True	none	N1	N1

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	P2	P2
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	P1	P1
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	R1	R1
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	M2	M2
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	M1	M1
IOB8A/D03/SO/SSI1	I/O	5	none	D03/SO/SSI1	True_of_IOB8B	True	x16	F8	F8
IOB8B/D02	I/O	5	none	D02	Comp_of_IOB8A	True	none	E8	E8
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	K6	K6
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	L6	L6
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	E11	E11
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	E10	E10
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	F9	F9
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	F10	F10
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	B13	B13
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	A13	A13
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	B12	B12
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	A12	A12
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	C11	C11
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	E9	E9
IOL23A	I/O	6	DQ6		True_of_IOL23B	True	x16	B11	B11
IOL23B	I/O	6	DQ6		Comp_of_IOL23A	True	none	A11	A11
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	B10	B10
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	A10	A10
IOL27A	I/O	6	DQ6		True_of_IOL27B	True	x16	D9	D9
IOL27B	I/O	6	DQ6		Comp_of_IOL27A	True	none	C9	C9
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	B9	B9
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	A9	A9

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
IOL3A/GCLKT_14/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14/LPLL0_T_IN2	True_of_IOL3B	True	x16	D14	D14
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	C14	C14
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	D12	D12
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	D11	D11
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	A15	A15
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	F11	F11
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	B14	B14
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	A14	A14
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	R7	R7
IOR12B	I/O	2	DQS2/DQ2		Comp_of_IOR12A	True	none	T7	T7
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	R6	R6
IOR14B	I/O	2	DQ2		Comp_of_IOR14A	True	none	T6	T6
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	R5	R5
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	T5	T5
IOR1A/TCK	I/O	10	none	TCK		none	none	H3	H3
IOR1B/TDI	I/O	10	none	TDI		none	none	H4	H4
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	P6	P6
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	M6	M6
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	N5	N5
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	N6	N6
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	R3	R3
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	T3	T3
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	M7	M7
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	K8	K8
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	N3	N3
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	P3	P3
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	R4	R4
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	T4	T4
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	T2	T2

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	L7	L7
IOR3A/TMS	I/O	10	none	TMS		none	none	J5	J5
IOR3B/TDO	I/O	10	none	TDO		none	none	J4	J4
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	R8	R8
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	T8	T8
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	N8	N8
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	P8	P8
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	L8	L8
IOR9B	I/O	2	DQ2		Comp_of_IOR9A	True	none	M8	M8
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	D15	D15
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	D16	D16
IOT19A/EMCCLK	I/O	7	none	EMCCLK	True_of_IOT19B	True	x16	F15	F15
IOT19B/CSO_B	I/O	7	none	CSO_B	Comp_of_IOT19A	True	none	F16	F16
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	F14	F14
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	B16	B16
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	G11	G11
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	F13	F13
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	G15	G15
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	G16	G16
IOT25A/MODE0	I/O	7	none	MODE0	True_of_IOT25B	True	x16	H13	H13
IOT25B/MODE1	I/O	7	none	MODE1	Comp_of_IOT25A	True	none	H12	H12
IOT27A/MODE2	I/O	7	none	MODE2	True_of_IOT27B	True	x16	G12	G12
IOT27B/DONE	I/O	7	none	DONE	Comp_of_IOT27A	True	none	H14	H14
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	J12	J12
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	J14	J14
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	J15	J15
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	J16	J16
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	L15	L15
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	L16	L16
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	J13	J13
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	L13	L13

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	E15	E15
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	E16	E16
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	L14	L14
IOT43A	I/O	0	none		True_of_IOT43B	True	x16	J11	J11
IOT43B	I/O	0	none		Comp_of_IOT43A	True	none	K12	K12
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	N15	N15
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	N16	N16
IOT48A	I/O	0	none		True_of_IOT48B	True	x16	L12	L12
IOT48B	I/O	0	none		Comp_of_IOT48A	True	none	K11	K11
IOT50A	I/O	0	none		True_of_IOT50B	True	x16	R16	R16
IOT50B	I/O	0	none		Comp_of_IOT50A	True	none	P16	P16
IOT52A	I/O	0	none		True_of_IOT52B	True	x16	N14	N14
IOT52B	I/O	0	none		Comp_of_IOT52A	True	none	P15	P15
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	N13	N13
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	M12	M12
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	K15	K15
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	K16	K16
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	M15	M15
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	M16	M16
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	T14	T14
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	T15	T15
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	P14	P14
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	L11	L11
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	R14	R14
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	M11	M11
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	N12	N12
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	R13	R13
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	T13	T13
IOT74A	I/O	1	DQ1/DQS_01		True_of_IOT74B	True	x16	K10	K10

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
IOT74B	I/O	1	DQ1/DQS_01		Comp_of_IOT74A	True	none	L10	L10
IOT76A	I/O	1	DQ1		True_of_IOT76B	True	x16	R12	R12
IOT76B	I/O	1	DQ1		Comp_of_IOT76A	True	none	T12	T12
IOT78A	I/O	1	DQ1		True_of_IOT78B	True	x16	M10	M10
IOT78B	I/O	1	DQ1		Comp_of_IOT78A	True	none	N11	N11
IOT80A	I/O	1	DQS1/DQ1		True_of_IOT80B	True	x16	R10	R10
IOT80B	I/O	1	DQS1/DQ1		Comp_of_IOT80A	True	none	T10	T10
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	R11	R11
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	T11	T11
IOT85A	I/O	1	DQ1		True_of_IOT85B	True	x16	M9	M9
IOT85B	I/O	1	DQ1		Comp_of_IOT85A	True	none	N9	N9
IOT87A	I/O	1	none		True_of_IOT87B	True	x16	K9	K9
IOT87B	I/O	1	none		Comp_of_IOT87A	True	none	L9	L9
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	P9	P9
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	P11	P11
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	R9	R9
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	T9	T9
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	C15	C15
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	C16	C16
VCC/VCCC	Power	N/A							H11
VCC/VCCC	Power	N/A							H6
VCC/VCCC	Power	N/A							N4
VCC/VCCC	Power	N/A							G8
VCC/VCCC	Power	N/A							G7
VCC/VCCC	Power	N/A							D13
VCC/VCCC	Power	N/A							G10
VCC/VCCC	Power	N/A							K7
VCC/VCCC	Power	N/A							G6
VCC_EXT	Power	N/A						H11	
VCC_EXT	Power	N/A						H6	
VCC_EXT	Power	N/A						G8	

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
VCC_EXT	Power	N/A						G7	
VCC_EXT	Power	N/A						D13	
VCC_EXT	Power	N/A						N4	
VCC_EXT	Power	N/A						G10	
VCC_EXT	Power	N/A						K7	
VCC_EXT	Power	N/A						G6	
VCC_REG	Power	N/A						G9	G9
VCCIO0	Power	N/A						M14	M14
VCCIO0	Power	N/A						K14	K14
VCCIO1	Power	N/A						P10	P10
VCCIO1	Power	N/A						P13	P13
VCCIO1	Power	N/A						T16	T16
VCCIO10/VCCIO4	Power	N/A						G3	E3
VCCIO10/VCCIO4	Power	N/A						E3	G3
VCCIO2	Power	N/A						P4	P4
VCCIO2	Power	N/A						P7	P7
VCCIO2	Power	N/A						T1	T1
VCCIO3	Power	N/A						K3	K3
VCCIO3	Power	N/A						M3	M3
VCCIO5	Power	N/A						C7	C7
VCCIO5	Power	N/A						A1	A1
VCCIO5	Power	N/A						C4	C4
VCCIO6	Power	N/A						C13	C13
VCCIO6	Power	N/A						A16	A16
VCCIO6	Power	N/A						C10	C10
VCCIO7	Power	N/A						G14	G14
VCCIO7	Power	N/A						E14	E14
VCCX	Power	N/A						F12	F12
VCCX	Power	N/A						L5	L5
VQPS	Power	N/A						M5	M5
VSS	Ground	N/A						E12	E12
VSS	Ground	N/A						H7	H7

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
VSS	Ground	N/A						H8	H8
VSS	Ground	N/A						H9	H9
VSS	Ground	N/A						H10	H10
VSS	Ground	N/A						J7	J7
VSS	Ground	N/A						J8	J8
VSS	Ground	N/A						J9	J9
VSS	Ground	N/A						J10	J10
VSS	Ground	N/A						B2	B2
VSS	Ground	N/A						B15	B15
VSS	Ground	N/A						C5	C5
VSS	Ground	N/A						C12	C12
VSS	Ground	N/A						D7	D7
VSS	Ground	N/A						D10	D10
VSS	Ground	N/A						E4	E4
VSS	Ground	N/A						E13	E13
VSS	Ground	N/A						G4	G4
VSS	Ground	N/A						G13	G13
VSS	Ground	N/A						K4	K4
VSS	Ground	N/A						K13	K13
VSS	Ground	N/A						M4	M4
VSS	Ground	N/A						M13	M13
VSS	Ground	N/A						N7	N7
VSS	Ground	N/A						N10	N10
VSS	Ground	N/A						P5	P5
VSS	Ground	N/A						P12	P12
VSS	Ground	N/A						R2	R2
VSS	Ground	N/A						R15	R15
VSS	Ground	N/A						E2	E2
VSS	Ground	N/A						H16	H16
VSS	Ground	N/A						H15	H15

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
IOB10A/D06/SSPI_CS_N	I/O	5	none	D06/SSPI_CS_N	True_of_IOB10B	True	x16	E6	E6
IOB10B/D05/SI/SSI0	I/O	5	none	D05/SI/SSI0	Comp_of_IOB10A	True	none	E7	E7
IOB12A/GCLKT_10B/D15/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D15/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	C6	C6
IOB12B/GCLKC_10B/D07/DOUT/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/D07/DOUT/LPLL1_C_IN1	Comp_of_IOB12A	True	none	A5	A5
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	B6	B6
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	A6	A6
IOB16A	I/O	5	none		True_of_IOB16B	True	x16	D6	D6
IOB16B	I/O	5	none		Comp_of_IOB16A	True	none	D5	D5
IOB18A	I/O	5	none		True_of_IOB18B	True	x16	B4	B4
IOB18B	I/O	5	none		Comp_of_IOB18A	True	none	A4	A4
IOB20A	I/O	5	none		True_of_IOB20B	True	x16	F7	F7
IOB20B	I/O	5	none		Comp_of_IOB20A	True	none	F6	F6
IOB22A	I/O	5	none		True_of_IOB22B	True	x16	B5	B5
IOB22B	I/O	5	none		Comp_of_IOB22A	True	none	A2	A2
IOB24A	I/O	5	none		True_of_IOB24B	True	x16	D3	D3
IOB24B	I/O	5	none		Comp_of_IOB24A	True	none	C3	C3
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	B3	B3
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	A3	A3
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	B1	B1
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	D4	D4
IOB2A	I/O	5	none		True_of_IOB2B	True	x16	B8	B8
IOB2B	I/O	5	none		Comp_of_IOB2A	True	none	A8	A8
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	C2	C2
IOB31B/GCLKC_10A/D01/MOSI/MI0/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D01/MOSI/MI0/BPLL_C_FB0	Comp_of_IOB31A	True	none	C1	C1
IOB33A/GCLKT_9/D13/MCS_N/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/MCS_N/BPLL_T_IN1	True_of_IOB33B	True	x16	D2	D2
IOB33B/GCLKC_9	I/O	4	none	GCLKC_9	Comp_of_IOB33A	True	none	D1	D1
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	E5	E5
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	E1	E1

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
IOB37A/D08	I/O	4	none	D08	True_of_IOB37B	True	x16	G5	G5
IOB37B	I/O	4	none		Comp_of_IOB37A	True	none	F5	F5
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	F3	F3
IOB4A/D04/SDA	I/O	5	none	D04/SDA	True_of_IOB4B	True	x16	B7	B7
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	A7	A7
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	F2	F2
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	F1	F1
IOB52A/RECONFIG_N	I/O	4	none	RECONFIG_N	True_of_IOB52B	True	x16	H5	H5
IOB54A/GCLKT_11B/D10/MI2	I/O	4	none	GCLKT_11B/D10/MI2	True_of_IOB54B	True	x16	G2	G2
IOB54B/GCLKC_11B/MI3/BPLL_C_FB1	I/O	4	none	GCLKC_11B/MI3/BPLL_C_FB1	Comp_of_IOB54A	True	none	G1	G1
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	H2	H2
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	H1	H1
IOB62B/CSI_B	I/O	4	none	CSI_B	Comp_of_IOB62A	True	none	J3	J3
IOB64A/READY	I/O	4	none	READY		none	none	F4	F4
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	J6	J6
IOB65B	I/O	3	none		Comp_of_IOB65A	True	none	L3	L3
IOB69A	I/O	3	DQ5		True_of_IOB69B	True	x16	J2	J2
IOB69B	I/O	3	DQ5		Comp_of_IOB69A	True	none	J1	J1
IOB6A	I/O	5	none		True_of_IOB6B	True	x16	C8	C8
IOB6B	I/O	5	none		Comp_of_IOB6A	True	none	D8	D8
IOB71A	I/O	3	DQ5		True_of_IOB71B	True	x16	K2	K2
IOB71B	I/O	3	DQ5		Comp_of_IOB71A	True	none	K1	K1
IOB73A	I/O	3	DQ5		True_of_IOB73B	True	x16	K5	K5
IOB73B	I/O	3	DQ5		Comp_of_IOB73A	True	none	L4	L4
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	L2	L2
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	L1	L1
IOB77A	I/O	3	DQ5		True_of_IOB77B	True	x16	N2	N2
IOB77B	I/O	3	DQ5		Comp_of_IOB77A	True	none	N1	N1
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	P2	P2

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	P1	P1
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	R1	R1
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	M2	M2
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	M1	M1
IOB8A/D03/SO/SSI1	I/O	5	none	D03/SO/SSI1	True_of_IOB8B	True	x16	F8	F8
IOB8B/D02	I/O	5	none	D02	Comp_of_IOB8A	True	none	E8	E8
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	K6	K6
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	L6	L6
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	E11	E11
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	E10	E10
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	F9	F9
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	F10	F10
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	B13	B13
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	A13	A13
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	B12	B12
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	A12	A12
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	C11	C11
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	E9	E9
IOL23A	I/O	6	DQ6		True_of_IOL23B	True	x16	B11	B11
IOL23B	I/O	6	DQ6		Comp_of_IOL23A	True	none	A11	A11
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	B10	B10
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	A10	A10
IOL27A	I/O	6	DQ6		True_of_IOL27B	True	x16	D9	D9
IOL27B	I/O	6	DQ6		Comp_of_IOL27A	True	none	C9	C9
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	B9	B9
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	A9	A9
IOL3A/GCLKT_14/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14/LPLL0_T_IN2	True_of_IOL3B	True	x16	D14	D14
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	C14	C14
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	D12	D12

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	D11	D11
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	A15	A15
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	F11	F11
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	B14	B14
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	A14	A14
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	R7	R7
IOR12B	I/O	2	DQS2/DQ2		Comp_of_IOR12A	True	none	T7	T7
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	R6	R6
IOR14B	I/O	2	DQ2		Comp_of_IOR14A	True	none	T6	T6
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	R5	R5
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	T5	T5
IOR1A/TCK	I/O	10	none	TCK		none	none	H3	H3
IOR1B/TDI	I/O	10	none	TDI		none	none	H4	H4
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	P6	P6
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	M6	M6
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	N5	N5
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	N6	N6
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	R3	R3
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	T3	T3
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	M7	M7
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	K8	K8
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	N3	N3
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	P3	P3
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	R4	R4
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	T4	T4
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	T2	T2
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	L7	L7
IOR3A/TMS	I/O	10	none	TMS		none	none	J5	J5
IOR3B/TDO	I/O	10	none	TDO		none	none	J4	J4

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	R8	R8
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	T8	T8
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	N8	N8
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	P8	P8
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	L8	L8
IOR9B	I/O	2	DQ2		Comp_of_IOR9A	True	none	M8	M8
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	D15	D15
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	D16	D16
IOT19A/EMCCLK	I/O	7	none	EMCCLK	True_of_IOT19B	True	x16	F15	F15
IOT19B/CSO_B	I/O	7	none	CSO_B	Comp_of_IOT19A	True	none	F16	F16
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	F14	F14
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	B16	B16
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	G11	G11
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	F13	F13
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	G15	G15
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	G16	G16
IOT25A/MODE0	I/O	7	none	MODE0	True_of_IOT25B	True	x16	H13	H13
IOT25B/MODE1	I/O	7	none	MODE1	Comp_of_IOT25A	True	none	H12	H12
IOT27A/MODE2	I/O	7	none	MODE2	True_of_IOT27B	True	x16	G12	G12
IOT27B/DONE	I/O	7	none	DONE	Comp_of_IOT27A	True	none	H14	H14
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	J12	J12
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	J14	J14
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	J15	J15
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	J16	J16
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	L15	L15
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	L16	L16
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	J13	J13
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	L13	L13
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	E15	E15
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	E16	E16
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	L14	L14

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
IOT43A	I/O	0	none		True_of_IOT43B	True	x16	J11	J11
IOT43B	I/O	0	none		Comp_of_IOT43A	True	none	K12	K12
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	N15	N15
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	N16	N16
IOT48A	I/O	0	none		True_of_IOT48B	True	x16	L12	L12
IOT48B	I/O	0	none		Comp_of_IOT48A	True	none	K11	K11
IOT50A	I/O	0	none		True_of_IOT50B	True	x16	R16	R16
IOT50B	I/O	0	none		Comp_of_IOT50A	True	none	P16	P16
IOT52A	I/O	0	none		True_of_IOT52B	True	x16	N14	N14
IOT52B	I/O	0	none		Comp_of_IOT52A	True	none	P15	P15
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	N13	N13
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	M12	M12
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	K15	K15
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	K16	K16
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	M15	M15
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	M16	M16
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	T14	T14
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	T15	T15
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	P14	P14
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	L11	L11
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	R14	R14
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	M11	M11
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	N12	N12
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	R13	R13
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	T13	T13
IOT74A	I/O	1	DQ1/DQS_01		True_of_IOT74B	True	x16	K10	K10
IOT74B	I/O	1	DQ1/DQS_01		Comp_of_IOT74A	True	none	L10	L10
IOT76A	I/O	1	DQ1		True_of_IOT76B	True	x16	R12	R12
IOT76B	I/O	1	DQ1		Comp_of_IOT76A	True	none	T12	T12
IOT78A	I/O	1	DQ1		True_of_IOT78B	True	x16	M10	M10

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
IOT78B	I/O	1	DQ1		Comp_of_IOT78A	True	none	N11	N11
IOT80A	I/O	1	DQS1/DQ1		True_of_IOT80B	True	x16	R10	R10
IOT80B	I/O	1	DQS1/DQ1		Comp_of_IOT80A	True	none	T10	T10
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	R11	R11
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	T11	T11
IOT85A	I/O	1	DQ1		True_of_IOT85B	True	x16	M9	M9
IOT85B	I/O	1	DQ1		Comp_of_IOT85A	True	none	N9	N9
IOT87A	I/O	1	none		True_of_IOT87B	True	x16	K9	K9
IOT87B	I/O	1	none		Comp_of_IOT87A	True	none	L9	L9
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	P9	P9
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	P11	P11
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	R9	R9
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	T9	T9
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	C15	C15
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	C16	C16
VCC/VCCC	Power	N/A							H11
VCC/VCCC	Power	N/A							H6
VCC/VCCC	Power	N/A							N4
VCC/VCCC	Power	N/A							G8
VCC/VCCC	Power	N/A							G7
VCC/VCCC	Power	N/A							D13
VCC/VCCC	Power	N/A							G10
VCC/VCCC	Power	N/A							K7
VCC/VCCC	Power	N/A							G6
VCC_EXT	Power	N/A						H11	
VCC_EXT	Power	N/A						H6	
VCC_EXT	Power	N/A						G8	
VCC_EXT	Power	N/A						G7	
VCC_EXT	Power	N/A						D13	
VCC_EXT	Power	N/A						N4	
VCC_EXT	Power	N/A						G10	
VCC_EXT	Power	N/A						K7	

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
					Note! [1] EV version [2] LV version				
VCC_EXT	Power	N/A						G6	
VCC_REG	Power	N/A						G9	G9
VCCIO0	Power	N/A						M14	M14
VCCIO0	Power	N/A						K14	K14
VCCIO1	Power	N/A						P10	P10
VCCIO1	Power	N/A						P13	P13
VCCIO1	Power	N/A						T16	T16
VCCIO10/VCCIO4	Power	N/A						G3	E3
VCCIO10/VCCIO4	Power	N/A						E3	G3
VCCIO2	Power	N/A						P4	P4
VCCIO2	Power	N/A						P7	P7
VCCIO2	Power	N/A						T1	T1
VCCIO3	Power	N/A						K3	K3
VCCIO3	Power	N/A						M3	M3
VCCIO5	Power	N/A						C7	C7
VCCIO5	Power	N/A						A1	A1
VCCIO5	Power	N/A						C4	C4
VCCIO6	Power	N/A						C13	C13
VCCIO6	Power	N/A						A16	A16
VCCIO6	Power	N/A						C10	C10
VCCIO7	Power	N/A						G14	G14
VCCIO7	Power	N/A						E14	E14
VCCX	Power	N/A						F12	F12
VCCX	Power	N/A						L5	L5
VQPS	Power	N/A						M5	M5
VSS	Ground	N/A						E12	E12
VSS	Ground	N/A						H7	H7
VSS	Ground	N/A						H8	H8
VSS	Ground	N/A						H9	H9
VSS	Ground	N/A						H10	H10
VSS	Ground	N/A						J7	J7
VSS	Ground	N/A						J8	J8

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
VSS	Ground	N/A						J9	J9
VSS	Ground	N/A						J10	J10
VSS	Ground	N/A						B2	B2
VSS	Ground	N/A						B15	B15
VSS	Ground	N/A						C5	C5
VSS	Ground	N/A						C12	C12
VSS	Ground	N/A						D7	D7
VSS	Ground	N/A						D10	D10
VSS	Ground	N/A						E4	E4
VSS	Ground	N/A						E13	E13
VSS	Ground	N/A						G4	G4
VSS	Ground	N/A						G13	G13
VSS	Ground	N/A						K4	K4
VSS	Ground	N/A						K13	K13
VSS	Ground	N/A						M4	M4
VSS	Ground	N/A						M13	M13
VSS	Ground	N/A						N7	N7
VSS	Ground	N/A						N10	N10
VSS	Ground	N/A						P5	P5
VSS	Ground	N/A						P12	P12
VSS	Ground	N/A						R2	R2
VSS	Ground	N/A						R15	R15
VSS	Ground	N/A						E2	E2
VSS	Ground	N/A						H16	H16
VSS	Ground	N/A						H15	H15

Note!

[1] You can ignore the short-circuited pins.

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG121N
BANK7 True LVDS Pair								
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	K11
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	L11
IOT15A	I/O	7	none			True	x16	L10
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	K10
IOT19A	I/O	7	none		True_of_IOT19B	True	x16	L7
IOT19B	I/O	7	none		Comp_of_IOT19A	True	none	L8
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	J11
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	J10
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	K7
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	J7
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	L6
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	K6
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	H11
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	H10
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	G11
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	G10
BANK6 True LVDS Pair								
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	B11
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	B10
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	A11
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	A10
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	E11
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	E10
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	C11
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	C10

Note!
[1] You can ignore the short-circuited pins.

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG121N
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	D11
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	D10
BANK5 True LVDS Pair								
IOB10A/D03/SSPI_CN/SSI0	I/O	5	none	D03/SSPI_CS_N/SSI0	True_of_IOB10B	True	x16	B1_B10A[1]
IOB10B/D04/SI	I/O	5	none	D04/SI	Comp_of_IOB10A	True	none	A3_B10B[1]
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	E2
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	E1
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	C1_B14A[1]
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	D1
IOB24A	I/O	5	none		True_of_IOB24B	True	x16	A1
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	F2
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	F1
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	B2
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	C2
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	A2_B8A[1]
BANK4 True LVDS Pair								
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	E8
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	E6
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	D5
IOB54B/GCLKC_11B/D02/BPLL_C_FB1/MI3	I/O	4	none	GCLKC_11B/D02/BPLL_C_FB1/MI3	Comp_of_IOB54A	True	none	E4
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	B3
IOB56B	I/O	4	none		Comp_of_IOB56A	True	none	C3
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	E5

Note!
[1] You can ignore the short-circuited pins.

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG121N
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	D6
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	E3
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	E7
BANK3 True LVDS Pair								
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	G4
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	H4
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	H1
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	H2
BANK2 True LVDS Pair								
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	L2
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	L1
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	K1
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	K2
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	J4
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	K4
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	G2
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	G1
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	L4
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	L3
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	J1
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	J2

Note!

[1] You can ignore the short-circuited pins.

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG121N
BANK1 True LVDS Pair								
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	H5
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	J5
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	L5
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	K5
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	H8
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	H7
IOT68A	I/O	1	DQ0		True_of_IOT68B	True	x16	G7
IOT68B	I/O	1	DQ0		Comp_of_IOT68A	True	none	G8
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	F5
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	G5
BANK0 True LVDS Pair								
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	L9
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	K9
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	J8
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	K8
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	F7
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	F6

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
BANK7 True LVDS Pair								
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	F2
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	F1
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	J7
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	J6
IOT15A	I/O	7	none		True_of_IOT15B	True	x16	E3
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	E1
IOT17A	I/O	7	none		True_of_IOT17B	True	x16	F4
IOT17B	I/O	7	none		Comp_of_IOT17A	True	none	F3
IOT19A	I/O	7	none		True_of_IOT19B	True	x16	D2
IOT19B	I/O	7	none		Comp_of_IOT19A	True	none	D1
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	H4
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	H3
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	L7
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	K6
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	E4
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	D3
IOT25A	I/O	7	none		True_of_IOT25B	True	x16	F6
IOT25B	I/O	7	none		Comp_of_IOT25A	True	none	F5
IOT27A	I/O	7	none		True_of_IOT27B	True	x16	C2
IOT27B	I/O	7	none		Comp_of_IOT27A	True	none	C1
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	L5
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	K5
IOT5A	I/O	7	none		True_of_IOT5B	True	x16	H6
IOT5B	I/O	7	none		Comp_of_IOT5A	True	none	H5
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	G3
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	G1

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	H7
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	G6
BANK6 True LVDS Pair								
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	K2
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	K1
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	L2
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	L1
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	M3
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	M1
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	N2
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	N1
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	P2
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	P1
IOL23A	I/O	6	DQ6		True_of_IOL23B	True	x16	T2
IOL23B	I/O	6	DQ6		Comp_of_IOL23A	True	none	T1
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	U2
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	U1
IOL27A	I/O	6	DQ6		True_of_IOL27B	True	x16	L6
IOL27B	I/O	6	DQ6		Comp_of_IOL27A	True	none	M5
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	N4
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	N3
IOL31A	I/O	6	none		True_of_IOL31B	True	x16	P4
IOL31B	I/O	6	none		Comp_of_IOL31A	True	none	P3
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	H2
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	H1
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	K4
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	K3
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	J3

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	J1
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	L4
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	L3
BANK5 True LVDS Pair								
IOB10A/D03/SSPI_CN/SSI0	I/O	5	none	D03/SSPI_CS_N/SSI0	True_of_IOB10B	True	x16	U5
IOB10B/D04/SI	I/O	5	none	D04/SI	Comp_of_IOB10A	True	none	V5
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	R5
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	T5
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	N6
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	P7
IOB16A	I/O	5	none		True_of_IOB16B	True	x16	T6
IOB16B	I/O	5	none		Comp_of_IOB16A	True	none	V6
IOB18A	I/O	5	none		True_of_IOB18B	True	x16	N7
IOB18B	I/O	5	none		Comp_of_IOB18A	True	none	P8
IOB20A	I/O	5	none		True_of_IOB20B	True	x16	U7
IOB20B	I/O	5	none		Comp_of_IOB20A	True	none	V7
IOB22A	I/O	5	none		True_of_IOB22B	True	x16	U8
IOB22B	I/O	5	none		Comp_of_IOB22A	True	none	V8
IOB24A	I/O	5	none		True_of_IOB24B	True	x16	M8
IOB24B	I/O	5	none		Comp_of_IOB24A	True	none	N8
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	M10
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	N9
IOB2A	I/O	5	none		True_of_IOB2B	True	x16	T4
IOB2B	I/O	5	none		Comp_of_IOB2A	True	none	V4
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	N5
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	P6

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB6A	I/O	5	none		True_of_IOB6B	True	x16	R7
IOB6B	I/O	5	none		Comp_of_IOB6A	True	none	T7
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	R3
IOB8B/D06	I/O	5	none	D06	Comp_of_IOB8A	True	none	T3
BANK4 True LVDS Pair								
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	T9
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	V9
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	R8
IOB31B/GCLKC_10A/D15/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D15/BPLL_C_FB0	Comp_of_IOB31A	True	none	T8
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	U10
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	V10
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	R10
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	T10
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	U3
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	V3
IOB39A	I/O	4	none		True_of_IOB39B	True	x16	U11
IOB39B	I/O	4	none		Comp_of_IOB39A	True	none	V11
IOB41A	I/O	4	none		True_of_IOB41B	True	x16	N10
IOB41B	I/O	4	none		Comp_of_IOB41A	True	none	P11
IOB43A	I/O	4	none		True_of_IOB43B	True	x16	T12
IOB43B	I/O	4	none		Comp_of_IOB43A	True	none	V12
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	R11
IOB45B	I/O	4	none		Comp_of_IOB45A	True	none	T11
IOB48A	I/O	4	none		True_of_IOB48B	True	x16	M11
IOB48B	I/O	4	none		Comp_of_IOB48A	True	none	N11
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	U13

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	V13
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	N12
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	P12
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	T14
IOB54B/GCLKC_11B/D02/BPLL_C_FB1/MI3	I/O	4	none	GCLKC_11B/D02/BPLL_C_FB1/MI3	Comp_of_IOB54A	True	none	V14
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	U15
IOB56B	I/O	4	none		Comp_of_IOB56A	True	none	V15
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	R13
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	T13
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	U16
IOB60B	I/O	4	none		Comp_of_IOB60A	True	none	V16
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	R15
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	T15
BANK3 True LVDS Pair								
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	P15
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	P16
IOB67A	I/O	3	none		True_of_IOB67B	True	x16	L14
IOB67B	I/O	3	none		Comp_of_IOB67A	True	none	M13
IOB69A	I/O	3	DQ5		True_of_IOB69B	True	x16	M14
IOB69B	I/O	3	DQ5		Comp_of_IOB69A	True	none	N14
IOB71A	I/O	3	DQ5		True_of_IOB71B	True	x16	U17
IOB71B	I/O	3	DQ5		Comp_of_IOB71A	True	none	U18
IOB73A	I/O	3	DQ5		True_of_IOB73B	True	x16	T17
IOB73B	I/O	3	DQ5		Comp_of_IOB73A	True	none	T18
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	N15
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	N16

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOB77A	I/O	3	DQ5		True_of_IOB77B	True	x16	P17
IOB77B	I/O	3	DQ5		Comp_of_IOB77A	True	none	P18
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	N17
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	N18
IOB81A	I/O	3	DQ4		True_of_IOB81B	True	x16	M16
IOB81B	I/O	3	DQ4		Comp_of_IOB81A	True	none	M18
IOB83A	I/O	3	DQ4		True_of_IOB83B	True	x16	L17
IOB83B	I/O	3	DQ4		Comp_of_IOB83A	True	none	L18
IOB85A	I/O	3	DQS4/DQ4		True_of_IOB85B	True	x16	K17
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	K18
IOB87A	I/O	3	DQ4		True_of_IOB87B	True	x16	J16
IOB87B	I/O	3	DQ4		Comp_of_IOB87A	True	none	J18
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	H17
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	H18
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	L15
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	L16
BANK2 True LVDS Pair								
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	D17
IOR12B/ADCINCLK	I/O	2	DQS2/DQ2	ADCINCLK	Comp_of_IOR12A	True	none	D18
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	F15
IOR14B/ADCOTEST	I/O	2	DQ2	ADCOTEST	Comp_of_IOR14A	True	none	F16
IOR16A	I/O	2	DQ2		True_of_IOR16B	True	x16	E16
IOR16B	I/O	2	DQ2		Comp_of_IOR16A	True	none	E18
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	H13
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	H14
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	F17
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	F18

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	G16
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	G18
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	H15
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	H16
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	J13
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	K14
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	K12
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	K13
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	L12
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	L13
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	K15
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	K16
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	H12
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	G13
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	F14
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	G14
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	C17
IOR9B/MCKTEST	I/O	2	DQ2	MCKTEST	Comp_of_IOR9A	True	none	C18
BANK1 True LVDS Pair								
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	D11
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	C11
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	C10
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	A10
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	G9
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	F9
IOT68A	I/O	1	DQ0		True_of_IOT68B	True	x16	B11
IOT68B	I/O	1	DQ0		Comp_of_IOT68A	True	none	A11

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	G11
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	F10
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	B12
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	A12
IOT74A	I/O	1	DQ1/DQS_01		True_of_IOT74B	True	x16	F11
IOT74B	I/O	1	DQ1/DQS_01		Comp_of_IOT74A	True	none	E11
IOT76A	I/O	1	DQ1		True_of_IOT76B	True	x16	D12
IOT76B	I/O	1	DQ1		Comp_of_IOT76A	True	none	C12
IOT78A	I/O	1	DQ1		True_of_IOT78B	True	x16	C13
IOT78B	I/O	1	DQ1		Comp_of_IOT78A	True	none	A13
IOT80A	I/O	1	DQS1/DQ1		True_of_IOT80B	True	x16	F12
IOT80B	I/O	1	DQS1/DQ1		Comp_of_IOT80A	True	none	E12
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	B14
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	A14
IOT85A	I/O	1	DQ1		True_of_IOT85B	True	x16	F13
IOT85B	I/O	1	DQ1		Comp_of_IOT85A	True	none	E13
IOT87A	I/O	1	none		True_of_IOT87B	True	x16	C15
IOT87B	I/O	1	none		Comp_of_IOT87A	True	none	A15
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	D14
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	C14
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	B16
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	A16
BANK0 True LVDS Pair								
IOT29A/PUDC_B	I/O	0	none	PUDC_B	True_of_IOT29B	True	x16	D4
IOT29B	I/O	0	none		Comp_of_IOT29A	True	none	C4
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	B2
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	A2

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324S
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	D6
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	C6
IOT35A	I/O	0	none		True_of_IOT35B	True	x16	B3
IOT35B	I/O	0	none		Comp_of_IOT35A	True	none	A3
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	B4
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	A4
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	C5
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	A5
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	F7
IOT41B	I/O	0	none		Comp_of_IOT41A	True	none	E6
IOT43A	I/O	0	none		True_of_IOT43B	True	x16	B6
IOT43B	I/O	0	none		Comp_of_IOT43A	True	none	A6
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	E7
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	E8
IOT48A	I/O	0	none		True_of_IOT48B	True	x16	C7
IOT48B	I/O	0	none		Comp_of_IOT48A	True	none	A7
IOT50A	I/O	0	none		True_of_IOT50B	True	x16	D8
IOT50B	I/O	0	none		Comp_of_IOT50A	True	none	C8
IOT52A	I/O	0	none		True_of_IOT52B	True	x16	G8
IOT52B	I/O	0	none		Comp_of_IOT52A	True	none	F8
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	B8
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	A8
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	D9
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	C9
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	B9
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	A9

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
BANK7 True LVDS Pair								
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	F2
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	F1
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	J7
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	J6
IOT15A	I/O	7	none		True_of_IOT15B	True	x16	E3
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	E1
IOT17A	I/O	7	none		True_of_IOT17B	True	x16	F4
IOT17B	I/O	7	none		Comp_of_IOT17A	True	none	F3
IOT19A	I/O	7	none		True_of_IOT19B	True	x16	D2
IOT19B	I/O	7	none		Comp_of_IOT19A	True	none	D1
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	H4
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	H3
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	L7
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	K6
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	E4
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	D3
IOT25A	I/O	7	none		True_of_IOT25B	True	x16	F6
IOT25B	I/O	7	none		Comp_of_IOT25A	True	none	F5
IOT27A	I/O	7	none		True_of_IOT27B	True	x16	C2
IOT27B	I/O	7	none		Comp_of_IOT27A	True	none	C1
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	L5
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	K5
IOT5B	I/O	7	none		Comp_of_IOT5A	True	none	H5
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	G3
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	G1
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	H7

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
BANK7 True LVDS Pair								
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	G6
BANK6 True LVDS Pair								
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	K2
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	K1
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	L2
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	L1
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	M3
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	M1
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	N2
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	N1
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	P2
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	P1
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	H2
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	H1
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	K4
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	K3
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	J1
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	L4
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	L3
BANK5 True LVDS Pair								
IOB10A/D03/SSPI_CN/SSI0	I/O	5	none	D03/SSPI_CS_N/SSI0	True_of_IOB10B	True	x16	U5
IOB10B/D04/SI	I/O	5	none	D04/SI	Comp_of_IOB10A	True	none	V5
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	R5
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	T5
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	N6

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
BANK7 True LVDS Pair								
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	P7
IOB16A	I/O	5	none		True_of_IOB16B	True	x16	T6
IOB16B	I/O	5	none		Comp_of_IOB16A	True	none	V6
IOB18B	I/O	5	none		Comp_of_IOB18A	True	none	P8
IOB20A	I/O	5	none		True_of_IOB20B	True	x16	U7
IOB20B	I/O	5	none		Comp_of_IOB20A	True	none	V7
IOB22A	I/O	5	none		True_of_IOB22B	True	x16	U8
IOB22B	I/O	5	none		Comp_of_IOB22A	True	none	V8
IOB24A	I/O	5	none		True_of_IOB24B	True	x16	M8
IOB24B	I/O	5	none		Comp_of_IOB24A	True	none	N8
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	M10
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	N9
IOB2A	I/O	5	none		True_of_IOB2B	True	x16	T4
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	N5
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	P6
IOB6A	I/O	5	none		True_of_IOB6B	True	x16	R7
IOB6B	I/O	5	none		Comp_of_IOB6A	True	none	T7
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	R3
IOB8B/D06	I/O	5	none	D06	Comp_of_IOB8A	True	none	T3
BANK4 True LVDS Pair								
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	T9
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	V9
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	R8
IOB31B/GCLKC_10A/D15/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D15/BPLL_C_FB0	Comp_of_IOB31A	True	none	T8

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
BANK7 True LVDS Pair								
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	U10
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	V10
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	R10
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	T10
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	U3
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	V3
IOB39A	I/O	4	none		True_of_IOB39B	True	x16	U11
IOB39B	I/O	4	none		Comp_of_IOB39A	True	none	V11
IOB41A	I/O	4	none		True_of_IOB41B	True	x16	N10
IOB41B	I/O	4	none		Comp_of_IOB41A	True	none	P11
IOB43A	I/O	4	none		True_of_IOB43B	True	x16	T12
IOB43B	I/O	4	none		Comp_of_IOB43A	True	none	V12
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	R11
IOB45B	I/O	4	none		Comp_of_IOB45A	True	none	T11
IOB48A	I/O	4	none		True_of_IOB48B	True	x16	M11
IOB48B	I/O	4	none		Comp_of_IOB48A	True	none	N11
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	U13
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	V13
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	N12
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	P12
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	T14
IOB54B/GCLKC_11B/D02/BPLL_C_FB1/MI3	I/O	4	none	GCLKC_11B/D02/BPLL_C_FB1/MI3	Comp_of_IOB54A	True	none	V14
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	U15
IOB56B	I/O	4	none		Comp_of_IOB56A	True	none	V15

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
BANK7 True LVDS Pair								
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	R13
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	T13
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	U16
IOB60B	I/O	4	none		Comp_of_IOB60A	True	none	V16
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	R15
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	T15
BANK3 True LVDS Pair								
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	P15
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	P16
IOB67A	I/O	3	none		True_of_IOB67B	True	x16	L14
IOB67B	I/O	3	none		Comp_of_IOB67A	True	none	M13
IOB69A	I/O	3	DQ5		True_of_IOB69B	True	x16	M14
IOB69B	I/O	3	DQ5		Comp_of_IOB69A	True	none	N14
IOB71A	I/O	3	DQ5		True_of_IOB71B	True	x16	U17
IOB71B	I/O	3	DQ5		Comp_of_IOB71A	True	none	U18
IOB73A	I/O	3	DQ5		True_of_IOB73B	True	x16	T17
IOB73B	I/O	3	DQ5		Comp_of_IOB73A	True	none	T18
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	N15
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	N16
IOB77A	I/O	3	DQ5		True_of_IOB77B	True	x16	P17
IOB77B	I/O	3	DQ5		Comp_of_IOB77A	True	none	P18
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	N17
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	N18
IOB81A	I/O	3	DQ4		True_of_IOB81B	True	x16	M16
IOB81B	I/O	3	DQ4		Comp_of_IOB81A	True	none	M18
IOB83A	I/O	3	DQ4		True_of_IOB83B	True	x16	L17

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
BANK7 True LVDS Pair								
IOB83B	I/O	3	DQ4		Comp_of_IOB83A	True	none	L18
IOB85A	I/O	3	DQS4/DQ4		True_of_IOB85B	True	x16	K17
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	K18
IOB87A	I/O	3	DQ4		True_of_IOB87B	True	x16	J16
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	H17
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	H18
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	L15
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	L16
BANK2 True LVDS Pair								
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	D17
IOR12B/ADCINCLK	I/O	2	DQS2/DQ2	ADCINCLK	Comp_of_IOR12A	True	none	D18
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	F15
IOR14B/ADCOTEST	I/O	2	DQ2	ADCOTEST	Comp_of_IOR14A	True	none	F16
IOR16A	I/O	2	DQ2		True_of_IOR16B	True	x16	E16
IOR16B	I/O	2	DQ2		Comp_of_IOR16A	True	none	E18
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	H13
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	H14
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	F17
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	F18
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	G16
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	G18
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	H15
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	J13
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	K14
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	K12
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	K13

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
BANK7 True LVDS Pair								
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	L12
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	L13
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	K15
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	K16
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	H12
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	G13
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	F14
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	G14
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	C17
IOR9B/MCKTEST	I/O	2	DQ2	MCKTEST	Comp_of_IOR9A	True	none	C18
BANK1 True LVDS Pair								
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	D11
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	C11
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	C10
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	A10
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	G9
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	F9
IOT68A	I/O	1	DQ0		True_of_IOT68B	True	x16	B11
IOT68B	I/O	1	DQ0		Comp_of_IOT68A	True	none	A11
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	G11
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	F10
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	A12
IOT74A	I/O	1	DQ1/DQS_01		True_of_IOT74B	True	x16	F11
IOT74B	I/O	1	DQ1/DQS_01		Comp_of_IOT74A	True	none	E11
IOT76A	I/O	1	DQ1		True_of_IOT76B	True	x16	D12
IOT76B	I/O	1	DQ1		Comp_of_IOT76A	True	none	C12

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
BANK7 True LVDS Pair								
IOT78A	I/O	1	DQ1		True_of_IOT78B	True	x16	C13
IOT78B	I/O	1	DQ1		Comp_of_IOT78A	True	none	A13
IOT80A	I/O	1	DQS1/DQ1		True_of_IOT80B	True	x16	F12
IOT80B	I/O	1	DQS1/DQ1		Comp_of_IOT80A	True	none	E12
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	B14
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	A14
IOT85A	I/O	1	DQ1		True_of_IOT85B	True	x16	F13
IOT85B	I/O	1	DQ1		Comp_of_IOT85A	True	none	E13
IOT87A	I/O	1	none		True_of_IOT87B	True	x16	C15
IOT87B	I/O	1	none		Comp_of_IOT87A	True	none	A15
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	D14
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	C14
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	B16
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	A16
BANK0 True LVDS Pair								
IOT29A/PUDC_B	I/O	0	none	PUDC_B	True_of_IOT29B	True	x16	D4
IOT29B	I/O	0	none		Comp_of_IOT29A	True	none	C4
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	B2
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	A2
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	D6
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	C6
IOT35A	I/O	0	none		True_of_IOT35B	True	x16	B3
IOT35B	I/O	0	none		Comp_of_IOT35A	True	none	A3
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	B4
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	A4
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	C5

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG324
BANK7 True LVDS Pair								
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	A5
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	F7
IOT41B	I/O	0	none		Comp_of_IOT41A	True	none	E6
IOT43A	I/O	0	none		True_of_IOT43B	True	x16	B6
IOT43B	I/O	0	none		Comp_of_IOT43A	True	none	A6
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	E7
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	E8
IOT48A	I/O	0	none		True_of_IOT48B	True	x16	C7
IOT48B	I/O	0	none		Comp_of_IOT48A	True	none	A7
IOT50A	I/O	0	none		True_of_IOT50B	True	x16	D8
IOT50B	I/O	0	none		Comp_of_IOT50A	True	none	C8
IOT52A	I/O	0	none		True_of_IOT52B	True	x16	G8
IOT52B	I/O	0	none		Comp_of_IOT52A	True	none	F8
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	B8
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	A8
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	D9
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	C9
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	B9
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	A9

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
BANK7 True LVDS Pair									
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	D4	D4
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	D3	D3
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	F2	F2
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	F1	F1
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	G2	G2
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	G1	G1
IOT5A	I/O	7	none		True_of_IOT5B	True	x16	E2	E2
IOT5B	I/O	7	none		Comp_of_IOT5A	True	none	E1	E1
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	D2	D2
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	D1	D1
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	C1	C1
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	B1	B1
BANK6 True LVDS Pair									
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	J4	J4
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	J3	J3
IOL27A	I/O	6	DQ6		True_of_IOL27B	True	x16	K2	K2
IOL27B	I/O	6	DQ6		Comp_of_IOL27A	True	none	K1	K1
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	L2	L2
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	L1	L1
IOL31A	I/O	6	none		True_of_IOL31B	True	x16	M2	M2
IOL31B	I/O	6	none		Comp_of_IOL31A	True	none	M1	M1
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	H2	H2
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	H1	H1
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	J2	J2
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	J1	J1
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	F4	F4

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	F3	F3
BANK5 True LVDS Pair									
IOB10A/D03/SSPI_CN/SSI0	I/O	5	none	D03/SSPI_CS_N/SSI0	True_of_IOB10B	True	x16	N5	N5
IOB10B/D04/SI	I/O	5	none	D04/SI	Comp_of_IOB10A	True	none	P5	P5
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	N6	N6
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	P6	P6
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	N4	N4
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	P4	P4
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	N3	N3
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	P3	P3
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	L4	L4
IOB8B/D06	I/O	5	none	D06	Comp_of_IOB8A	True	none	M4	M4
BANK4 True LVDS Pair									
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	N7	N7
IOB31B/GCLKC_10A/D15/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D15/BPLL_C_FB0	Comp_of_IOB31A	True	none	P7	P7
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	N8	N8
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	P8	P8
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	N2	N2
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	P2	P2
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	L8	L8
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	M8	M8
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	N9	N9
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	P9	P9

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	N10	N10
IOB54B/GCLKC_11B/D02/BPLL_C_FB1/MI3	I/O	4	none	GCLKC_11B/D02/BPLL_C_FB1/MI3	Comp_of_IOB54A	True	none	P10	P10
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	N12	N12
IOB56B	I/O	4	none		Comp_of_IOB56A	True	none	P12	P12
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	N11	N11
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	P11	P11
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	M12	M12
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	N13	N13
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	P13	P13
BANK3 True LVDS Pair									
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	M13	M13
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	M14	M14
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	L13	L13
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	L14	L14
IOB85A	I/O	3	DQS4/DQ4		True_of_IOB85B	True	x16	K13	K13
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	K14	K14
IOB87A	I/O	3	DQ4		True_of_IOB87B	True	x16	J13	J13
IOB87B	I/O	3	DQ4		Comp_of_IOB87A	True	none	J14	J14
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	H11	H11
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	H12	H12
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	H13	H13
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	H14	H14
BANK2 True LVDS Pair									
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	F11	F11
IOR12B/ADCINCLK	I/O	2	DQS2/DQ2	ADCINCLK	Comp_of_IOR12A	True	none	F12	F12

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	MG196S[1]	MG196S[2]
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	J11	J11
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	J12	J12
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	F13	F13
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	F14	F14
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	G13	G13
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	G14	G14
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	C12	C12
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	C13	C13
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	D13	D13
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	D14	D14
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	E13	E13
IOR9B/MCKTEST	I/O	2	DQ2	MCKTEST	Comp_of_IOR9A	True	none	E14	E14
BANK1 True LVDS Pair									
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	D8	D8
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	C8	C8
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	B8	B8
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	A8	A8
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	B9	B9
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	A9	A9
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	B10	B10
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	A10	A10
IOT78A	I/O	1	DQ1		True_of_IOT78B	True	x16	B11	B11
IOT78B	I/O	1	DQ1		Comp_of_IOT78A	True	none	A11	A11
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	D11	D11
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	C11	C11
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	B12	B12

Note! [1] EV version [2] LV version									
Pin Name	**Function**	**BANK**	**DQS**	**Configuration Function**	**Differential Pair**	**LVDS**	**X16**	**MG196S[1]**	**MG196S[2]**
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	A12	A12
BANK0 True LVDS Pair									
IOT29A/PUDC_B	I/O	0	none	PUDC_B	True_of_IOT29B	True	x16	B2	B2
IOT29B	I/O	0	none		Comp_of_IOT29A	True	none	A2	A2
IOT35A	I/O	0	none		True_of_IOT35B	True	x16	B3	B3
IOT35B	I/O	0	none		Comp_of_IOT35A	True	none	A3	A3
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	B4	B4
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	A4	A4
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	B5	B5
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	A5	A5
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	B6	B6
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	A6	A6
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	B7	B7
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	A7	A7

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
BANK7 True LVDS Pair									
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	D3	D3
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	D1	D1
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	C2	C2
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	C1	C1
IOT15A	I/O	7	none		True_of_IOT15B	True	x16	D4	D4
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	E3	E3
IOT17A	I/O	7	none		True_of_IOT17B	True	x16	E5	E5
IOT17B	I/O	7	none		Comp_of_IOT17A	True	none	E4	E4
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	G2	G2
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	G1	G1
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	H3	H3
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	H1	H1
IOT5A	I/O	7	none		True_of_IOT5B	True	x16	F5	F5
IOT5B	I/O	7	none		Comp_of_IOT5A	True	none	F4	F4
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	F3	F3
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	F1	F1
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	E2	E2
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	E1	E1
BANK6 True LVDS Pair									
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	M3	M3
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	M1	M1
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	N2	N2
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	N1	N1
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	G5	G5
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	G3	G3
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	H6	H6

Note!
[1] EV version
[2] LV version

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	H5	H5
IOL23A	I/O	6	DQ6		True_of_IOL23B	True	x16	K5	K5
IOL23B	I/O	6	DQ6		Comp_of_IOL23A	True	none	J4	J4
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	J5	J5
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	H4	H4
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	M4	M4
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	L3	L3
IOL31A	I/O	6	none		True_of_IOL31B	True	x16	P2	P2
IOL31B	I/O	6	none		Comp_of_IOL31A	True	none	P1	P1
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	J2	J2
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	J1	J1
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	K4	K4
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	J3	J3
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	K3	K3
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	K1	K1
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	L2	L2
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	L1	L1
BANK5 True LVDS Pair									
IOB10A/D03/SSPI_CN/SSI0	I/O	5	none	D03/SSPI_CS_N/SSI0	True_of_IOB10B	True	x16	P5	P5
IOB10B/D04/SI	I/O	5	none	D04/SI	Comp_of_IOB10A	True	none	R5	R5
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	N6	N6
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	R6	R6
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	N4	N4
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	R4	R4
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	L7	L7

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	M6	M6
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	M5	M5
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	N5	N5
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	L6	L6
IOB8B/D06	I/O	5	none	D06	Comp_of_IOB8A	True	none	L5	L5
BANK4 True LVDS Pair									
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	P7	P7
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	R7	R7
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	K8	K8
IOB31B/GCLKC_10A/D15/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D15/BPLL_C_FB0	Comp_of_IOB31A	True	none	L8	L8
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	M8	M8
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	N7	N7
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	N8	N8
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	R8	R8
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	P3	P3
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	R3	R3
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	P9	P9
IOB45B	I/O	4	none		Comp_of_IOB45A	True	none	R9	R9
IOB48A	I/O	4	none		True_of_IOB48B	True	x16	M9	M9
IOB48B	I/O	4	none		Comp_of_IOB48A	True	none	N9	N9
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	L9	L9
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	M10	M10
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	N10	N10
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	R10	R10
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	M11	M11

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
IOB54B/GCLKC_11B/D02/BPLL_C_FB1/MI3	I/O	4	none	GCLKC_11B/D02/BPLL_C_FB1/MI3	Comp_of_IOB54A	True	none	N11	N11
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	P13	P13
IOB56B	I/O	4	none		Comp_of_IOB56A	True	none	R13	R13
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	P11	P11
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	R11	R11
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	L10	L10
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	N12	N12
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	R12	R12
BANK3 True LVDS Pair									
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	P14	P14
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	P15	P15
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	K12	K12
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	L12	L12
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	K10	K10
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	K11	K11
IOB81A	I/O	3	DQ4		True_of_IOB81B	True	x16	N14	N14
IOB81B	I/O	3	DQ4		Comp_of_IOB81A	True	none	N15	N15
IOB83A	I/O	3	DQ4		True_of_IOB83B	True	x16	M13	M13
IOB83B	I/O	3	DQ4		Comp_of_IOB83A	True	none	M15	M15
IOB85A	I/O	3	DQS4/DQ4		True_of_IOB85B	True	x16	L14	L14
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	L15	L15
IOB87A	I/O	3	DQ4		True_of_IOB87B	True	x16	K13	K13
IOB87B	I/O	3	DQ4		Comp_of_IOB87A	True	none	K15	K15
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	J14	J14
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	J15	J15

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
BANK2 True LVDS Pair									
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	D13	D13
IOR12B/ADCINCLK	I/O	2	DQS2/DQ2	ADCINCLK	Comp_of_IOR12A	True	none	D15	D15
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	E14	E14
IOR14B/ADCOTEST	I/O	2	DQ2	ADCOTEST	Comp_of_IOR14A	True	none	E15	E15
IOR16A	I/O	2	DQ2		True_of_IOR16B	True	x16	F13	F13
IOR16B	I/O	2	DQ2		Comp_of_IOR16A	True	none	F15	F15
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	G11	G11
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	G12	G12
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	H12	H12
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	G13	G13
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	H10	H10
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	H11	H11
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	J11	J11
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	J13	J13
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	G14	G14
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	G15	G15
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	H13	H13
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	H15	H15
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	F11	F11
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	F12	F12
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	B14	B14
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	B15	B15
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	C14	C14
IOR9B/MCKTEST	I/O	2	DQ2	MCKTEST	Comp_of_IOR9A	True	none	C15	C15

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
BANK1 True LVDS Pair									
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	C8	C8
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	A8	A8
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	B9	B9
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	A9	A9
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	F10	F10
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	E9	E9
IOT68A	I/O	1	DQ0		True_of_IOT68B	True	x16	C10	C10
IOT68B	I/O	1	DQ0		Comp_of_IOT68A	True	none	A10	A10
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	D10	D10
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	C9	C9
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	B11	B11
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	A11	A11
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	D11	D11
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	C11	C11
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	C12	C12
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	A12	A12
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	B13	B13
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	A13	A13
BANK0 True LVDS Pair									
IOT29A/PUDC_B	I/O	0	none	PUDC_B	True_of_IOT29B	True	x16	B2	B2
IOT29B	I/O	0	none		Comp_of_IOT29A	True	none	A2	A2
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	B3	B3
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	A3	A3
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	C4	C4
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	A4	A4

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG225S[1]	UG225S[2]
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	D5	D5
IOT41B	I/O	0	none		Comp_of_IOT41A	True	none	C5	C5
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	E6	E6
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	D6	D6
IOT48A	I/O	0	none		True_of_IOT48B	True	x16	B5	B5
IOT48B	I/O	0	none		Comp_of_IOT48A	True	none	A5	A5
IOT50A	I/O	0	none		True_of_IOT50B	True	x16	C6	C6
IOT50B	I/O	0	none		Comp_of_IOT50A	True	none	A6	A6
IOT52A	I/O	0	none		True_of_IOT52B	True	x16	D7	D7
IOT52B	I/O	0	none		Comp_of_IOT52A	True	none	C7	C7
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	F8	F8
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	E8	E8
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	E7	E7
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	D8	D8
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	B7	B7
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	A7	A7

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	LQ100[1]
BANK7 True LVDS Pair								
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	21
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	20
IOT15A	I/O	7	none		True_of_IOT15B	True	x16	19
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	18
IOT19A	I/O	7	none		True_of_IOT19B	True	x16	17
IOT19B	I/O	7	none		Comp_of_IOT19A	True	none	16
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	25
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	24
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	15
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	14
IOT27A	I/O	7	none		True_of_IOT27B	True	x16	13
IOT27B	I/O	7	none		Comp_of_IOT27A	True	none	12
BANK6 True LVDS Pair								
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	35
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	36
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	37
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	38
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	39
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	40
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	41
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	42
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	43
IOL27B	I/O	6	DQ6		Comp_of_IOL27A	True	none	47
IOL31A	I/O	6	none		True_of_IOL31B	True	x16	48
IOL31B	I/O	6	none		Comp_of_IOL31A	True	none	49

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	LQ100[1]
Note! [1] EV version								
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	28
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	27
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	30
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	29
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	31
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	34
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	32
BANK5 True LVDS Pair								
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	52
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	51
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	54
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	53
BANK4 True LVDS Pair								
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	57
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	58
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	59
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	60
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	61
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	62
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	63
IOB54B/GCLKC_11B/D02/BPLL_C_FB1/MI3	I/O	4	none	GCLKC_11B/D02/BPLL_C_FB1/MI3	Comp_of_IOB54A	True	none	64
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	65
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	66

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	LQ100[1]
Note! [1] EV version								
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	67
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	68
BANK3 True LVDS Pair								
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	70
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	71
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	74
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	75
BANK2 True LVDS Pair								
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	91
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	90
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	89
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	88
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	87
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	86
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	85
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	84
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	83
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	82
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	81
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	78
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	77
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	76
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	95
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	94

Note!								
[1] EV version								
Pin Name	**Function**	**BANK**	**DQS**	**Configuration Function**	**Differential Pair**	**LVDS**	**X16**	**LQ100[1]**
BANK1 True LVDS Pair								
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	10
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	9
IOT74A	I/O	1	DQ1/DQS_01		True_of_IOT74B	True	x16	8
IOT74B	I/O	1	DQ1/DQS_01		Comp_of_IOT74A	True	none	7
IOT80A	I/O	1	DQS1/DQ1		True_of_IOT80B	True	x16	4
IOT80B	I/O	1	DQS1/DQ1		Comp_of_IOT80A	True	none	3
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	2
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	1

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[2]
BANK7 True LVDS Pair								
IOT11A	I/O	7	none		True_of_IOT11B	True	x16	F4
IOT11B	I/O	7	none		Comp_of_IOT11A	True	none	F3
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	E2
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	E1
IOT15A	I/O	7	none		True_of_IOT15B	True	x16	F6
IOT15B	I/O	7	none		Comp_of_IOT15A	True	none	F5
IOT17A	I/O	7	none		True_of_IOT17B	True	x16	E4
IOT17B	I/O	7	none		Comp_of_IOT17A	True	none	E3
IOT19A	I/O	7	none		True_of_IOT19B	True	x16	D3
IOT19B	I/O	7	none		Comp_of_IOT19A	True	none	D1
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	H4
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	H3
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	C1
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	B1
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	C3
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	C2
IOT25A	I/O	7	none		True_of_IOT25B	True	x16	B2
IOT25B	I/O	7	none		Comp_of_IOT25A	True	none	A2
IOT27A	I/O	7	none		True_of_IOT27B	True	x16	B3
IOT27B	I/O	7	none		Comp_of_IOT27A	True	none	A3
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	J6
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	H5
IOT5A	I/O	7	none		True_of_IOT5B	True	x16	L4
IOT5B	I/O	7	none		Comp_of_IOT5A	True	none	L5
IOT7A	I/O	7	none		True_of_IOT7B	True	x16	K5

Note! [1] EV version								
Pin Name	**Function**	**BANK**	**DQS**	**Configuration Function**	**Differential Pair**	**LVDS**	**X16**	**PG256S[2]**
IOT7B	I/O	7	none		Comp_of_IOT7A	True	none	K6
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	G6
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	G5
BANK6 True LVDS Pair								
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	J3
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	J1
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	K2
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	K1
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	L3
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	L1
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	M2
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	M1
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	N3
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	N1
IOL23A	I/O	6	DQ6		True_of_IOL23B	True	x16	P2
IOL23B	I/O	6	DQ6		Comp_of_IOL23A	True	none	P1
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	R2
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	R1
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	M4
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	M3
IOL31A	I/O	6	none		True_of_IOL31B	True	x16	M5
IOL31B	I/O	6	none		Comp_of_IOL31A	True	none	N4
IOL3A/GCLKT_14A/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14A/LPLL0_T_IN2	True_of_IOL3B	True	x16	K3
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	J4
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	F2
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	F1

Note!								
[1] EV version								
Pin Name	**Function**	**BANK**	**DQS**	**Configuration Function**	**Differential Pair**	**LVDS**	**X16**	**PG256S[2]**
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	G3
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	G1
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	H2
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	H1
BANK5 True LVDS Pair								
IOB10A/D03/SSPI_CN/SSI0	I/O	5	none	D03/SSPI_CS_N/SSI0	True_of_IOB10B	True	x16	N5
IOB10B/D04/SI	I/O	5	none	D04/SI	Comp_of_IOB10A	True	none	P5
IOB12A/GCLKT_10B/D07/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D07/SSPI_WPN/SSI2	True_of_IOB12B	True	x16	R5
IOB12B/GCLKC_10B/RDWR_B/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/RDWR_B/LPLL1_C_IN1	Comp_of_IOB12A	True	none	T5
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	P6
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	T6
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	R7
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	T7
IOB2A	I/O	5	none		True_of_IOB2B	True	x16	P4
IOB2B	I/O	5	none		Comp_of_IOB2A	True	none	T4
IOB4A/D08/SDA	I/O	5	none	D08/SDA	True_of_IOB4B	True	x16	M6
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	N6
IOB8A/D05/SO/SSI1	I/O	5	none	D05/SO/SSI1	True_of_IOB8B	True	x16	L8
IOB8B/D06	I/O	5	none	D06	Comp_of_IOB8A	True	none	L7
BANK4 True LVDS Pair								
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	M9
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	N8
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	P7

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[2]
IOB31B/GCLKC_10A/D15/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D15/BPLL_C_FB0	Comp_of_IOB31A	True	none	M7
IOB33A/GCLKT_9/D13/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/BPLL_T_IN1	True_of_IOB33B	True	x16	P8
IOB33B/GCLKC_9/EMCCLK	I/O	4	none	GCLKC_9/EMCCLK	Comp_of_IOB33A	True	none	T8
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	R9
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	T9
IOB37A/READY	I/O	4	none	READY	True_of_IOB37B	True	x16	R3
IOB37B/MCS_N/CSO_B	I/O	4	none	MCS_N/CSO_B	Comp_of_IOB37A	True	none	T3
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	L10
IOB45B	I/O	4	none		Comp_of_IOB45A	True	none	M10
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	N9
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	P9
IOB52A/MODE1	I/O	4	none	MODE1	True_of_IOB52B	True	x16	N11
IOB52B/D10	I/O	4	none	D10	Comp_of_IOB52A	True	none	P11
IOB54A/GCLKT_11B/D01/MI2	I/O	4	none	GCLKT_11B/D01/MI2	True_of_IOB54B	True	x16	N12
IOB54B/GCLKC_11B/D02/BPLL_C_FB1/MI3	I/O	4	none	GCLKC_11B/D02/BPLL_C_FB1/MI3	Comp_of_IOB54A	True	none	P12
IOB56A	I/O	4	none		True_of_IOB56B	True	x16	L11
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	P10
IOB58B/MOSI/CSI_B/MI0	I/O	4	none	MOSI/CSI_B/MI0	Comp_of_IOB58A	True	none	T10
IOB60A	I/O	4	none		True_of_IOB60B	True	x16	M12
IOB60B	I/O	4	none		Comp_of_IOB60A	True	none	M11
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	R11
IOB62B/MODE0	I/O	4	none	MODE0	Comp_of_IOB62A	True	none	T11

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[2]
BANK3 True LVDS Pair								
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	M13
IOB65B/DOUT	I/O	3	none	DOUT	Comp_of_IOB65A	True	none	M14
IOB69A	I/O	3	DQ5		True_of_IOB69B	True	x16	L12
IOB69B	I/O	3	DQ5		Comp_of_IOB69A	True	none	L13
IOB71A	I/O	3	DQ5		True_of_IOB71B	True	x16	R12
IOB71B	I/O	3	DQ5		Comp_of_IOB71A	True	none	T12
IOB73A	I/O	3	DQ5		True_of_IOB73B	True	x16	T14
IOB73B	I/O	3	DQ5		Comp_of_IOB73A	True	none	T13
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	R14
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	T15
IOB77A	I/O	3	DQ5		True_of_IOB77B	True	x16	R15
IOB77B	I/O	3	DQ5		Comp_of_IOB77A	True	none	R16
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	P15
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	P16
IOB81A	I/O	3	DQ4		True_of_IOB81B	True	x16	L14
IOB81B	I/O	3	DQ4		Comp_of_IOB81A	True	none	L16
IOB83A	I/O	3	DQ4		True_of_IOB83B	True	x16	M15
IOB83B	I/O	3	DQ4		Comp_of_IOB83A	True	none	M16
IOB85A	I/O	3	DQS4/DQ4		True_of_IOB85B	True	x16	N14
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	N16
IOB87A	I/O	3	DQ4		True_of_IOB87B	True	x16	K15
IOB87B	I/O	3	DQ4		Comp_of_IOB87A	True	none	K16
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	J14
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	J16

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[2]
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	K12
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	K11
BANK2 True LVDS Pair								
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	D14
IOR12B/ADCINCLK	I/O	2	DQS2/DQ2	ADCINCLK	Comp_of_IOR12A	True	none	D16
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	F13
IOR14B/ADCOTEST	I/O	2	DQ2	ADCOTEST	Comp_of_IOR14A	True	none	F14
IOR16A	I/O	2	DQ2		True_of_IOR16B	True	x16	C15
IOR16B	I/O	2	DQ2		Comp_of_IOR16A	True	none	C16
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	E15
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	E16
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	F15
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	F16
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	G14
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	G16
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	H15
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	H16
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	G12
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	H11
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	H13
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	H14
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	J11
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	J12
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	J13
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	K14

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[2]
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	E13
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	E12
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	B15
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	B16
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	F12
IOR9B/MCKTEST	I/O	2	DQ2	MCKTEST	Comp_of_IOR9A	True	none	G11
BANK1 True LVDS Pair								
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	E7
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	E8
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	E10
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	C10
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	D8
IOT66B	I/O	1	DQ0		Comp_of_IOT66A	True	none	C8
IOT68A	I/O	1	DQ0		True_of_IOT68B	True	x16	C11
IOT68B	I/O	1	DQ0		Comp_of_IOT68A	True	none	A11
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	F9
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	D9
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	B12
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	A12
IOT85A	I/O	1	DQ1		True_of_IOT85B	True	x16	C13
IOT85B	I/O	1	DQ1		Comp_of_IOT85A	True	none	A13
IOT87A	I/O	1	none		True_of_IOT87B	True	x16	F10
IOT87B	I/O	1	none		Comp_of_IOT87A	True	none	E11
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	B14
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	A14

Note!
[1] EV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256S[2]
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	D11
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	D12
BANK0 True LVDS Pair								
IOT29A/PUDC_B	I/O	0	none	PUDC_B	True_of_IOT29B	True	x16	C4
IOT29B	I/O	0	none		Comp_of_IOT29A	True	none	A4
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	B5
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	A5
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	D5
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	C5
IOT35A	I/O	0	none		True_of_IOT35B	True	x16	B6
IOT35B	I/O	0	none		Comp_of_IOT35A	True	none	A6
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	F7
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	E6
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	C7
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	A7
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	D6
IOT41B	I/O	0	none		Comp_of_IOT41A	True	none	C6
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	B8
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	A8
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	C9
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	A9
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	B10
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	A10

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
BANK7 True LVDS Pair									
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	D15	D15
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	D16	D16
IOT19A/EMCCLK	I/O	7	none	EMCCLK	True_of_IOT19B	True	x16	F15	F15
IOT19B/CSO_B	I/O	7	none	CSO_B	Comp_of_IOT19A	True	none	F16	F16
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	F14	F14
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	B16	B16
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	G11	G11
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	F13	F13
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	G15	G15
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	G16	G16
IOT25A/MODE0	I/O	7	none	MODE0	True_of_IOT25B	True	x16	H13	H13
IOT25B/MODE1	I/O	7	none	MODE1	Comp_of_IOT25A	True	none	H12	H12
IOT27A/MODE2	I/O	7	none	MODE2	True_of_IOT27B	True	x16	G12	G12
IOT27B/DONE	I/O	7	none	DONE	Comp_of_IOT27A	True	none	H14	H14
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	E15	E15
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	E16	E16
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	C15	C15
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	C16	C16
BANK6 True LVDS Pair									
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	E11	E11
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	E10	E10
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	F9	F9
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	F10	F10
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	B13	B13
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	A13	A13

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	B12	B12
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	A12	A12
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	C11	C11
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	E9	E9
IOL23A	I/O	6	DQ6		True_of_IOL23B	True	x16	B11	B11
IOL23B	I/O	6	DQ6		Comp_of_IOL23A	True	none	A11	A11
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	B10	B10
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	A10	A10
IOL27A	I/O	6	DQ6		True_of_IOL27B	True	x16	D9	D9
IOL27B	I/O	6	DQ6		Comp_of_IOL27A	True	none	C9	C9
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	B9	B9
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	A9	A9
IOL3A/GCLKT_14/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14/LPLL0_T_IN2	True_of_IOL3B	True	x16	D14	D14
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	C14	C14
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	D12	D12
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	D11	D11
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	A15	A15
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	F11	F11
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	B14	B14
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	A14	A14
BANK5 True LVDS Pair									
IOB10A/D06/SSPI_CS_N	I/O	5	none	D06/SSPI_CS_N	True_of_IOB10B	True	x16	E6	E6
IOB10B/D05/SI/SSI0	I/O	5	none	D05/SI/SSI0	Comp_of_IOB10A	True	none	E7	E7
IOB12A/GCLKT_10B/D15/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D15/SSPI_WPN//SSI2	True_of_IOB12B	True	x16	C6	C6
IOB12B/GCLKC_10B/D07/DOUT/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/D07/DOUT/LPLL1_C_IN1	Comp_of_IOB12A	True	none	A5	A5

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	B6	B6
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	A6	A6
IOB16A	I/O	5	none		True_of_IOB16B	True	x16	D6	D6
IOB16B	I/O	5	none		Comp_of_IOB16A	True	none	D5	D5
IOB18A	I/O	5	none		True_of_IOB18B	True	x16	B4	B4
IOB18B	I/O	5	none		Comp_of_IOB18A	True	none	A4	A4
IOB20A	I/O	5	none		True_of_IOB20B	True	x16	F7	F7
IOB20B	I/O	5	none		Comp_of_IOB20A	True	none	F6	F6
IOB22A	I/O	5	none		True_of_IOB22B	True	x16	B5	B5
IOB22B	I/O	5	none		Comp_of_IOB22A	True	none	A2	A2
IOB24A	I/O	5	none		True_of_IOB24B	True	x16	D3	D3
IOB24B	I/O	5	none		Comp_of_IOB24A	True	none	C3	C3
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	B3	B3
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	A3	A3
IOB2A	I/O	5	none		True_of_IOB2B	True	x16	B8	B8
IOB2B	I/O	5	none		Comp_of_IOB2A	True	none	A8	A8
IOB4A/D04/SDA	I/O	5	none	D04/SDA	True_of_IOB4B	True	x16	B7	B7
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	A7	A7
IOB6A	I/O	5	none		True_of_IOB6B	True	x16	C8	C8
IOB6B	I/O	5	none		Comp_of_IOB6A	True	none	D8	D8
IOB8A/D03/SO/SSI1	I/O	5	none	D03/SO/SSI1	True_of_IOB8B	True	x16	F8	F8
IOB8B/D02	I/O	5	none	D02	Comp_of_IOB8A	True	none	E8	E8

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
BANK4 True LVDS Pair									
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	B1	B1
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	D4	D4
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	C2	C2
IOB31B/GCLKC_10A/D01/MOSI/MI0/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D01/MOSI/MI0/BPLL_C_FB0	Comp_of_IOB31A	True	none	C1	C1
IOB33A/GCLKT_9/D13/MCS_N/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/MCS_N/BPLL_T_IN1	True_of_IOB33B	True	x16	D2	D2
IOB33B/GCLKC_9	I/O	4	none	GCLKC_9	Comp_of_IOB33A	True	none	D1	D1
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	E5	E5
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	E1	E1
IOB37A/D08	I/O	4	none	D08	True_of_IOB37B	True	x16	G5	G5
IOB37B	I/O	4	none		Comp_of_IOB37A	True	none	F5	F5
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	F3	F3
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	F2	F2
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	F1	F1
IOB52A/RECONFIG_N	I/O	4	none	RECONFIG_N	True_of_IOB52B	True	x16	H5	H5
IOB52B	I/O	4	none		Comp_of_IOB52A	True	none		
IOB54A/GCLKT_11B/D10/MI2	I/O	4	none	GCLKT_11B/D10/MI2	True_of_IOB54B	True	x16	G2	G2
IOB54B/GCLKC_11B/MI3/BPLL_C_FB1	I/O	4	none	GCLKC_11B/MI3/BPLL_C_FB1	Comp_of_IOB54A	True	none	G1	G1
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	H2	H2
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	H1	H1
IOB62B/CSI_B	I/O	4	none	CSI_B	Comp_of_IOB62A	True	none	J3	J3

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
BANK3 True LVDS Pair									
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	J6	J6
IOB65B	I/O	3	none		Comp_of_IOB65A	True	none	L3	L3
IOB69A	I/O	3	DQ5		True_of_IOB69B	True	x16	J2	J2
IOB69B	I/O	3	DQ5		Comp_of_IOB69A	True	none	J1	J1
IOB71A	I/O	3	DQ5		True_of_IOB71B	True	x16	K2	K2
IOB71B	I/O	3	DQ5		Comp_of_IOB71A	True	none	K1	K1
IOB73A	I/O	3	DQ5		True_of_IOB73B	True	x16	K5	K5
IOB73B	I/O	3	DQ5		Comp_of_IOB73A	True	none	L4	L4
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	L2	L2
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	L1	L1
IOB77A	I/O	3	DQ5		True_of_IOB77B	True	x16	N2	N2
IOB77B	I/O	3	DQ5		Comp_of_IOB77A	True	none	N1	N1
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	P2	P2
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	P1	P1
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	R1	R1
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	M2	M2
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	M1	M1
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	K6	K6
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	L6	L6
BANK2 True LVDS Pair									
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	R7	R7
IOR12B	I/O	2	DQS2/DQ2		Comp_of_IOR12A	True	none	T7	T7
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	R6	R6
IOR14B	I/O	2	DQ2		Comp_of_IOR14A	True	none	T6	T6
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	R5	R5

Note! [1] EV version [2] LV version									
Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	T5	T5
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	P6	P6
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	M6	M6
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	N5	N5
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	N6	N6
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	R3	R3
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	T3	T3
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	M7	M7
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	K8	K8
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	N3	N3
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	P3	P3
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	R4	R4
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	T4	T4
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	T2	T2
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	L7	L7
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	R8	R8
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	T8	T8
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	N8	N8
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	P8	P8
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	L8	L8
IOR9B	I/O	2	DQ2		Comp_of_IOR9A	True	none	M8	M8
BANK1 True LVDS Pair									
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	T14	T14
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	T15	T15
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	P14	P14
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	L11	L11

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	R14	R14
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	M11	M11
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	N12	N12
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	R13	R13
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	T13	T13
IOT74A	I/O	1	DQ1/DQS_01		True_of_IOT74B	True	x16	K10	K10
IOT74B	I/O	1	DQ1/DQS_01		Comp_of_IOT74A	True	none	L10	L10
IOT76A	I/O	1	DQ1		True_of_IOT76B	True	x16	R12	R12
IOT76B	I/O	1	DQ1		Comp_of_IOT76A	True	none	T12	T12
IOT78A	I/O	1	DQ1		True_of_IOT78B	True	x16	M10	M10
IOT78B	I/O	1	DQ1		Comp_of_IOT78A	True	none	N11	N11
IOT80A	I/O	1	DQS1/DQ1		True_of_IOT80B	True	x16	R10	R10
IOT80B	I/O	1	DQS1/DQ1		Comp_of_IOT80A	True	none	T10	T10
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	R11	R11
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	T11	T11
IOT85A	I/O	1	DQ1		True_of_IOT85B	True	x16	M9	M9
IOT85B	I/O	1	DQ1		Comp_of_IOT85A	True	none	N9	N9
IOT87A	I/O	1	none		True_of_IOT87B	True	x16	K9	K9
IOT87B	I/O	1	none		Comp_of_IOT87A	True	none	L9	L9
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	P9	P9
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	P11	P11
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	R9	R9
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	T9	T9

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	UG256C[1]	UG256C[2]
BANK0 True LVDS Pair									
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	J12	J12
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	J14	J14
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	J15	J15
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	J16	J16
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	L15	L15
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	L16	L16
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	J13	J13
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	L13	L13
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	L14	L14
IOT43A	I/O	0	none		True_of_IOT43B	True	x16	J11	J11
IOT43B	I/O	0	none		Comp_of_IOT43A	True	none	K12	K12
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	N15	N15
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	N16	N16
IOT48A	I/O	0	none		True_of_IOT48B	True	x16	L12	L12
IOT48B	I/O	0	none		Comp_of_IOT48A	True	none	K11	K11
IOT50A	I/O	0	none		True_of_IOT50B	True	x16	R16	R16
IOT50B	I/O	0	none		Comp_of_IOT50A	True	none	P16	P16
IOT52A	I/O	0	none		True_of_IOT52B	True	x16	N14	N14
IOT52B	I/O	0	none		Comp_of_IOT52A	True	none	P15	P15
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	N13	N13
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	M12	M12
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	K15	K15
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	K16	K16
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	M15	M15
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	M16	M16

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
BANK7 True LVDS Pair									
IOT13A	I/O	7	none		True_of_IOT13B	True	x16	D15	D15
IOT13B	I/O	7	none		Comp_of_IOT13A	True	none	D16	D16
IOT19A/EMCCLK	I/O	7	none	EMCCLK	True_of_IOT19B	True	x16	F15	F15
IOT19B/CSO_B	I/O	7	none	CSO_B	Comp_of_IOT19A	True	none	F16	F16
IOT1A/GCLKT_15/LPLL0_T_IN0	I/O	7	none	GCLKT_15/LPLL0_T_IN0	True_of_IOT1B	True	x16	F14	F14
IOT1B/GCLKC_15	I/O	7	none	GCLKC_15	Comp_of_IOT1A	True	none	B16	B16
IOT21A	I/O	7	none		True_of_IOT21B	True	x16	G11	G11
IOT21B	I/O	7	none		Comp_of_IOT21A	True	none	F13	F13
IOT23A	I/O	7	none		True_of_IOT23B	True	x16	G15	G15
IOT23B	I/O	7	none		Comp_of_IOT23A	True	none	G16	G16
IOT25A/MODE0	I/O	7	none	MODE0	True_of_IOT25B	True	x16	H13	H13
IOT25B/MODE1	I/O	7	none	MODE1	Comp_of_IOT25A	True	none	H12	H12
IOT27A/MODE2	I/O	7	none	MODE2	True_of_IOT27B	True	x16	G12	G12
IOT27B/DONE	I/O	7	none	DONE	Comp_of_IOT27A	True	none	H14	H14
IOT3A/GCLKT_16	I/O	7	none	GCLKT_16	True_of_IOT3B	True	x16	E15	E15
IOT3B/GCLKC_16	I/O	7	none	GCLKC_16	Comp_of_IOT3A	True	none	E16	E16
IOT9A	I/O	7	none		True_of_IOT9B	True	x16	C15	C15
IOT9B	I/O	7	none		Comp_of_IOT9A	True	none	C16	C16
BANK6 True LVDS Pair									
IOL12A	I/O	6	DQ7		True_of_IOL12B	True	x16	E11	E11
IOL12B	I/O	6	DQ7		Comp_of_IOL12A	True	none	E10	E10
IOL14A/LPLL1_T_IN0	I/O	6	DQ7	LPLL1_T_IN0	True_of_IOL14B	True	x16	F9	F9
IOL14B	I/O	6	DQ7		Comp_of_IOL14A	True	none	F10	F10
IOL16A	I/O	6	DQ6/DQS_67		True_of_IOL16B	True	x16	B13	B13
IOL16B	I/O	6	DQ6/DQS_67		Comp_of_IOL16A	True	none	A13	A13

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
IOL18A	I/O	6	DQ6		True_of_IOL18B	True	x16	B12	B12
IOL18B	I/O	6	DQ6		Comp_of_IOL18A	True	none	A12	A12
IOL21A	I/O	6	DQS6/DQ6		True_of_IOL21B	True	x16	C11	C11
IOL21B	I/O	6	DQS6/DQ6		Comp_of_IOL21A	True	none	E9	E9
IOL23A	I/O	6	DQ6		True_of_IOL23B	True	x16	B11	B11
IOL23B	I/O	6	DQ6		Comp_of_IOL23A	True	none	A11	A11
IOL25A	I/O	6	DQ6		True_of_IOL25B	True	x16	B10	B10
IOL25B	I/O	6	DQ6		Comp_of_IOL25A	True	none	A10	A10
IOL27A	I/O	6	DQ6		True_of_IOL27B	True	x16	D9	D9
IOL27B	I/O	6	DQ6		Comp_of_IOL27A	True	none	C9	C9
IOL29A	I/O	6	none		True_of_IOL29B	True	x16	B9	B9
IOL29B	I/O	6	none		Comp_of_IOL29A	True	none	A9	A9
IOL3A/GCLKT_14/LPLL0_T_IN2	I/O	6	DQ7	GCLKT_14/LPLL0_T_IN2	True_of_IOL3B	True	x16	D14	D14
IOL3B/GCLKC_14/LPLL0_C_FB0	I/O	6	DQ7	GCLKC_14/LPLL0_C_FB0	Comp_of_IOL3A	True	none	C14	C14
IOL5A/GCLKT_13/LPLL0_T_IN1	I/O	6	DQ7	GCLKT_13/LPLL0_T_IN1	True_of_IOL5B	True	x16	D12	D12
IOL5B/GCLKC_13/LPLL0_C_FB1	I/O	6	DQ7	GCLKC_13/LPLL0_C_FB1	Comp_of_IOL5A	True	none	D11	D11
IOL7A	I/O	6	DQ7		True_of_IOL7B	True	x16	A15	A15
IOL7B	I/O	6	DQ7		Comp_of_IOL7A	True	none	F11	F11
IOL9A	I/O	6	DQS7/DQ7		True_of_IOL9B	True	x16	B14	B14
IOL9B	I/O	6	DQS7/DQ7		Comp_of_IOL9A	True	none	A14	A14
BANK5 True LVDS Pair									
IOB10A/D06/SSPI_CS_N	I/O	5	none	D06/SSPI_CS_N	True_of_IOB10B	True	x16	E6	E6
IOB10B/D05/SI/SSI0	I/O	5	none	D05/SI/SSI0	Comp_of_IOB10A	True	none	E7	E7
IOB12A/GCLKT_10B/D15/SSPI_WPN/SSI2	I/O	5	none	GCLKT_10B/D15/SSPI_WPN//SSI2	True_of_IOB12B	True	x16	C6	C6
IOB12B/GCLKC_10B/D07/DOUT/LPLL1_C_IN1	I/O	5	none	GCLKC_10B/D07/DOUT/LPLL1_C_IN1	Comp_of_IOB12A	True	none	A5	A5

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
IOB14A/SSPI_CLK	I/O	5	none	SSPI_CLK	True_of_IOB14B	True	x16	B6	B6
IOB14B/CLKHOLD_N/SSI3	I/O	5	none	CLKHOLD_N/SSI3	Comp_of_IOB14A	True	none	A6	A6
IOB16A	I/O	5	none		True_of_IOB16B	True	x16	D6	D6
IOB16B	I/O	5	none		Comp_of_IOB16A	True	none	D5	D5
IOB18A	I/O	5	none		True_of_IOB18B	True	x16	B4	B4
IOB18B	I/O	5	none		Comp_of_IOB18A	True	none	A4	A4
IOB20A	I/O	5	none		True_of_IOB20B	True	x16	F7	F7
IOB20B	I/O	5	none		Comp_of_IOB20A	True	none	F6	F6
IOB22A	I/O	5	none		True_of_IOB22B	True	x16	B5	B5
IOB22B	I/O	5	none		Comp_of_IOB22A	True	none	A2	A2
IOB24A	I/O	5	none		True_of_IOB24B	True	x16	D3	D3
IOB24B	I/O	5	none		Comp_of_IOB24A	True	none	C3	C3
IOB26A/GCLKT_12	I/O	5	none	GCLKT_12	True_of_IOB26B	True	x16	B3	B3
IOB26B/GCLKC_12	I/O	5	none	GCLKC_12	Comp_of_IOB26A	True	none	A3	A3
IOB2A	I/O	5	none		True_of_IOB2B	True	x16	B8	B8
IOB2B	I/O	5	none		Comp_of_IOB2A	True	none	A8	A8
IOB4A/D04/SDA	I/O	5	none	D04/SDA	True_of_IOB4B	True	x16	B7	B7
IOB4B/D09/SCL/LPLL1_C_FB0	I/O	5	none	D09/SCL/LPLL1_C_FB0	Comp_of_IOB4A	True	none	A7	A7
IOB6A	I/O	5	none		True_of_IOB6B	True	x16	C8	C8
IOB6B	I/O	5	none		Comp_of_IOB6A	True	none	D8	D8
IOB8A/D03/SO/SSI1	I/O	5	none	D03/SO/SSI1	True_of_IOB8B	True	x16	F8	F8
IOB8B/D02	I/O	5	none	D02	Comp_of_IOB8A	True	none	E8	E8

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
BANK4 True LVDS Pair									
IOB29A/GCLKT_11A	I/O	4	none	GCLKT_11A	True_of_IOB29B	True	x16	B1	B1
IOB29B/GCLKC_11A	I/O	4	none	GCLKC_11A	Comp_of_IOB29A	True	none	D4	D4
IOB31A/GCLKT_10A/D14	I/O	4	none	GCLKT_10A/D14	True_of_IOB31B	True	x16	C2	C2
IOB31B/GCLKC_10A/D01/MOSI/MI0/BPLL_C_FB0	I/O	4	none	GCLKC_10A/D01/MOSI/MI0/BPLL_C_FB0	Comp_of_IOB31A	True	none	C1	C1
IOB33A/GCLKT_9/D13/MCS_N/BPLL_T_IN1	I/O	4	none	GCLKT_9/D13/MCS_N/BPLL_T_IN1	True_of_IOB33B	True	x16	D2	D2
IOB33B/GCLKC_9	I/O	4	none	GCLKC_9	Comp_of_IOB33A	True	none	D1	D1
IOB35A/GCLKT_8	I/O	4	none	GCLKT_8	True_of_IOB35B	True	x16	E5	E5
IOB35B/GCLKC_8	I/O	4	none	GCLKC_8	Comp_of_IOB35A	True	none	E1	E1
IOB37A/D08	I/O	4	none	D08	True_of_IOB37B	True	x16	G5	G5
IOB37B	I/O	4	none		Comp_of_IOB37A	True	none	F5	F5
IOB45A	I/O	4	none		True_of_IOB45B	True	x16	F3	F3
IOB50A/D11	I/O	4	none	D11	True_of_IOB50B	True	x16	F2	F2
IOB50B/D12	I/O	4	none	D12	Comp_of_IOB50A	True	none	F1	F1
IOB52A/RECONFIG_N	I/O	4	none	RECONFIG_N	True_of_IOB52B	True	x16	H5	H5
IOB54A/GCLKT_11B/D10/MI2	I/O	4	none	GCLKT_11B/D10/MI2	True_of_IOB54B	True	x16	G2	G2
IOB54B/GCLKC_11B/MI3/BPLL_C_FB1	I/O	4	none	GCLKC_11B/MI3/BPLL_C_FB1	Comp_of_IOB54A	True	none	G1	G1
IOB58A/D00/MISO/MI1/DIN	I/O	4	none	D00/MISO/MI1/DIN	True_of_IOB58B	True	x16	H2	H2
IOB62A/CCLK	I/O	4	none	CCLK	True_of_IOB62B	True	x16	H1	H1
IOB62B/CSI_B	I/O	4	none	CSI_B	Comp_of_IOB62A	True	none	J3	J3

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
BANK3 True LVDS Pair									
IOB65A	I/O	3	none		True_of_IOB65B	True	x16	J6	J6
IOB65B	I/O	3	none		Comp_of_IOB65A	True	none	L3	L3
IOB69A	I/O	3	DQ5		True_of_IOB69B	True	x16	J2	J2
IOB69B	I/O	3	DQ5		Comp_of_IOB69A	True	none	J1	J1
IOB71A	I/O	3	DQ5		True_of_IOB71B	True	x16	K2	K2
IOB71B	I/O	3	DQ5		Comp_of_IOB71A	True	none	K1	K1
IOB73A	I/O	3	DQ5		True_of_IOB73B	True	x16	K5	K5
IOB73B	I/O	3	DQ5		Comp_of_IOB73A	True	none	L4	L4
IOB75A/GCLKT_6B	I/O	3	DQS5/DQ5	GCLKT_6B	True_of_IOB75B	True	x16	L2	L2
IOB75B/GCLKC_6B	I/O	3	DQS5/DQ5	GCLKC_6B	Comp_of_IOB75A	True	none	L1	L1
IOB77A	I/O	3	DQ5		True_of_IOB77B	True	x16	N2	N2
IOB77B	I/O	3	DQ5		Comp_of_IOB77A	True	none	N1	N1
IOB79A	I/O	3	DQ5/DQS_45		True_of_IOB79B	True	x16	P2	P2
IOB79B	I/O	3	DQ5/DQS_45		Comp_of_IOB79A	True	none	P1	P1
IOB85B	I/O	3	DQS4/DQ4		Comp_of_IOB85A	True	none	R1	R1
IOB89A/GCLKT_7/BPLL_T_IN0	I/O	3	DQ4	GCLKT_7/BPLL_T_IN0	True_of_IOB89B	True	x16	M2	M2
IOB89B/GCLKC_7	I/O	3	DQ4	GCLKC_7	Comp_of_IOB89A	True	none	M1	M1
IOB91A/GCLKT_6A	I/O	3	DQ4	GCLKT_6A	True_of_IOB91B	True	x16	K6	K6
IOB91B/GCLKC_6A	I/O	3	DQ4	GCLKC_6A	Comp_of_IOB91A	True	none	L6	L6
BANK2 True LVDS Pair									
IOR12A	I/O	2	DQS2/DQ2		True_of_IOR12B	True	x16	R7	R7
IOR12B	I/O	2	DQS2/DQ2		Comp_of_IOR12A	True	none	T7	T7
IOR14A	I/O	2	DQ2		True_of_IOR14B	True	x16	R6	R6
IOR14B	I/O	2	DQ2		Comp_of_IOR14A	True	none	T6	T6
IOR18A	I/O	2	DQ2/DQS_23		True_of_IOR18B	True	x16	R5	R5

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
IOR18B	I/O	2	DQ2/DQS_23		Comp_of_IOR18A	True	none	T5	T5
IOR20A	I/O	2	DQ3		True_of_IOR20B	True	x16	P6	P6
IOR20B	I/O	2	DQ3		Comp_of_IOR20A	True	none	M6	M6
IOR22A	I/O	2	DQ3		True_of_IOR22B	True	x16	N5	N5
IOR22B	I/O	2	DQ3		Comp_of_IOR22A	True	none	N6	N6
IOR24A	I/O	2	DQ3		True_of_IOR24B	True	x16	R3	R3
IOR24B	I/O	2	DQ3		Comp_of_IOR24A	True	none	T3	T3
IOR26A	I/O	2	DQS3/DQ3		True_of_IOR26B	True	x16	M7	M7
IOR26B	I/O	2	DQS3/DQ3		Comp_of_IOR26A	True	none	K8	K8
IOR29A	I/O	2	DQ3		True_of_IOR29B	True	x16	N3	N3
IOR29B	I/O	2	DQ3		Comp_of_IOR29A	True	none	P3	P3
IOR31A/GCLKT_4/RPLL1_T_IN0	I/O	2	DQ3	GCLKT_4/RPLL1_T_IN0	True_of_IOR31B	True	x16	R4	R4
IOR31B/GCLKC_4/RPLL1_C_FB1	I/O	2	DQ3	GCLKC_4/RPLL1_C_FB1	Comp_of_IOR31A	True	none	T4	T4
IOR33A/GCLKT_5/RPLL1_T_IN1	I/O	2	none	GCLKT_5/RPLL1_T_IN1	True_of_IOR33B	True	x16	T2	T2
IOR33B/GCLKC_5/RPLL1_C_FB0	I/O	2	none	GCLKC_5/RPLL1_C_FB0	Comp_of_IOR33A	True	none	L7	L7
IOR5A	I/O	2	none		True_of_IOR5B	True	x16	R8	R8
IOR5B	I/O	2	none		Comp_of_IOR5A	True	none	T8	T8
IOR7A	I/O	2	DQ2		True_of_IOR7B	True	x16	N8	N8
IOR7B	I/O	2	DQ2		Comp_of_IOR7A	True	none	P8	P8
IOR9A	I/O	2	DQ2		True_of_IOR9B	True	x16	L8	L8
IOR9B	I/O	2	DQ2		Comp_of_IOR9A	True	none	M8	M8
BANK1 True LVDS Pair									
IOT61A/GCLKT_2/TPLL_T_IN0	I/O	1	DQS0/DQ0	GCLKT_2/TPLL_T_IN0	True_of_IOT61B	True	x16	T14	T14
IOT61B/GCLKC_2	I/O	1	DQS0/DQ0	GCLKC_2	Comp_of_IOT61A	True	none	T15	T15
IOT63A/GCLKT_3/RPLL0_T_IN0	I/O	1	DQ0	GCLKT_3/RPLL0_T_IN0	True_of_IOT63B	True	x16	P14	P14
IOT63B/GCLKC_3	I/O	1	DQ0	GCLKC_3	Comp_of_IOT63A	True	none	L11	L11

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
IOT66A	I/O	1	DQ0		True_of_IOT66B	True	x16	R14	R14
IOT70A	I/O	1	DQ0		True_of_IOT70B	True	x16	M11	M11
IOT70B	I/O	1	DQ0		Comp_of_IOT70A	True	none	N12	N12
IOT72A	I/O	1	DQ0		True_of_IOT72B	True	x16	R13	R13
IOT72B	I/O	1	DQ0		Comp_of_IOT72A	True	none	T13	T13
IOT74A	I/O	1	DQ1/DQS_01		True_of_IOT74B	True	x16	K10	K10
IOT74B	I/O	1	DQ1/DQS_01		Comp_of_IOT74A	True	none	L10	L10
IOT76A	I/O	1	DQ1		True_of_IOT76B	True	x16	R12	R12
IOT76B	I/O	1	DQ1		Comp_of_IOT76A	True	none	T12	T12
IOT78A	I/O	1	DQ1		True_of_IOT78B	True	x16	M10	M10
IOT78B	I/O	1	DQ1		Comp_of_IOT78A	True	none	N11	N11
IOT80A	I/O	1	DQS1/DQ1		True_of_IOT80B	True	x16	R10	R10
IOT80B	I/O	1	DQS1/DQ1		Comp_of_IOT80A	True	none	T10	T10
IOT83A	I/O	1	DQ1		True_of_IOT83B	True	x16	R11	R11
IOT83B	I/O	1	DQ1		Comp_of_IOT83A	True	none	T11	T11
IOT85A	I/O	1	DQ1		True_of_IOT85B	True	x16	M9	M9
IOT85B	I/O	1	DQ1		Comp_of_IOT85A	True	none	N9	N9
IOT87A	I/O	1	none		True_of_IOT87B	True	x16	K9	K9
IOT87B	I/O	1	none		Comp_of_IOT87A	True	none	L9	L9
IOT89A/RPLL0_T_FB1	I/O	1	none	RPLL0_T_FB1	True_of_IOT89B	True	x16	P9	P9
IOT89B	I/O	1	none		Comp_of_IOT89A	True	none	P11	P11
IOT91A/RPLL0_T_IN1	I/O	1	none	RPLL0_T_IN1	True_of_IOT91B	True	x16	R9	R9
IOT91B/RPLL0_C_FB0	I/O	1	none	RPLL0_C_FB0	Comp_of_IOT91A	True	none	T9	T9

Note!
[1] EV version
[2] LV version

Pin Name	Function	BANK	DQS	Configuration Function	Differential Pair	LVDS	X16	PG256C[1]	PG256C[2]
BANK0 True LVDS Pair									
IOT31A	I/O	0	none		True_of_IOT31B	True	x16	J12	J12
IOT31B	I/O	0	none		Comp_of_IOT31A	True	none	J14	J14
IOT33A	I/O	0	none		True_of_IOT33B	True	x16	J15	J15
IOT33B	I/O	0	none		Comp_of_IOT33A	True	none	J16	J16
IOT37A	I/O	0	none		True_of_IOT37B	True	x16	L15	L15
IOT37B	I/O	0	none		Comp_of_IOT37A	True	none	L16	L16
IOT39A	I/O	0	none		True_of_IOT39B	True	x16	J13	J13
IOT39B	I/O	0	none		Comp_of_IOT39A	True	none	L13	L13
IOT41A	I/O	0	none		True_of_IOT41B	True	x16	L14	L14
IOT43A	I/O	0	none		True_of_IOT43B	True	x16	J11	J11
IOT43B	I/O	0	none		Comp_of_IOT43A	True	none	K12	K12
IOT45A	I/O	0	none		True_of_IOT45B	True	x16	N15	N15
IOT45B	I/O	0	none		Comp_of_IOT45A	True	none	N16	N16
IOT48A	I/O	0	none		True_of_IOT48B	True	x16	L12	L12
IOT48B	I/O	0	none		Comp_of_IOT48A	True	none	K11	K11
IOT50A	I/O	0	none		True_of_IOT50B	True	x16	R16	R16
IOT50B	I/O	0	none		Comp_of_IOT50A	True	none	P16	P16
IOT52A	I/O	0	none		True_of_IOT52B	True	x16	N14	N14
IOT52B	I/O	0	none		Comp_of_IOT52A	True	none	P15	P15
IOT54A	I/O	0	none		True_of_IOT54B	True	x16	N13	N13
IOT54B	I/O	0	none		Comp_of_IOT54A	True	none	M12	M12
IOT56A/GCLKT_0/TPLL_T_IN1	I/O	0	none	GCLKT_0/TPLL_T_IN1	True_of_IOT56B	True	x16	K15	K15
IOT56B/GCLKC_0/TPLL_C_FB1	I/O	0	none	GCLKC_0/TPLL_C_FB1	Comp_of_IOT56A	True	none	K16	K16
IOT58A/GCLKT_1/TPLL_T_IN2	I/O	0	none	GCLKT_1/TPLL_T_IN2	True_of_IOT58B	True	x16	M15	M15
IOT58B/GCLKC_1/TPLL_C_FB0	I/O	0	none	GCLKC_1/TPLL_C_FB0	Comp_of_IOT58A	True	none	M16	M16

Recommended Operating Conditions of MG121N Package in GW5A-25			
Name	**Description**	**Min.**	**Max.**
VCC	Core voltage	0.87V	1V
VCCIO4	I/O Bank voltage	1.14V	3.465V
VCC/VCCC	Core voltage and clock tree voltage are internally short-circuited.	0.87V	1V
VQPS	Voltage required for eFuse writing	1.8V	1.8V
VCC_REG	Regulator voltage	1.14V	3.3V
M0_VDDA	MIPI M0 analog core voltage	0.87V	1V
M0_VDDD	MIPI M0 digital core voltage	0.87V	1V
M0_VDD_12/VCC_EXT	MIPI voltage M0_VDD_12 and VCC_EXT are internally short-circuited.	1.14V	1.32V
M0_VDDX/VCCX	MIPI voltage M0_VDDX and auxilliary voltage VCCX are internally short-circuited.	2.375V	3.465V
VCCIO0/VCCIO1	I/O Bank voltage VCCIO0/VCCIO1 are internally short-circuited.	1.14V	3.465V
VCCIO10/VCCIO5	I/O Bank voltage VCCIO10/VCCIO5 are internally short-circuited.	1.14V	3.465V
VCCIO2/VCCIO3	I/O Bank voltage VCCIO2/VCCIO3 are internally short-circuited.	1.14V	3.465V
VCCIO6/VCCIO7	I/O Bank voltage VCCIO6/VCCIO7 are internally short-circuited.	1.14V	3.465V
Recommended Operating Conditions of UG324S Package in GW5A-25			
Name	**Description**	**Min.**	**Max.**
VQPS	Voltage required for eFuse writing	1.62V	1.98V
VCC_REG	Regulator voltage	1.14V	3.3V
VCC_EXT	VCC_EXT voltage	1.14V	1.32V
VCCIO10/VCCX	Auxilliary voltage VCCX and I/O Bank voltage VCCIO10 are internally short-circuited.	2.375V	3.465V
VCCIO0/VCCIO1	I/O Bank voltage VCCIO0/VCCIO1 are internally short-circuited.	1.14V	3.465V
VCCIO2/VCCIO3	I/O Bank voltage VCCIO2/VCCIO3 are internally short-circuited.	1.14V	3.465V
VCCIO4/VCCIO5	I/O Bank voltage VCCIO4/VCCIO5 are internally short-circuited.	1.14V	3.465V
VCCIO6/VCCIO7	I/O Bank voltage VCCIO6/VCCIO7 are internally short-circuited.	1.14V	3.465V

Recommended Operating Conditions of UG256C/PG256C Package in GW5A-25 (EV Version)			
Name	**Description**	**Min.**	**Max.**
VCCIO0	I/O Bank voltage	1.14V	3.465V
VCCIO1	I/O Bank voltage	1.14V	3.465V
VCCIO2	I/O Bank voltage	1.14V	3.465V
VCCIO3	I/O Bank voltage	1.14V	3.465V
VCCIO5	I/O Bank voltage	1.14V	3.465V
VCCIO6	I/O Bank voltage	1.14V	3.465V
VCCIO7	I/O Bank voltage	1.14V	3.465V
VCCIO10/VCCIO4	I/O Bank voltage VCCIO10/VCCIO4 are internally short-circuited.	1.14V	3.465V
VQPS	Voltage required for eFuse writing	1.62V	1.98V
VCCX	Auxilliary voltage	2.375V	3.465V
VCC_REG	Regulator voltage	1.14V	3.3V
VCC_EXT	VCC/VCCC Regulator and MIPI LP voltage	1.14V	1.32V
Recommended Operating Conditions of UG256C/PG256C Package in GW5A-25 (LV Version)			
Name	**Description**	**Min.**	**Max.**
VCCIO0	I/O Bank voltage	1.14V	3.465V
VCCIO1	I/O Bank voltage	1.14V	3.465V
VCCIO2	I/O Bank voltage	1.14V	3.465V
VCCIO3	I/O Bank voltage	1.14V	3.465V
VCCIO5	I/O Bank voltage	1.14V	3.465V
VCCIO6	I/O Bank voltage	1.14V	3.465V
VCCIO7	I/O Bank voltage	1.14V	3.465V
VCCIO10/VCCIO4	I/O Bank voltage VCCIO10/VCCIO4 are internally short-circuited.	1.14V	3.465V
VQPS	Voltage required for eFuse writing	1.62V	1.98V
VCCX	Auxilliary voltage	2.375V	3.465V
VCC/VCCC	Core voltage and clock tree voltage are internally short-circuited.	0.87V	1V
VCC_REG	Regulator voltage	1.14V	3.3V

Recommended Operating Conditions of UG324 Package in GW5A-25			
Name	**Description**	**Min.**	**Max.**
VCCIO0/VCCIO1	I/O Bank voltage VCCIO0/VCCIO1 are internally short-circuited.	1.14V	3.465V
VCCIO2/VCCIO3	I/O Bank voltage VCCIO2/VCCIO3 are internally short-circuited.	1.14V	3.465V
VCCIO4/VCCIO5	I/O Bank voltage VCCIO4/VCCIO5 are internally short-circuited.	1.14V	3.465V
VCCIO6/VCCIO7	I/O Bank voltage VCCIO6/VCCIO7 are internally short-circuited.	1.14V	3.465V
M0_VDDA/M0_VDDD/VCC/VCCC	MIPI voltage M0_VDDA and M0_VDDD, core voltage, and clock tree voltage are internally short-circuited.	0.87V	1V
M0_VDDX/VCCIO10/VCCX	MIPI voltage M0_VDDX, I/O Bank voltage, VCCIO10 and auxilliary voltage VCCX are internally short-circuited.	2.375V	3.465V
VCC_REG	Regulator voltage	1.14V	3.3V
M0_VDD_12	MIPI voltage M0_VDD_12	1.14V	1.32V
VCC_EXT	MIPI LP voltage	1.14V	1.32V
VQPS	Voltage required for eFuse writing	1.62V	1.98V
Recommended Operating Conditions of MG196S/UG225S Package in GW5A-25 (EV Version)			
Name	**Description**	**Min.**	**Max.**
VCCIO0, VCCIO1, VCCIO2, VCCIO3, VCCIO4, VCCIO5, VCCIO6, VCCIO7	I/O Bank voltage	1.14V	3.465V
VCCIO10/VCCX	I/O Bank voltage VCCIO10 and auxilliary voltage VCCX are internally short-circuited.	2.375V	3.465V
VCC_REG	Regulator voltage	1.14V	3.3V
VCC_EXT	VCC/VCCC Regulator and MIPI LP voltage	1.14V	1.32V
VQPS	Voltage required for eFuse writing	1.62V	1.98V
Recommended Operating Conditions of MG196S/UG225S Package in GW5A-25 (LV Version)			
Name	**Description**	**Min.**	**Max.**
VCCIO0, VCCIO1, VCCIO2, VCCIO3, VCCIO4, VCCIO5, VCCIO6, VCCIO7	I/O Bank voltage	1.14V	3.465V
VCCIO10/VCCX	I/O Bank voltage VCCIO10 and auxilliary voltage VCCX are internally short-circuited.	2.375V	3.465V
VCC_REG	Regulator voltage	1.14V	3.3V
VCC/VCCC	Core voltage and Clock tree voltage are internally short-circuited.	0.87V	1V
VQPS	Voltage required for eFuse writing	1.62V	1.98V

Recommended Operating Conditions of LQ100 Package in GW5A-25 (EV Version)			
Name	Description	Min.	Max.
VCCIO1, VCCIO2, VCCIO6, VCCIO7	I/O Bank voltage	1.14V	3.465V
VCCIO3/VCCIO4/VCCIO5	I/O Bank voltage VCCIO3/VCCIO4/VCCIO5 are internally short-circuited.	1.14V	3.465V
VCCIO10/VCCX	I/O Bank voltage VCCIO10 and auxilliary voltage VCCX are internally short-circuited.	2.375V	3.465V
VCC_EXT/VCC_REG/VCCIO0	VCC/VCCC Regulator is internally short-circuited with MIPI LP voltage, Regulator voltage, and I/O Bank voltage VCCIO0.	1.14V	1.32V
Recommended Operating Conditions of PG256S Package in GW5A-25 (EV Version)			
Name	Description	Min.	Max.
VCCIO0, VCCIO1, VCCIO2, VCCIO3, VCCIO4, VCCIO5, VCCIO6, VCCIO7	I/O Bank voltage	1.14V	3.465V
VCCIO10/VCCX	I/O Bank voltage VCCIO10 and auxilliary voltage VCCX are internally short-circuited.	2.375V	3.465V
VCC_REG	Regulator voltage	1.14V	3.3V
VCC_EXT	VCC/VCCC Regulator and MIPI LP voltage	1.14V	1.32V
VQPS	Voltage required for eFuse writing	1.62V	1.98V