ADA-MIPI20-RISCV-24 Adapter Documentation
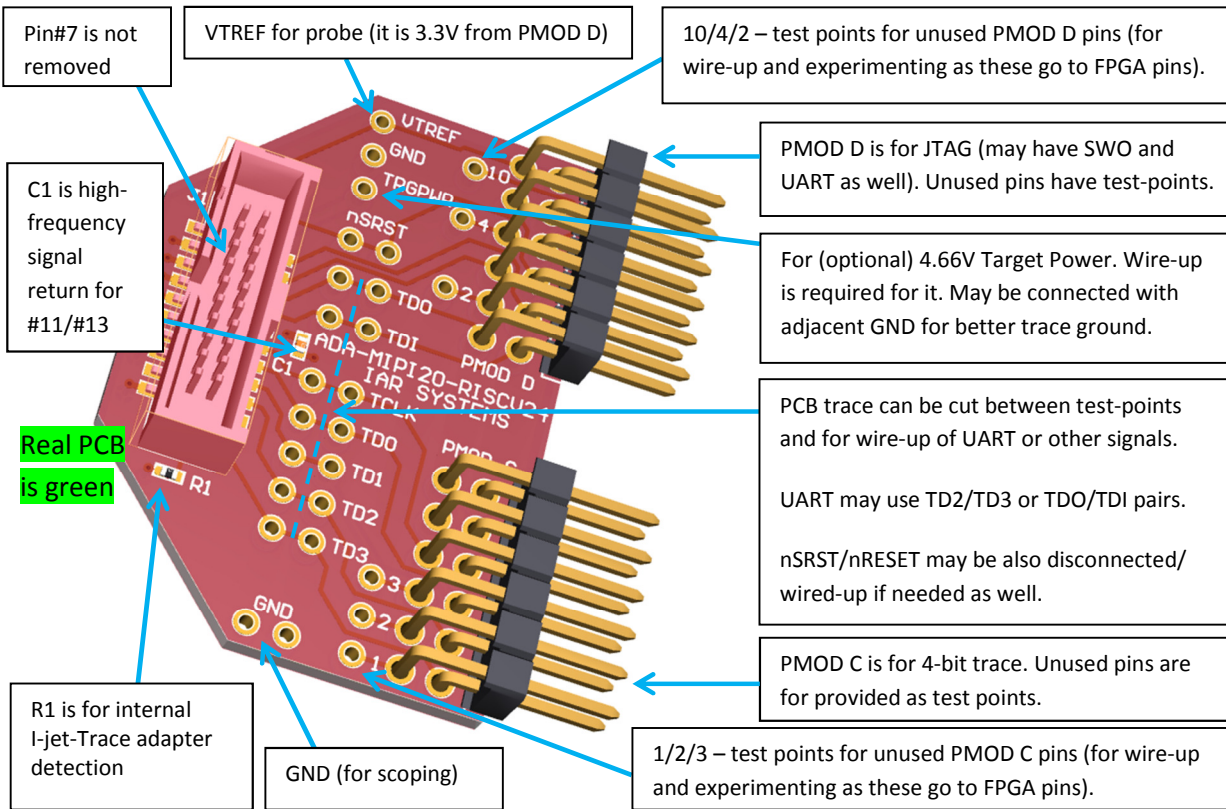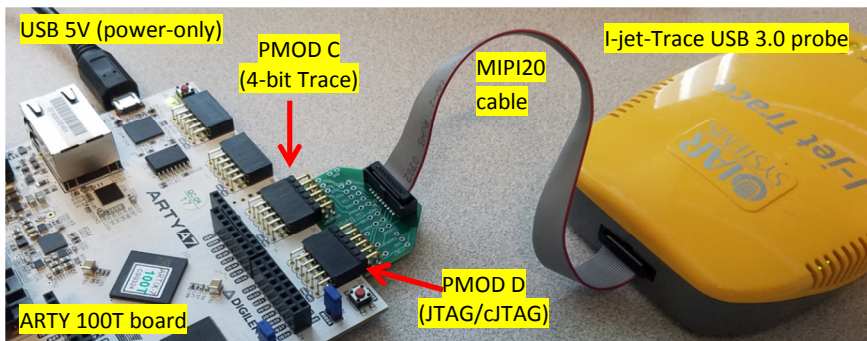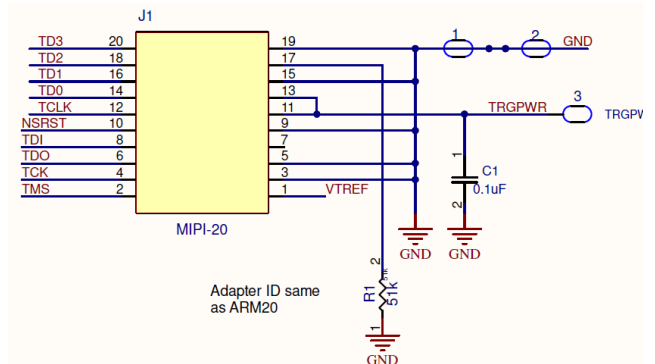
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Pin#7 is not removed

VTREF for probe (it is 3.3V from PMOD D)

10/4/2 – test points for unused PMOD D pins (for wire-up and experimenting as these go to FPGA pins).

C1 is high-frequency signal return for #11/#13

PMOD D is for JTAG (may have SWO and UART as well). Unused pins have test-points.

For (optional) 4.66V Target Power. Wire-up is required for it. May be connected with adjacent GND for better trace ground.

Real PCB is green

PCB trace can be cut between test-points and for wire-up of UART or other signals.

UART may use TD2/TD3 or TDO/TDI pairs.

nSRST/nRESET may be also disconnected/ wired-up if needed as well.

R1 is for internal I-jet-Trace adapter detection

GND (for scoping)

PMOD C is for 4-bit trace. Unused pins are for provided as test points.

1/2/3 – test points for unused PMOD C pins (for wire-up and experimenting as these go to FPGA pins).

Part of schematic with PMOD D (JTAG) and PMOD C (Trace)

Test points for PMOD D #2/4/10 not shown

TMS and TCK do not have TPs

Some PCB traces may be cut for experiments

All signals (VTREF/TCK/…) go to corresponding MIPI20 pins
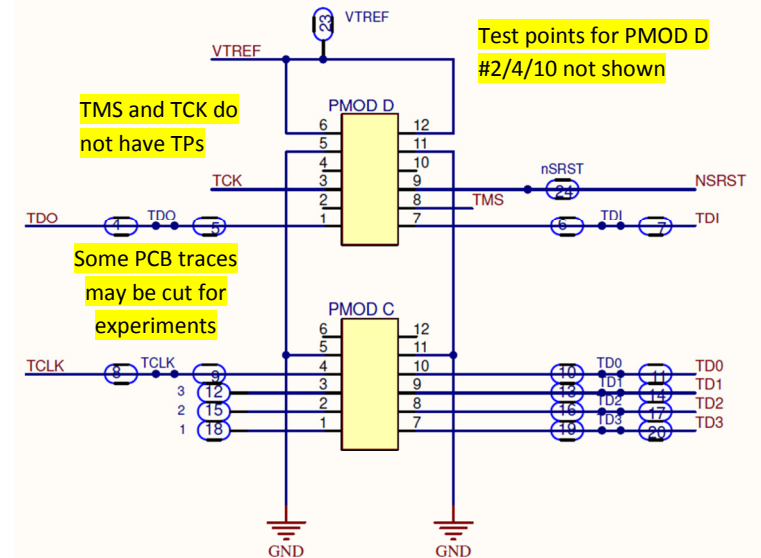
Part of schematic with MIPI20 connector

Adapter ID same as ARM20

USB 5V (power-only)

PMOD C (4-bit Trace)

MIPI20 cable

I-jet-Trace USB 3.0 probe

ARTY PMOD D&C pinout (looking from outside of PCB edge)

VCC GND   8 signals

Pin 6   Pin 1

Pin 12

ARTY 100T board

PMOD D (JTAG/cJTAG)