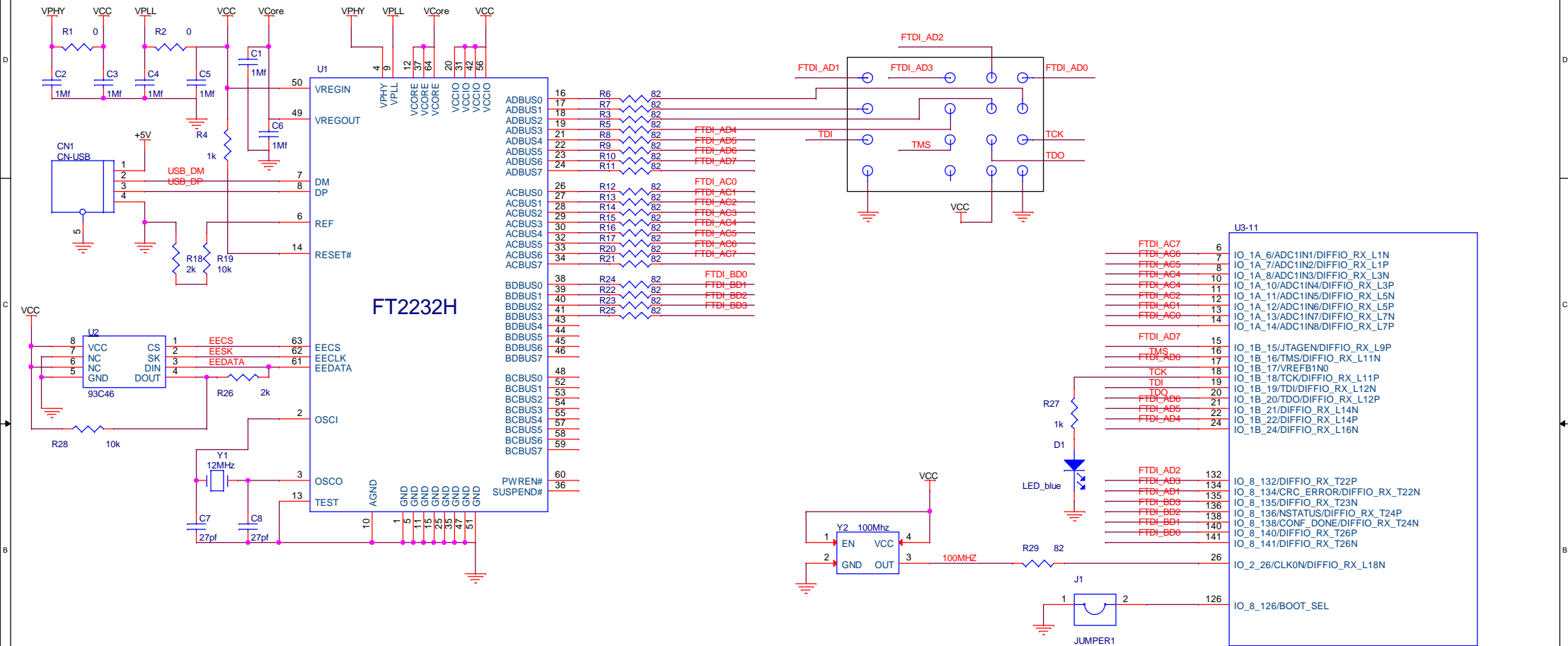


PROGRAMMING, PC-CONNECTING & CLOCK

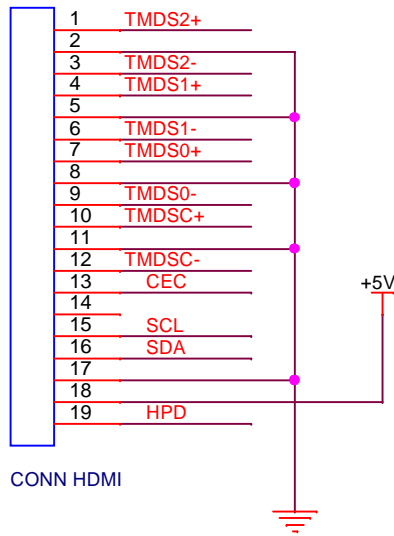


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HDMI & CONNECTORS

J2



CONN HDMI

TMDSC-	R30	33	120
TMDSC+	R31	33	121
TMDSC0-	R32	33	122
TMDSC0+	R33	33	123
TMDSC1-	R34	33	124
TMDSC1+	R35	33	127
TMDSC2-	R36	33	130
TMDSC2+	R37	33	131

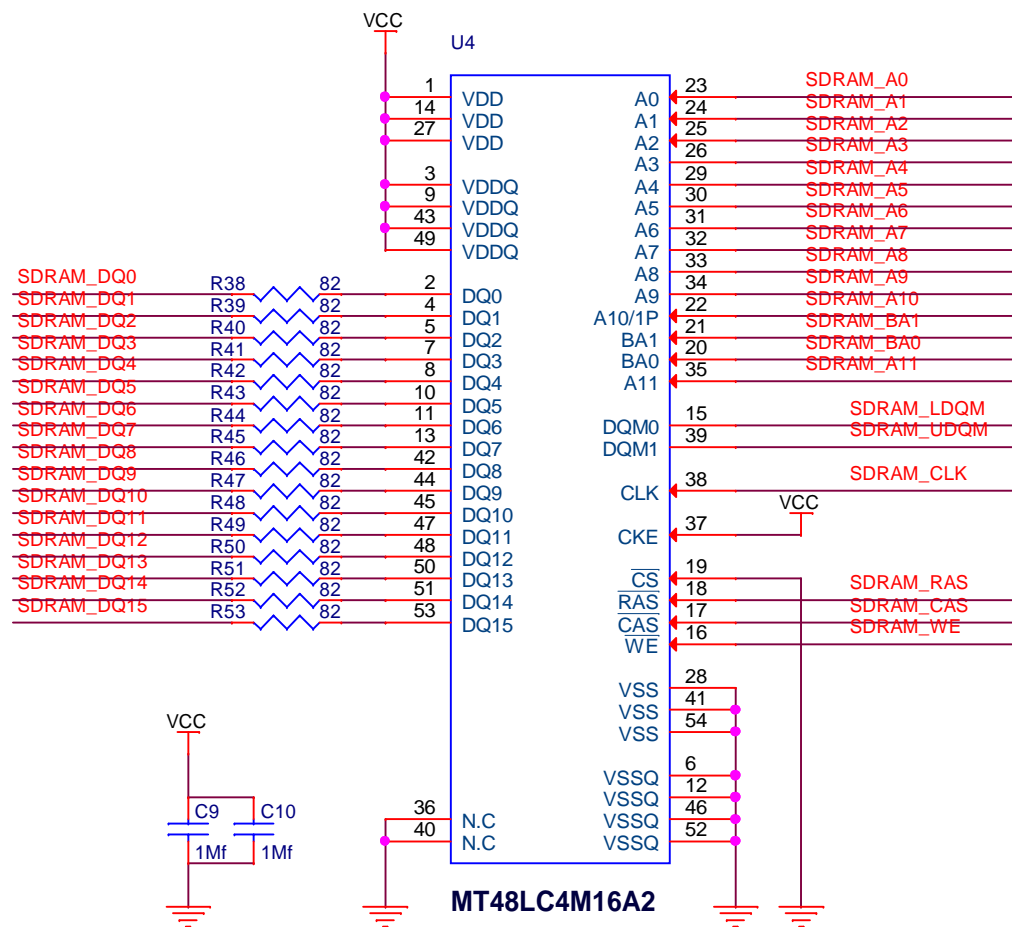
U3-9

IO_8_120/DIFFIO_RX_T16P
IO_8_121/DEV_CLRN/DIFFIO_RX_T16N
IO_8_122/DEV_OE/DIFFIO_RX_T18P
IO_8_123/VREFB8N0
IO_8_124/DIFFIO_RX_T19P
IO_8_127/DIFFIO_RX_T19N
IO_8_130/DIFFIO_RX_T20P
IO_8_131/DIFFIO_RX_T20N

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SDR SDRAM INTERFACE

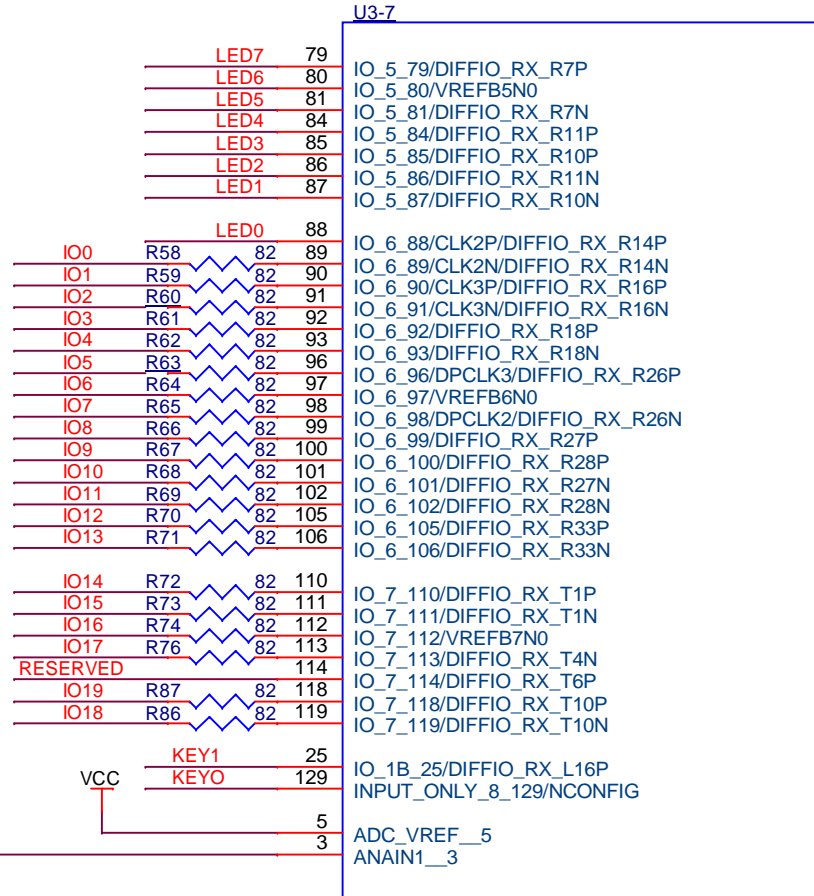
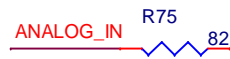
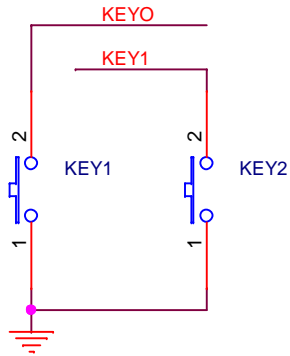
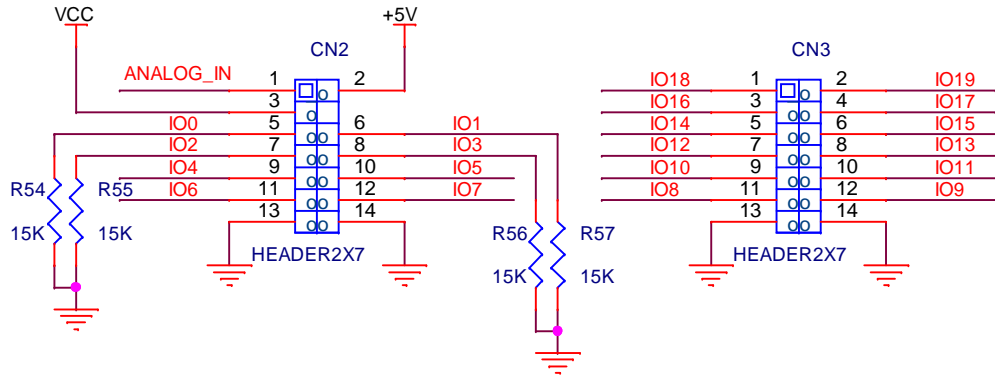


SDRAM_DQ15	27	IO_2_27/CLK0P/DIFFIO_RX_L18P
SDRAM_DQ14	28	IO_2_28/CLK1N/DIFFIO_RX_L20N
SDRAM_DQ13	29	IO_2_29/CLK1P/DIFFIO_RX_L20P
SDRAM_DQ12	30	IO_2_30/VREFB2N0
SDRAM_DQ11	32	IO_2_32/PLL_L_CLKOUTN/DIFFIO_RX_L27N
SDRAM_DQ10	33	IO_2_33/PLL_L_CLKOUTP/DIFFIO_RX_L27P
SDRAM_DQ9	38	IO_3_38/DIFFIO_TX_RX_B1N
SDRAM_DQ8	39	IO_3_39/DIFFIO_TX_RX_B1P
SDRAM_UDQM	41	IO_3_41/DIFFIO_TX_RX_B3N
SDRAM_CLK	43	IO_3_43/DIFFIO_TX_RX_B3P
SDRAM_A4	44	IO_3_44/DIFFIO_TX_RX_B5N
SDRAM_A5	45	IO_3_45/DIFFIO_TX_RX_B5P
SDRAM_A6	46	IO_3_46/DIFFIO_TX_RX_B7N
SDRAM_A7	47	IO_3_47/DIFFIO_TX_RX_B7P
SDRAM_A8	48	IO_3_48/VREFB3N0
SDRAM_A9	50	IO_3_50/DIFFIO_TX_RX_B9N
SDRAM_BA0	52	IO_3_52/DIFFIO_TX_RX_B9P
SDRAM_A11	54	IO_3_54/DIFFIO_TX_RX_B10N
SDRAM_BA1	55	IO_3_55/DIFFIO_TX_RX_B12N
SDRAM_A10	56	IO_3_56/DIFFIO_TX_RX_B12P
SDRAM_A0	57	IO_3_57/DIFFIO_TX_RX_B14N
SDRAM_A1	58	IO_3_58/DIFFIO_TX_RX_B14P
SDRAM_A2	59	IO_3_59/DIFFIO_TX_RX_B16N
SDRAM_A3	60	IO_3_60/DIFFIO_TX_RX_B16P
SDRAM_RAS	61	IO_4_61/VREFB4N0
SDRAM_CAS	62	IO_4_62/DIFFIO_TX_RX_B20P
SDRAM_WE	64	IO_4_64/DIFFIO_TX_RX_B23N
SDRAM_LDQM	65	IO_4_65/DIFFIO_TX_RX_B23P
SDRAM_DQ7	66	IO_4_66/DIFFIO_TX_RX_B25N
SDRAM_DQ6	69	IO_4_69/DIFFIO_TX_RX_B27N
SDRAM_DQ5	70	IO_4_70/DIFFIO_TX_RX_B27P
SDRAM_DQ4	74	IO_5_74/DIFFIO_RX_R2P
SDRAM_DQ3	75	IO_5_75/DIFFIO_RX_R1P
SDRAM_DQ2	76	IO_5_76/DIFFIO_RX_R2N
SDRAM_DQ1	77	IO_5_77/DIFFIO_RX_R1N
SDRAM_DQ0	78	IO_5_78

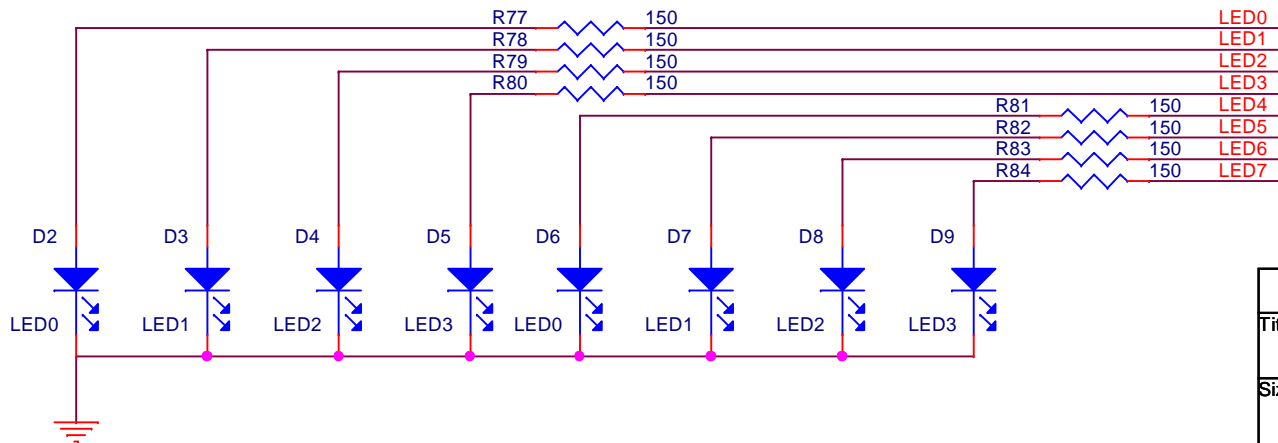
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SHIELD CONNECTOR, KEYS & LEDS

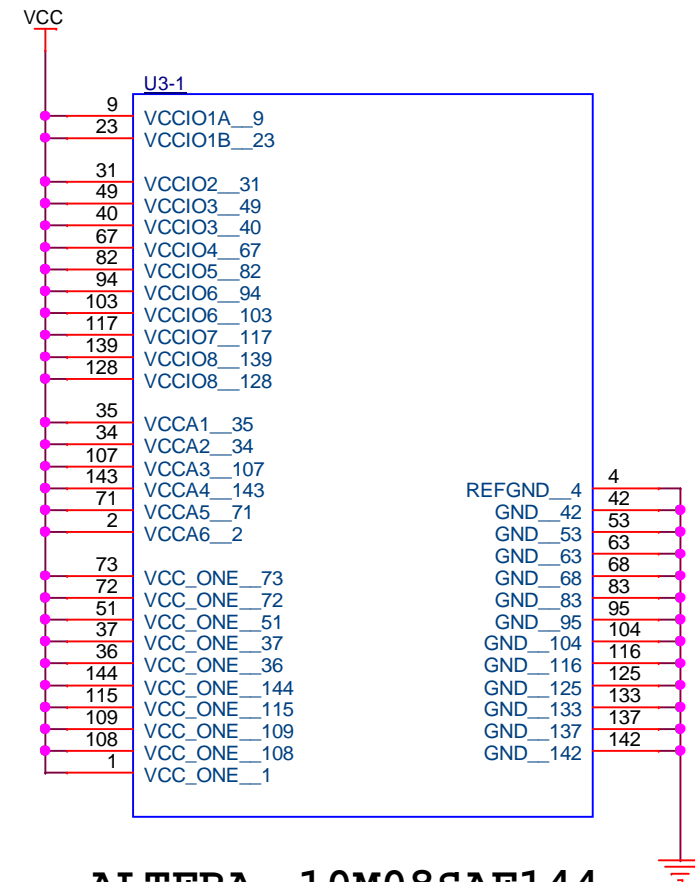
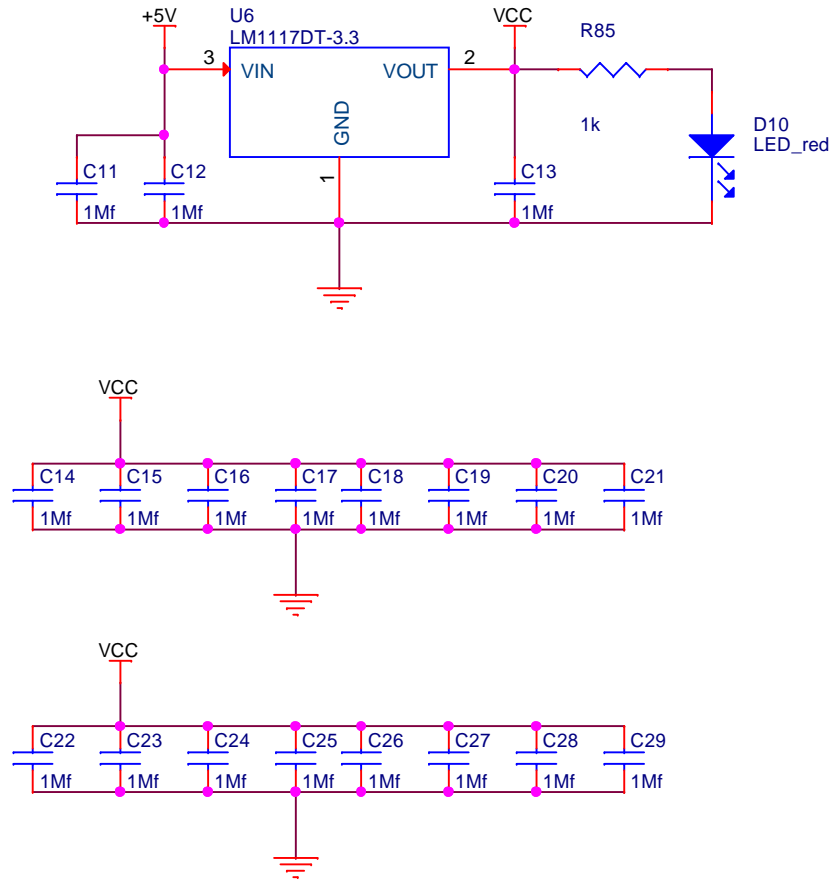


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MAX10 POWERING



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